

ZL8

CPU CORE
SENTECH
SC451ITSTR
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SYSTEM 3V/5V
MAXIM
MAX1999
 +3VPCU
 +3V_S5/+3VSUS
 +3V
 +5VPCU
 +5VSUS
 +5V
 +15V
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+1.8VSUS
 +1.8V
 +0.9VSUS
 +0.9V ON
 NCP5214
 +1.5V SENTECH
 SC1470
 +1.5V_S5
 SI9183-AD
 +2.5V SENTECH
 SC1565
 +1.05V SENTECH
 SC4215
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MAX8724
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Bluetooth
USB
interface
USB4
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CRT
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(Option)
MINI-PCIE slot
Wireless LAN
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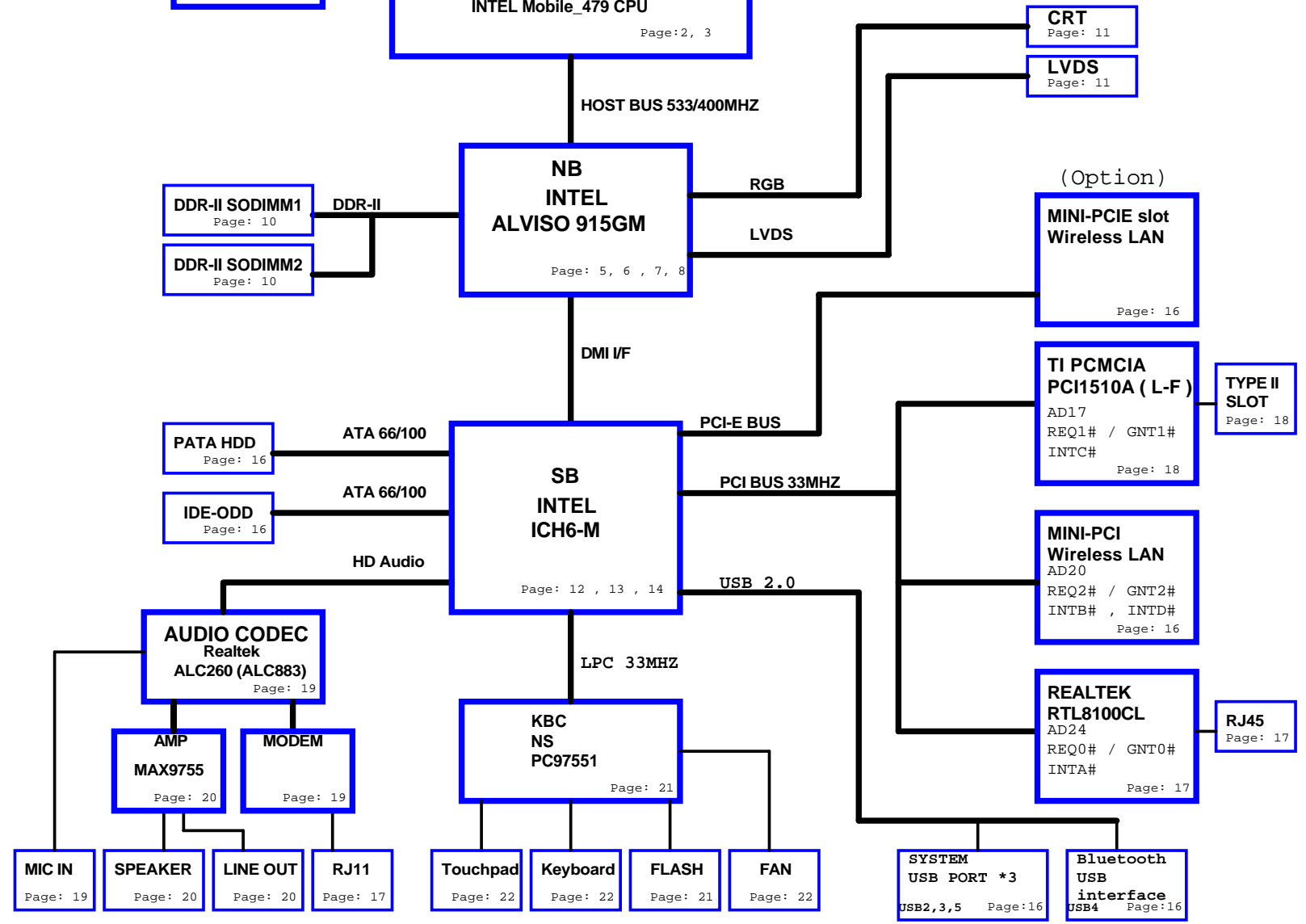
TI PCMCIA
PCI1510A (L-F)
 AD17
 REQ1# / GNT1#
 INTC#
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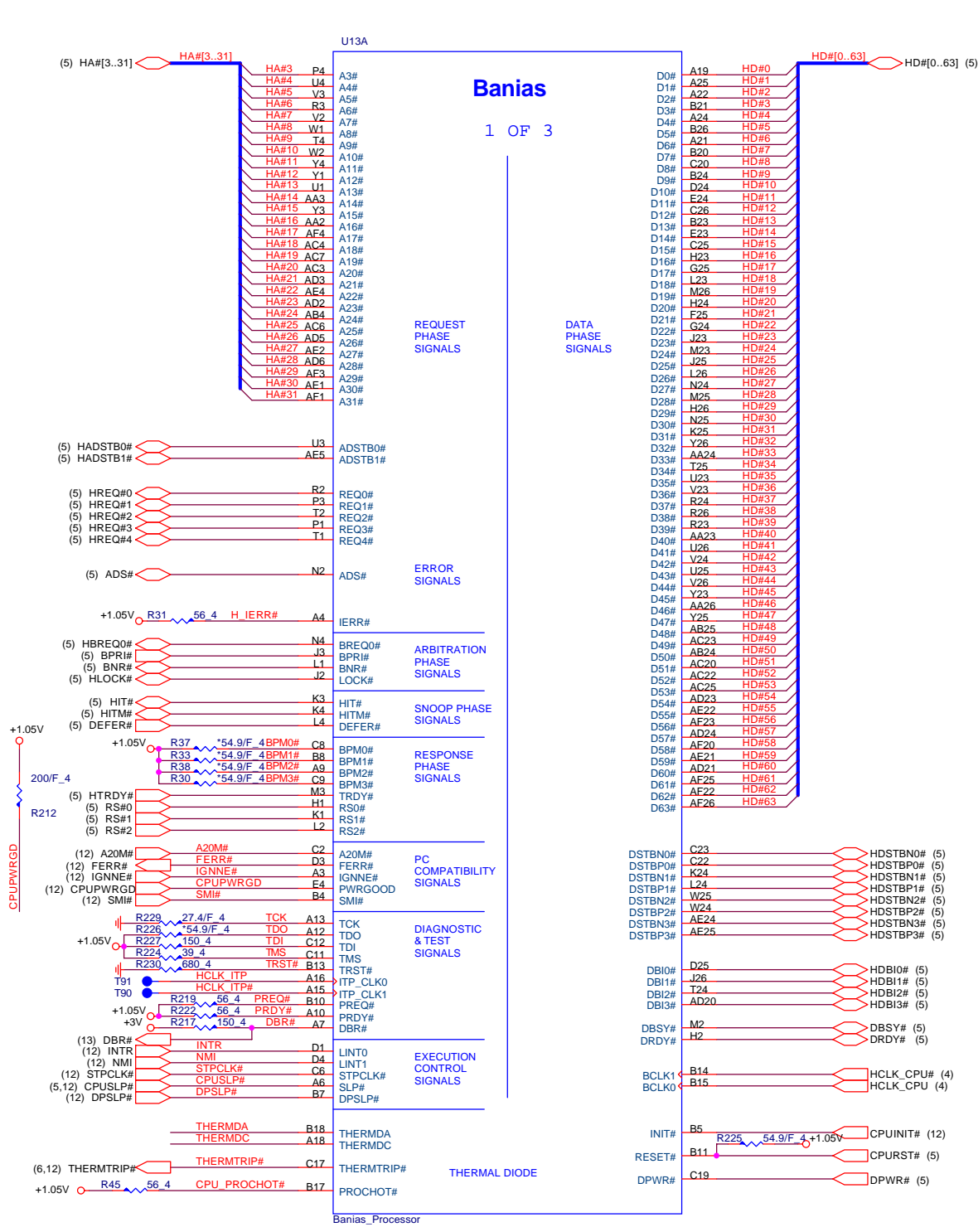
TYPE II
SLOT
 Page: 18

MINI-PCI
Wireless LAN
 AD20
 REQ2# / GNT2#
 INTB# , INTD#
 Page: 16

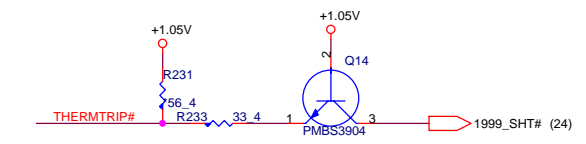
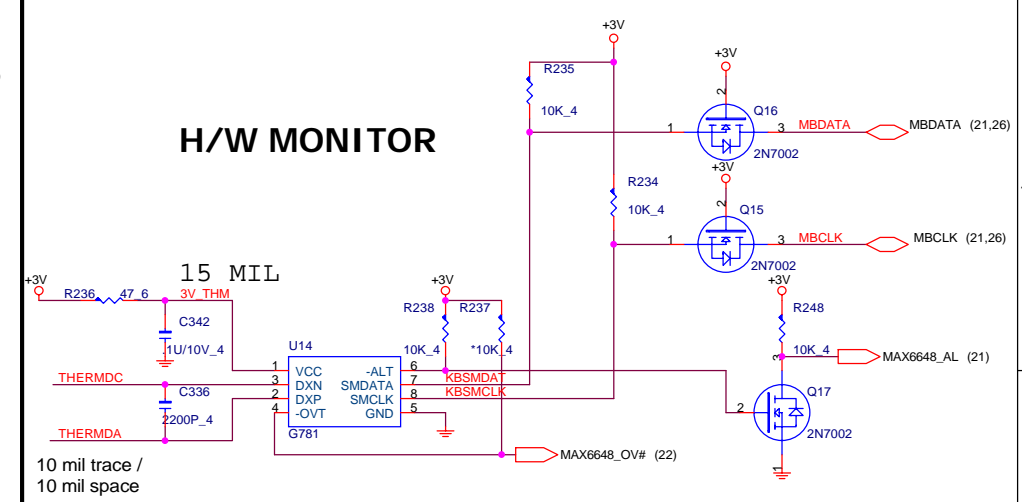
REALTEK
RTL8100CL
 AD24
 REQ0# / GNT0#
 INTA#
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RJ45
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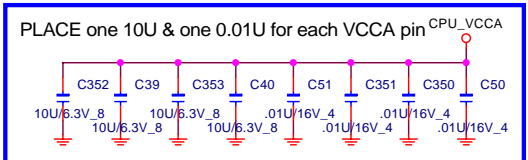
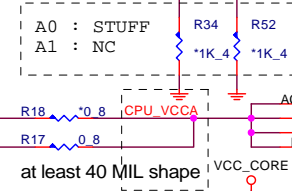
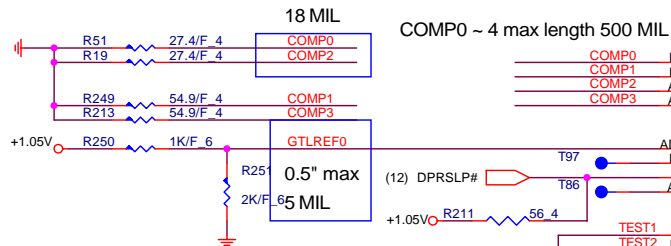


H/W MONITOR

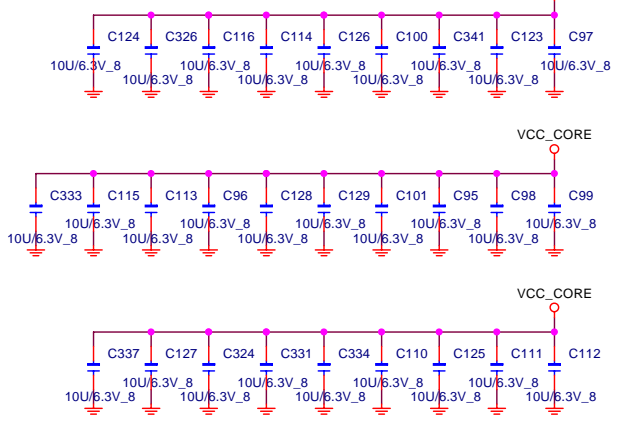



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CPU (HOST BUS) -1		3A
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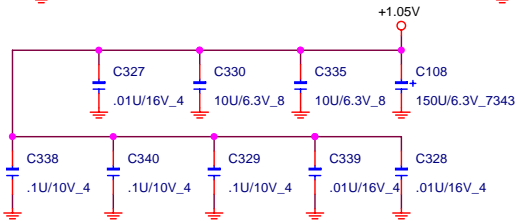
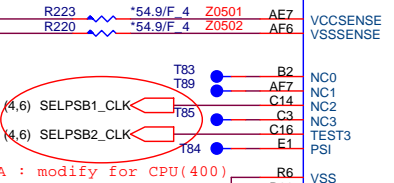
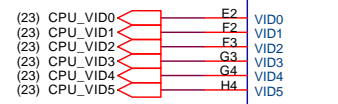
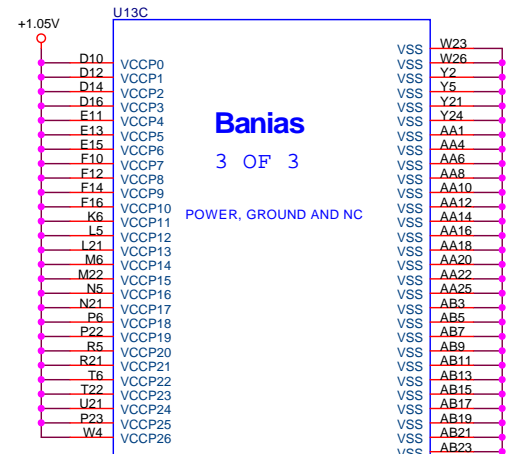


10U/6.3V/X5R(CC0805) *30



COMP0	P25
COMP1	P26
COMP2	AB2
COMP3	AB1
Banias	
2 OF 3	
POWER, GROUND, RESERVED SIGNALS	
VCC3	AC26
VCC2	N1
VCC1	B1
VCC0	F26
VCC00	D6
VCC01	D8
VCC02	D18
VCC03	D20
VCC04	D22
VCC05	E5
VCC06	E7
VCC07	E9
VCC08	E17
VCC09	E19
VCC10	E21
VCC11	F6
VCC12	F8
VCC13	F18
VCC14	F20
VCC15	F22
VCC16	G5
VCC17	G21
VCC18	H6
VCC19	H22
VCC20	J5
VCC21	J21
VCC22	K22
VCC23	U5
VCC24	V6
VCC25	V22
VCC26	W5
VCC27	Y4
VCC28	Y22
VCC29	AA5
VCC30	AA7
VCC31	AA9
VCC32	AA11
VCC33	AA13
VCC34	AA15
VCC35	AA17
VCC36	AA19
VCC37	AA21
VCC38	AB6
VCC39	AB8
VCC40	AB10
VCC41	AB12
VCC42	AB14
VCC43	AB16
VCC44	AB18
VCC45	AB20
VCC46	AB22
VCC47	AC9
VCC48	AC11
VCC49	AC13
VCC50	AC15
VCC51	AC17
VCC52	AC19
VCC53	AD8
VCC54	AD10
VCC55	AD12
VCC56	AD14
VCC57	AD16
VCC58	AD18
VCC59	AE9
VCC60	AE11
VCC61	AE13
VCC62	AE15
VCC63	AE17
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VCC69	AF16
VCC70	AF18
VCC71	

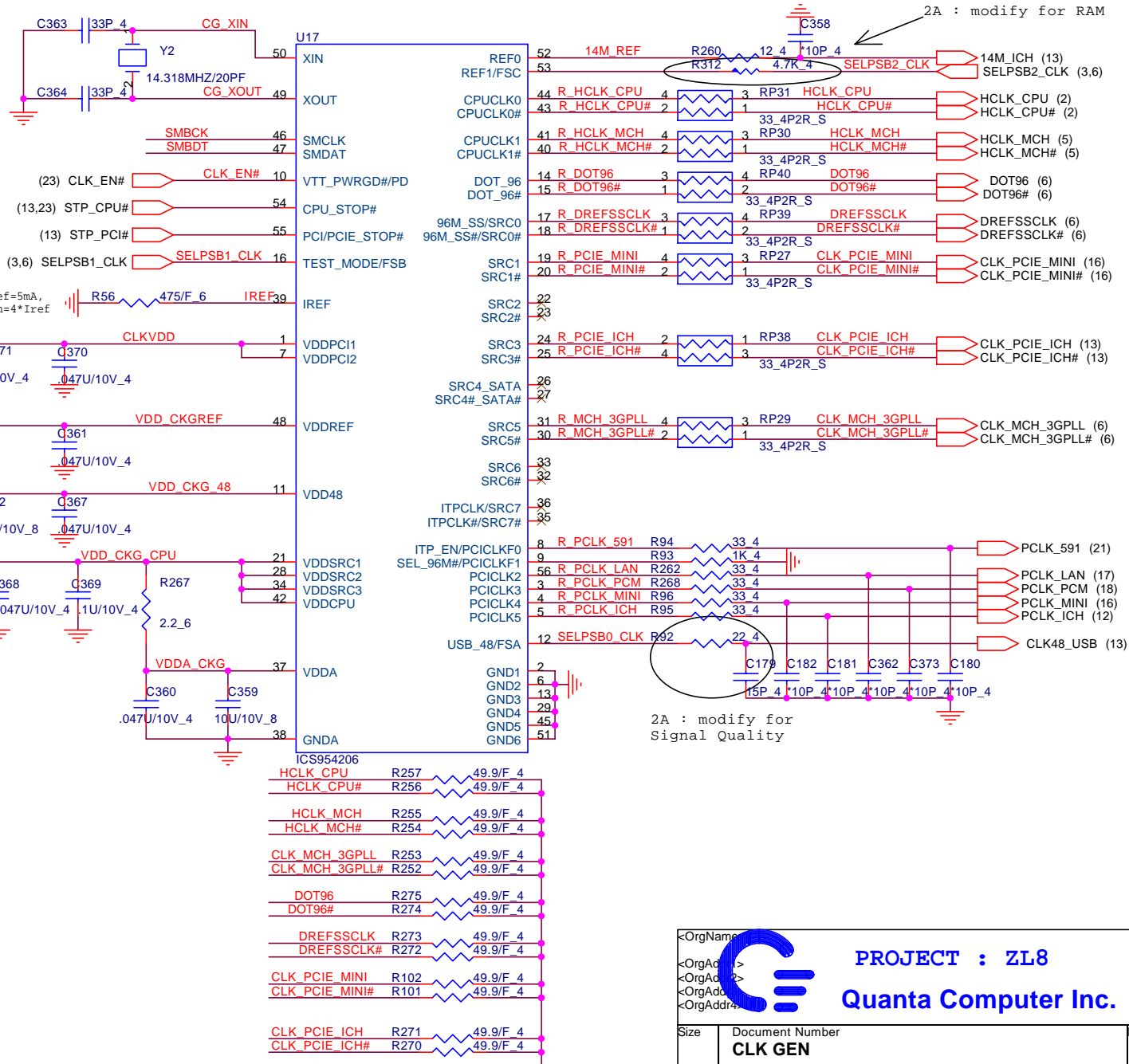
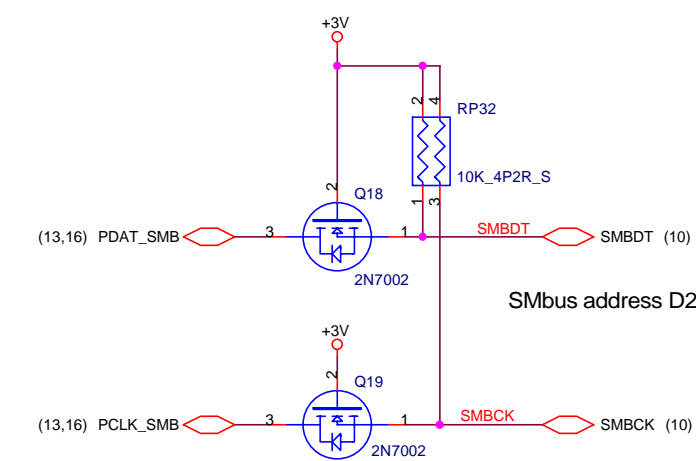
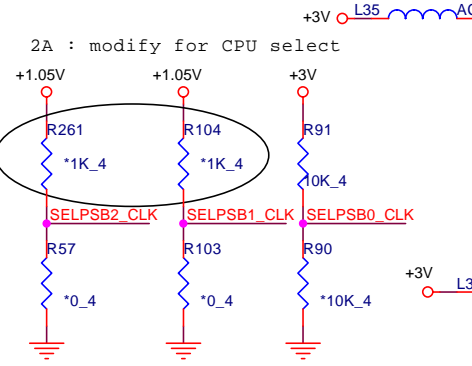
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VSS	A5
VSS	A8
VSS	A11
VSS	A14
VSS	A17
VSS	A20
VSS	A23
VSS	A26
VSS	B3
VSS	B6
VSS	B9
VSS	B12
VSS	B16
VSS	B19
VSS	B22
VSS	B25
VSS	C1
VSS	C4
VSS	C7
VSS	C10
VSS	C13
VSS	C15
VSS	C18
VSS	C21
VSS	C24
VSS	D2
VSS	D5
VSS	D7
VSS	D9
VSS	D11
VSS	D13
VSS	D15
VSS	D17
VSS	D19
VSS	D21
VSS	D23
VSS	D26
VSS	E3
VSS	E6
VSS	E8
VSS	E10
VSS	E12
VSS	E14
VSS	E16
VSS	E18
VSS	E20
VSS	E22
VSS	E25
VSS	F1
VSS	F4
VSS	F5
VSS	F7
VSS	F9
VSS	F11
VSS	F13
VSS	F15
VSS	F17
VSS	F19
VSS	F21
VSS	F24
VSS	G2
VSS	G6
VSS	G22
VSS	G23
VSS	G26
VSS	H3
VSS	H5
VSS	H21
VSS	H25
VSS	J1
VSS	J4
VSS	J6
VSS	J22
VSS	J24
VSS	K2
VSS	K5
VSS	K21
VSS	K23
VSS	K26
VSS	L3
VSS	L6
VSS	L22
VSS	L25
VSS	M1
VSS	M4
VSS	M5
VSS	M21
VSS	M24
VSS	N3
VSS	N6
VSS	N22
VSS	N23
VSS	N26
VSS	P2
VSS	P5
VSS	P21
VSS	P24
VSS	R1
VSS	R4



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FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33

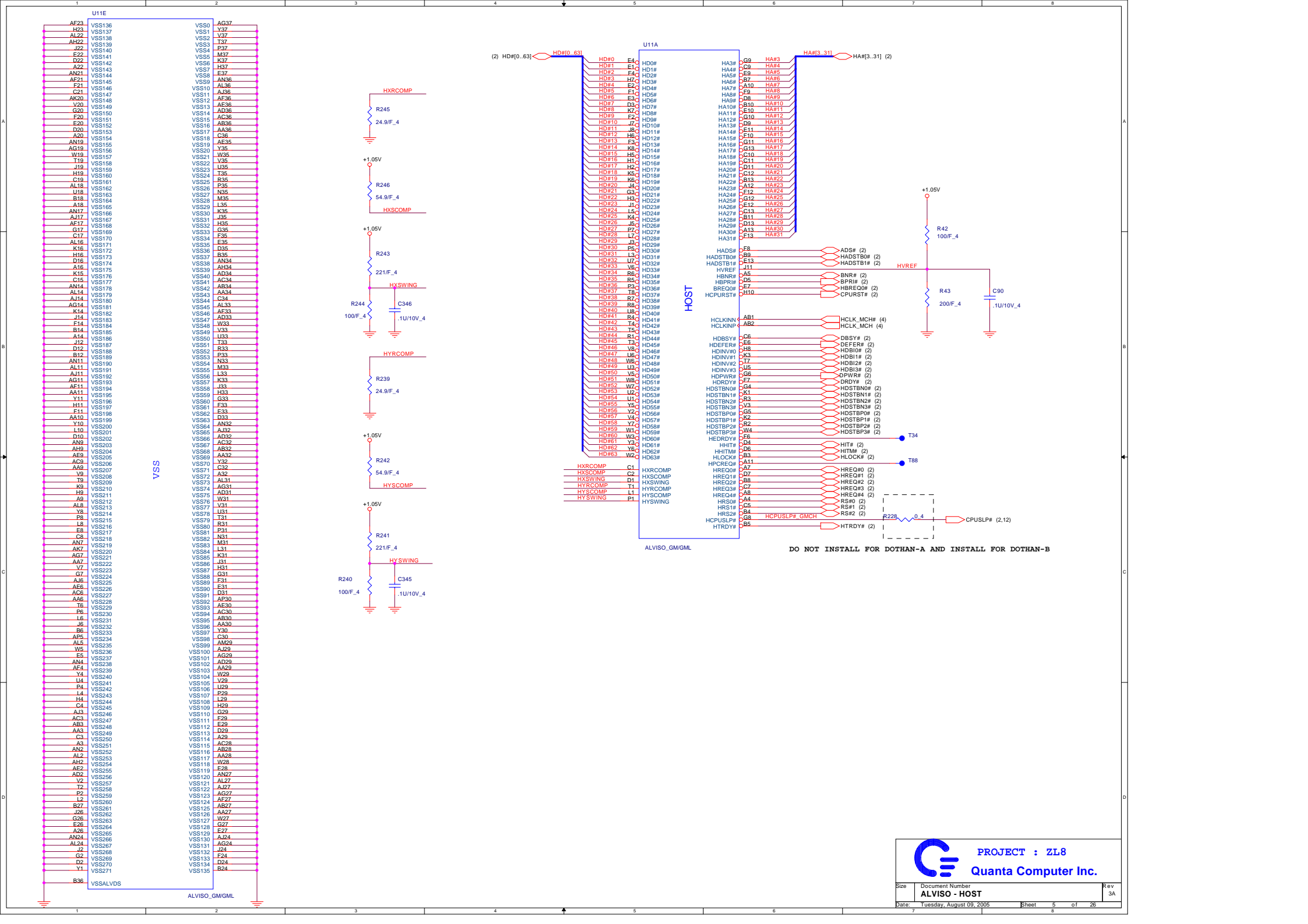
DOTHAN-A 400
DOTHAN-A 533



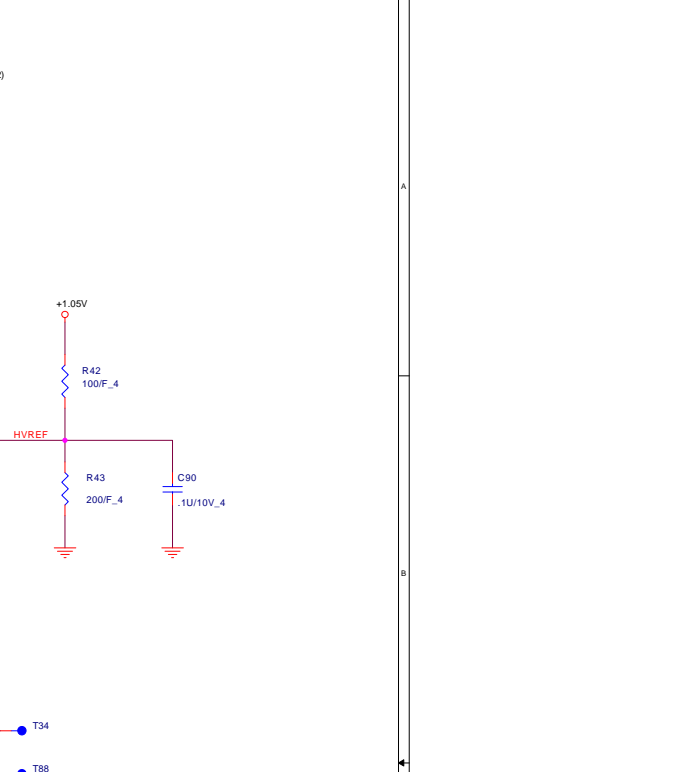
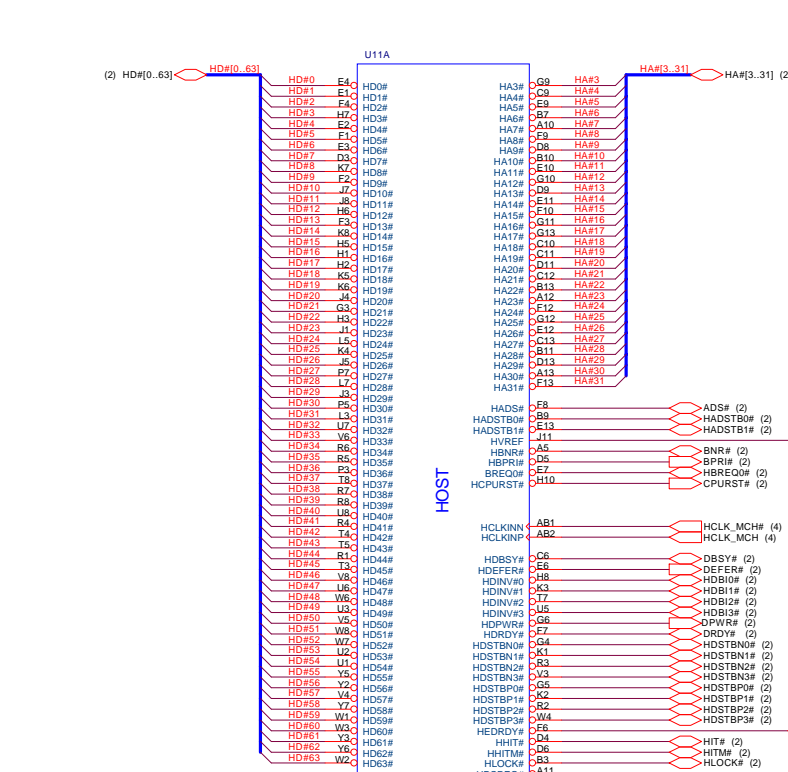
PROJECT : ZL8
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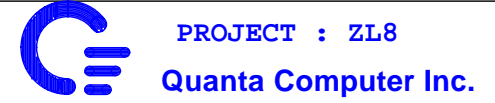
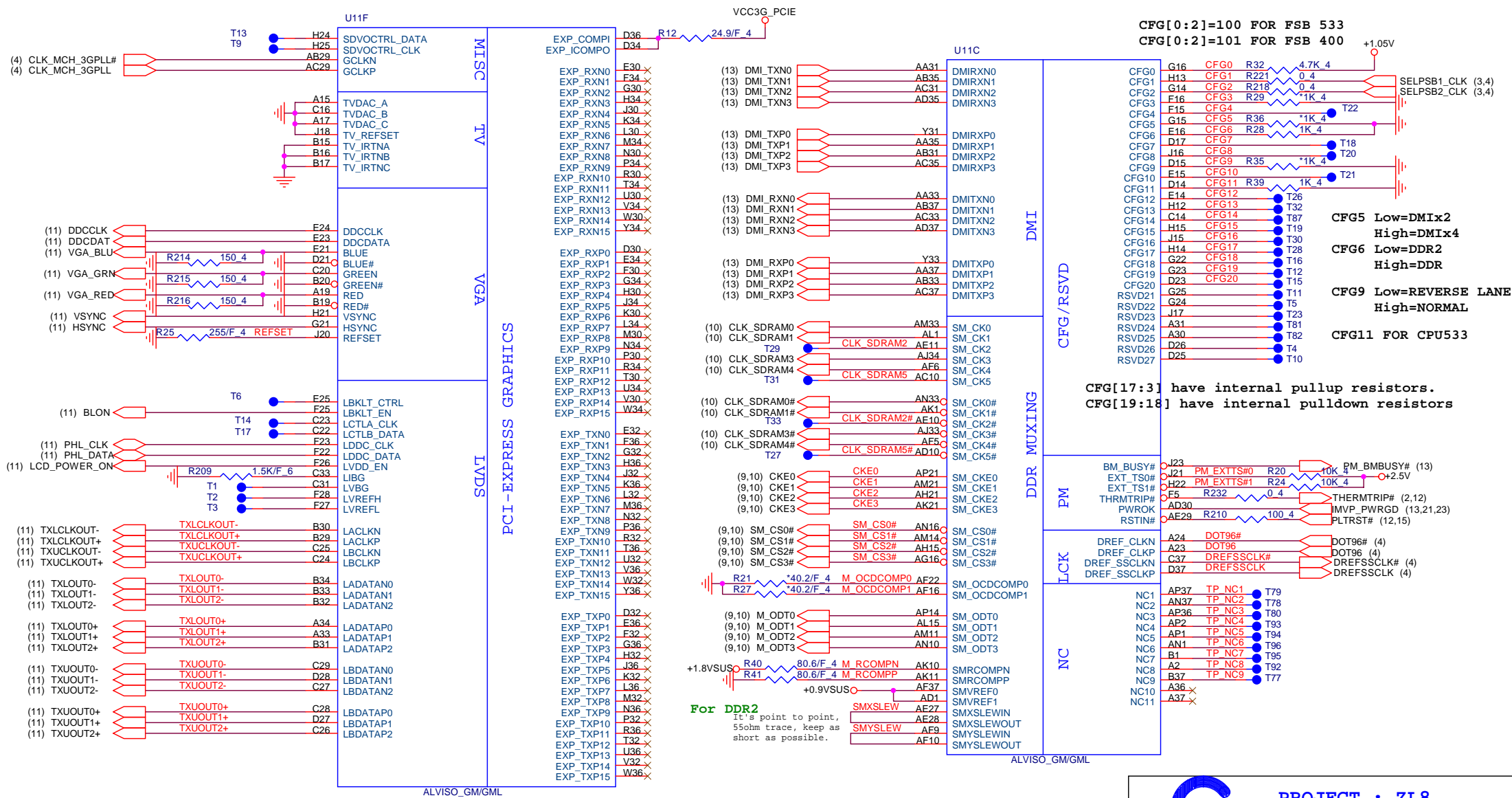
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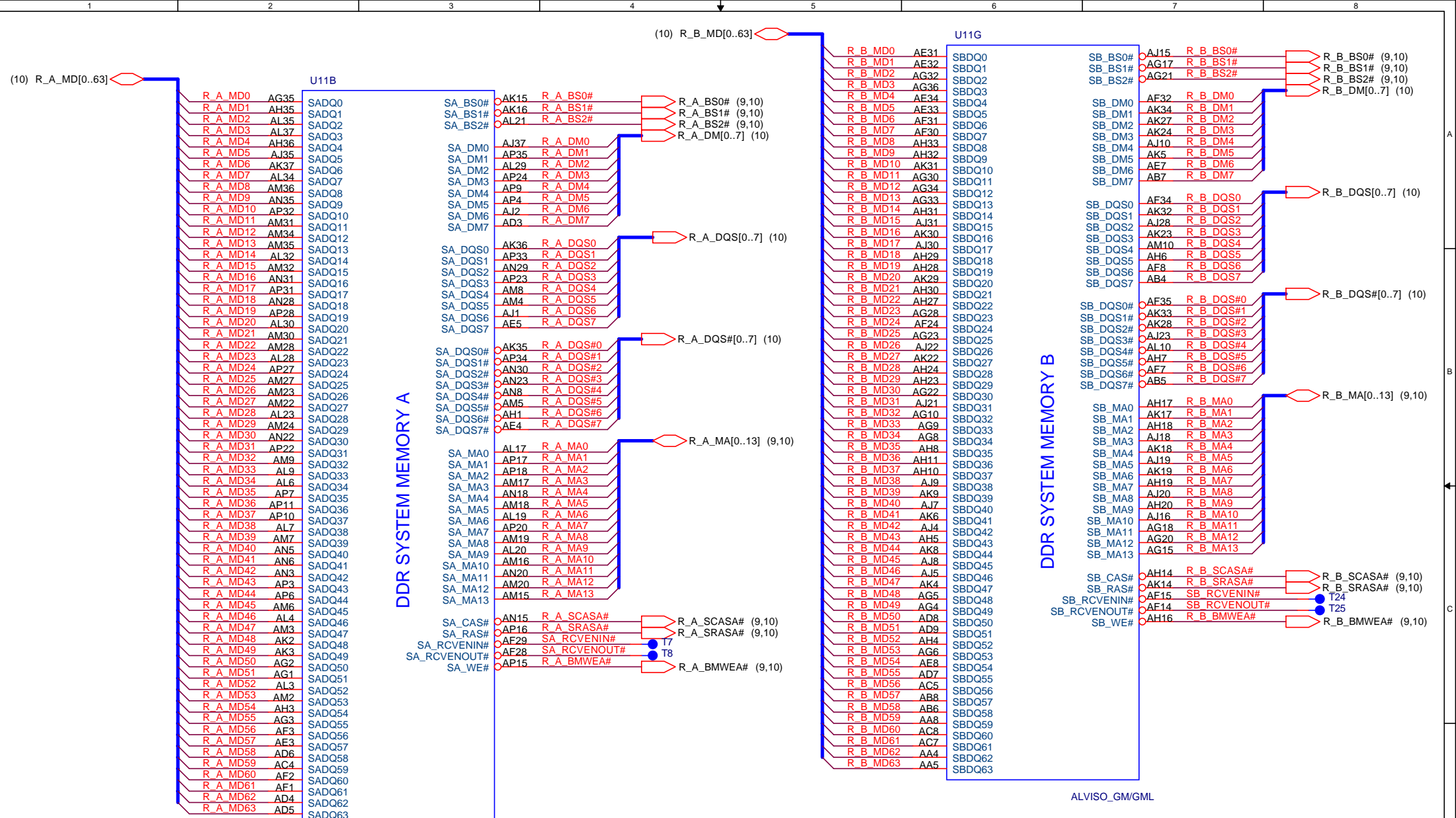


AF23	VSS136	VSS0	AG37
AF23	VSS137	VSS1	V37
AF22	VSS138	VSS2	T37
AH22	VSS139	VSS3	P37
J22	VSS140	VSS4	M37
VSS141	VSS5	VSS5	K37
VSS142	VSS6	VSS6	H37
AN21	VSS143	VSS7	E37
AF21	VSS144	VSS8	AN36
VSS145	VSS145	VSS9	AL36
F21	VSS146	VSS10	AL36
C21	VSS147	VSS11	AF36
AK20	VSS148	VSS12	AF36
VSS149	VSS149	VSS13	AE36
V20	VSS150	VSS14	AD36
C20	VSS151	VSS15	AC36
F20	VSS152	VSS16	AB36
D20	VSS153	VSS17	AA36
A20	VSS154	VSS18	C36
AN19	VSS155	VSS19	AE35
AG19	VSS156	VSS20	Y35
W19	VSS157	VSS21	W35
T19	VSS158	VSS22	V35
H19	VSS159	VSS23	T35
H19	VSS160	VSS24	T35
C19	VSS161	VSS25	R35
UL18	VSS162	VSS26	N35
UL18	VSS163	VSS27	N35
B18	VSS164	VSS28	M35
A18	VSS165	VSS29	L35
AN17	VSS166	VSS30	J35
AF17	VSS167	VSS31	J35
C17	VSS168	VSS32	H35
VSS169	VSS169	VSS33	G35
AL16	VSS170	VSS34	F35
K16	VSS171	VSS35	E35
H16	VSS172	VSS36	D35
D16	VSS173	VSS37	B35
A16	VSS174	VSS38	AN34
VSS175	VSS175	VSS39	AH34
K15	VSS176	VSS40	AD34
C15	VSS177	VSS41	AC34
AN14	VSS178	VSS42	AA34
AL14	VSS179	VSS43	C34
AG14	VSS180	VSS44	C34
K14	VSS181	VSS45	AF33
J14	VSS182	VSS46	AD33
F14	VSS183	VSS47	W33
B14	VSS184	VSS48	V33
A14	VSS185	VSS49	V33
J12	VSS186	VSS50	U33
D12	VSS187	VSS51	T33
AH11	VSS188	VSS52	R33
B12	VSS189	VSS53	P33
AL11	VSS190	VSS54	M33
AJ11	VSS191	VSS55	L33
AG11	VSS192	VSS56	L33
AF11	VSS193	VSS57	K33
AA11	VSS194	VSS58	J33
VSS195	VSS195	VSS59	H33
Y11	VSS196	VSS60	G33
H11	VSS197	VSS61	F33
F11	VSS198	VSS62	E33
AL10	VSS199	VSS63	D33
L10	VSS200	VSS64	C33
D10	VSS201	VSS65	B33
AH9	VSS202	VSS66	A32
AE9	VSS203	VSS67	AD32
AC9	VSS204	VSS68	AC32
AA9	VSS205	VSS69	AA32
V9	VSS206	VSS70	C32
T9	VSS207	VSS71	C32
K9	VSS208	VSS72	A32
H9	VSS209	VSS73	A32
A9	VSS210	VSS74	AG31
AL8	VSS211	VSS75	AD31
PK8	VSS212	VSS76	W31
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CB8	VSS215	VSS79	T31
AN7	VSS216	VSS80	R31
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AG7	VSS218	VSS82	M31
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AE6	VSS223	VSS87	H31
AC6	VSS224	VSS88	G31
AE6	VSS225	VSS89	F31
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AE6	VSS227	VSS91	D31
AC6	VSS228	VSS92	AE30
AE6	VSS229	VSS93	AE30
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L6	VSS231	VSS95	AA30
J6	VSS232	VSS96	AA30
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AL6	VSS234	VSS98	C30
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AL4	VSS239	VSS103	AD29
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P4	VSS241	VSS105	U29
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AC3	VSS245	VSS109	H29
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AA3	VSS247	VSS111	F29
C3	VSS248	VSS112	E29
A3	VSS249	VSS113	E29
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AE2	VSS252	VSS116	AC28
V2	VSS253	VSS117	AB28
T2	VSS254	VSS118	W28
P2	VSS255	VSS119	W28
L2	VSS256	VSS120	E28
BZ2	VSS257	VSS121	AN27
JZ2	VSS258	VSS122	AL27
GZ2	VSS259	VSS123	AJ27
EZ2	VSS260	VSS124	AG27
AN24	VSS261	VSS125	AF27
AL24	VSS262	VSS126	AB27
G2	VSS263	VSS127	AO27
D2	VSS264	VSS128	W27
Y2	VSS265	VSS129	G27
B36	VSS266	VSS130	E27
	VSS267	VSS131	AJ24
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	VSS271	VSS135	D24
			B24






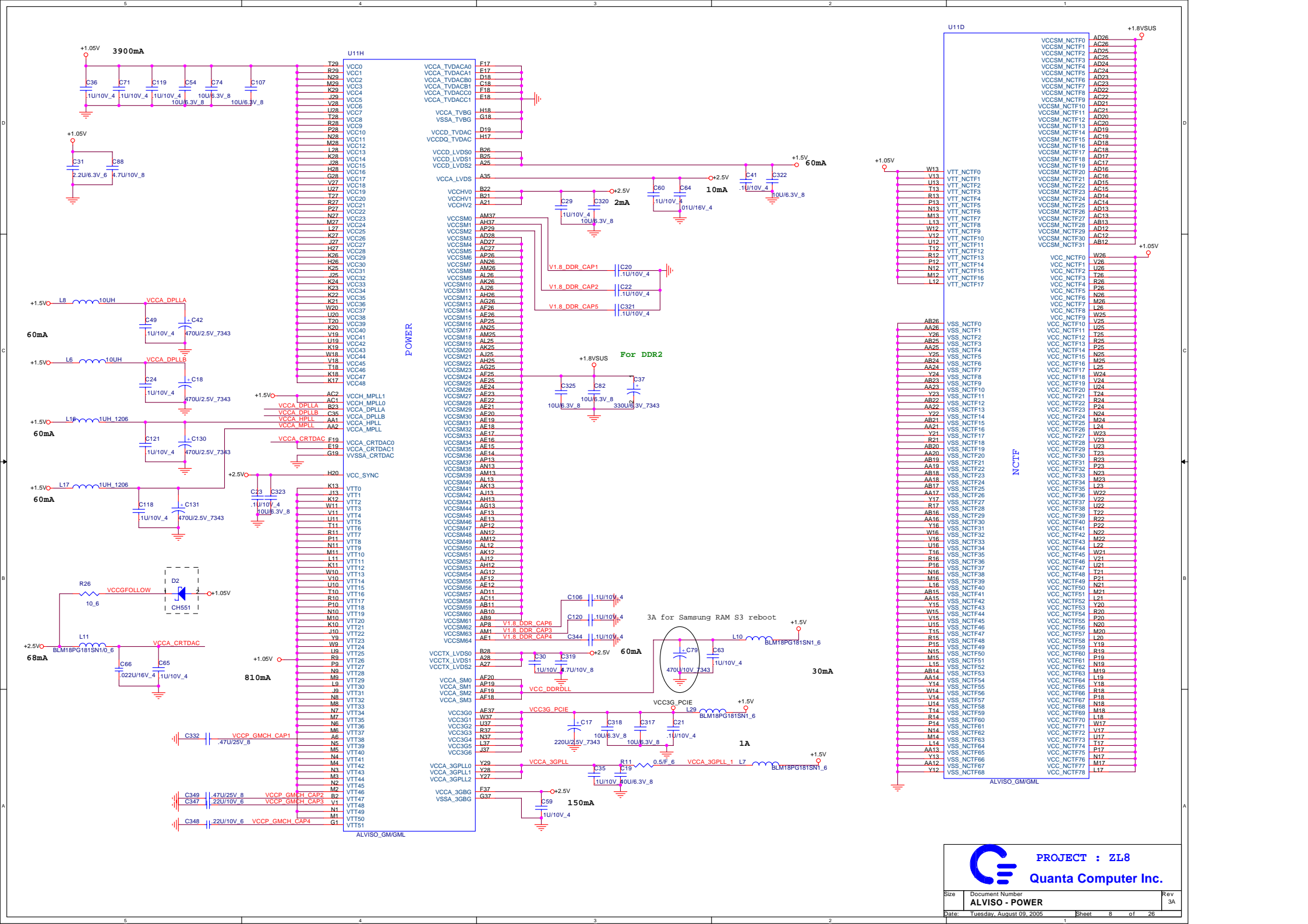
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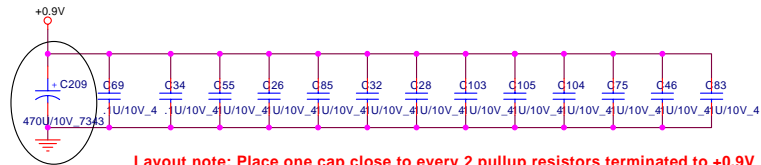


ALVISO_GM/GML


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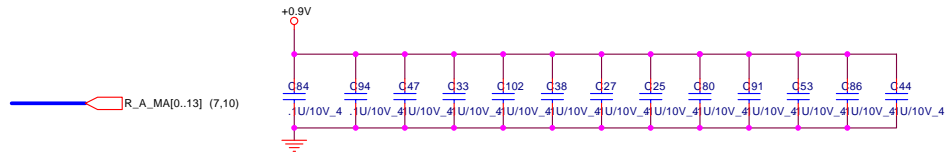
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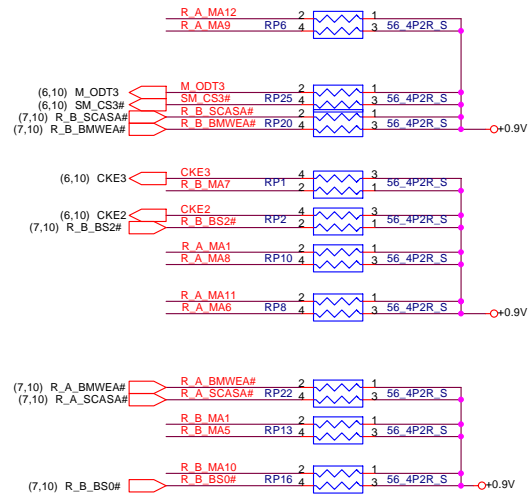
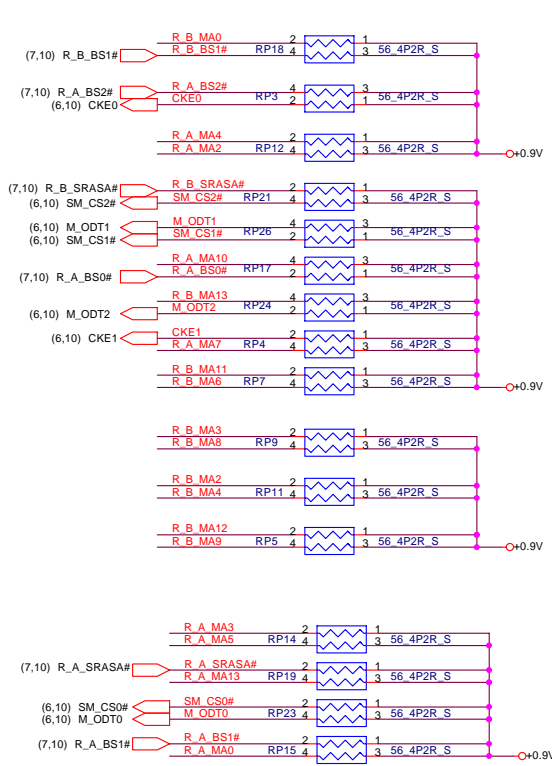


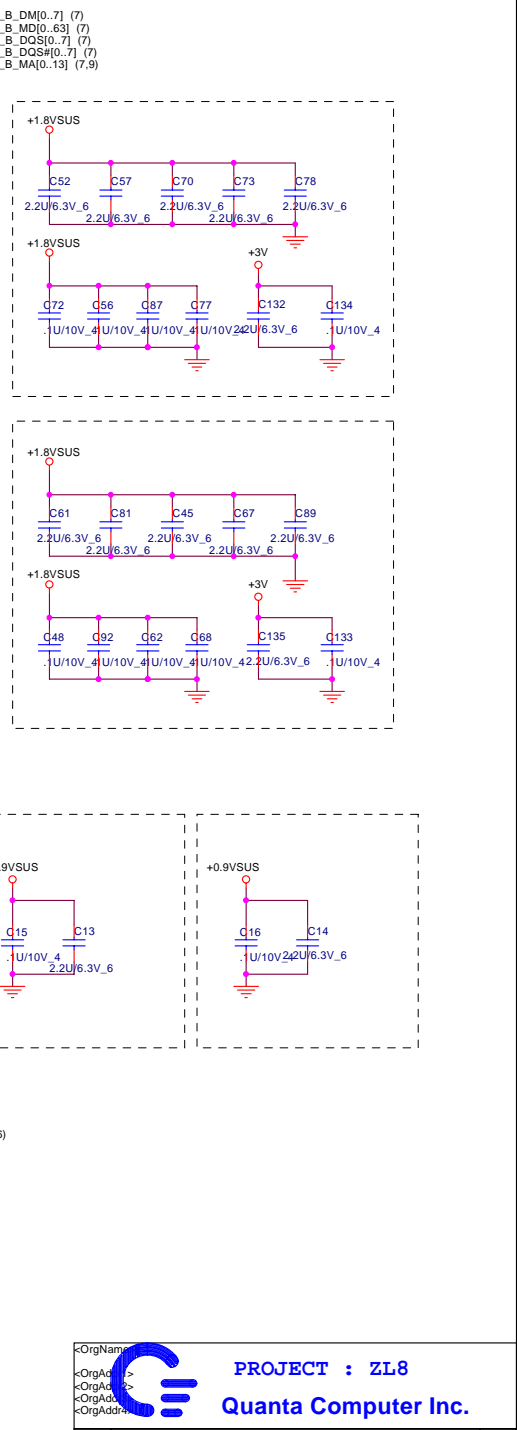
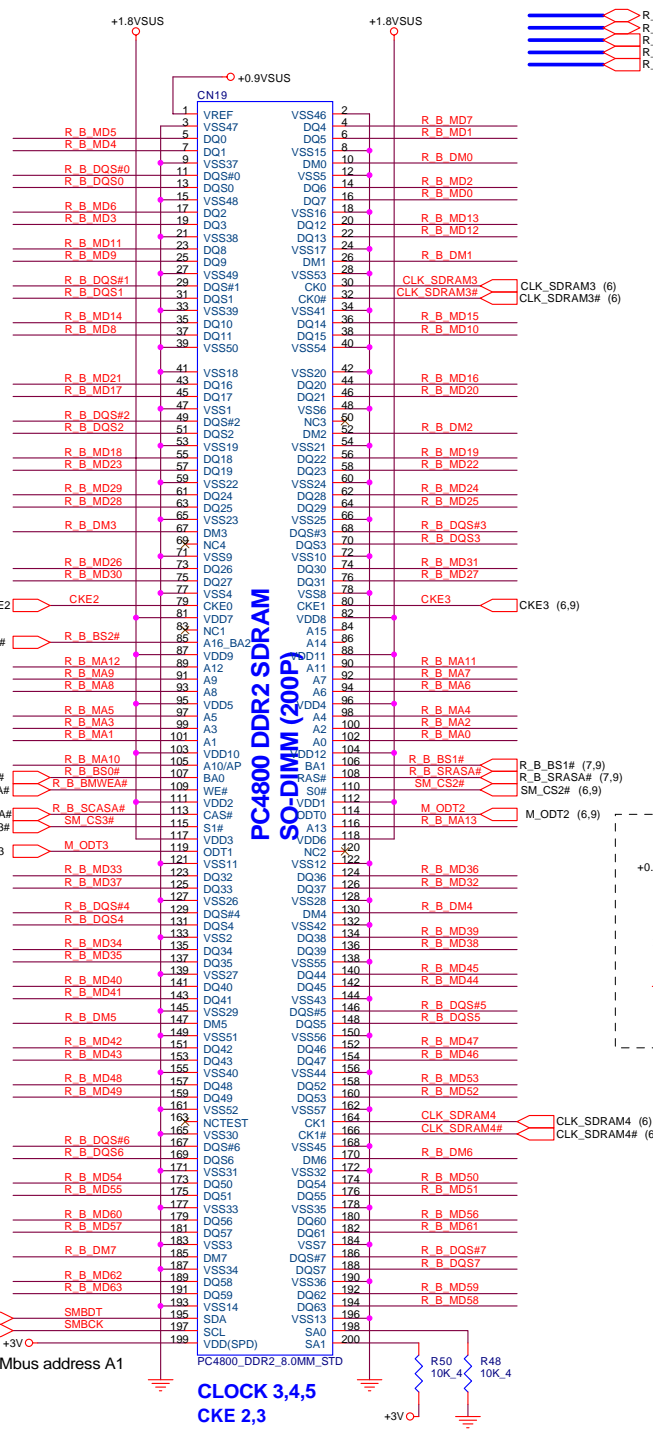
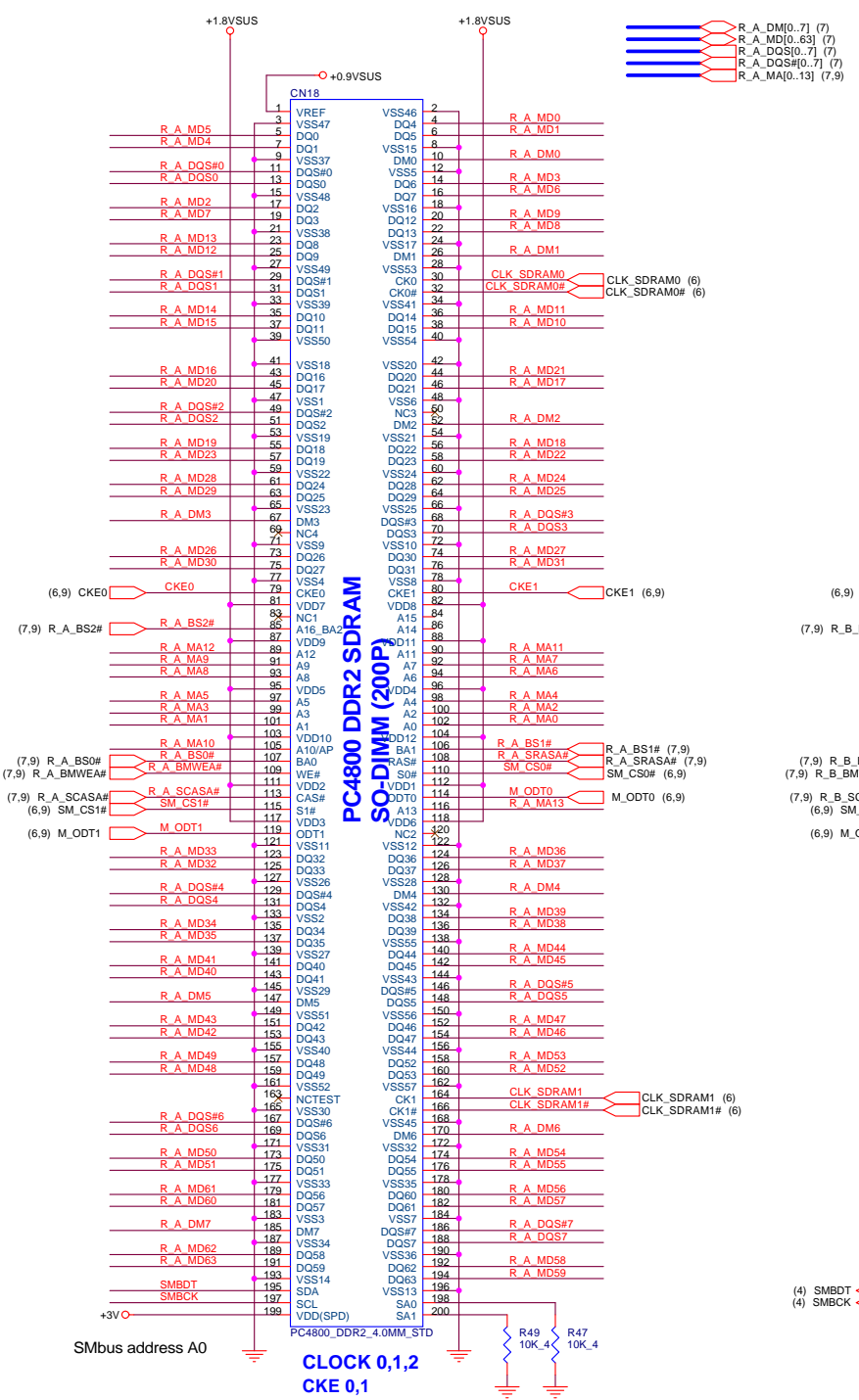
3A for Samsung RAM S3 reboot

Layout note: Place one cap close to every 2 pullup resistors terminated to +0.9V



Layout note: Place one cap close to every 2 pullup resistors terminated to +0.9V



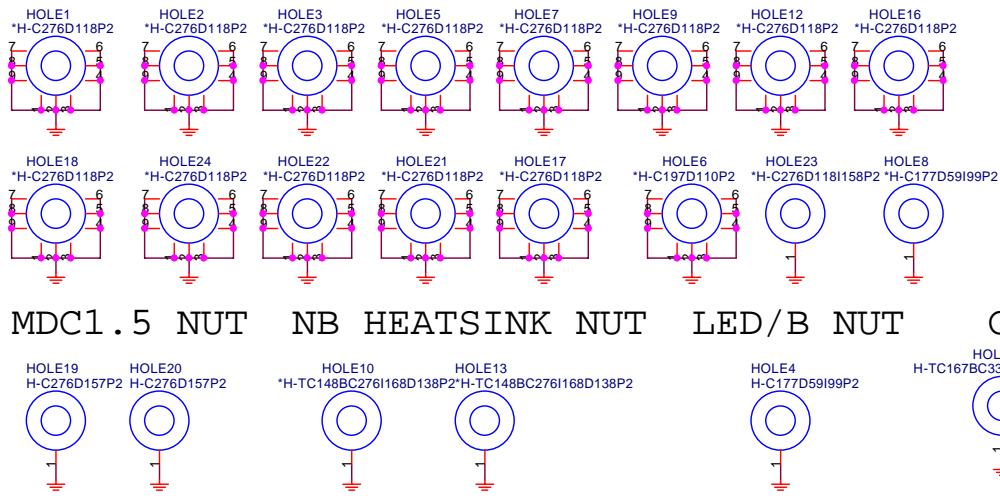
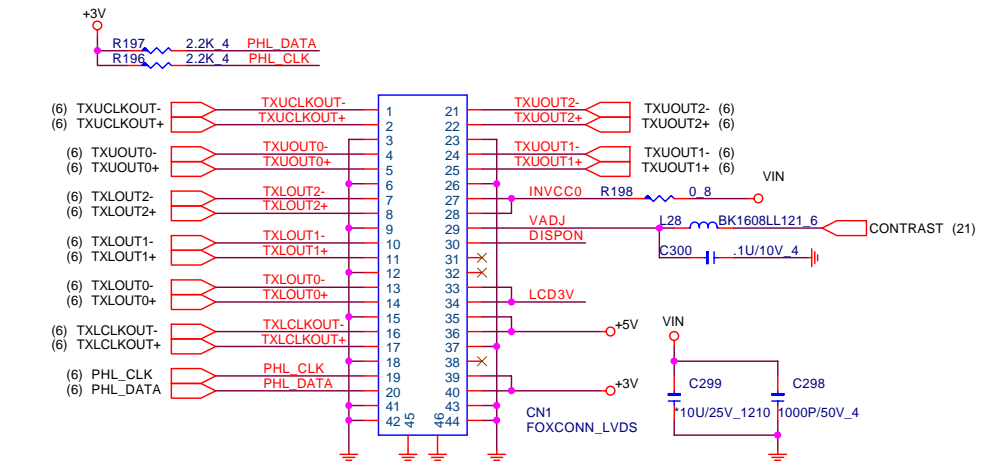
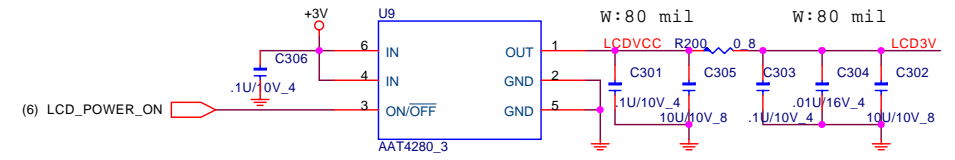
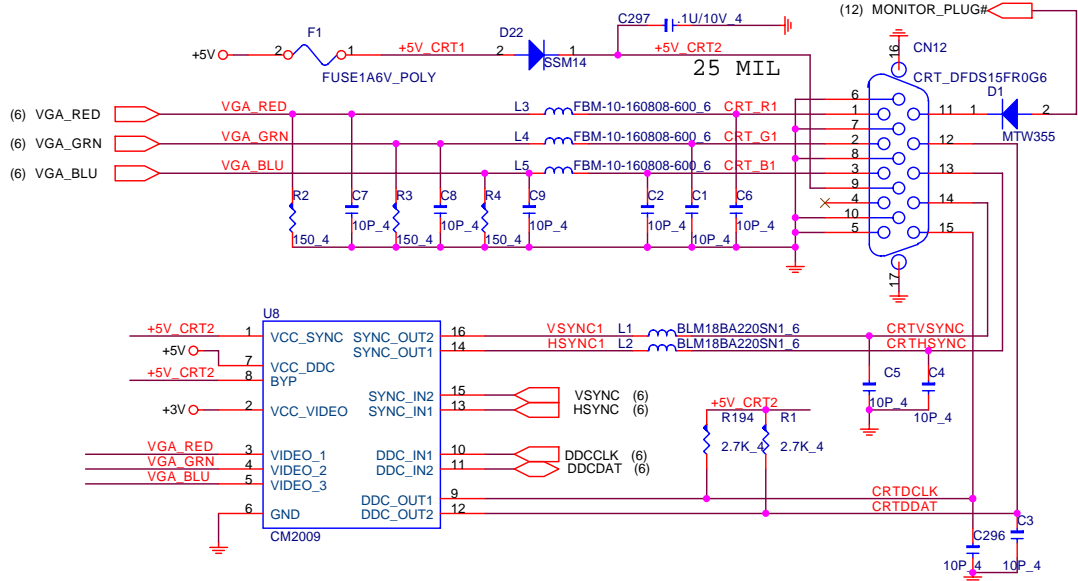


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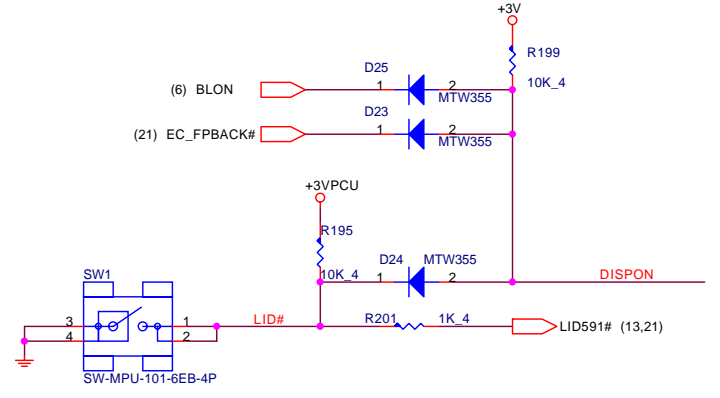
PROJECT : ZL8
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Size	Document Number	Rev
	DDR2 SO-DIMM (200P)	3A
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CRT PORT



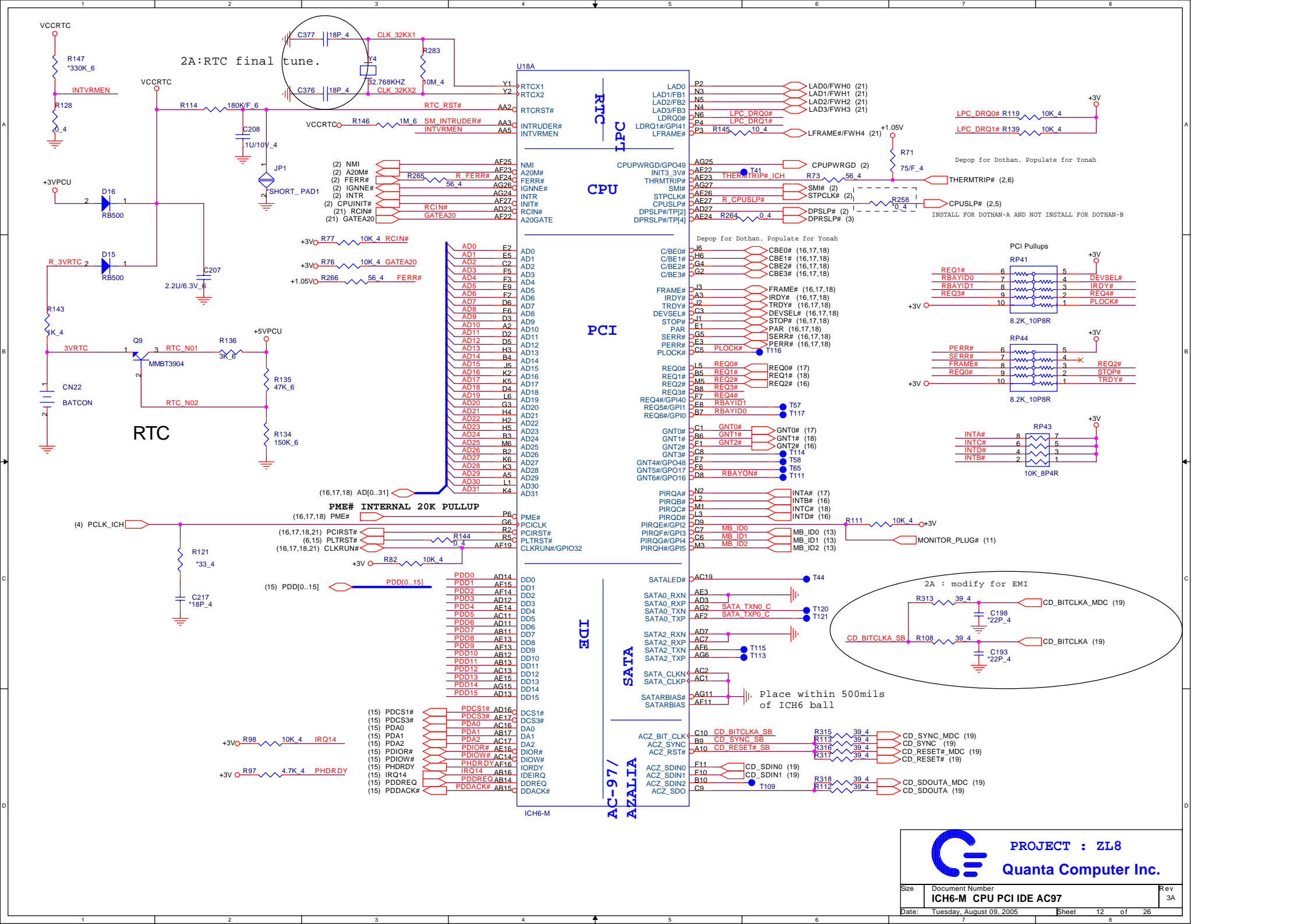
PCIE-CARD NUT



Lid Switch

PROJECT : ZL8
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Size	Document Number	Rev
	LVDS,VGA Ports, LID, & HOLES	3A
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2A:RTC final tune.

RTC

CPU

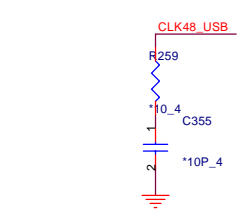
PCI

IDE

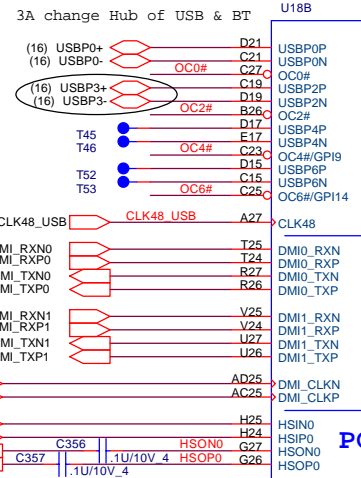
AC-97/
AZALIA



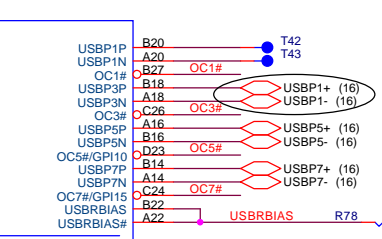
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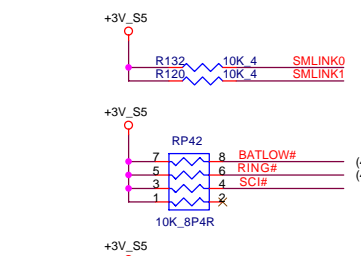
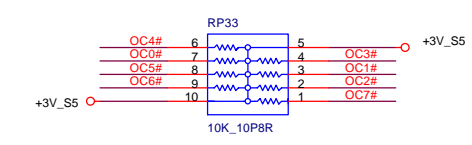
BT



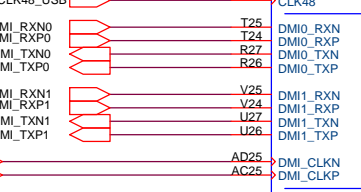
USB



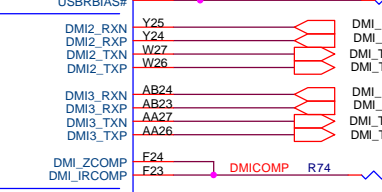
M/B USB
M/B USB
M/B USB
PCI-E CARD



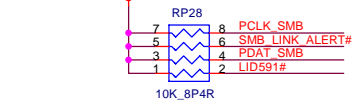
DMI



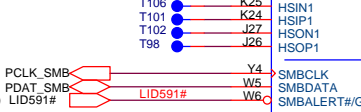
DMI



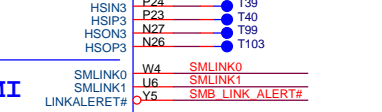
Place within 500mils of ICH-6



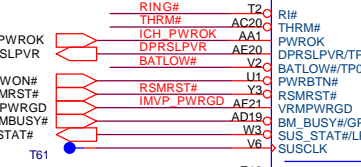
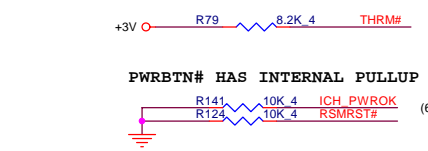
SM&SMB



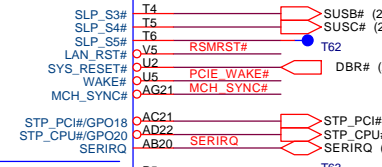
SM&SMB



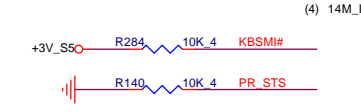
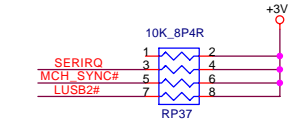
Place within 500mils of ICH-6



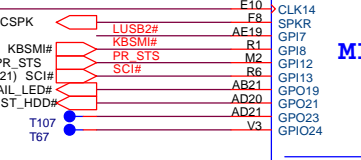
PM



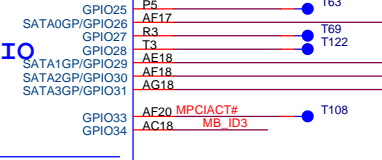
PM



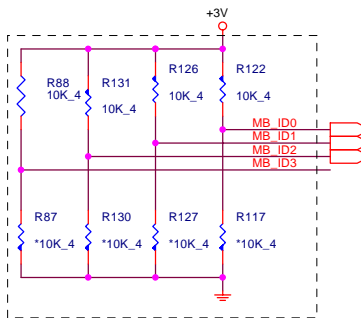
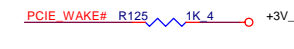
MISC&GPIO



MISC&GPIO



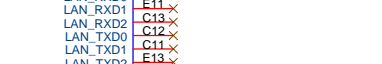
MISC&GPIO



LAN



LAN



R86 needs to be pulled down if programmed as SATA



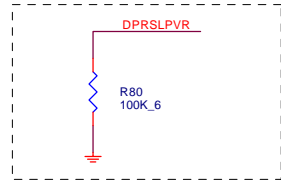
RESERVED



RESERVED

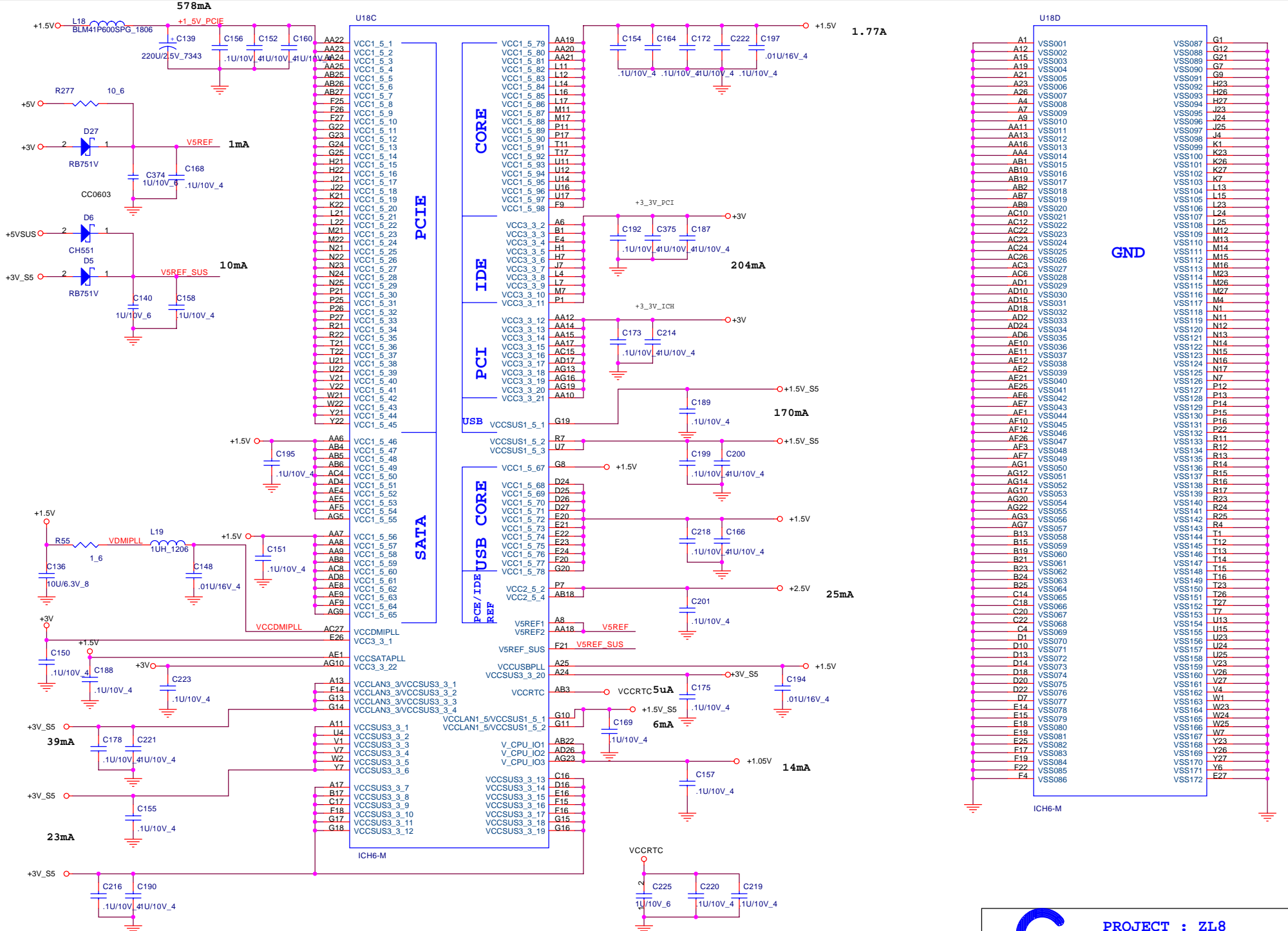


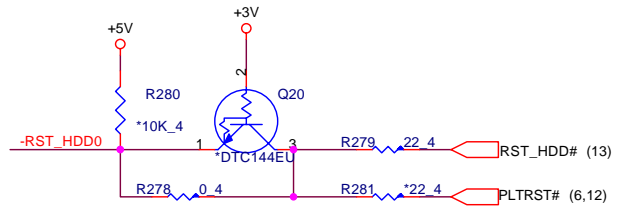
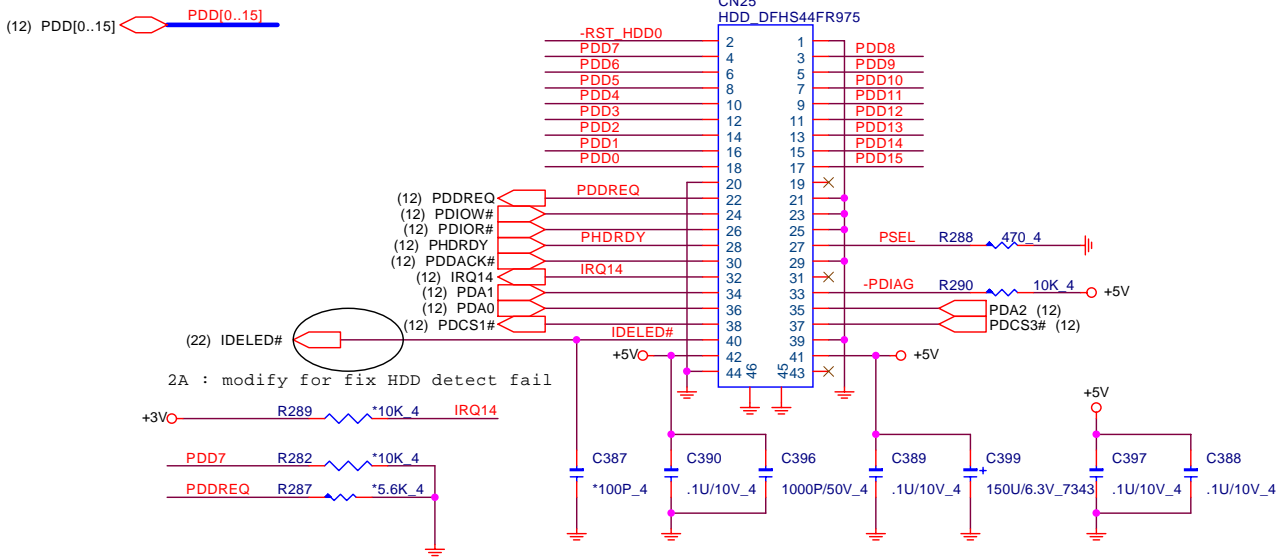
RESERVED



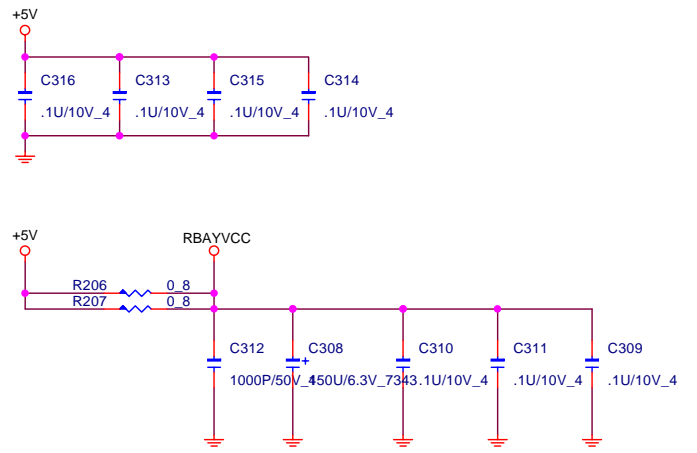
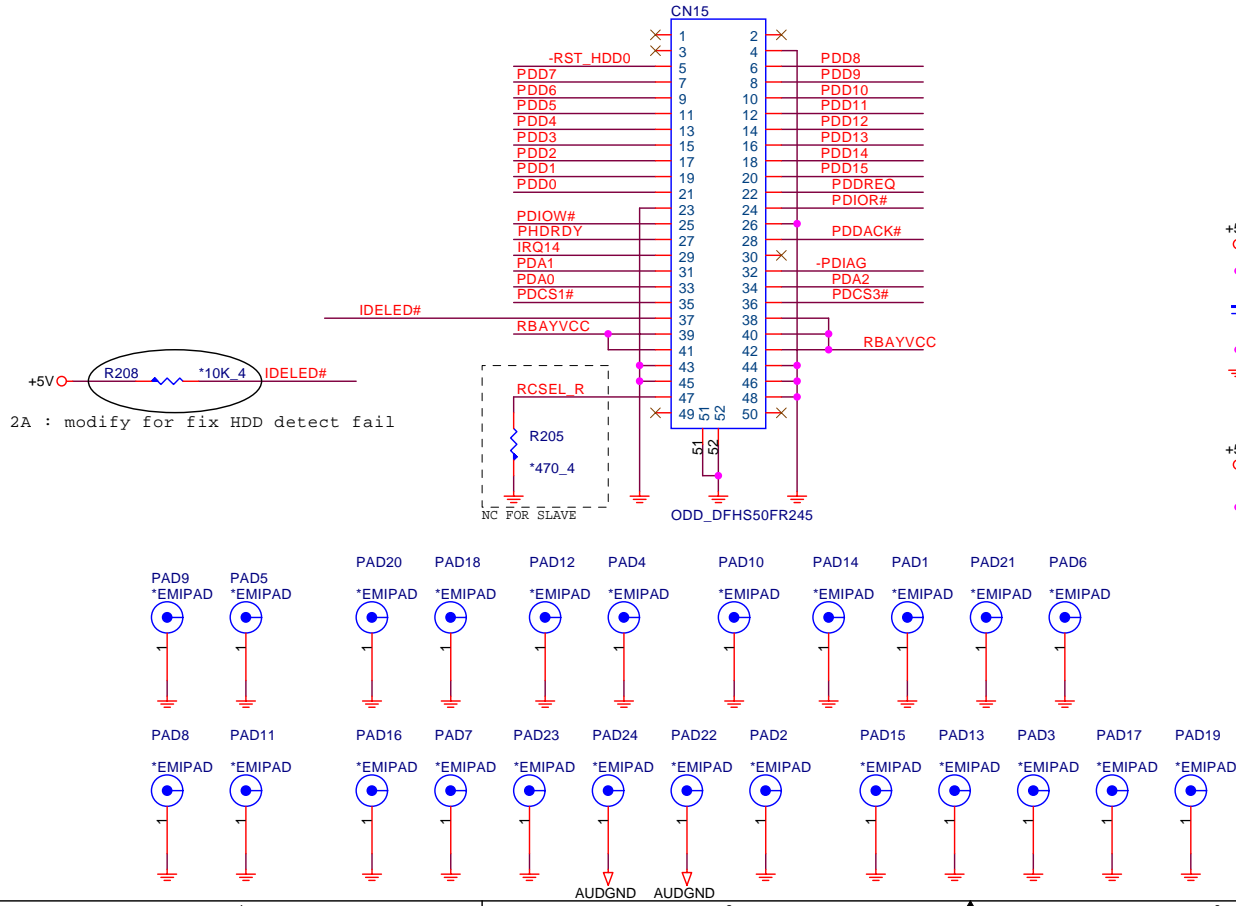
ADD PULLLOW

PROJECT : ZL8
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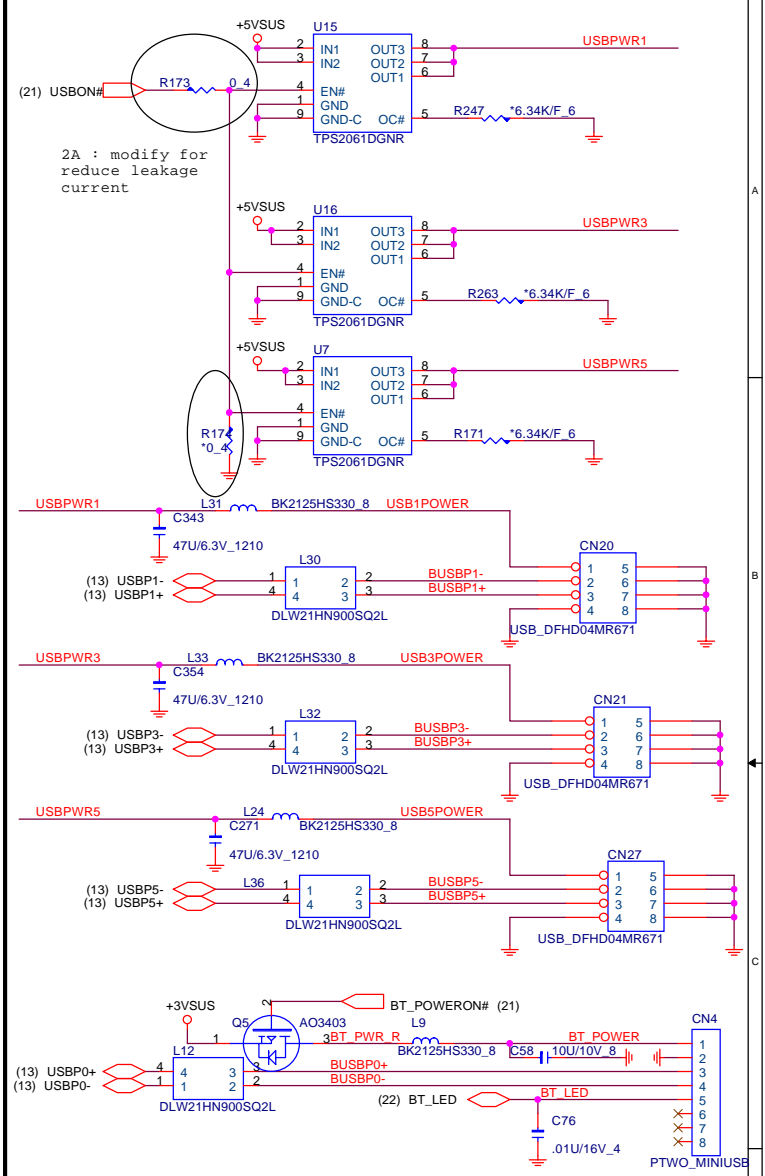
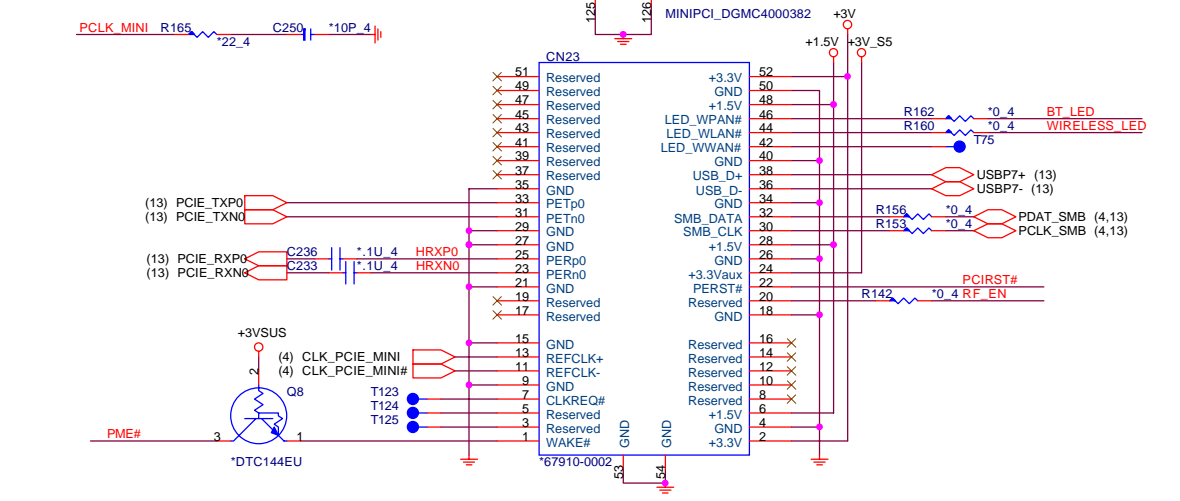
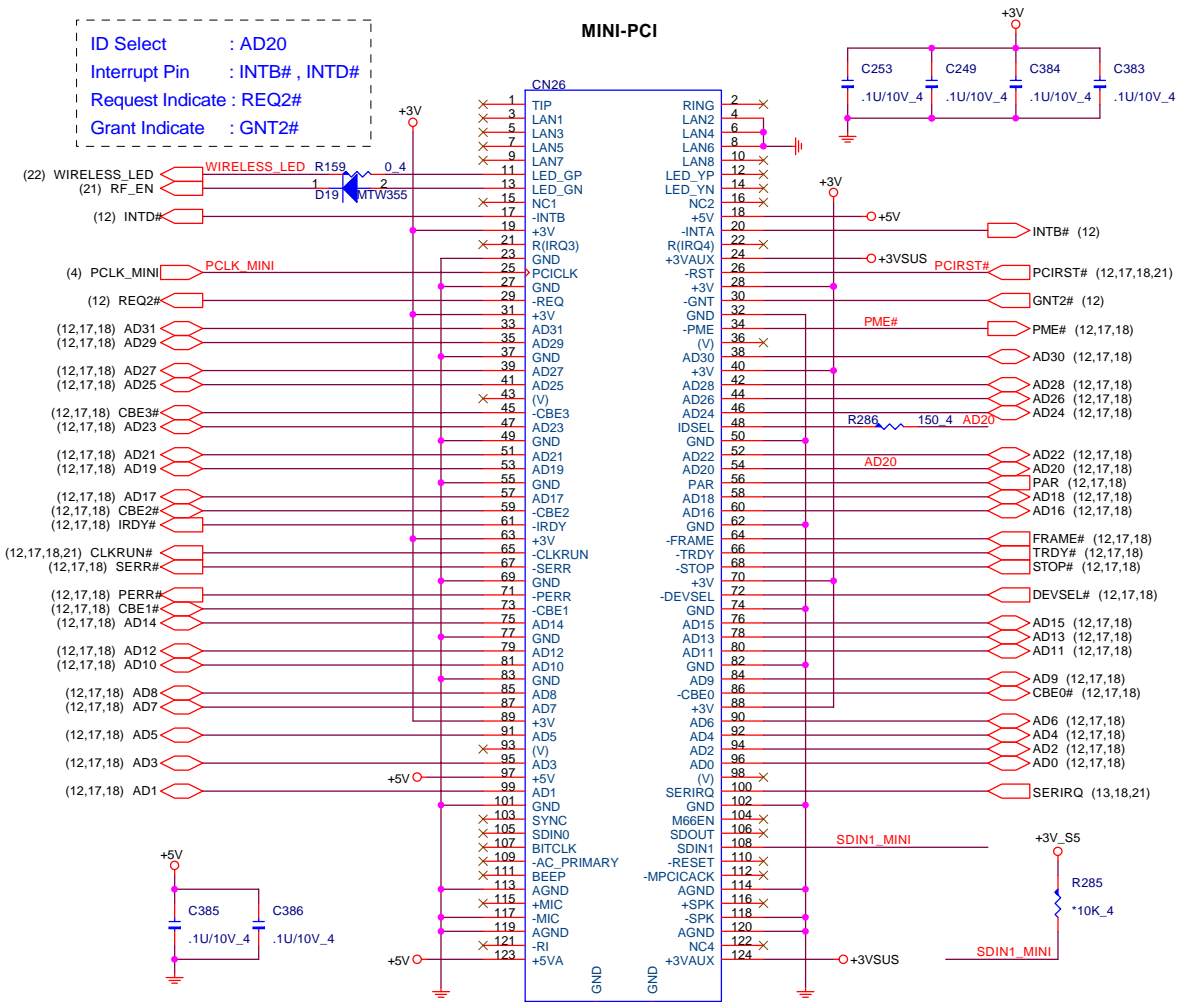
ODD Connector




PROJECT : ZL8
Quanta Computer Inc.

ID Select : AD20
 Interrupt Pin : INTB# , INTD#
 Request Indicate : REQ2#
 Grant Indicate : GNT2#

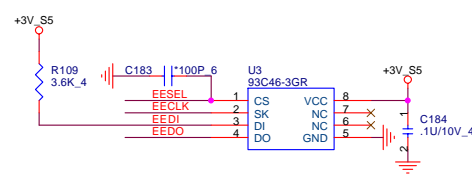
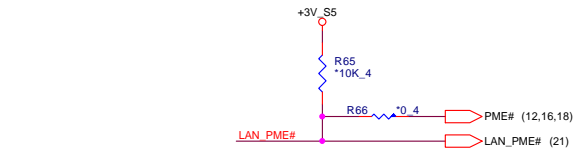
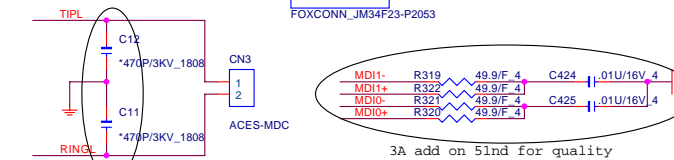
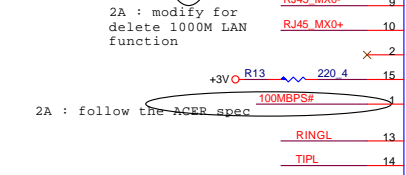
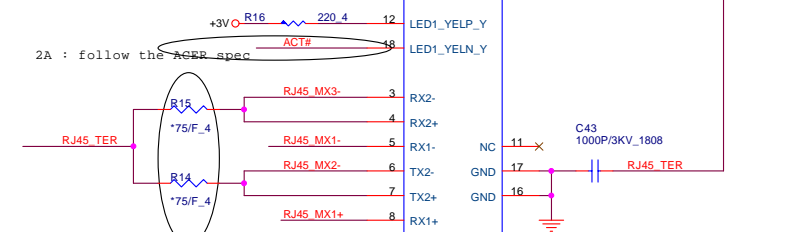
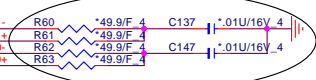
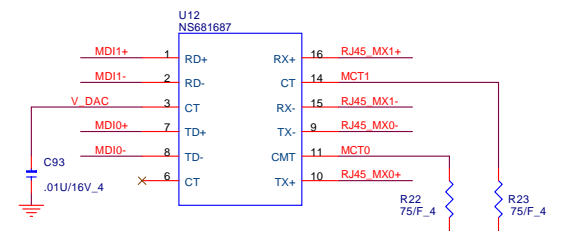
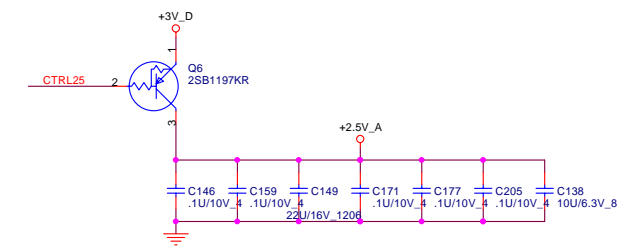
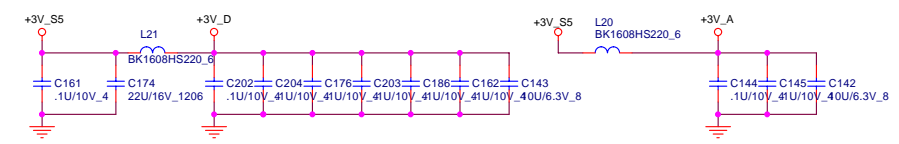
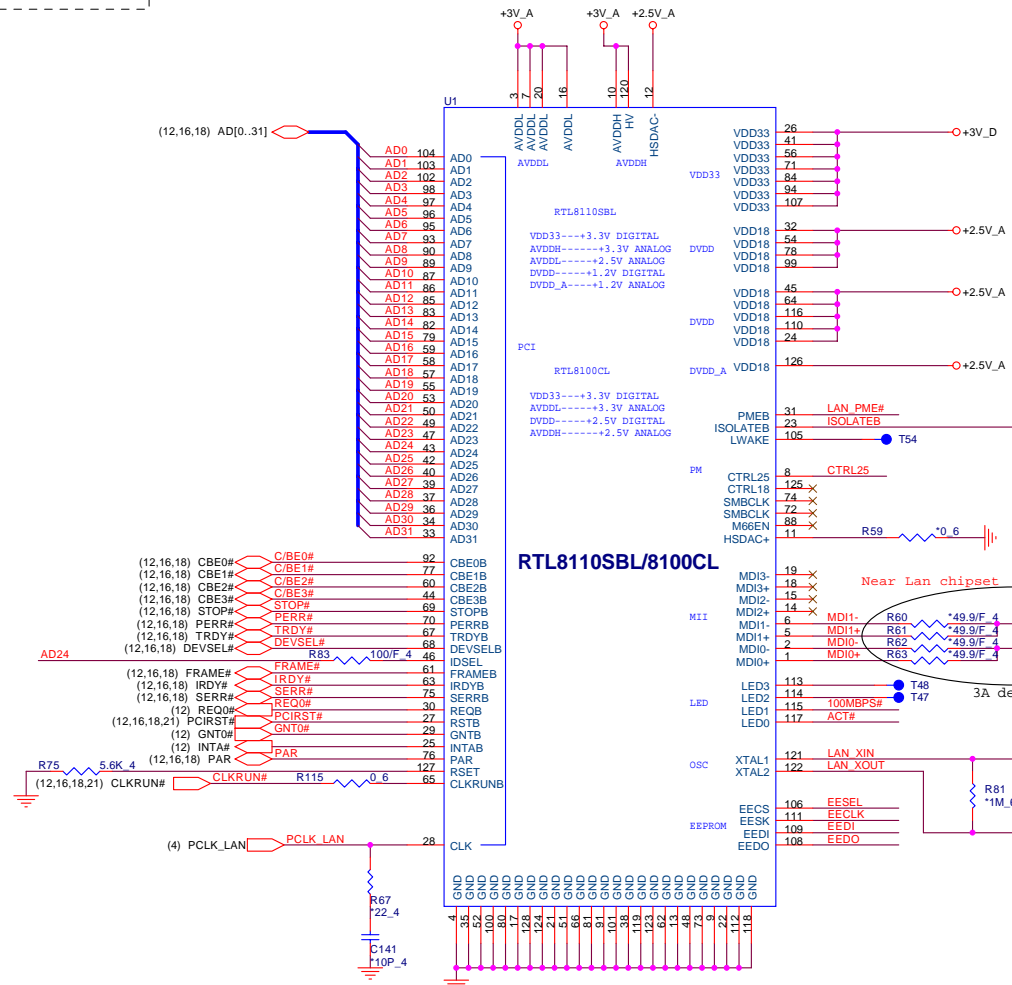
MINI-PCI




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Quanta Computer Inc.

Size	Document Number	Rev
	MINI PCI,USB	3A
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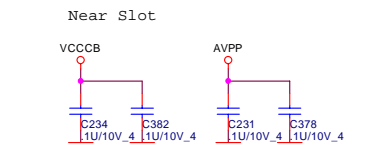
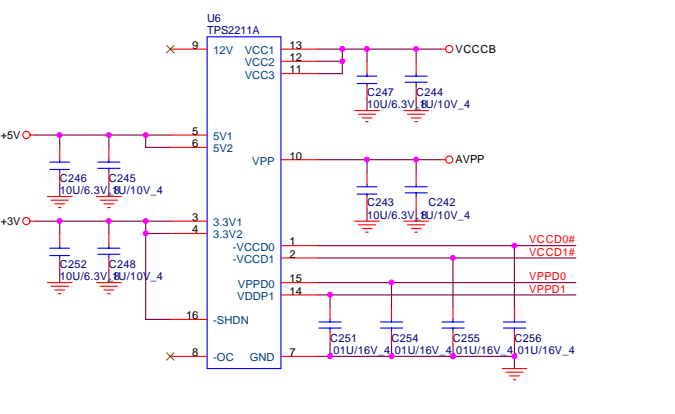
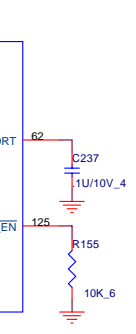
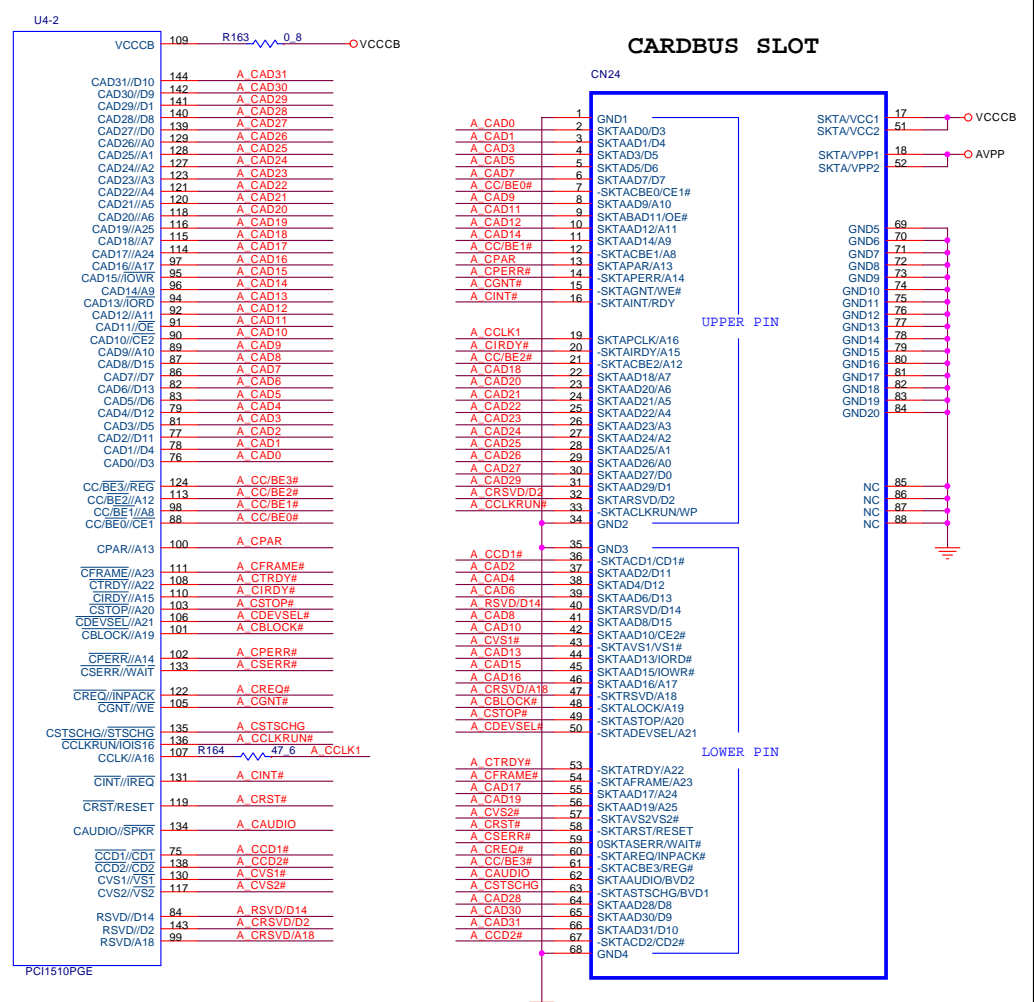
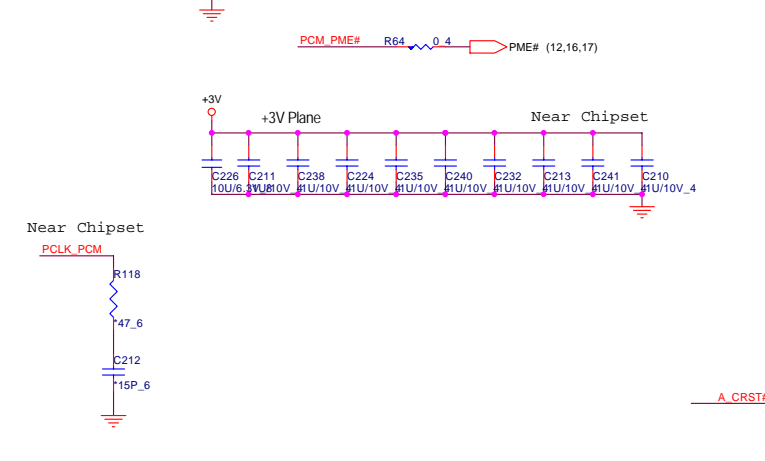
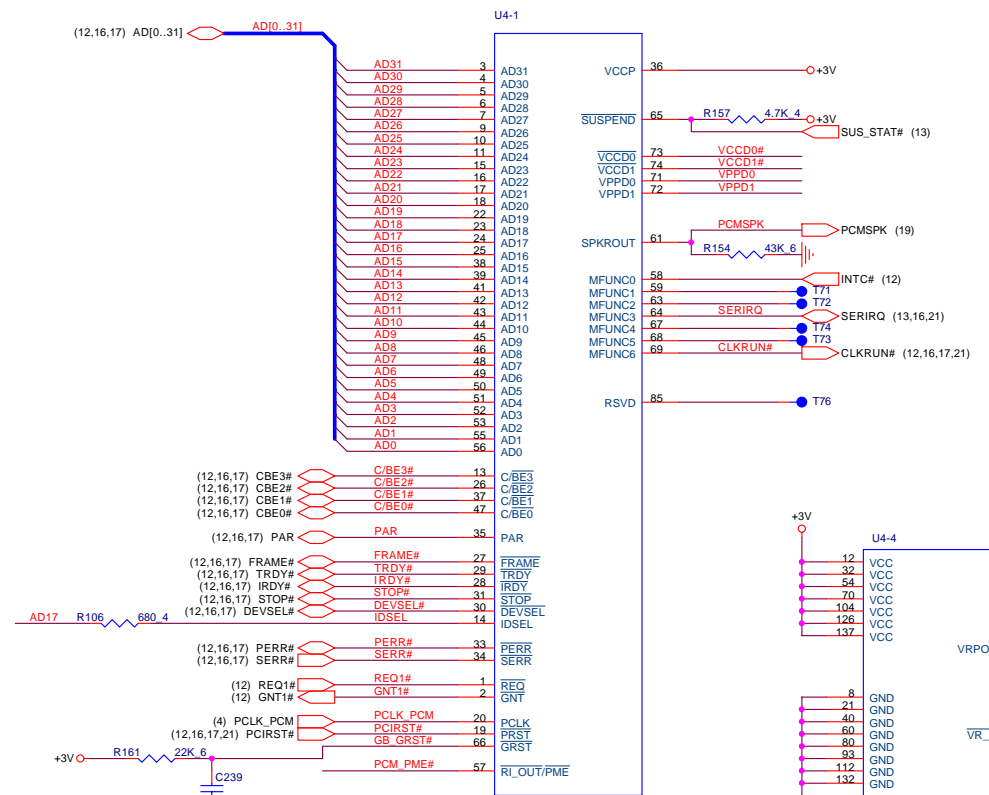
ID Select : AD24
 Interrupt Pin : INTA#
 Request Indicate : REQ0#
 Grant Indicate : GNT0#

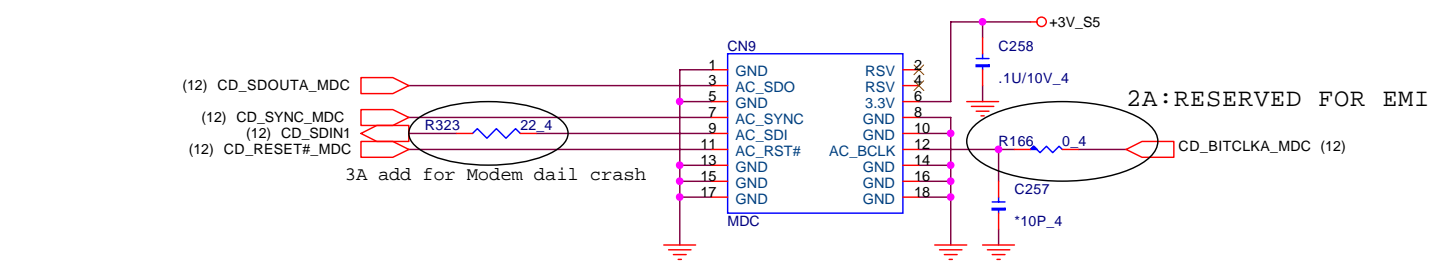
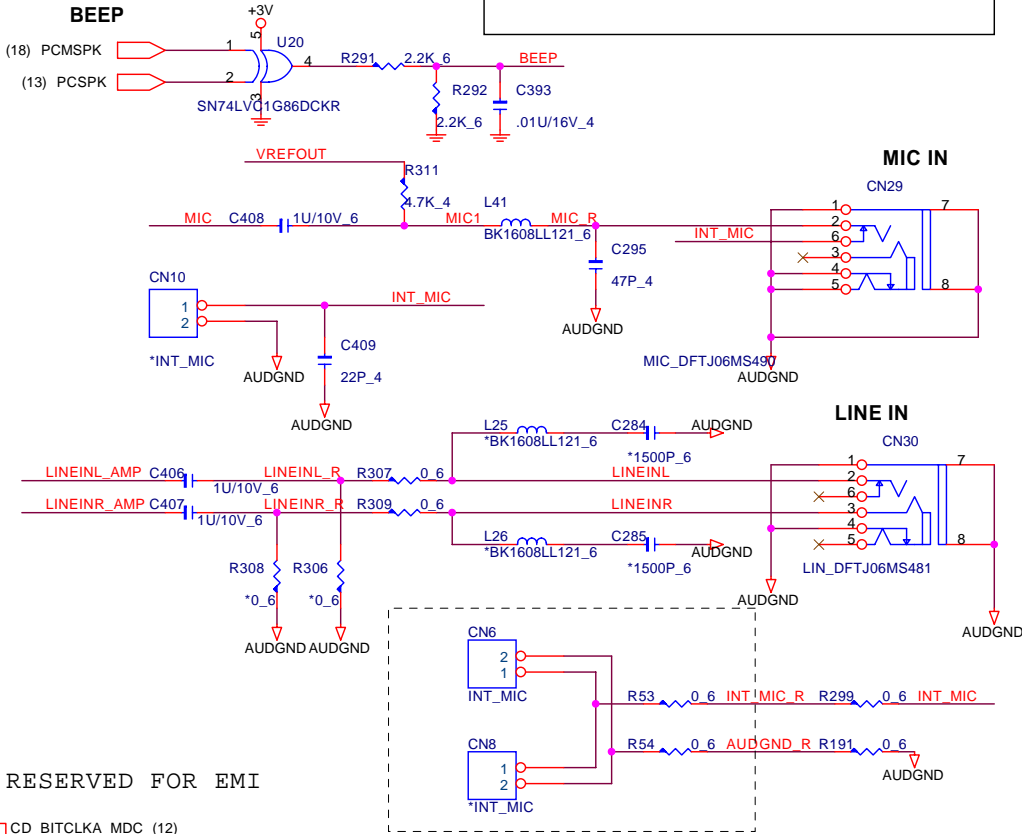
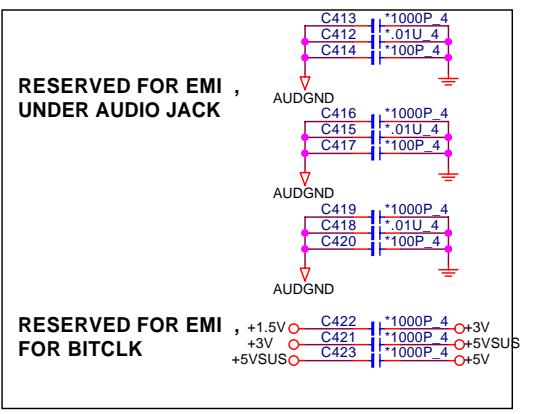
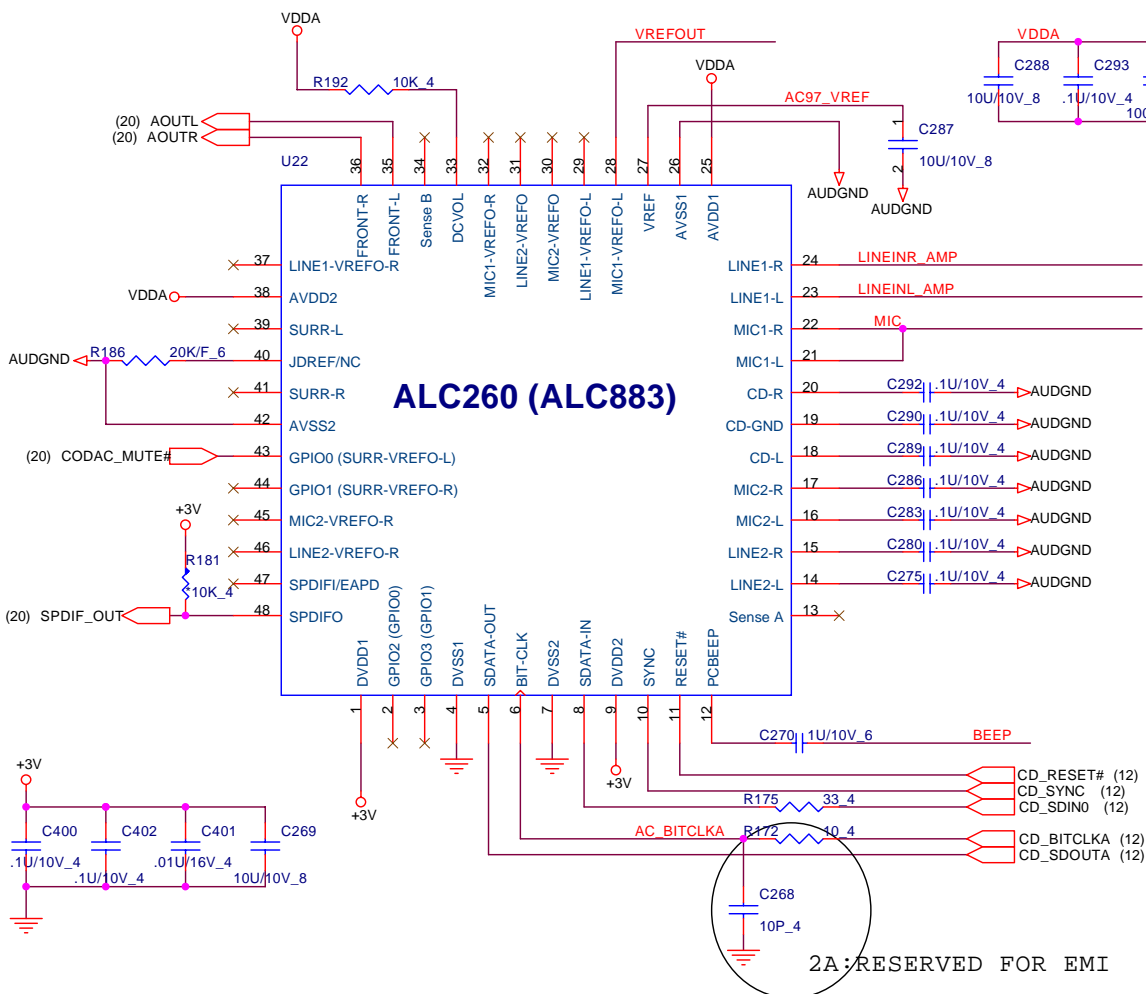


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Size	Document Number	Rev
	LAN RTL8110SBL/8100CL	3A
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ID Select : AD17
 Interrupt Pin : INTC#
 Request Indicate : REQ1#
 Grant Indicate : GNT1#



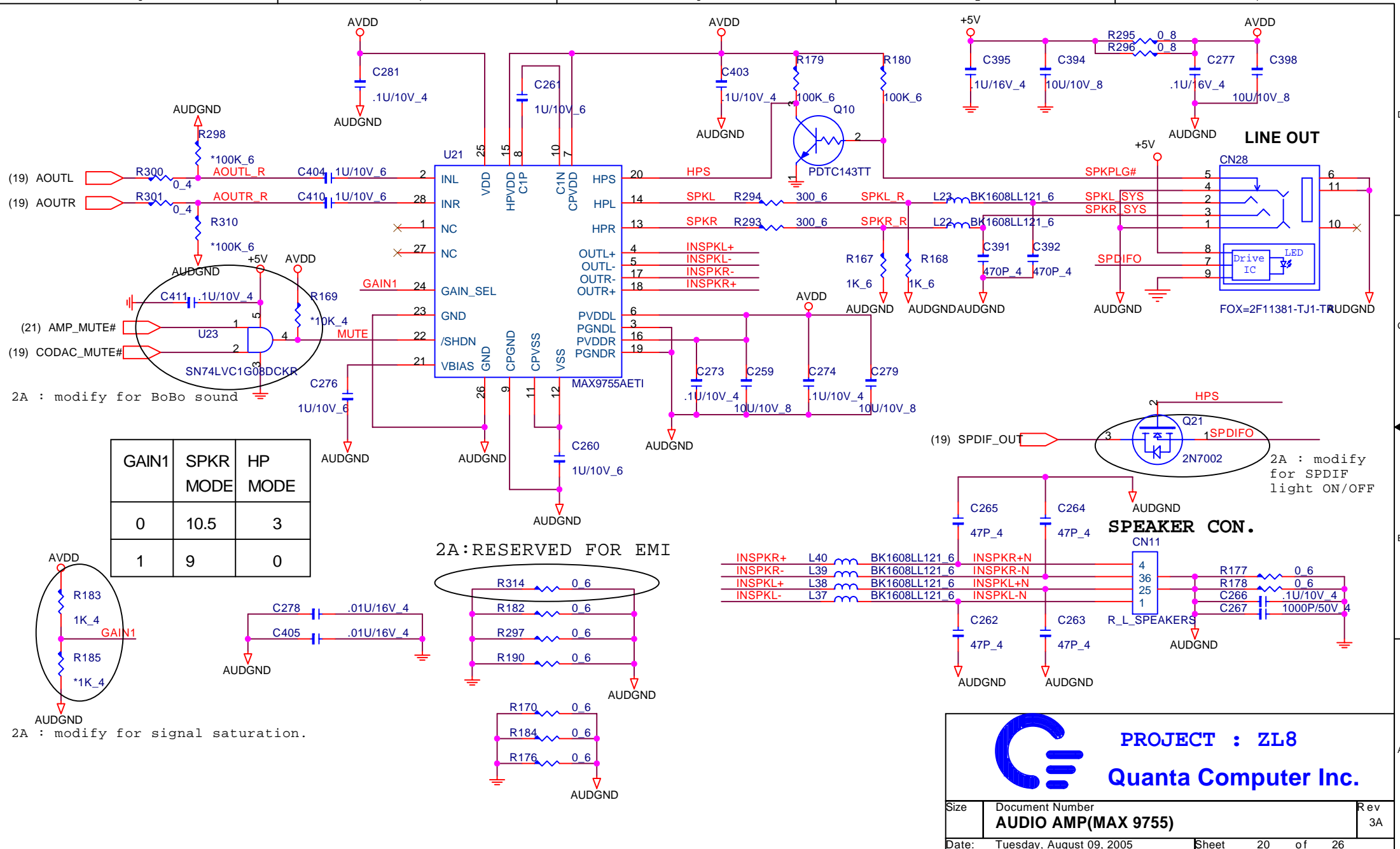


PROJECT : ZL8

Quanta Computer Inc.

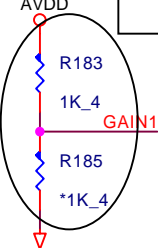
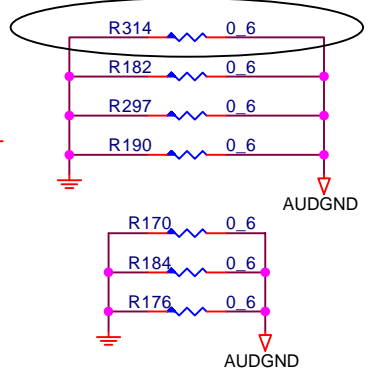
Size: Document Number **REALTEK ALC883** Rev 3A

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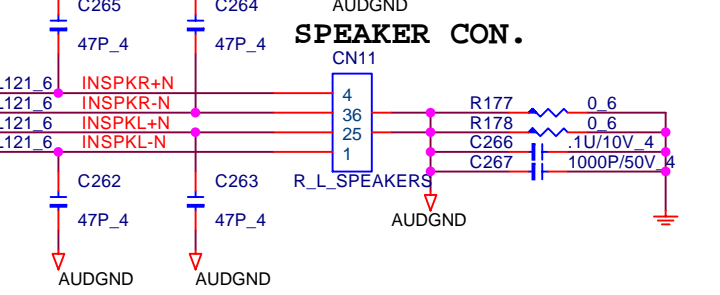
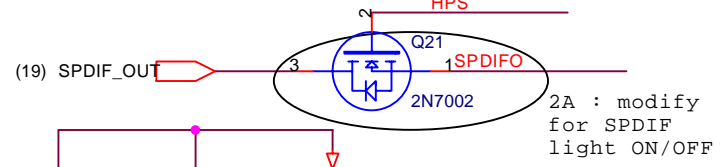
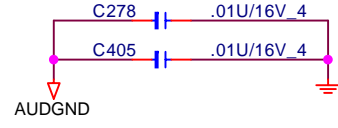



GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0

2A: RESERVED FOR EMI



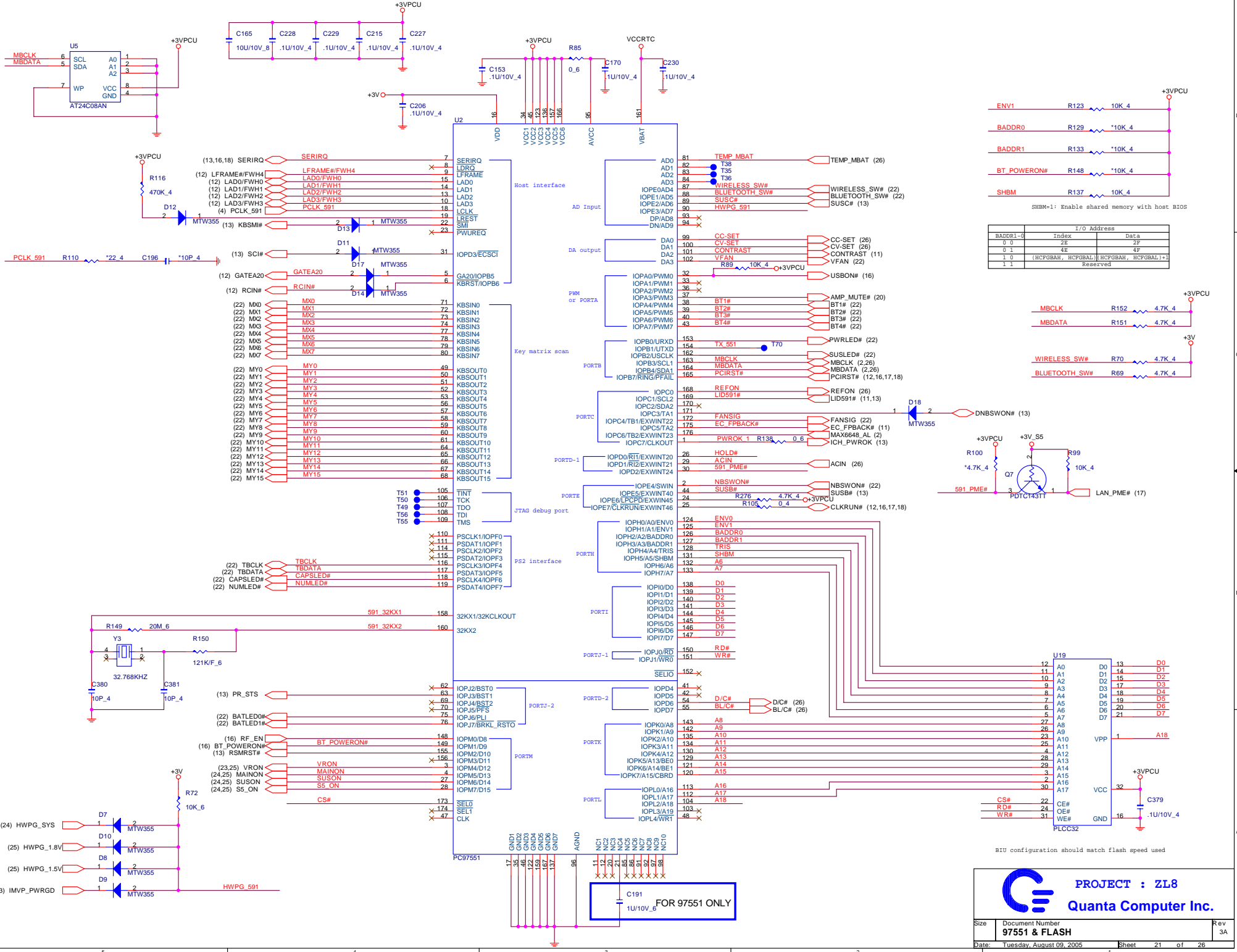
2A : modify for signal saturation.





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Quanta Computer Inc.

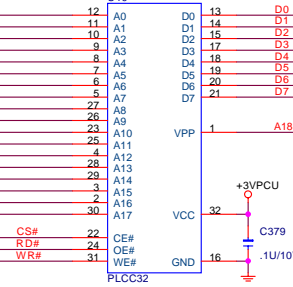
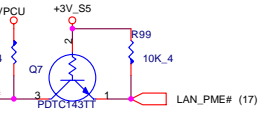
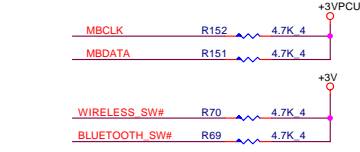
Size	Document Number	Rev
	AUDIO AMP(MAX 9755)	3A
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SHBM=1: Enable shared memory with host BIOS

BADDR1-0	I/O Address	Index	Data
0 0	2E	D2	2F
0 1	4E	D2	4F
1 0	(HCFGBAH, HCFGBAL) (HCFGBAH, HCFGBAL)+1		
1 1	Reserved		

BADDR1-0	I/O Address	Index	Data
0 0	2E	D2	2F
0 1	4E	D2	4F
1 0	(HCFGBAH, HCFGBAL) (HCFGBAH, HCFGBAL)+1		
1 1	Reserved		



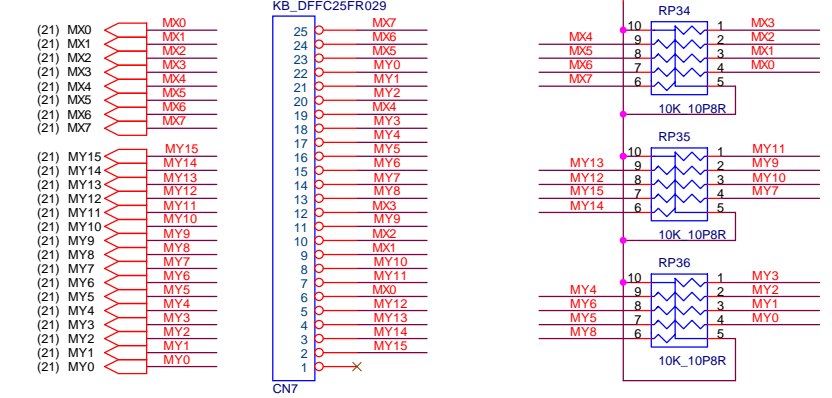
BIU configuration should match flash speed used

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Quanta Computer Inc.

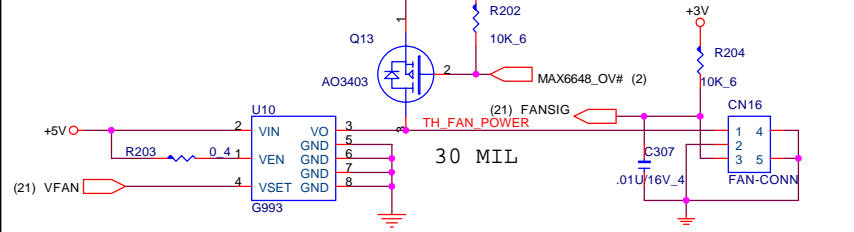
Size	Document Number	Rev	
	97551 & FLASH	3A	
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C191
1U/10V_6
FOR 97551 ONLY

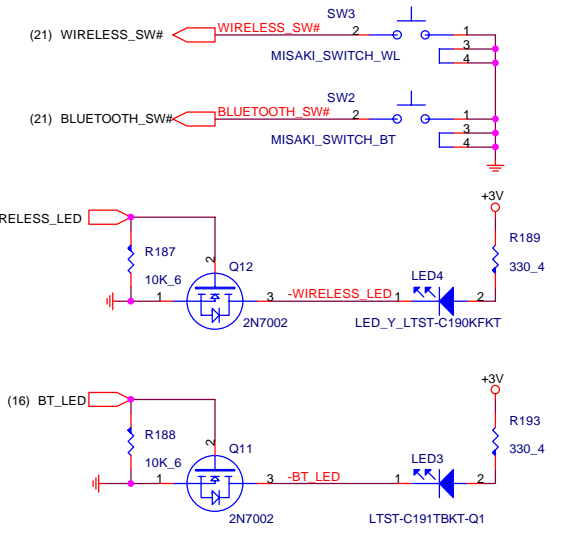
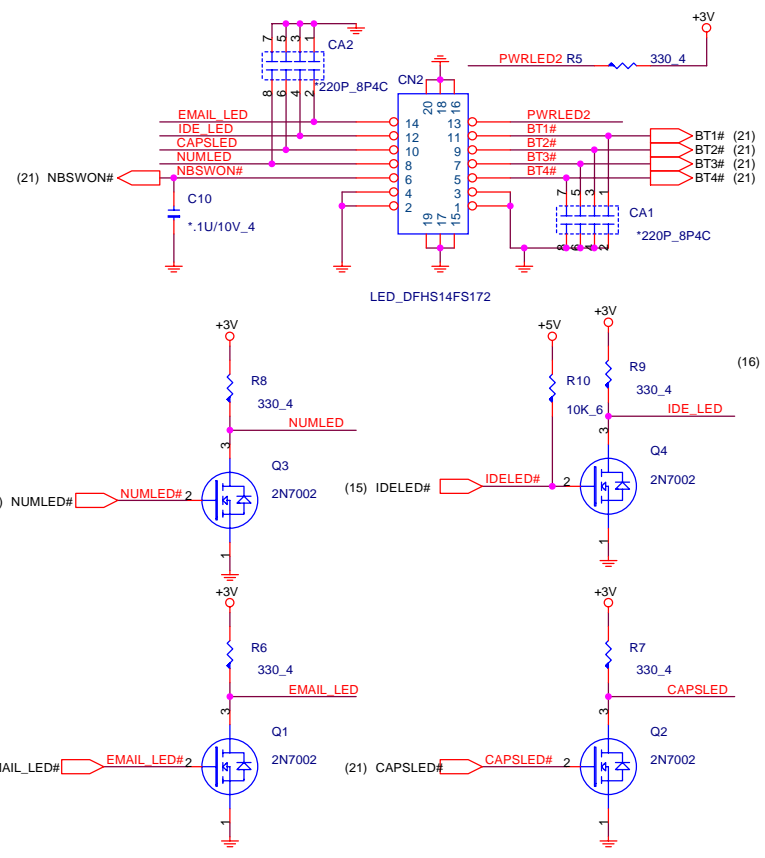
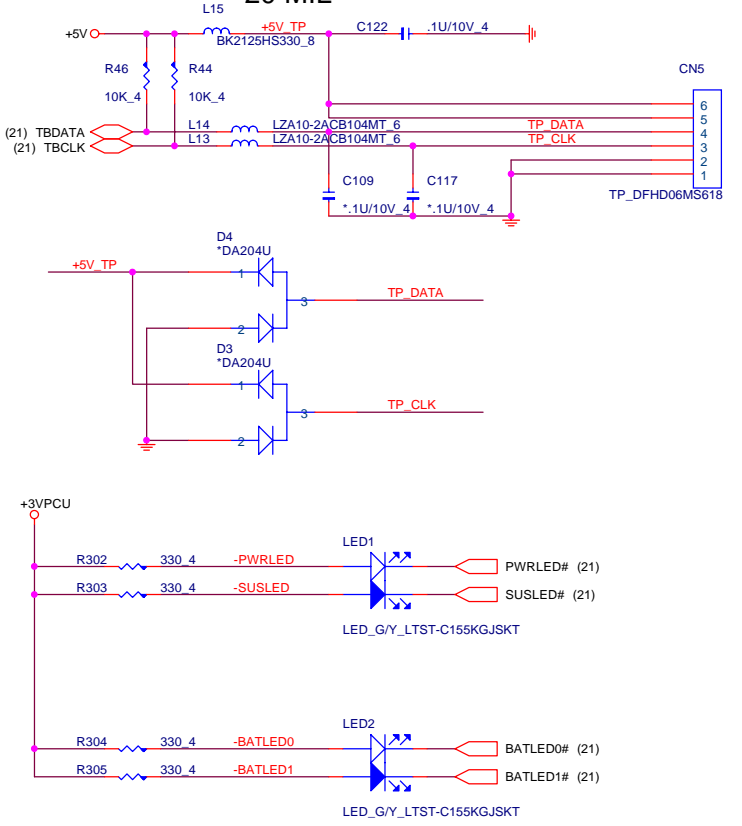
INT K/B



FAN CONTROL



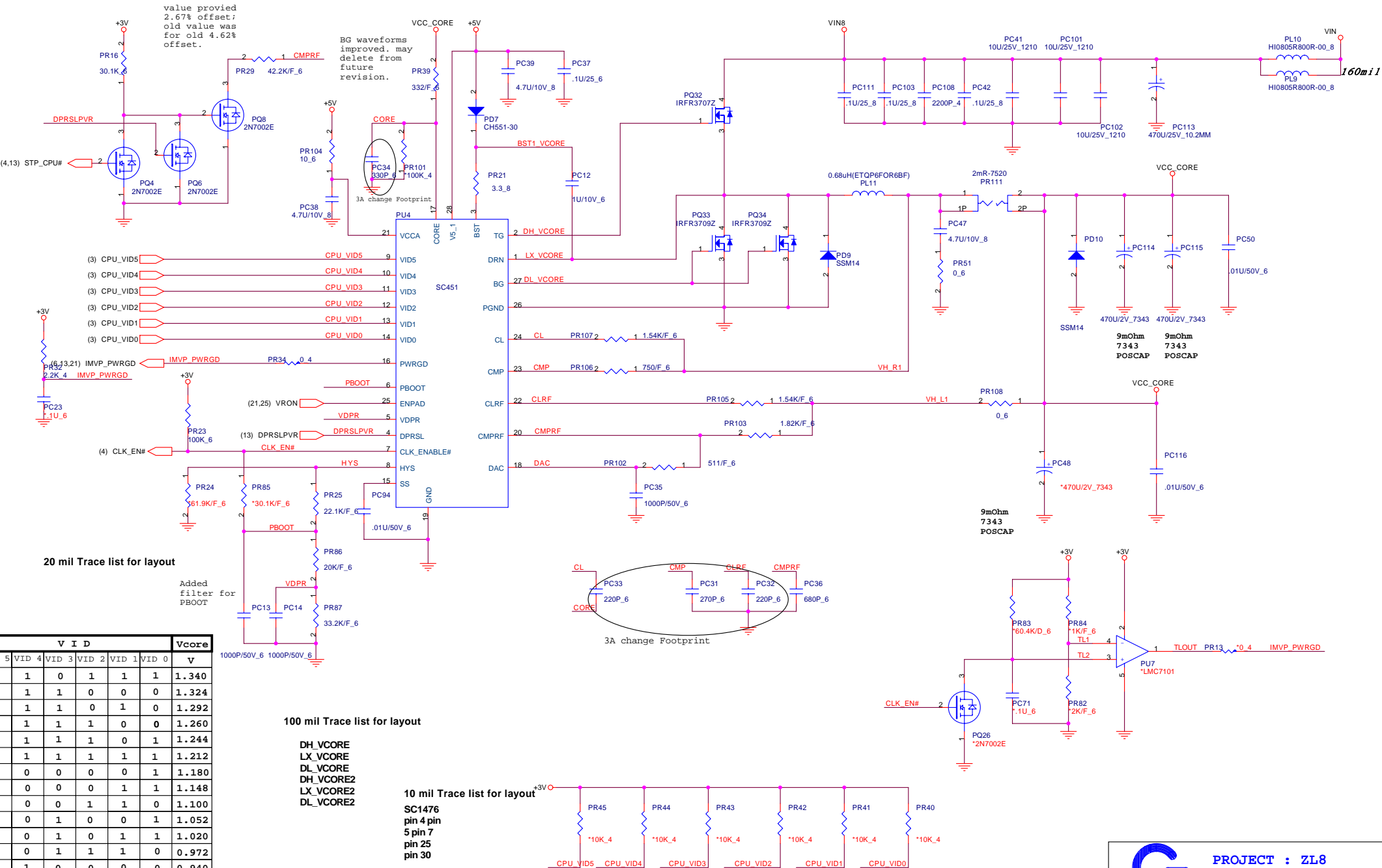
TOUCH PAD



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Quanta Computer Inc.

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value provided
2.67% offset;
old value was
for old 4.62%
offset.



20 mil Trace list for layout

V I D							Vcore
VID 5	VID 4	VID 3	VID 2	VID 1	VID 0		v
0	1	0	1	1	1	1	1.340
0	1	1	0	0	0	0	1.324
0	1	1	0	1	0	0	1.292
0	1	1	1	0	0	0	1.260
0	1	1	1	1	0	1	1.244
0	1	1	1	1	1	1	1.212
1	0	0	0	0	1	1	1.180
1	0	0	0	1	1	0	1.148
1	0	0	1	1	0	1	1.100
1	0	1	0	0	1	1	1.052
1	0	1	0	1	1	1	1.020
1	0	1	1	1	0	0	0.972
1	1	0	0	0	0	0	0.940

100 mil Trace list for layout

- DH_VCORE
- LX_VCORE
- DL_VCORE
- DH_VCORE2
- LX_VCORE2
- DL_VCORE2

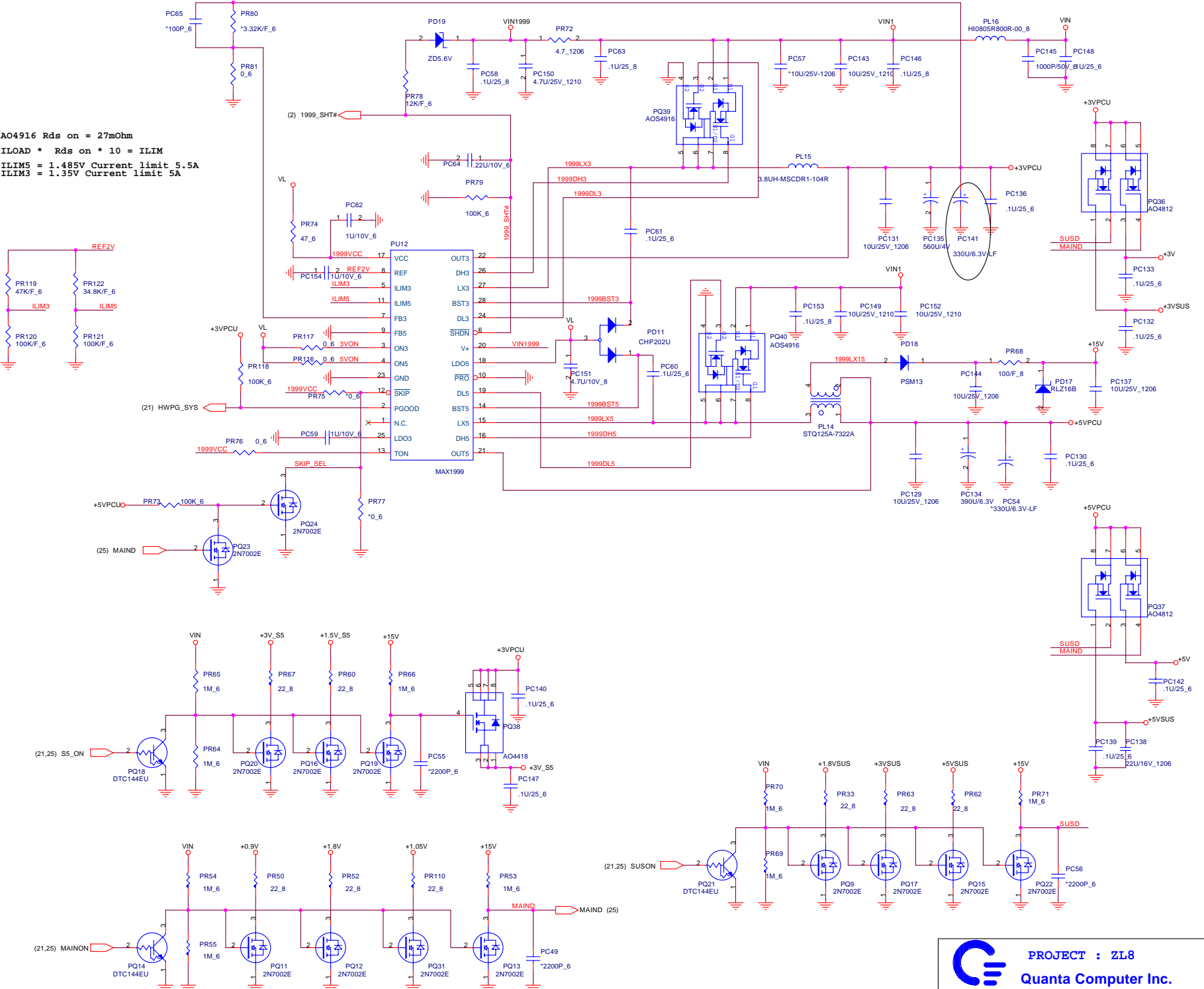
10 mil Trace list for layout

- SC1476
- pin 4 pin
- 5 pin 7
- pin 25
- pin 30

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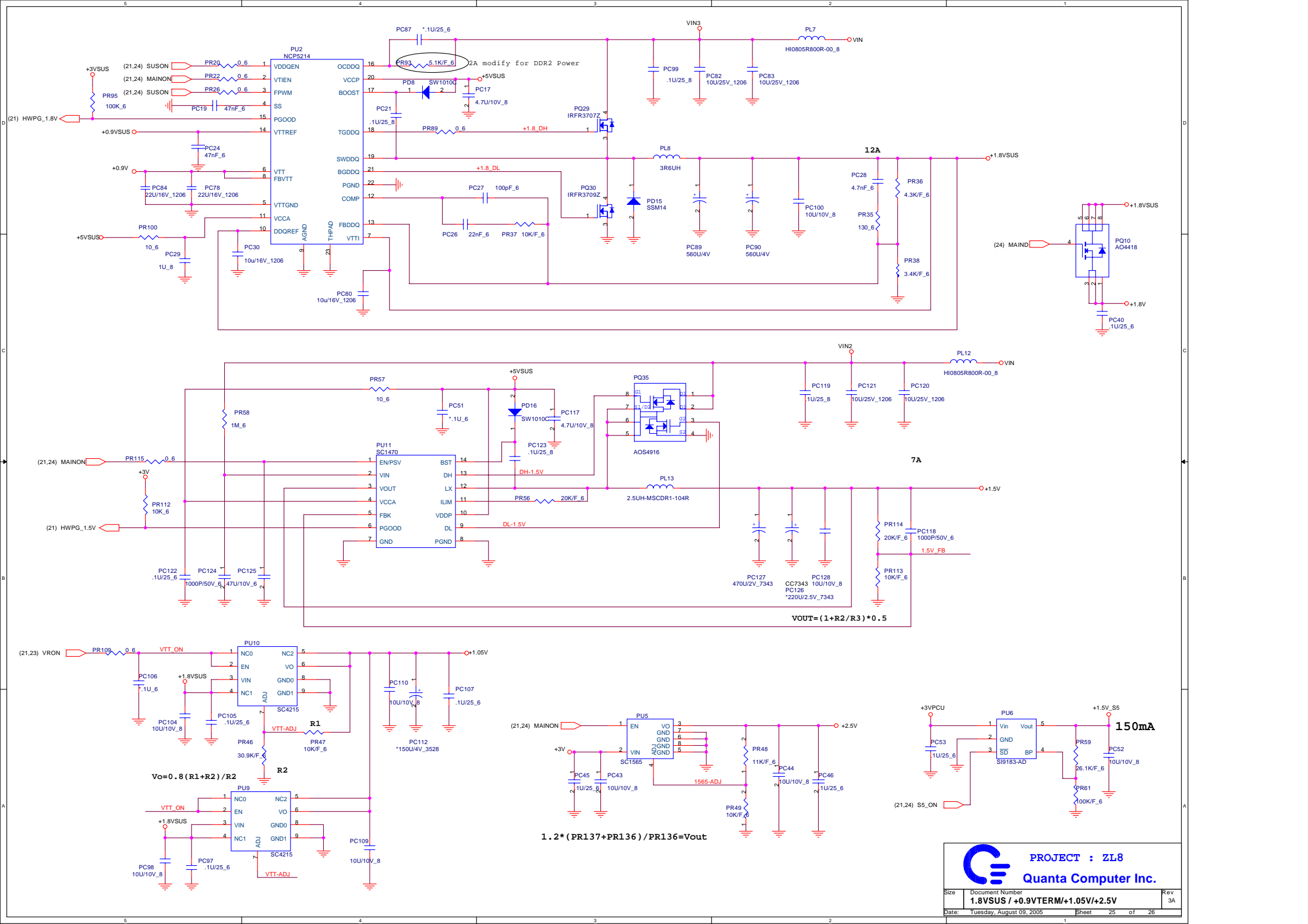
Size	Document Number	Rev
	CPU CORE (SC451)	3A
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AO4916 Rds on = 27mOhm
 ILOAD * Rds on * 10 = ILIM
 ILIM5 = 1.485V Current limit 5.5A
 ILIM3 = 1.35V Current limit 5A



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	5V/3.3V (MAX1999)	3A
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2A modify for DDR2 Power

12A

7A

150mA

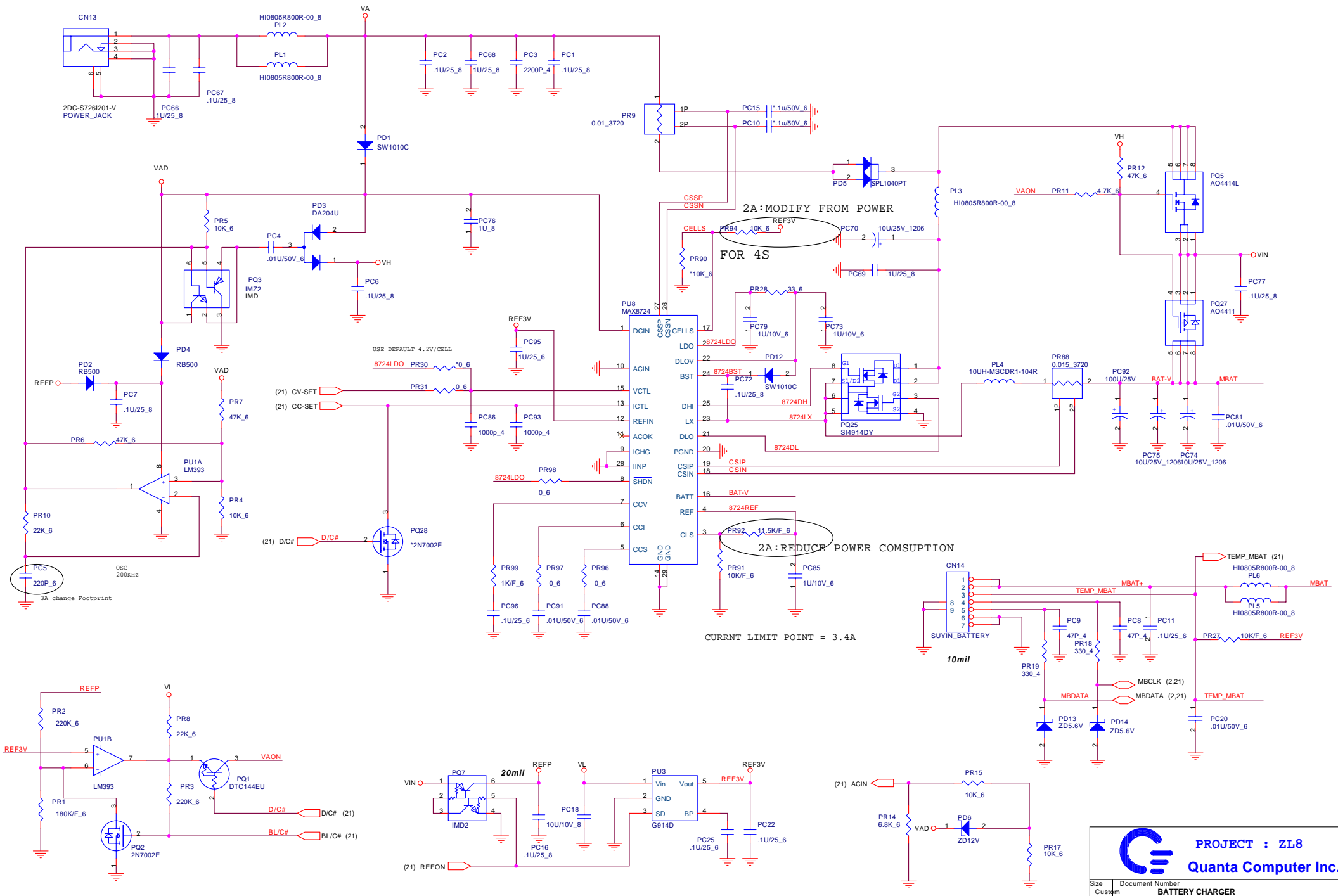
$$V_o = 0.8 \cdot (R1 + R2) / R2$$


$$1.2 \cdot (PR137 + PR136) / PR136 = V_{out}$$

$$V_{OUT} = (1 + R2 / R3) \cdot 0.5$$

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Quanta Computer Inc.

Size	Document Number	Rev
	1.8VSUS / +0.9VTERM/+1.05V/+2.5V	3A
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Quanta Computer Inc.

Size	Document Number	Rev
Custom	BATTERY CHARGER	3A
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