

- 1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
- 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
- 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ZONE	ECN	DESCRIPTION OF CHANGE	CK APPD DATE	ENG APPD DATE
?		?	?	?	?

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Q26 MLB  
GEN X  
REV D

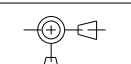

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POWER RAIL DEFINITIONS

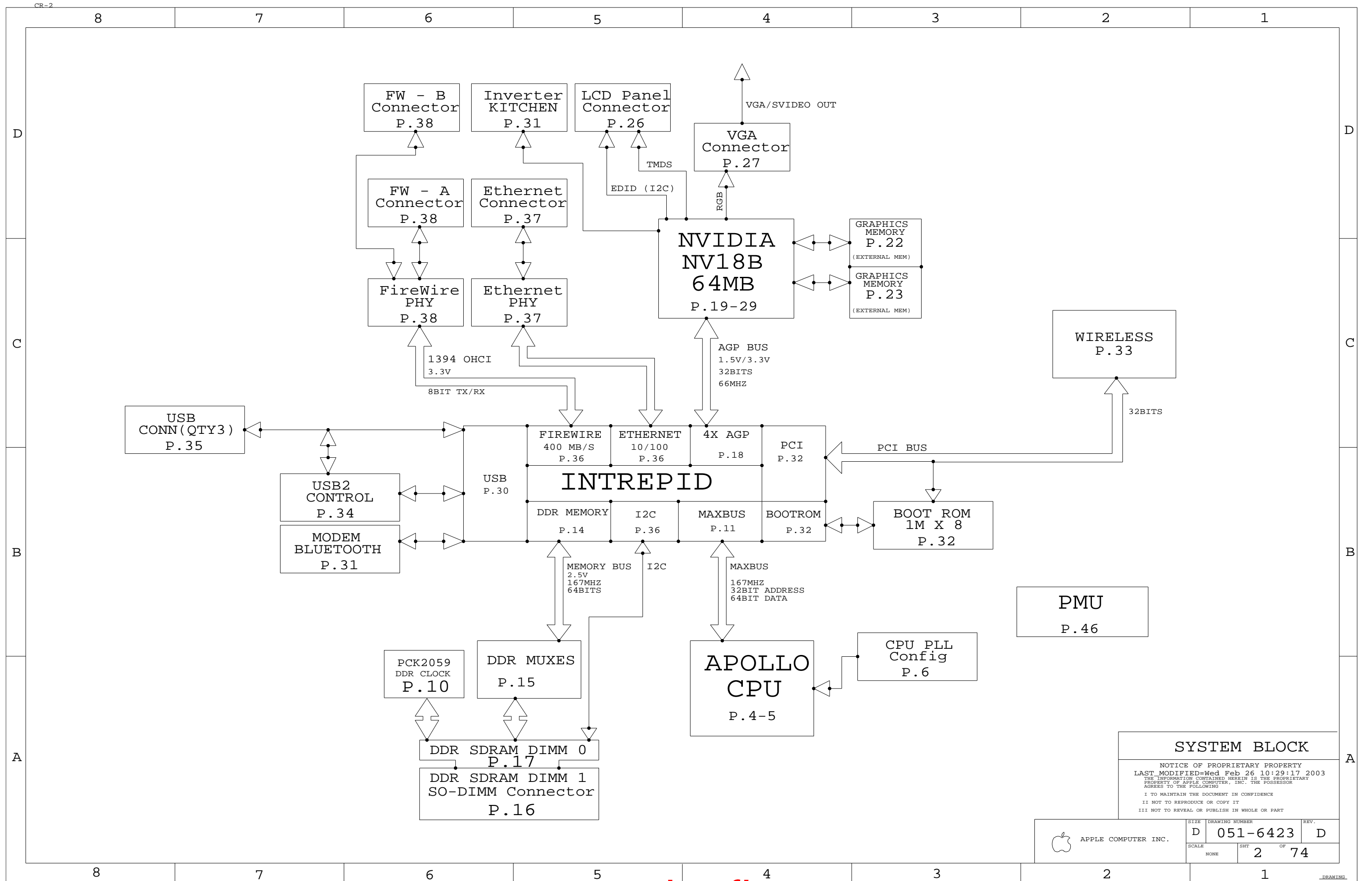
	RUN	SLEEP	SHUTDOWN
+2_5V_MAIN	ON	ON	OFF
+3V_MAIN	ON	ON	OFF
+5V_MAIN	ON	ON	OFF
+5V_SLEEP	ON	OFF	OFF
+12V_MAIN	ON	ON	ON
+12V_SLEEP	ON	OFF	OFF
FW_PWR	ON	ON	OFF
+1.8V_SLEEP	ON	OFF	OFF
+MAXBUS_SLEEP	ON	OFF	OFF

SCHEMATIC AND PCB SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
051-6423	1	SCHEM,MLB,Q26,GENX	SCH1	CRITICAL	
820-1501	1	PCB,MLB,IMACG4	PCB1	CRITICAL	
825-2029	1	LBL,SER #,BARCODE	PCB1		
056-1158	1	DESIGN GUIDE,MCO,IMACG4	PCB1	CRITICAL	
057-0040	1	DFM,PNLZN DWG,MLB,Q26	PCB1	CRITICAL	
630-4578	1	630-4578,PCBA,H,Q26,EEE P9V	HYNIX		OMIT
630-4579	1	630-4579,PCBA,S,Q26,EEE P9W	SAMSUNG		OMIT

DIMENSIONS ARE IN MILLIMETERS xx : _____ x.xx : _____ x.xxx : _____ ANGLES : _____ DO NOT SCALE DRAWING  THIRD ANGLE PROJECTION	METRIC		 Apple Computer Inc.		
	DRAFTER ENG APPD QA APPD RELEASE	DESIGN CK MFG APPD DESIGNER SCALE	NOTICE OF PROPRIETARY PROPERTY THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART		
	MATERIAL/FINISH NOTED AS APPLICABLE		TITLE <b>SCHEM,MLB,Q26,GEN X</b>		
	SIZE <b>D</b>		DRAWING NUMBER <b>051-6423</b>		

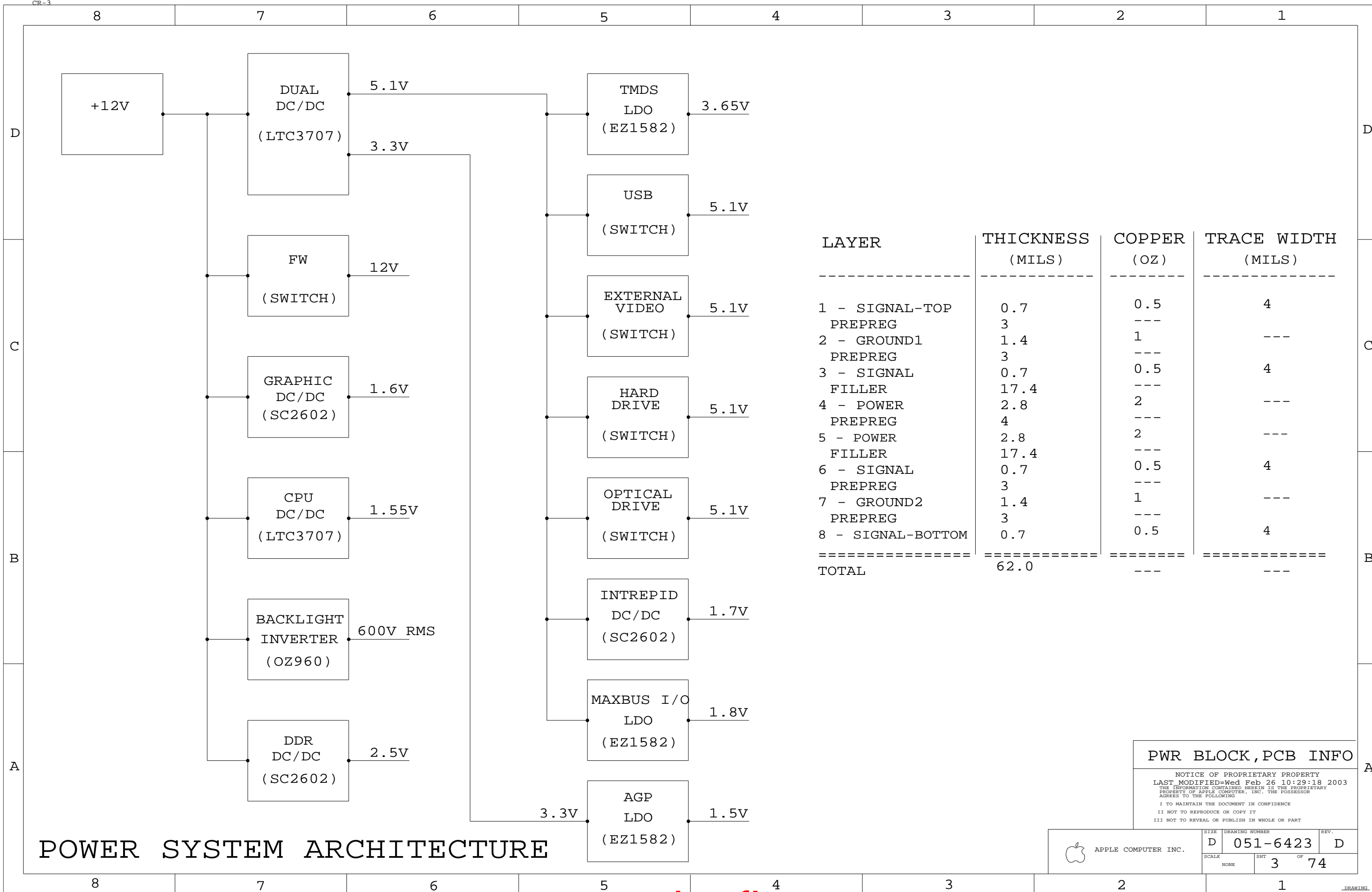
REV. **D**  
SHT 1 OF 74



**SYSTEM BLOCK**

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	NONE	051-6423	D
SCALE		SHT	OF
		2	74



LAYER	THICKNESS (MILS)	COPPER (OZ)	TRACE WIDTH (MILS)
1 - SIGNAL-TOP	0.7	0.5	4
PREPREG	3	---	---
2 - GROUND1	1.4	1	---
PREPREG	3	---	---
3 - SIGNAL	0.7	0.5	4
FILLER	17.4	---	---
4 - POWER	2.8	2	---
PREPREG	4	---	---
5 - POWER	2.8	2	---
FILLER	17.4	---	---
6 - SIGNAL	0.7	0.5	4
PREPREG	3	---	---
7 - GROUND2	1.4	1	---
PREPREG	3	---	---
8 - SIGNAL-BOTTOM	0.7	0.5	4
=====	=====	=====	=====
TOTAL	62.0	---	---

# POWER SYSTEM ARCHITECTURE

## PWR BLOCK, PCB INFO

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	D	051-6423	D
SCALE	SHT	OF	
NONE	3	74	

CPU INTERNAL PLL FILTERING

CPU\_VCORE\_SLEEP 4D7 9B6 9C2 47B3 47C1 54C6 61B4 61D7

61D7 61B4 54C6 47C1 47B3 9C2 9B6 4D3 CPU\_VCORE\_SLEEP

+MAXBUS\_SLEEP 48D4 54C6 61C7 6D8 7A3 7B3 7C3 7C5 7C7 8D3 9B2 9D2 9D4 11B8 11D8 46B7 46D1 46D2 47D2

D

D

C

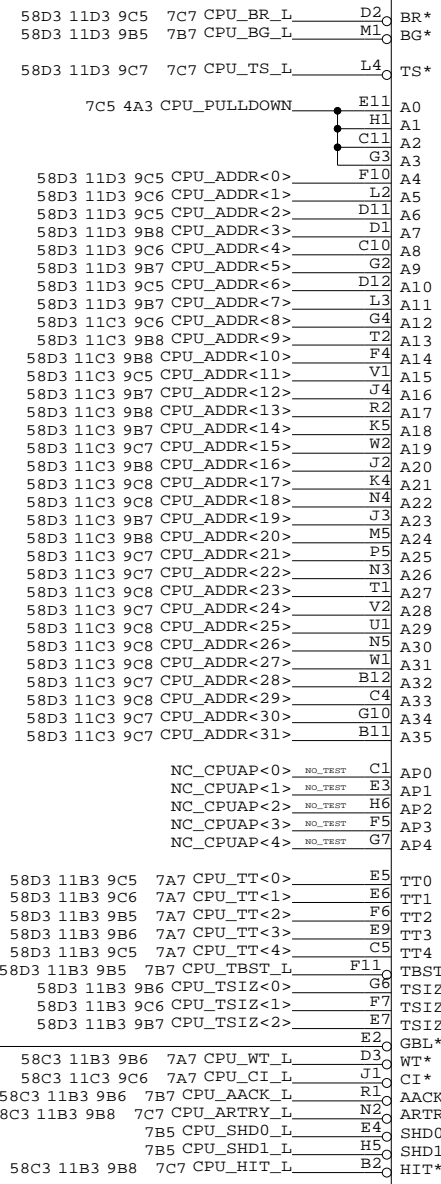
C

B

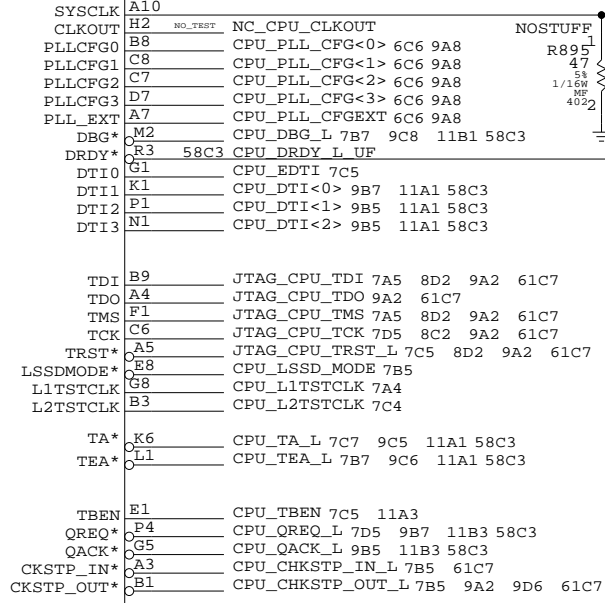
B

A

A



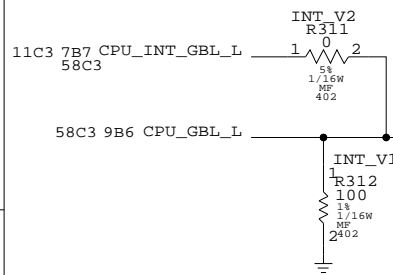
U34  
800MHZ  
APOLLO\_MPC7445\_360  
BGA  
(1 OF 3)  
SEE\_TABLE



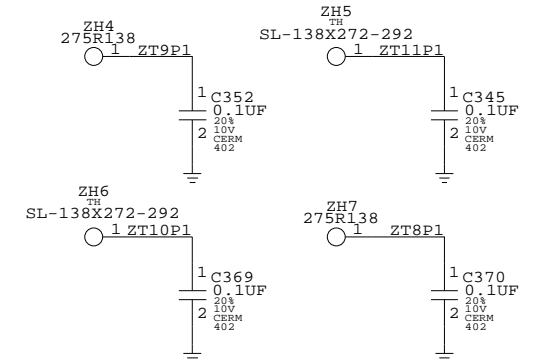
RC GLITCH FILTER  
PLACE CLOSE TO PIN

NOSTUFF

FILTERS A WAKE FROM SLEEP GLITCH  
IF NECESSARY



INTREPID VERSION 1 PULLS GBL  
ALL THE TIME. NEED TO  
CUT THE TRACE AND YANK  
DOWN HARD FOR SNOOPING.  
FIXED IN INTREPID VERSION 2.



CPU MECHANICAL PARTS SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
875-1475	1	PAD, THERMAL, CPU, U34	U341	CRITICAL	
870-1113	1	HEAT SINK, CPU, Q26, U34	U342	CRITICAL	DEV
870-1114	1	CLIP, HEAT SINK, CPU, Q26, U34	U343	CRITICAL	DEV
412-0042	1	SCREW, MACH, 3MM W, 8MM L, U34	U344	CRITICAL	DEV
835-0251	1	NUT, 3MM, U34	U345	CRITICAL	DEV

MPC7450 MAXBUS

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APPLE COMPUTER INC. DRAWING NUMBER 051-6423 REV. D  
SCALE NONE SHT 4 OF 74

APOLLO\_MPC7445\_360

NC\_CPUCRUD<0> NO\_TEST F18 NC\_F18  
 NC\_CPUCRUD<1> NO\_TEST F17 NC\_F17  
 NC\_CPUCRUD<2> NO\_TEST F19 NC\_F19  
 NC\_CPUCRUD<3> NO\_TEST H19 NC\_H19  
 NC\_CPUCRUD<4> NO\_TEST H18 NC\_H18  
 NC\_CPUCRUD<5> NO\_TEST H17 NC\_H17  
 NC\_CPUCRUD<6> NO\_TEST H16 NC\_H16  
 NC\_CPUCRUD<7> NO\_TEST E19 NC\_E19  
 NC\_CPUCRUD<8> NO\_TEST D18 NC\_D18  
 NC\_CPUCRUD<9> NO\_TEST F16 NC\_F16  
 NC\_CPUCRUD<10> NO\_TEST G16 NC\_G16  
 NC\_CPUCRUD<11> NO\_TEST D19 NC\_D19  
 NC\_CPUCRUD<12> NO\_TEST F15 NC\_F15  
 NC\_CPUCRUD<13> NO\_TEST G19 NC\_G19  
 NC\_CPUCRUD<14> NO\_TEST E16 NC\_E16  
 NC\_CPUCRUD<15> NO\_TEST D17 NC\_D17  
 NC\_CPUCRUD<16> NO\_TEST D16 NC\_D16

U34  
 800MHZ  
 BGA  
 (3 OF 3)

NC\_CPUCRUD<17> NO\_TEST P15 NC\_P15  
 NC\_CPUCRUD<18> NO\_TEST L15 NC\_L15  
 NC\_CPUCRUD<19> NO\_TEST N15 NC\_N15  
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 NC\_CPUCRUD<24> NO\_TEST N13 NC\_N13  
 NC\_CPUCRUD<25> NO\_TEST N16 NC\_N16  
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 NC\_CPUCRUD<28> NO\_TEST P19 NC\_P19  
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 NC\_CPUCRUD<33> NO\_TEST K15 NC\_K15  
 NC\_CPUCRUD<34> NO\_TEST J14 NC\_J14  
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 NC\_CPUCRUD<57> NO\_TEST E15 NC\_E15  
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 NC\_CPUCRUD<60> NO\_TEST C17 NC\_C17  
 NC\_CPUCRUD<61> NO\_TEST C16 NC\_C16  
 NC\_CPUCRUD<62> NO\_TEST G13 NC\_G13  
 NC\_CPUCRUD<63> NO\_TEST E14 NC\_E14  
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 NC\_CPUCRUD<80> NO\_TEST C13 NC\_C13

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 NC\_CPUCRUD<89> NO\_TEST A6 NC\_A6

APOLLO\_MPC7445\_360

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 58D3 11D1 9C7 CPU\_DATA<1> W15 D1  
 58D3 11D1 9C8 CPU\_DATA<2> T14 D2  
 58D3 11D1 9C6 CPU\_DATA<3> V16 D3  
 58D3 11D1 9C7 CPU\_DATA<4> W16 D4  
 58D3 11D1 9C8 CPU\_DATA<5> T15 D5  
 58D3 11D1 9C5 CPU\_DATA<6> U15 D6  
 58D3 11D1 9C8 CPU\_DATA<7> P14 D7  
 58D3 11D1 9D6 CPU\_DATA<8> V13 D8  
 58D3 11D1 9D5 CPU\_DATA<9> W13 D9  
 58D3 11D1 9C7 CPU\_DATA<10> T13 D10  
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 58D3 11D1 9C6 CPU\_DATA<12> U14 D12  
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 58D3 11D1 9C8 CPU\_DATA<14> R12 D14  
 58D3 11C1 9D6 CPU\_DATA<15> T12 D15  
 58D3 11C1 9C5 CPU\_DATA<16> W12 D16  
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 58D3 11C1 9C5 CPU\_DATA<18> N11 D18  
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 58D3 11C1 9D5 CPU\_DATA<20> R11 D20  
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 58D3 11C1 9D7 CPU\_DATA<22> W11 D22  
 58D3 11C1 9D8 CPU\_DATA<23> T11 D23  
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 58D3 11C5 11B1 9C5 CPU\_DATA<46> U19 D46  
 58D3 11C5 11B1 9C7 CPU\_DATA<47> W19 D47  
 58D3 11B8 11B1 9C6 CPU\_DATA<48> U18 D48  
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 58D3 11B8 11B1 9C6 CPU\_DATA<55> V17 D55  
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 58D3 11B5 11B1 9D5 CPU\_DATA<61> R8 D61  
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 58D3 11B5 11B1 9D6 CPU\_DATA<63> T8 D63

U34  
 800MHZ  
 BGA  
 (2 OF 3)

NC\_CPUDP<0> NO\_TEST T3 DP0  
 NC\_CPUDP<1> NO\_TEST W4 DP1  
 NC\_CPUDP<2> NO\_TEST T4 DP2  
 NC\_CPUDP<3> NO\_TEST W9 DP3  
 NC\_CPUDP<4> NO\_TEST M6 DP4  
 NC\_CPUDP<5> NO\_TEST V3 DP5  
 NC\_CPUDP<6> NO\_TEST N8 DP6  
 NC\_CPUDP<7> NO\_TEST W6 DP7

MPC7450 - 2

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SCALE	SHT	OF
NONE	5	74

BOMOPTIONS FOR UPPER-SET OF RESISTORS

1200@133&1500@167&1333@133&1667@167&1467@133&1833@167&1600@133&2000@167&1733@133&2167@167&1867@133&2333@167&2000@133&2500@167&2133@133&2667@167  
 667@133&833@167&733@133&917@167&800@133&1000@167&1067@133&1333@167&1333@133&1667@167&1467@133&1833@167&1600@133&2000@167&1867@133&2333@167&2133@133&2667@167  
 800@133&1000@167&867@133&1083@167&1067@133&1333@167&1200@133&1500@167&1733@133&2167@167&1867@133&2333@167&2133@133&2667@167  
 667@133&833@167&933@133&1167@167&1200@133&1500@167&1333@133&1667@167&1600@133&2000@167  
 667@133&833@167&733@133&917@167&800@133&1000@167&867@133&1083@167&1000@133&1250@167&1200@133&1500@167&1467@133&1833@167&1600@133&2000@167&1733@133&2167@167&2000@133&2500@167&2133@133&2667@167

CPU FREQUENCY CONFIGURATION (SUPPORTED CPU & BUS SPEEDS)

MULTIPLIER (BUS-TO-CORE)	CORE FREQUENCY (AT BUS FREQUENCY)		CPU_PLL_CFG E 0123 HEX
	167MHZ	133MHZ	
5.0X	833	667	0 1011 0B
5.5X	917	733	0 1001 09
6.0X	1000	800	0 1101 0D
6.5X	1083	867	0 0101 05
7.0X	1167	933	0 0010 02
7.5X	1250	1000	0 0001 01
8.0X	1333	1067	0 1100 0C
9.0X	1500	1200	1 0111 17
10.0X	1667	1333	1 1010 1A
11.0X	1833	1467	1 1001 19
12.0X	2000	1600	1 1011 1B
13.0X	2167	1733	1 0101 15
14.0X	2333	1867	1 1100 1C
15.0X	2500	2000	1 0001 11
16.0X	2667	2133	1 1101 1D

CPU FREQUENCY CONFIGURATION (OTHER POSSIBLE CPU & BUS SPEEDS)

MULTIPLIER (BUS-TO-CORE)	CORE FREQUENCY (AT BUS FREQUENCY)		CPU_PLL_CFG E 0123 HEX
	167MHZ	133MHZ	
0.0X	PLL OFF		0 1111 0F
1.0X (11.5X)	167 (1917)	133 (1533)	0 0000 00
PLL BYPASS	PLL BYPASS		0 0011 03
2.0X	333	267	0 0100 04
2.5X (8.5X)	417 (1417)	333 (1133)	0 0110 06
3.0X	500	400	0 1000 08
3.5X (13.5X)	583 (2250)	467 (1800)	0 1110 0E
4.0X	667	533	0 1010 0A
4.5X (9.5X)	750 (1583)	600 (1267)	0 0111 07

17.0X	2833	2267	1 0000 10
18.0X	3000	2400	1 0010 12
20.0X	3333	2667	1 0011 13
21.0X	3500	2800	1 0100 14
24.0X	4000	3200	1 0110 16
10.5X	1750	1400	1 1000 18
28.0X	4667	3733	1 1110 1E
12.5X	2083	1667	1 1111 1F

BOMOPTIONS FOR LOWER-SET OF RESISTORS

933@133&1167@167&1067@133&1333@167&1333@133&1667@167&1867@133&2333@167  
 733@133&917@167&800@133&1000@167&867@133&1083@167&1000@133&1250@167&1067@133&1333@167&1467@133&1833@167&1733@133&2167@167&1867@133&2333@167&2000@133&2500@167&2133@133&2667@167  
 667@133&833@167&733@133&917@167&933@133&1167@167&1000@133&1250@167&1333@133&1667@167&1467@133&1833@167&1600@133&2000@167&2000@133&2500@167  
 867@133&1083@167&933@133&1167@167&1000@133&1250@167&1200@133&1500@167&1733@133&2167@167&2000@133&2500@167  
 667@133&833@167&733@133&917@167&800@133&1000@167&867@133&1083@167&933@133&1167@167&1000@133&1250@167&1067@133&1333@167

CPU SPEED & BUS RATIO SUPPORT

THE CONFIGURATION RESISTORS BELOW ARE SELF CONFIGURING WHEN THE ENGINEER SELECTS THE APPROPRIATE CPU AND BUS SPEED BOM OPTION, THE APPROPRIATE RESISTORS ARE AUTOMATICALLY SELECTED

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
337S2674	1	IC, APOLLO, N5, V3.3, 1.0GHZ	U34		1000@133

CPU BUS RATIO BITS

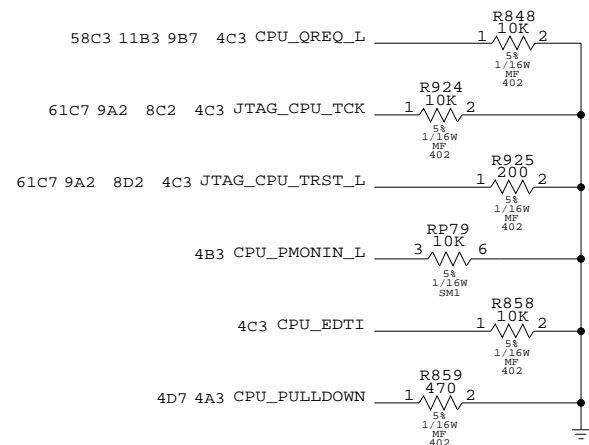
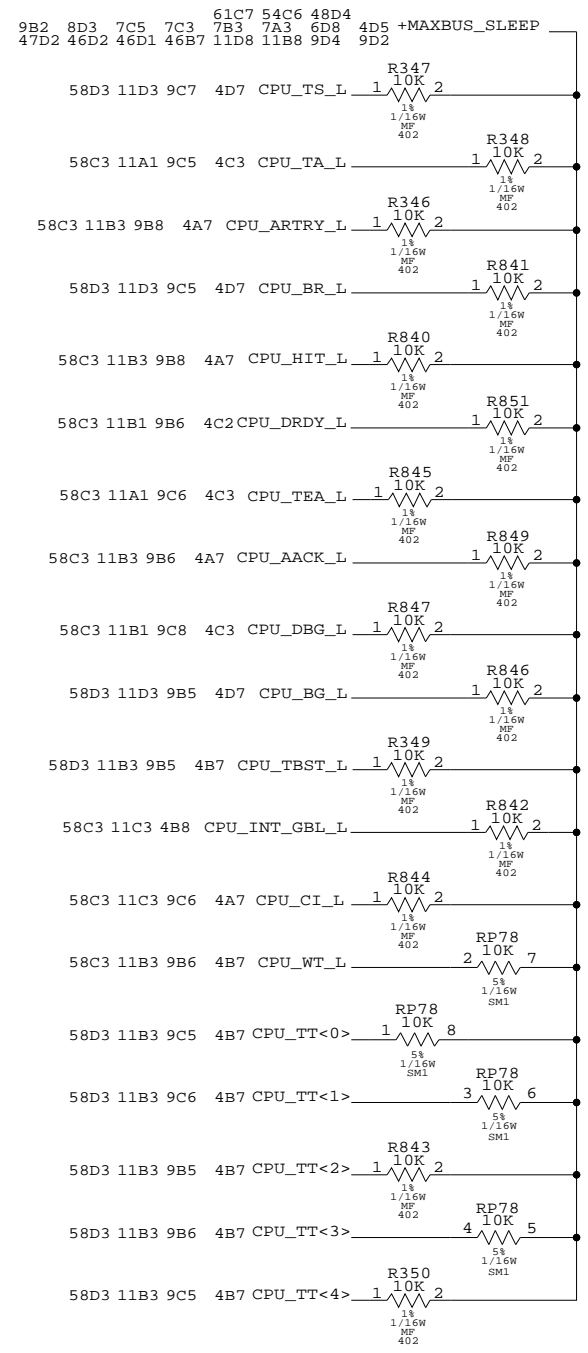
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	D	051-6423	D
SCALE	NONE	SHT	6 OF 74

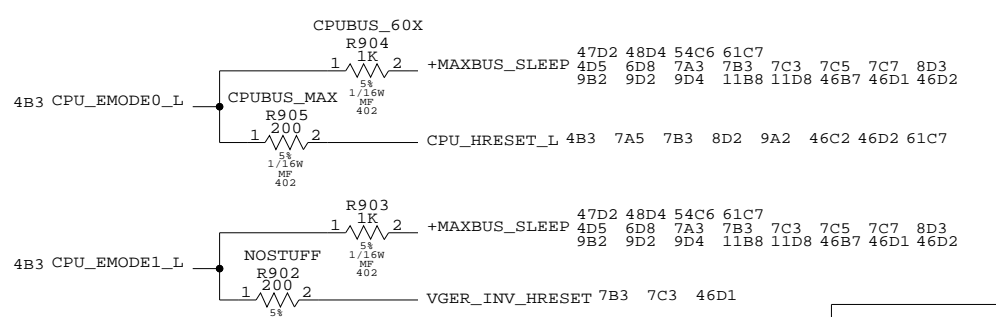
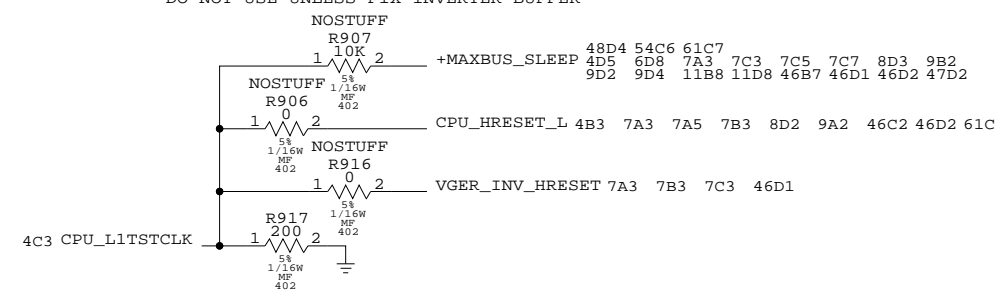
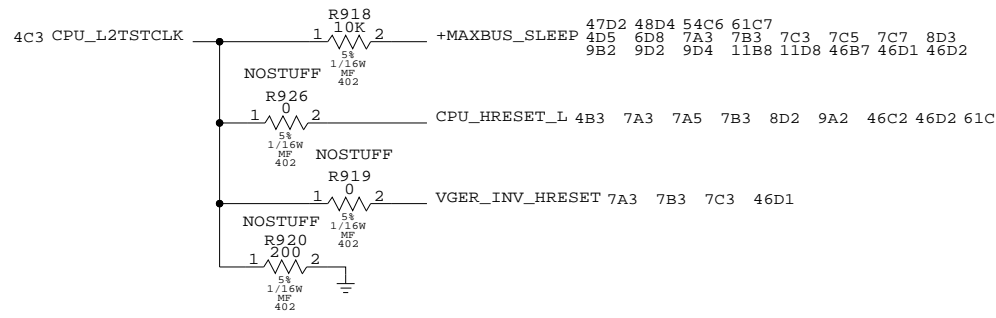
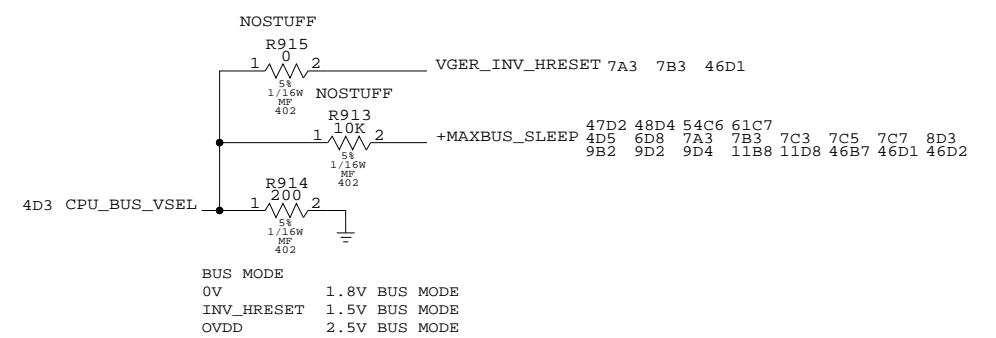
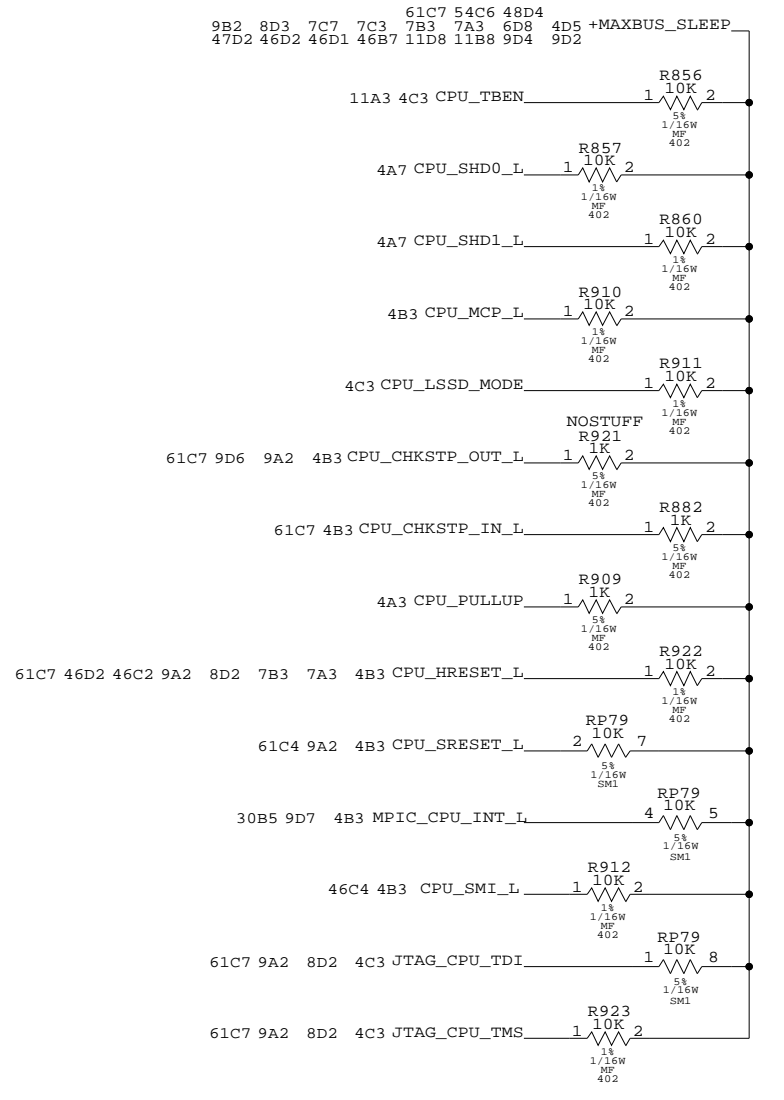
BMODE <0> <1>	MSSCR0 <16:17>	Sys Bus	Vger ID	Addr Drve
L L	1 1	???	01	yes unavail
L !hr	1 0	Max	01	yes unavail
L hr	1 1	???	00	yes unavail
L H	1 0	Max	00	yes unavail
!hr L	0 1	MB+	01	yes unavail
!hr !hr	0 0	60x	01	yes unavail
!hr hr	0 1	MB+	00	yes unavail
!hr H	0 0	60x	00	yes unavail
hr L	1 1	???	01	norm unavail
hr !hr	1 0	Max	01	norm
hr hr	1 1	???	00	norm unavail
HR H	1 0	MAX	00	NORM <- DEFAULT
H L	0 1	MB+	01	norm unavail
H !hr	0 0	60x	01	norm
H hr	0 1	MB+	00	norm unavail
H H	0 0	60x	00	norm

SIGNAL	TIED	APPLICATION
CPU_EMODE0_L	HIGH	60X BUS MODE
CPU_BUS_VSEL	CPU_HRESET_L	MAX BUS MODE
	CPU_HRESET_L	2.5V INTERFACE
	LOW	1.8V INTERFACE
	CPU_HRESET_H	1.5V INTERFACE
CPU_L3_VSEL	CPU_HRESET_L or L3_OVDD	2.5V INTERFACE
	LOW	1.8V INTERFACE
	CPU_HRESET_H	1.5V INTERFACE

MAXBUS PULL-UPS



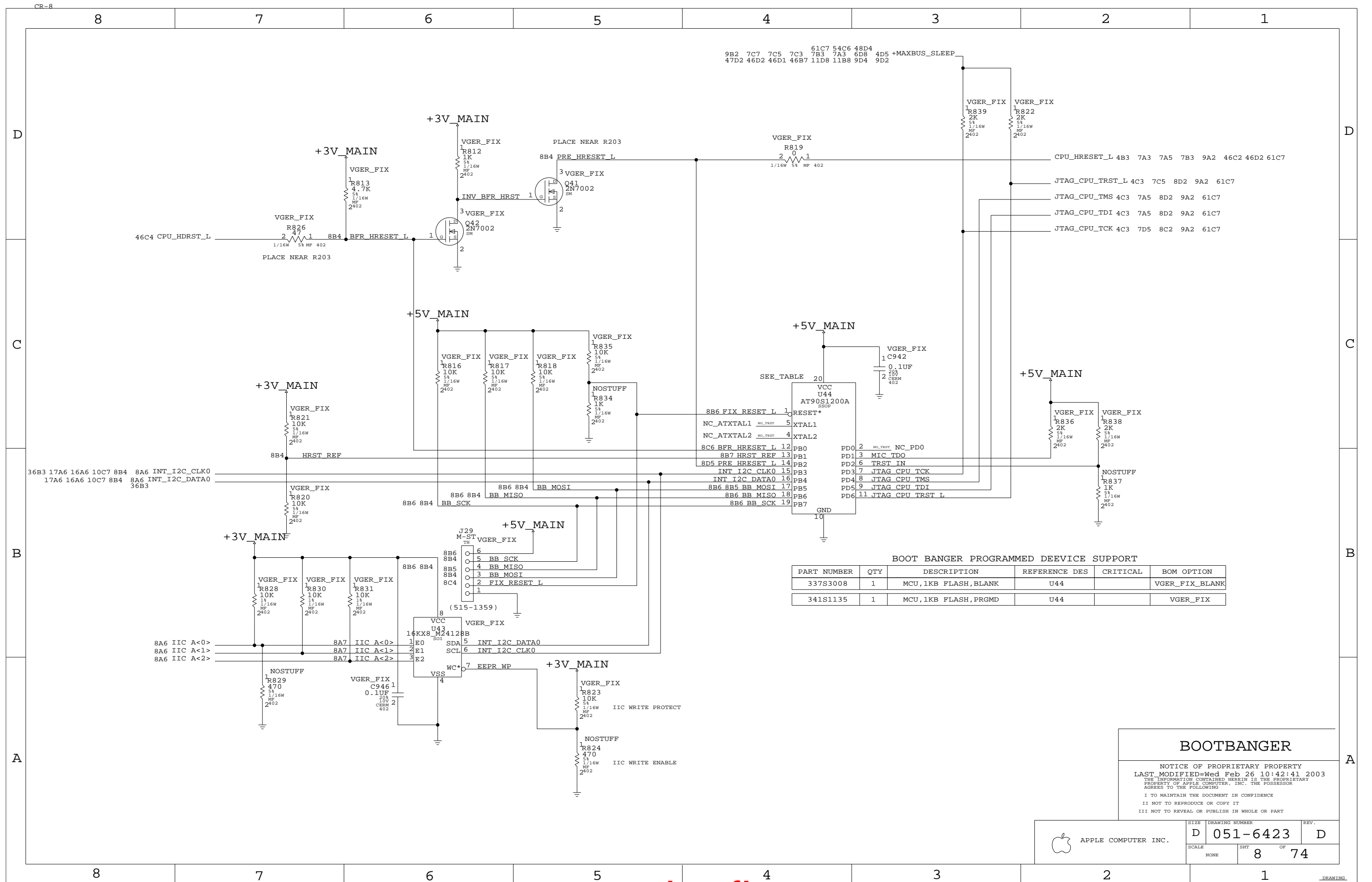
MPC7450 PULL-UPS



CPU CONFIG OPTIONS

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SCALE	SHT		OF
NONE	7		74



**BOOT BANGER PROGRAMMED DEEVICE SUPPORT**

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
337S3008	1	MCU,1KB FLASH,BLANK	U44		VGER_FIX_BLANK
341S1135	1	MCU,1KB FLASH,PRGMD	U44		VGER_FIX

**BOOTBANGER**

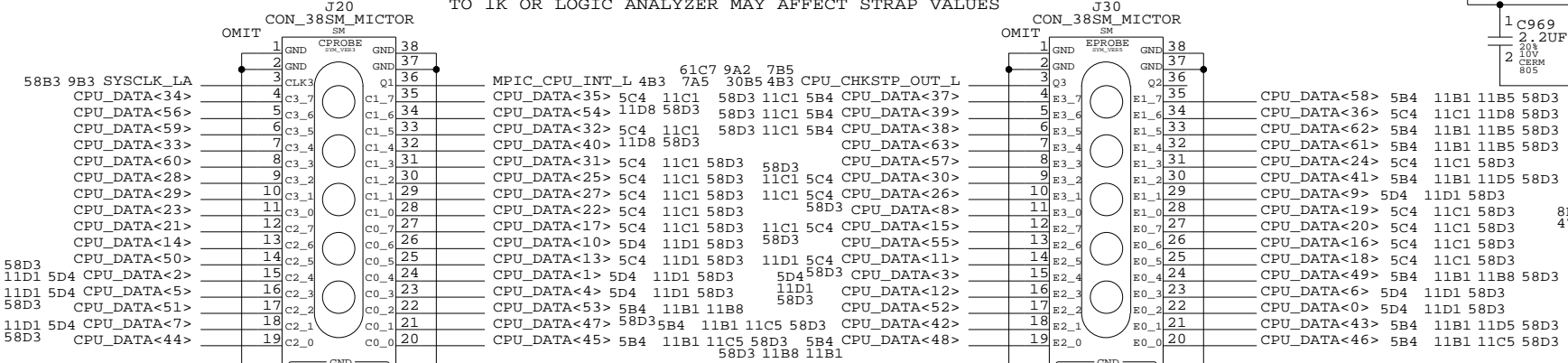
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	SIZE	DRAWING NUMBER	REV.
	D	051-6423	D
SCALE	SHT	OF	
NONE	8	74	

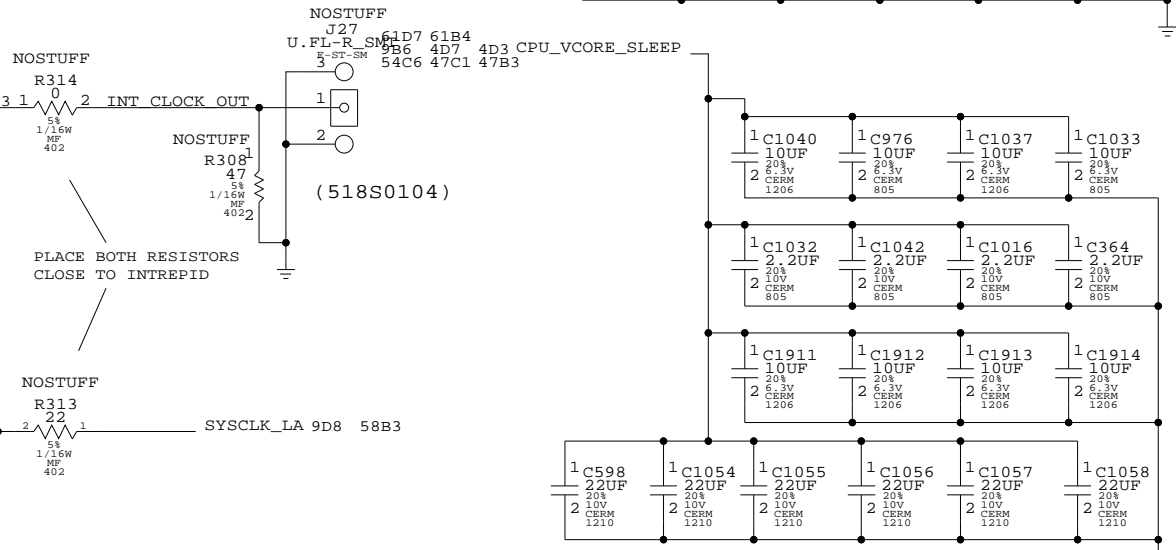


### MAXBUS LOGIC ANALYZER SUPPORT

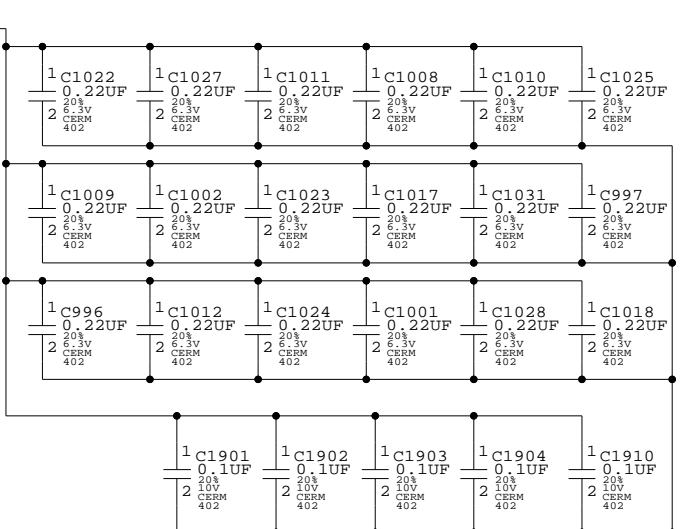
NOTE: INTREPID MAXBUS CONFIG STRAPS MUST DROP TO 1K OR LOGIC ANALYZER MAY AFFECT STRAP VALUES



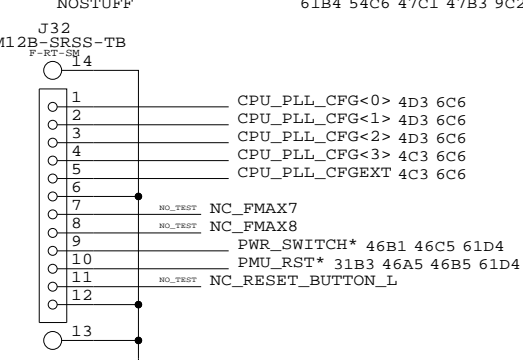
### INTREPID CLOCK OUTPUT



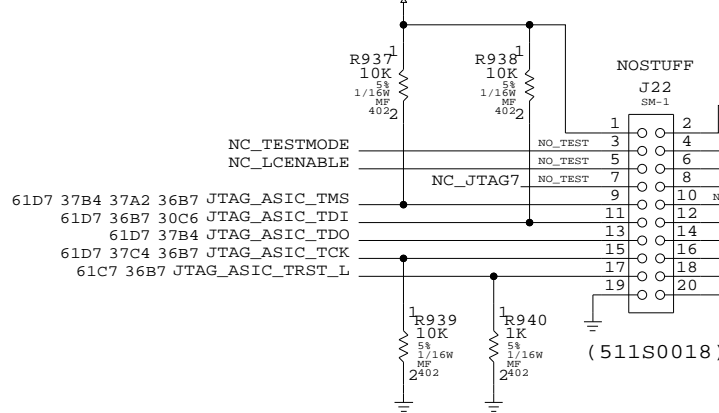
### CPU CORE DECOUPLING



### FMAX DEBUG CONNECTOR



### PULLDOWN ON TRST\* STRONGER TO OVERCOME POSSIBLE LEAKAGE



### LA CONS & ESP

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SCALE	NONE	SHT	9	OF	74	REV.	
APPLE COMPUTER INC.		D 051-6423		D			

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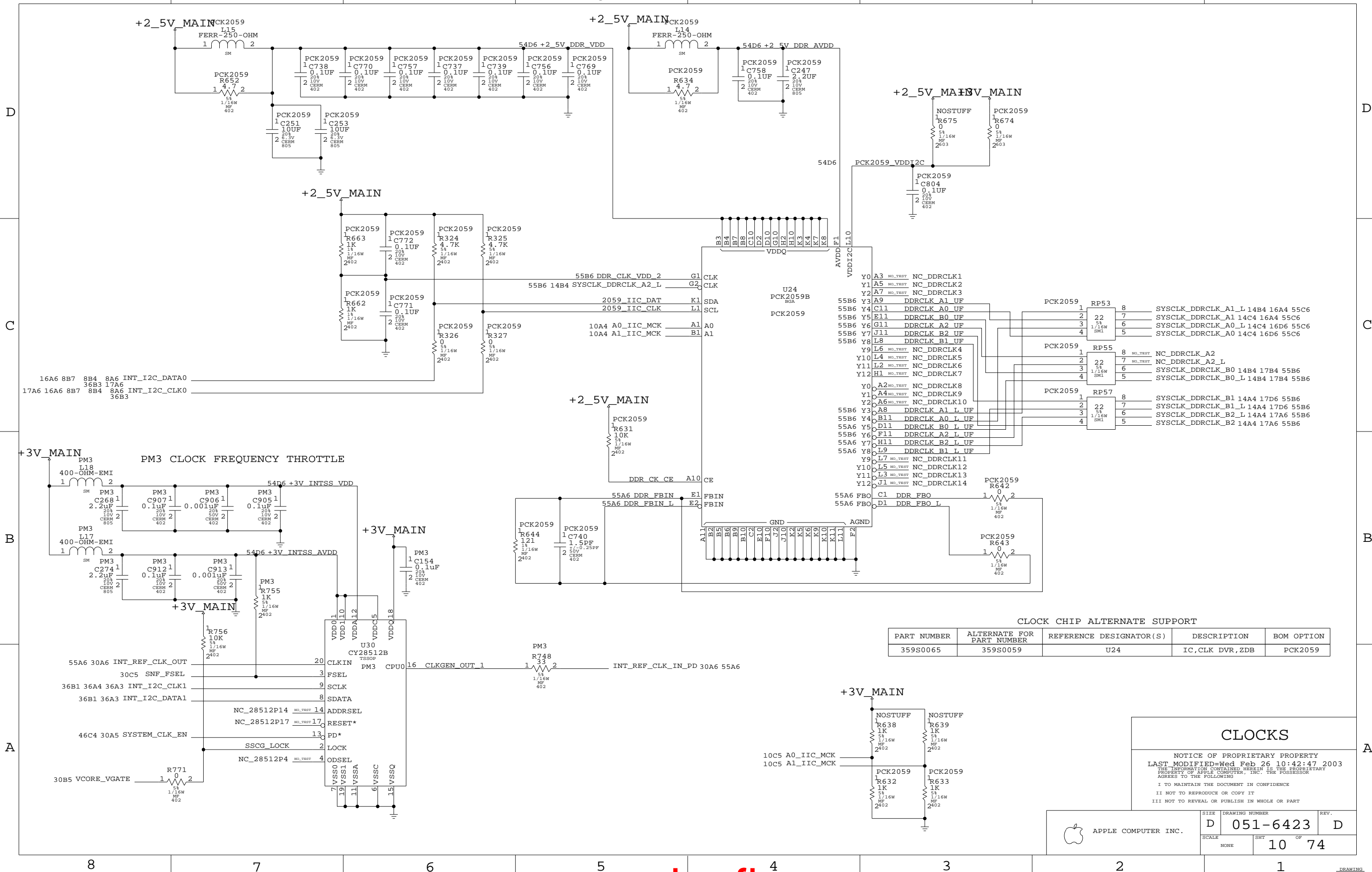
C

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CLOCK CHIP ALTERNATE SUPPORT

PART NUMBER	ALTERNATE FOR PART NUMBER	REFERENCE DESIGNATOR(S)	DESCRIPTION	BOM OPTION
359S0065	359S0059	U24	IC, CLK DVR, ZDB	PCK2059

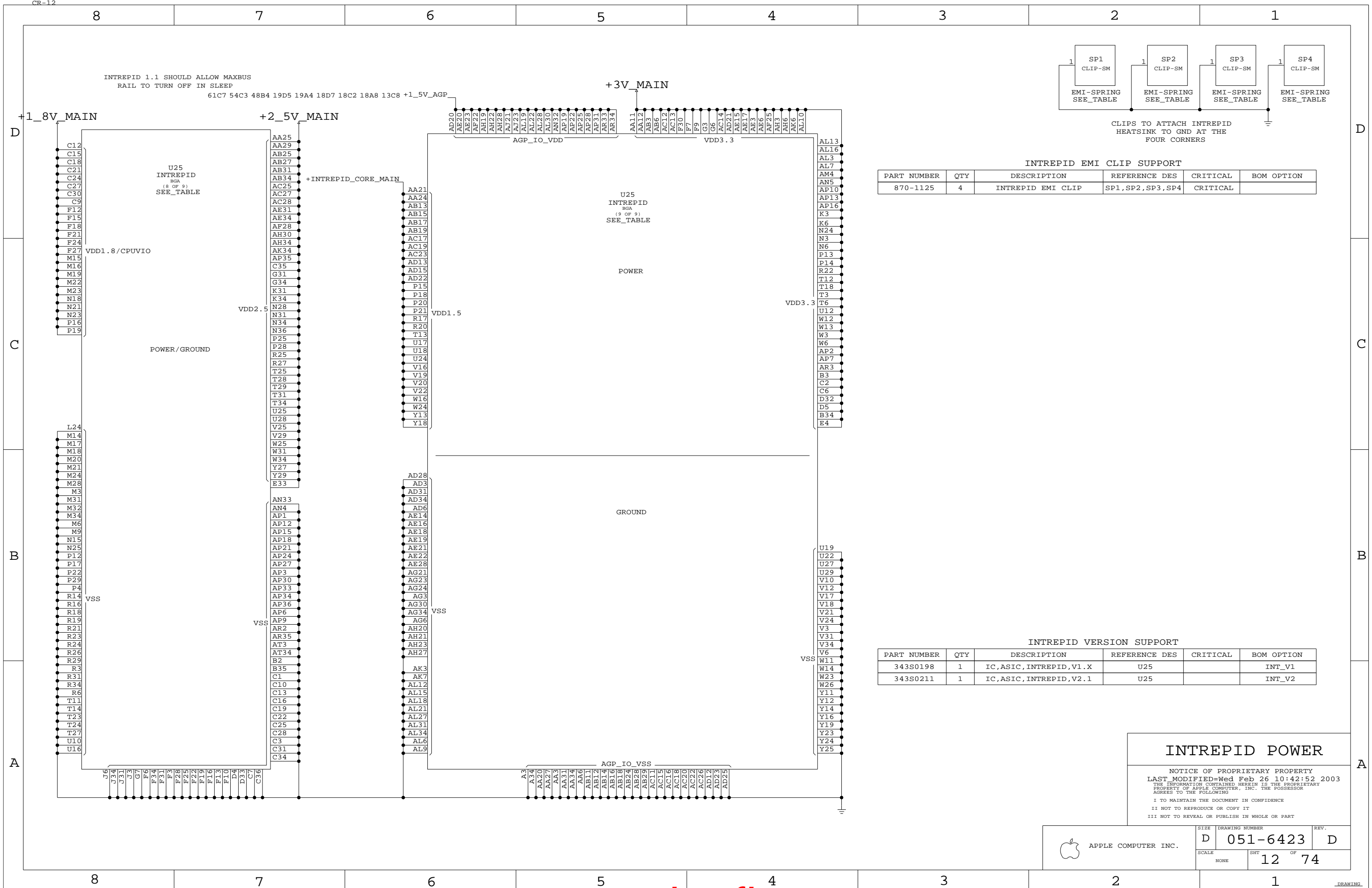
**CLOCKS**

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SCALE	SHT	OF	
NONE	10	74	





**INTREPID EMI CLIP SUPPORT**

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
870-1125	4	INTREPID EMI CLIP	SP1,SP2,SP3,SP4	CRITICAL	

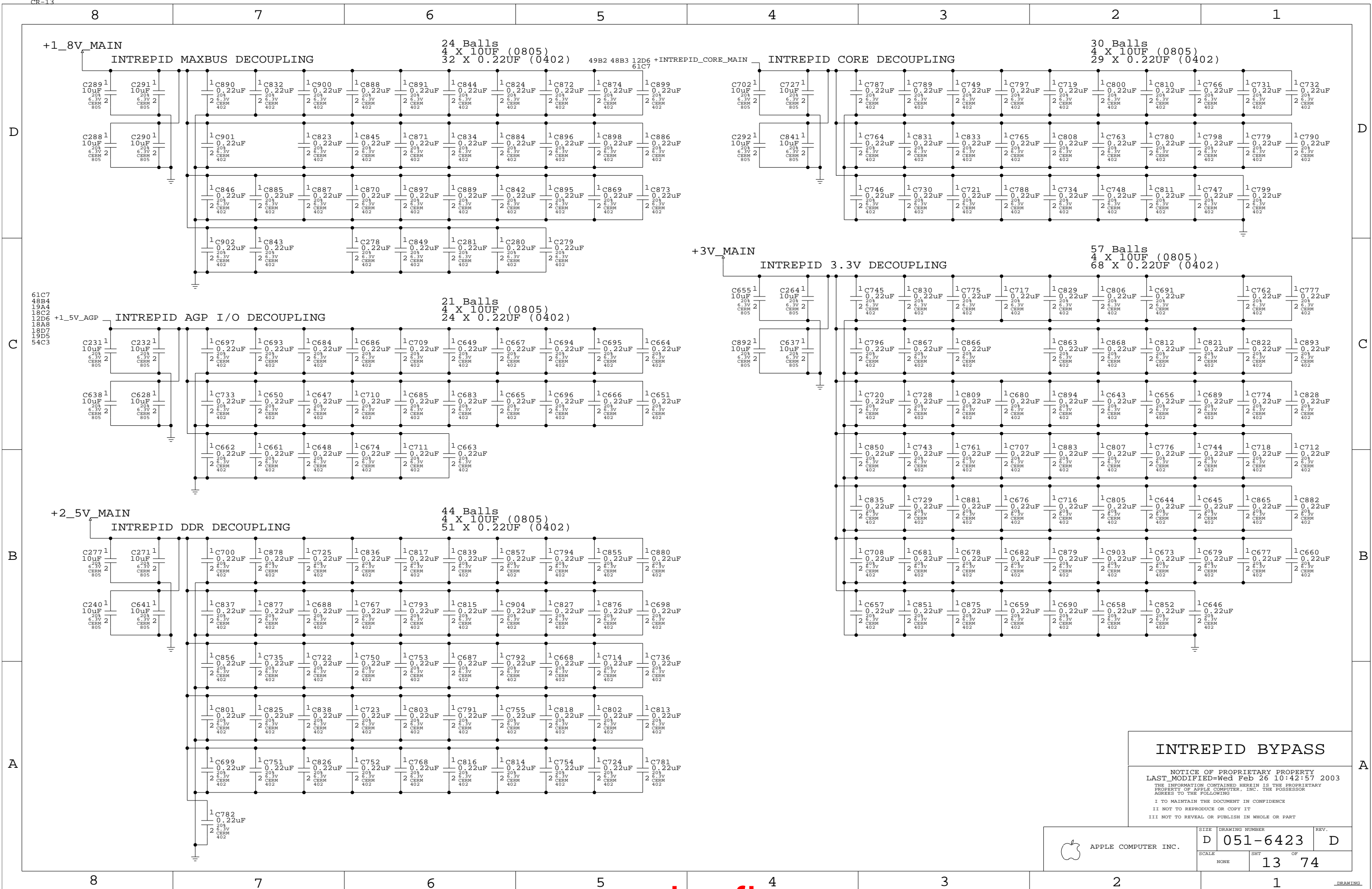
**INTREPID VERSION SUPPORT**

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
343S0198	1	IC,ASIC,INTREPID,V1.X	U25		INT_V1
343S0211	1	IC,ASIC,INTREPID,V2.1	U25		INT_V2

**INTREPID POWER**

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	SCALE	SHT	OF
	NONE	12	74



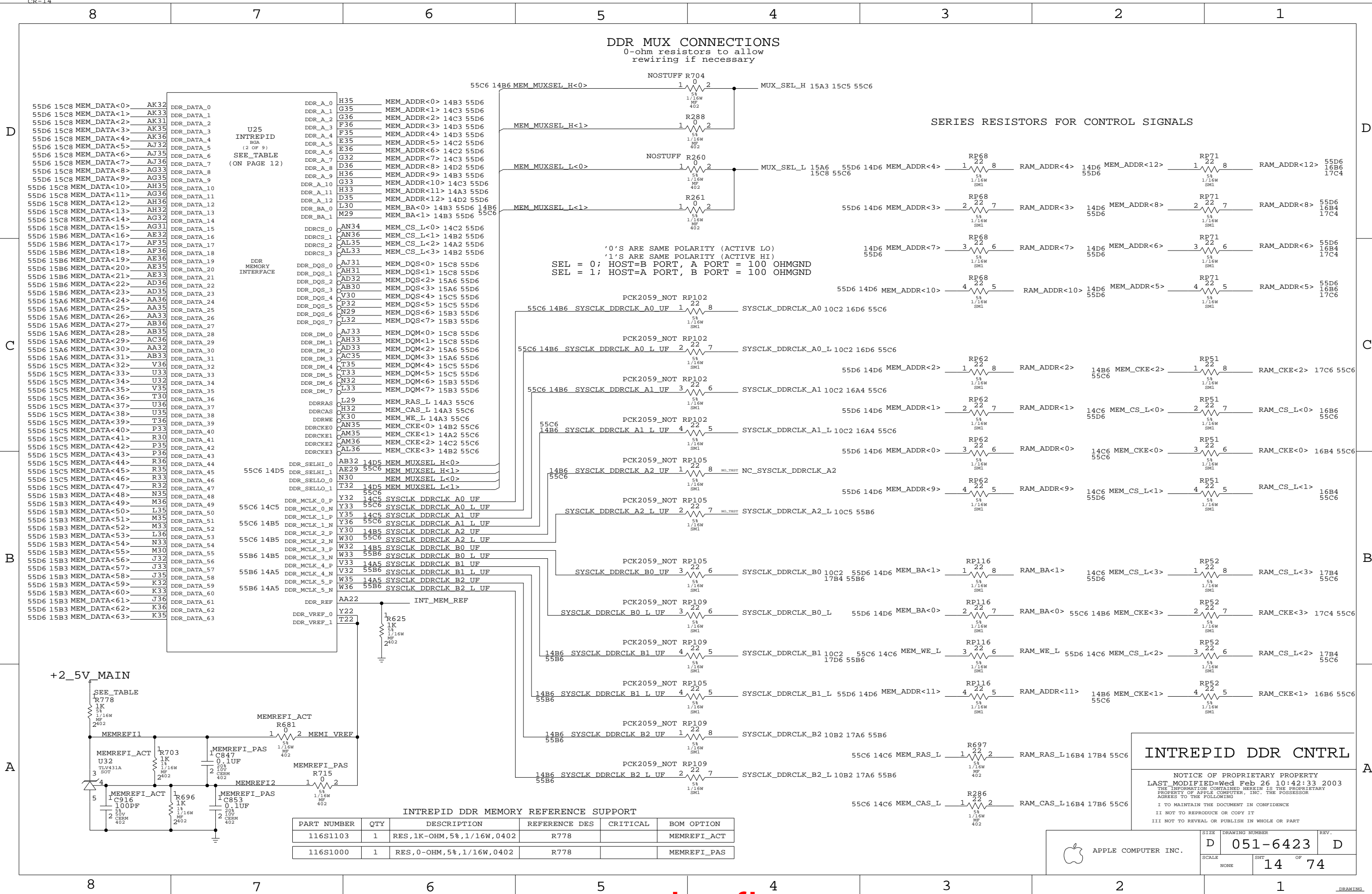
### INTREPID BYPASS

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	D	051-6423	D
SCALE	SHT	OF	
NONE	13	74	

### DDR MUX CONNECTIONS

0-ohm resistors to allow rewiring if necessary



#### INTREPID DDR MEMORY REFERENCE SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
116S1103	1	RES, 1K-OHM, 5%, 1/16W, 0402	R778		MEMREFI_ACT
116S1000	1	RES, 0-OHM, 5%, 1/16W, 0402	R778		MEMREFI_PAS

#### INTREPID DDR CNTRL

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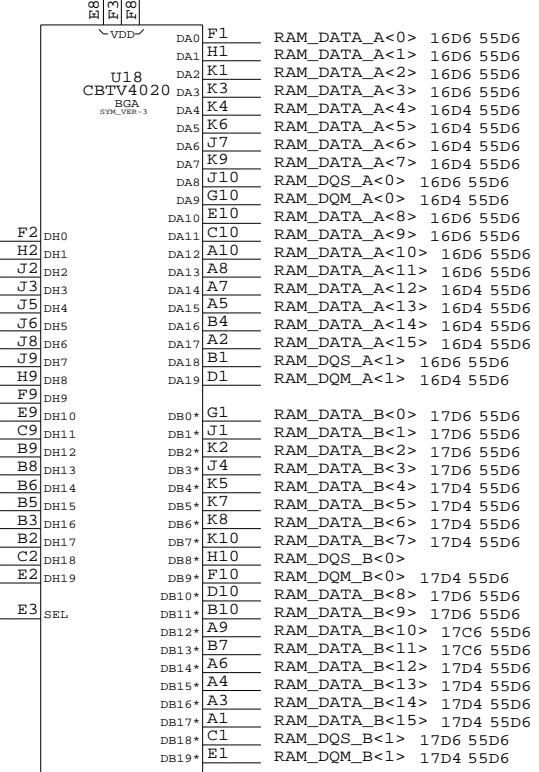
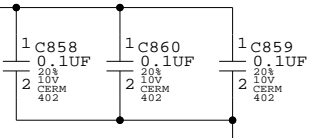
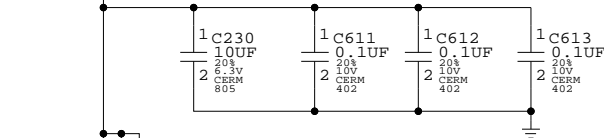
APPLE COMPUTER INC.

SCALE: NONE SHT: 14 OF 74 REV. D

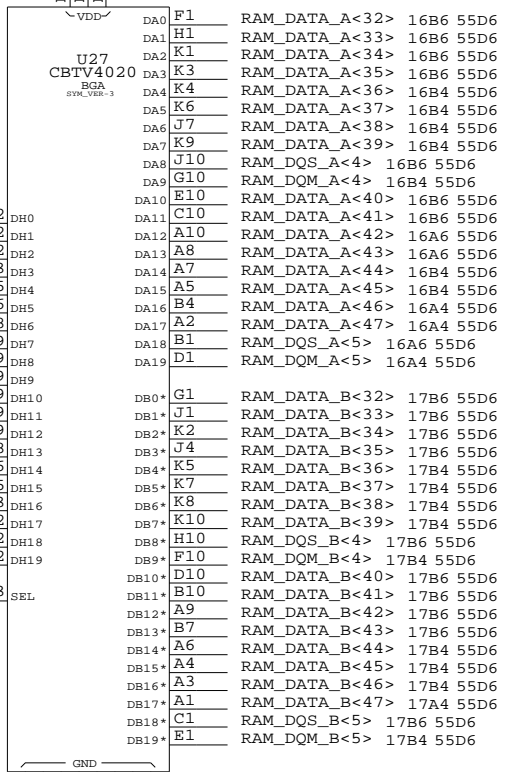
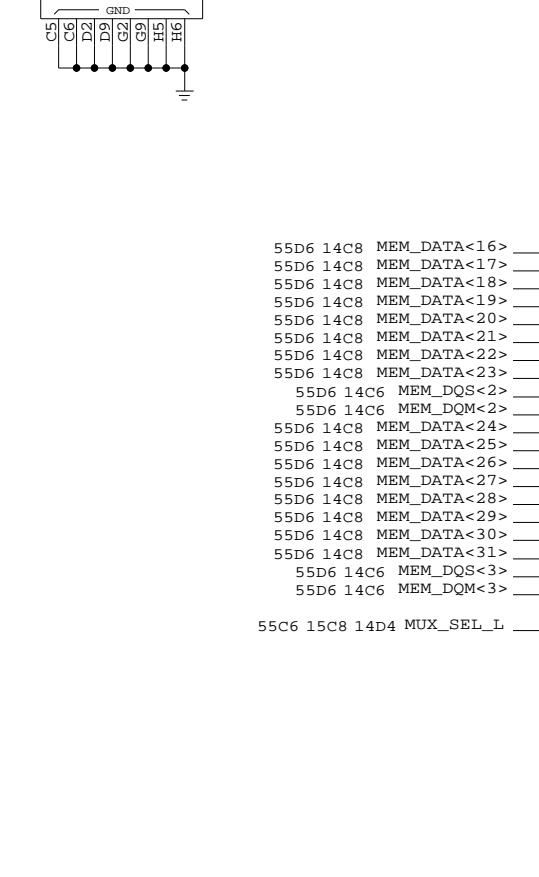
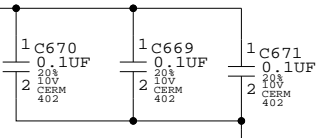
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+2\_5V\_MAIN

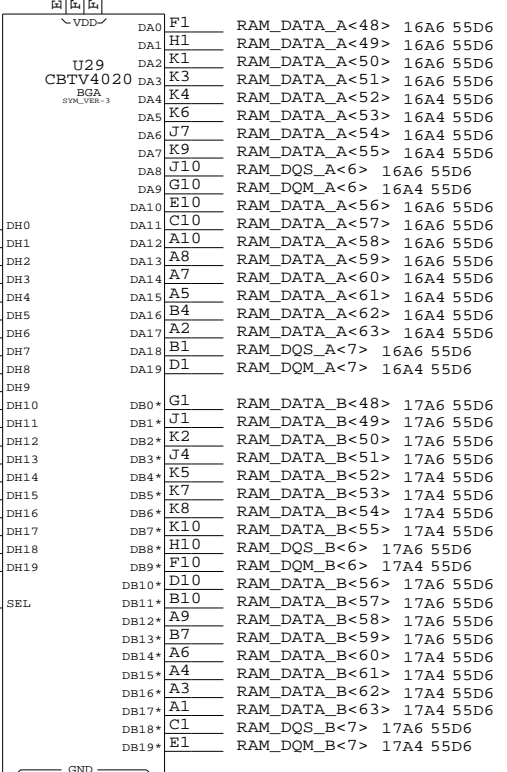
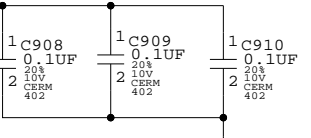
+2\_5V\_MAIN



+2\_5V\_MAIN



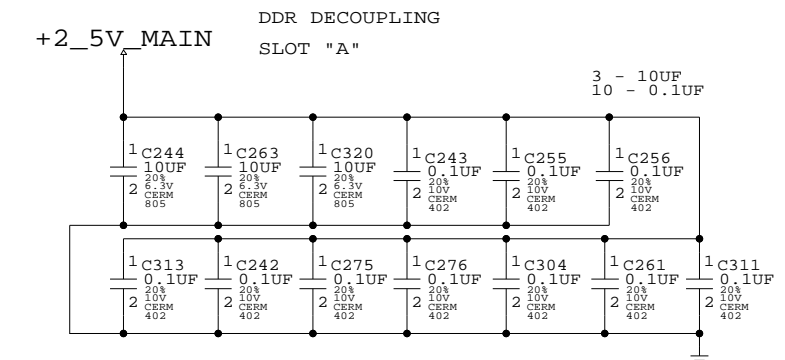
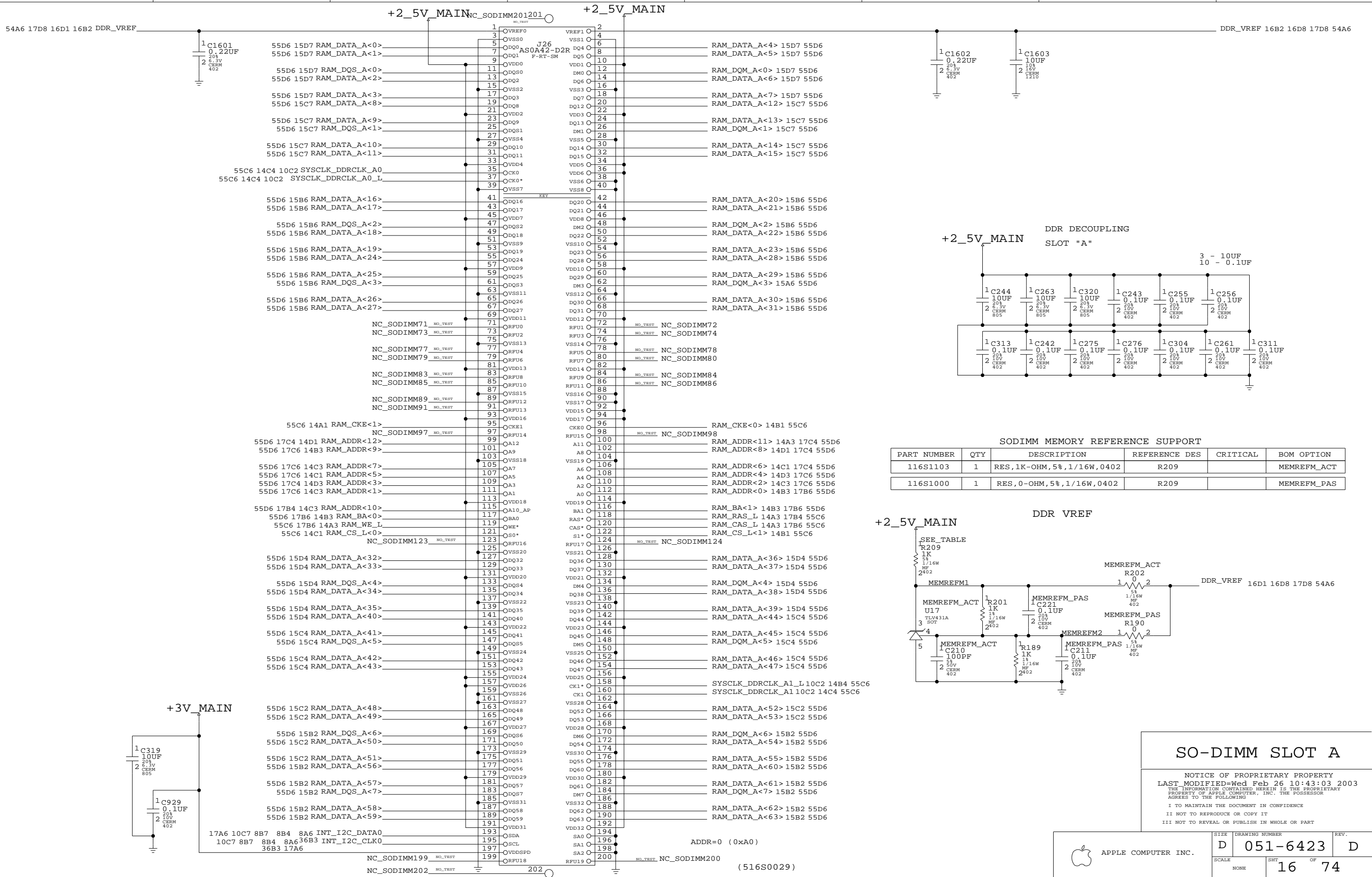
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### DDR MUXES

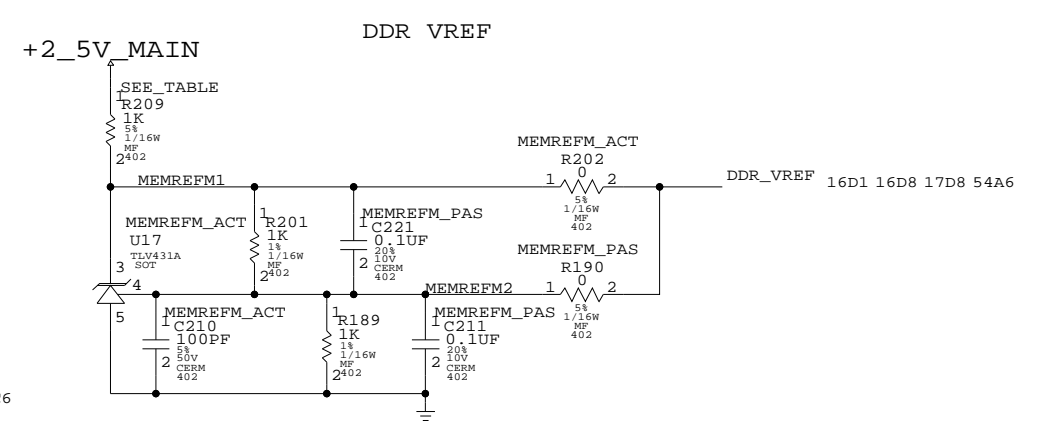
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 SCALE: NONE SHEET: 15 OF 74



**SODIMM MEMORY REFERENCE SUPPORT**

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
116S1103	1	RES, 1K-OHM, 5%, 1/16W, 0402	R209		MEMREFM_ACT
116S1000	1	RES, 0-OHM, 5%, 1/16W, 0402	R209		MEMREFM_PAS



**SO-DIMM SLOT A**

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SIZE	DRAWING NUMBER	REV.
D	051-6423	D
SCALE	SHT	OF
NONE	16	74



D

D

C

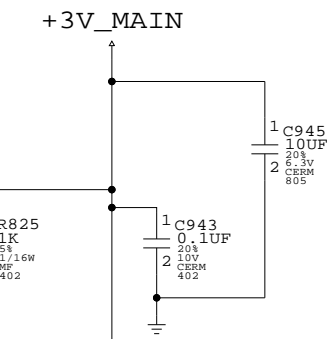
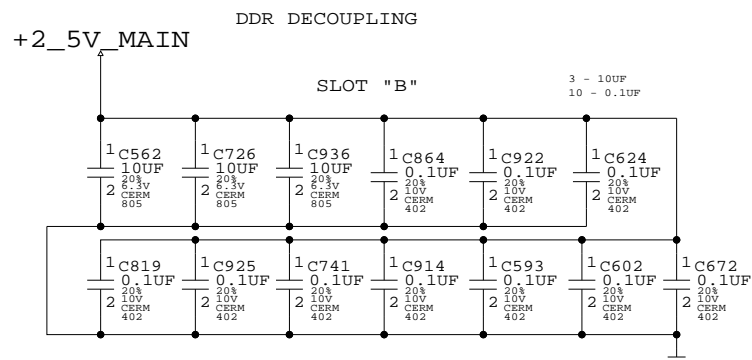
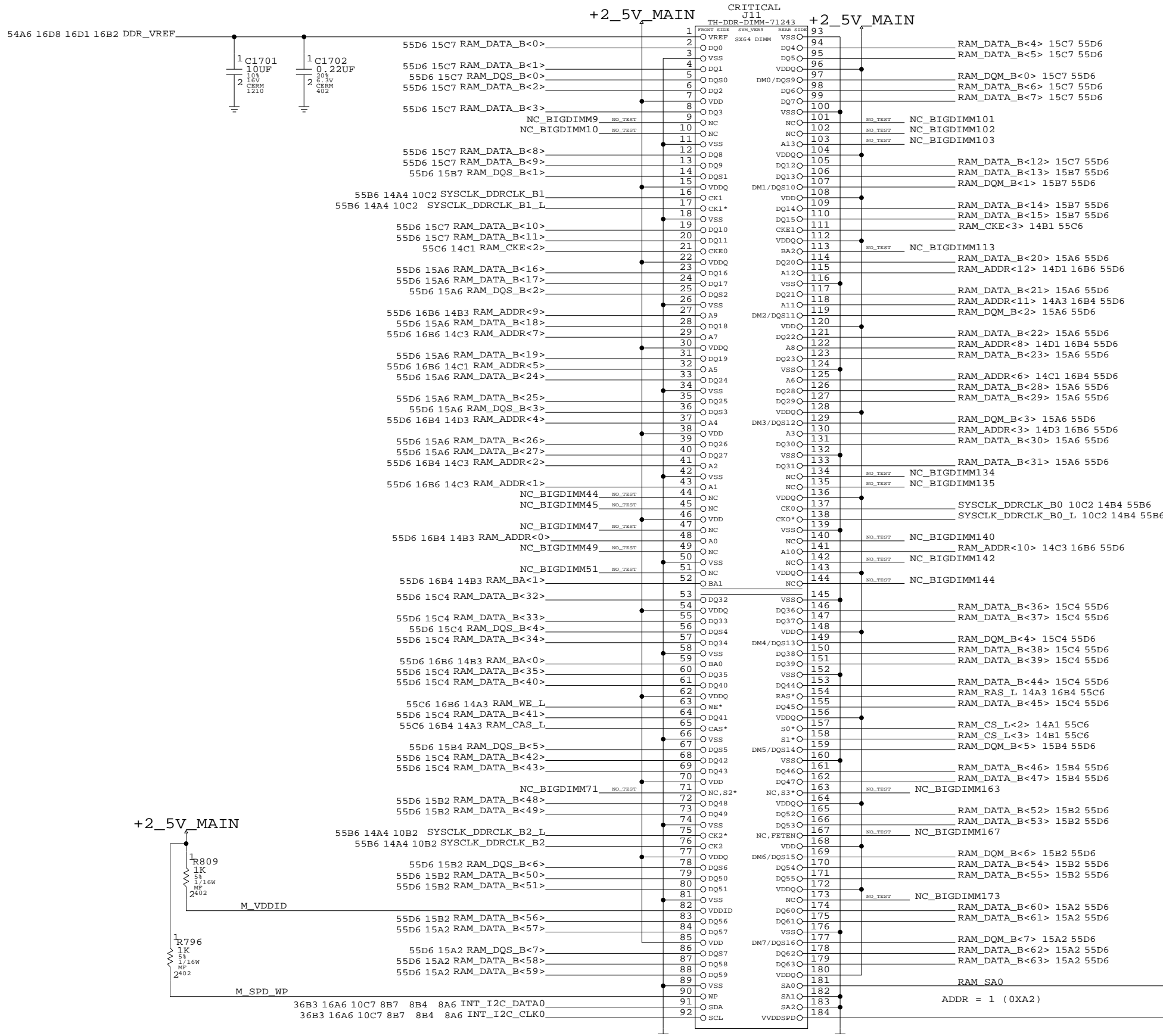
C

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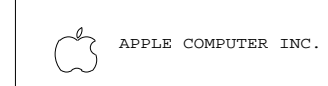
A



**BIG DIMM SLOT B**

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D	051-6423	D
SCALE	SHT	OF
NONE	17	74



D

C

B

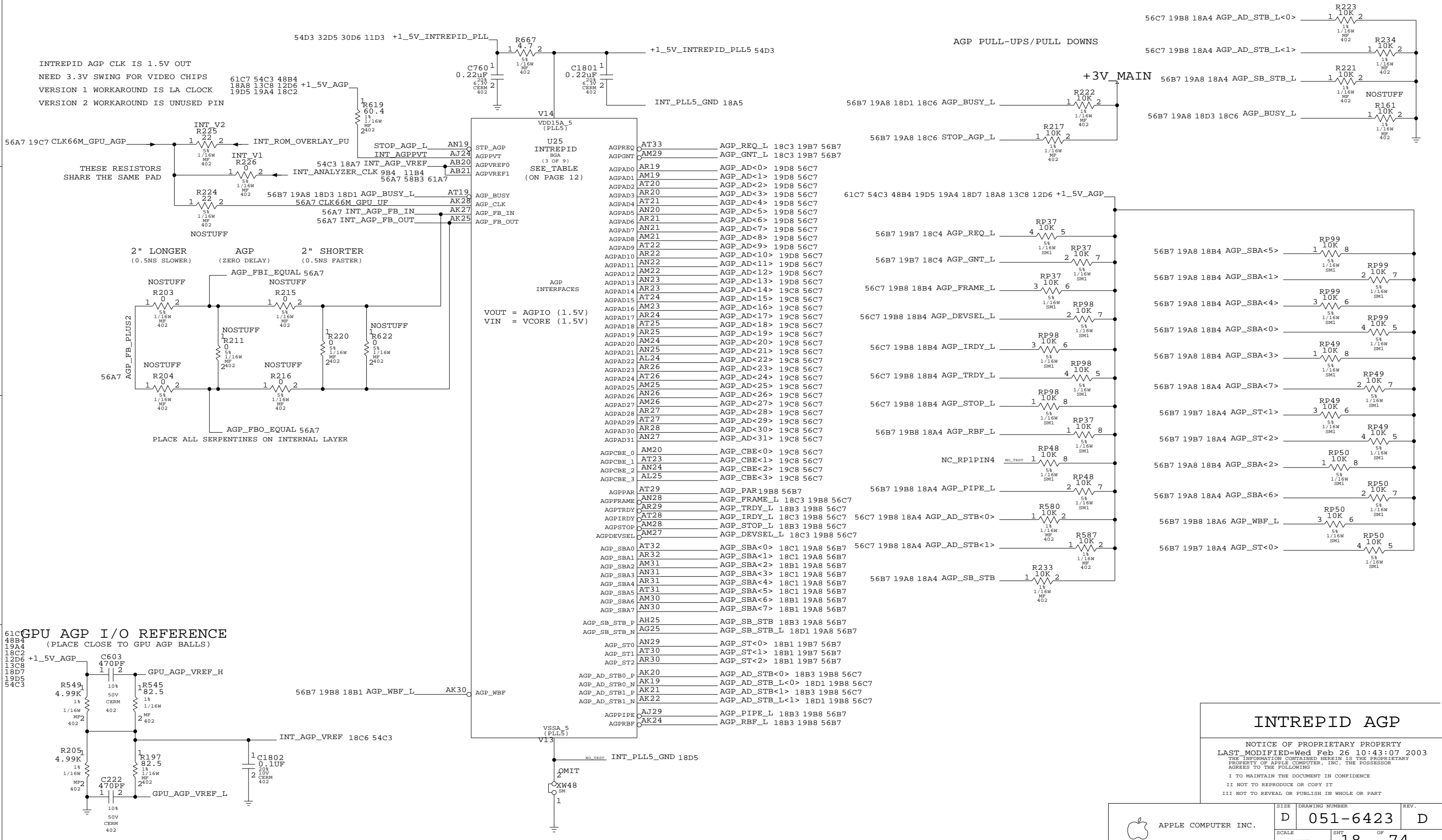
A

D

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B

A



**INTREPID AGP**

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SIZE	D	DRAWING NUMBER	051-6423	REV.	D
SCALE	NONE	SHT	18	OF	74

61C7 54C3 48B4 19A4 18D7 18C2 18A8 13C8 12D6 +1\_5V\_AGP

U39  
NV18B  
BGA  
(1 OF 5)

54A6 50B1 25C6 25C4

56C7 18C4 AGP_AD<0>	1	RP40	8	56B7 GPU AGP AD<0>	AJ28	PCIAD0
56C7 18C4 AGP_AD<1>	2	22	7	NO_TEST 56B7 GPU AGP AD<1>	AK28	PCIAD1
56C7 18C4 AGP_AD<2>	3	5	6	NO_TEST 56B7 GPU AGP AD<2>	AH27	PCIAD2
56C7 18C4 AGP_AD<3>	4	1/16W SM1	5	NO_TEST 56B7 GPU AGP AD<3>	AK27	PCIAD3
56C7 18C4 AGP_AD<4>	1	8	8	NO_TEST 56B7 GPU AGP AD<4>	AJ27	PCIAD4
56C7 18C4 AGP_AD<5>	56B7 2	RP30	7	NO_TEST GPU AGP AD<5>	AH26	PCIAD5
56C7 18C4 AGP_AD<6>	56B7 3	22	6	NO_TEST GPU AGP AD<6>	AJ26	PCIAD6
56C7 18C4 AGP_AD<7>	1	1/16W SM1	5	56B7 GPU AGP AD<7>	AH25	PCIAD7
56C7 18C4 AGP_AD<8>	1	8	8	NO_TEST 56B7 GPU AGP AD<8>	AH23	PCIAD8
56C7 18C4 AGP_AD<9>	2	RP32	7	56B7 GPU AGP AD<9>	AJ23	PCIAD9
56C7 18C4 AGP_AD<10>	3	22	6	NO_TEST 56B7 GPU AGP AD<10>	AH22	PCIAD10
56C7 18C4 AGP_AD<11>	4	1/16W SM1	5	NO_TEST 56B7 GPU AGP AD<11>	AJ22	PCIAD11
56C7 18C4 AGP_AD<12>	1	8	8	NO_TEST 56B7 GPU AGP AD<12>	AJ21	PCIAD12
56C7 18C4 AGP_AD<13>	2	22	7	NO_TEST 56B7 GPU AGP AD<13>	AK21	PCIAD13
56C7 18C4 AGP_AD<14>	3	5	6	NO_TEST 56B7 GPU AGP AD<14>	AH20	PCIAD14
56C7 18C4 AGP_AD<15>	4	1/16W SM1	5	NO_TEST 56B7 GPU AGP AD<15>	AJ20	PCIAD15
56C7 18C4 AGP_AD<16>	1	8	8	NO_TEST 56B7 GPU AGP AD<16>	AG26	PCIAD16
56C7 18C4 AGP_AD<17>	2	RP31	7	NO_TEST 56B7 GPU AGP AD<17>	AE24	PCIAD17
56C7 18C4 AGP_AD<18>	3	22	6	NO_TEST GPU AGP AD<18>	AG25	PCIAD18
56C7 18C4 AGP_AD<19>	4	1/16W SM1	5	NO_TEST 56B7 GPU AGP AD<19>	AG24	PCIAD19
56C7 18C4 AGP_AD<20>	1	8	8	NO_TEST 56B7 GPU AGP AD<20>	AF24	PCIAD20
56C7 18C4 AGP_AD<21>	2	RP41	7	NO_TEST 56B7 GPU AGP AD<21>	AG23	PCIAD21
56C7 18C4 AGP_AD<22>	3	22	6	NO_TEST 56B7 GPU AGP AD<22>	AE22	PCIAD22
56C7 18C4 AGP_AD<23>	4	1/16W SM1	5	NO_TEST 56B7 GPU AGP AD<23>	AF22	PCIAD23
56C7 18C4 AGP_AD<24>	1	8	8	NO_TEST 56B7 GPU AGP AD<24>	AE21	PCIAD24
56C7 18C4 AGP_AD<25>	2	RP44	7	NO_TEST 56B7 GPU AGP AD<25>	AG20	PCIAD25
56C7 18C4 AGP_AD<26>	3	22	6	NO_TEST 56B7 GPU AGP AD<26>	AG19	PCIAD26
56C7 18C4 AGP_AD<27>	4	1/16W SM1	5	NO_TEST 56B7 GPU AGP AD<27>	AF19	PCIAD27
56C7 18C4 AGP_AD<28>	1	8	8	NO_TEST 56B7 GPU AGP AD<28>	AE19	PCIAD28
56C7 18C4 AGP_AD<29>	2	RP34	7	NO_TEST 56B7 GPU AGP AD<29>	AF18	PCIAD29
56C7 18C4 AGP_AD<30>	3	22	6	NO_TEST 56B7 GPU AGP AD<30>	AG18	PCIAD30
56C7 18C4 AGP_AD<31>	4	1/16W SM1	5	NO_TEST 56B7 GPU AGP AD<31>	AE18	PCIAD31

56C7 18B4 AGP_CBE<0>	1	RP43	8	56B7 GPU AGP CBE<0>	AJ24	PCIC0/BE0*
56C7 18B4 AGP_CBE<1>	2	22	7	NO_TEST 56B7 GPU AGP CBE<1>	AH19	PCIC1/BE1*
56C7 18B4 AGP_CBE<2>	3	5	6	NO_TEST GPU AGP CBE<2>	AF25	PCIC2/BE2*
56C7 18B4 AGP_CBE<3>	4	1/16W SM1	5	NO_TEST GPU AGP CBE<3>	AG22	PCIC3/BE3*

56A7 18D8 CLK66M_GPU_AGP				AG12	PCICLK*	CLK
46D3 AGP_RESET_L				AF15	PCIRST*	RST*
56B7 18C4 18C3 AGP_GNT_L				AE15	PCIGNT*	GNT
56B7 18C4 18C3 AGP_REQ_L				AF13	PCIREQ*	REQ

56C7 18B4 AGP_TRDY_L					PCIFRAME*	FRAME
18B4 AGP_FRAME_L					PCITRDY*	IRDY
18C3 18B4 AGP_IRDY_L					PCIDEVSEL*	DEVSEL
56C7 18B4 AGP_PAR					PCISTOP*	STOP
18B4 18B3 AGP_STOP_L					PCIPAR	PAR

56B7 18B4 AGP_DEVSEL_L					PCIIINTA*	INTA
30B8 30B5					NC_PCIINTB*	INTB
56B7 18B6 AGP_WBF_L					AGPRBF*	RBF
18B3 18A4 AGP_PIPE_L					AGPWBF*	WBF
18A4 56B7 AGP_RBF_L					AGPPPIPE*	DBI_HI

18B3 18A4 AGP_ST<0>				AG13	AGPST0	ST0
56B7 18B1 18A4 AGP_ST<1>				AE16	AGPST1	ST1
56B7 18B1 18A4 AGP_ST<2>				AE13	AGPST2	ST2

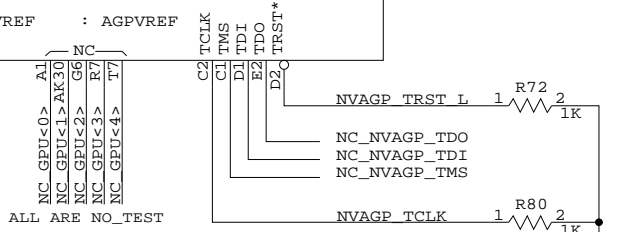
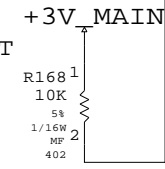
56C7 18B3 18A4 AGP_AD_STB<0>					AGPADSTBF0*	ADSTBF0
AGP_AD_STB_L<0>					AGPADSTBS0*	ADSTBS0
AGP_AD_STB_L<1>					AGPADSTBF1	ADSTBF1
AGP_AD_STB<1>					AGPADSTBS1*	ADSTBS1

56B7 18B4 AGP_SB_STB					AGPSBSTBF	SBSTBF
56B7 18B4 AGP_SB_STB_L					AGPSBSTBS*	SBSTBS
18B4 56B7 AGP_SBA<3>					AGPSBA0*	SBA0*
56B7 18B1 AGP_SBA<2>					AGPSBA1*	SBA1*
18B4 18B4 AGP_SBA<1>					AGPSBA2*	SBA2*

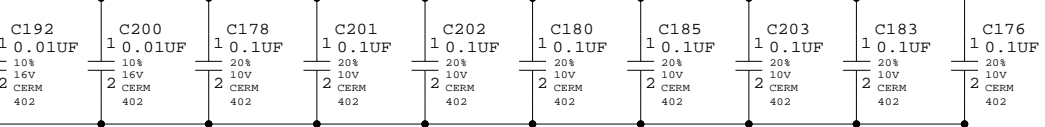
18C1 56B7 AGP_SBA<0>					AGPSBA3*	SBA3*
56B7 18B1 AGP_SBA<7>					AGPSBA4*	SBA4*
18A4 56B7 AGP_SBA<6>					AGPSBA5*	SBA5*
18C1 18B4 AGP_SBA<4>					AGPSBA6*	SBA6*
18C1 18B4 AGP_SBA<5>					AGPSBA7*	SBA7*

56B7 18D3 18D1 18C6 AGP_BUSY_L					<RESRVD>	MBDET*
18D3 18C6 STOP_AGP_L					AGPBUSY*	BUSY*
56B7 19A3 GPU_AGP_VREF					AGPSTOP*	STOP*
54A6					AGPVREF	AGPVREF

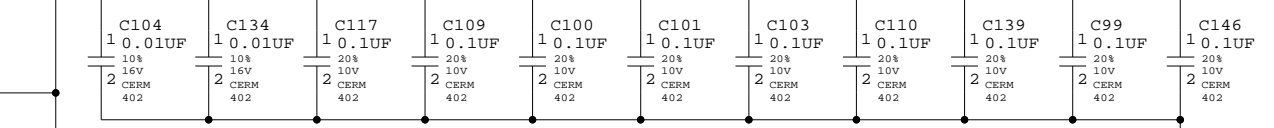
AGP VERSION SELECT  
(LOW = AGP V3.X)  
(HIGH = AGP V2.X)



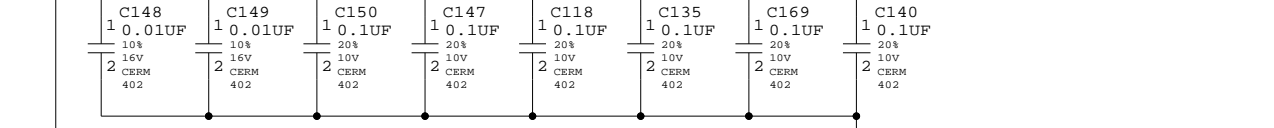
OUTPUT DRIVER BYPASS



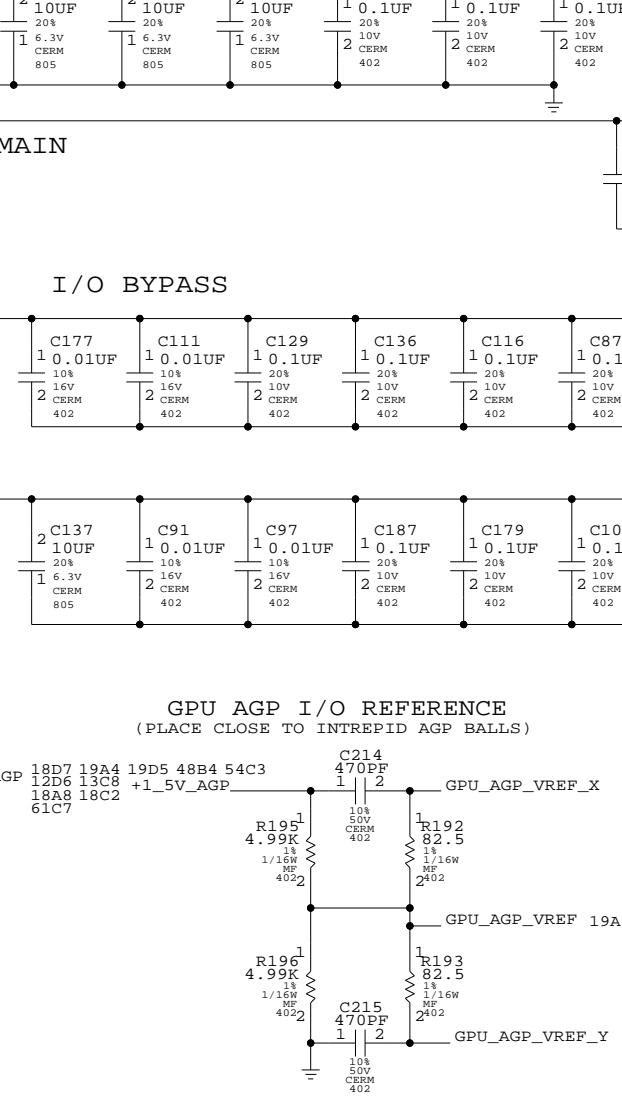
CORE BYPASS



I/O BYPASS



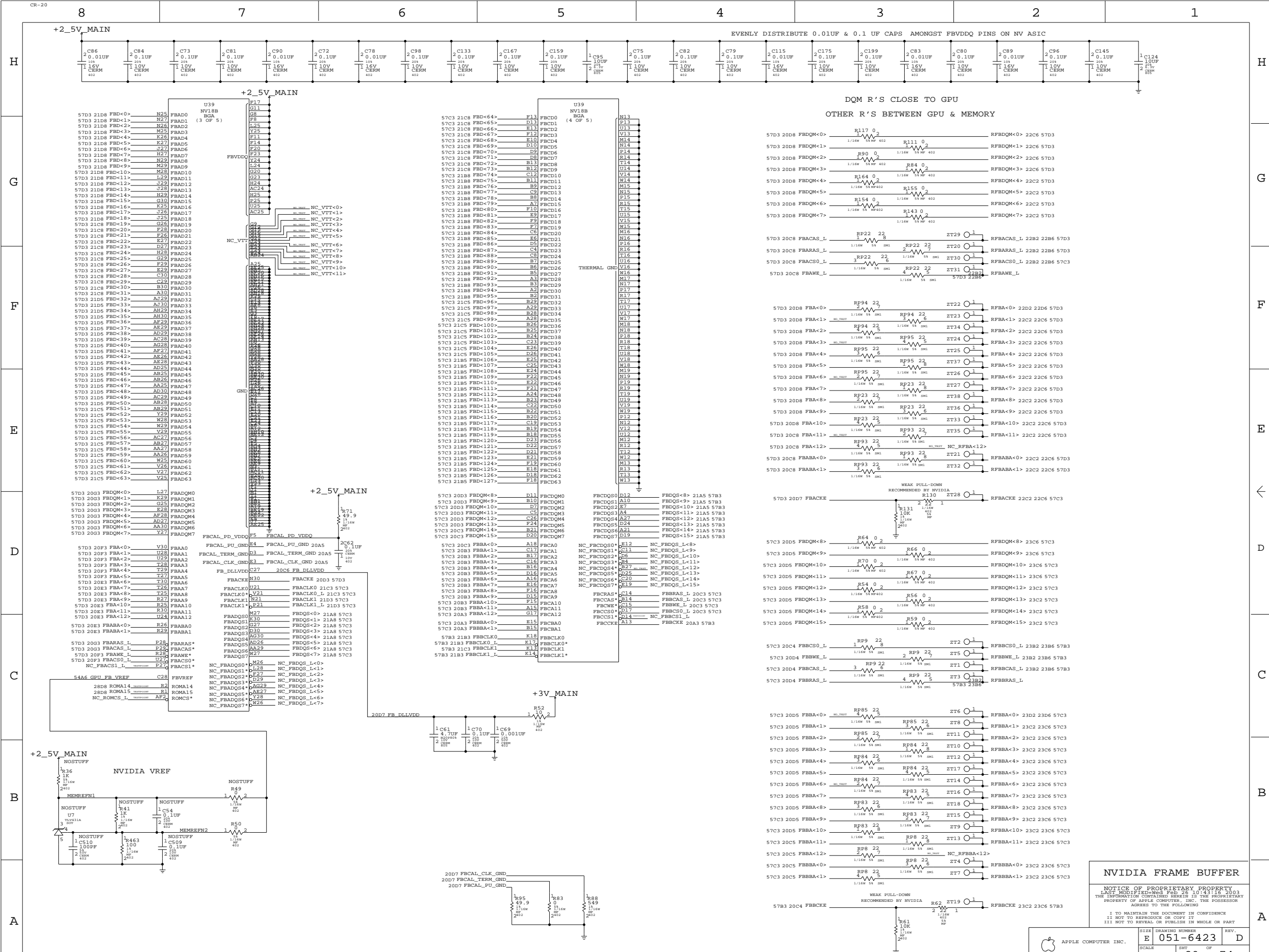
GPU AGP I/O REFERENCE



NVIDIA AGP

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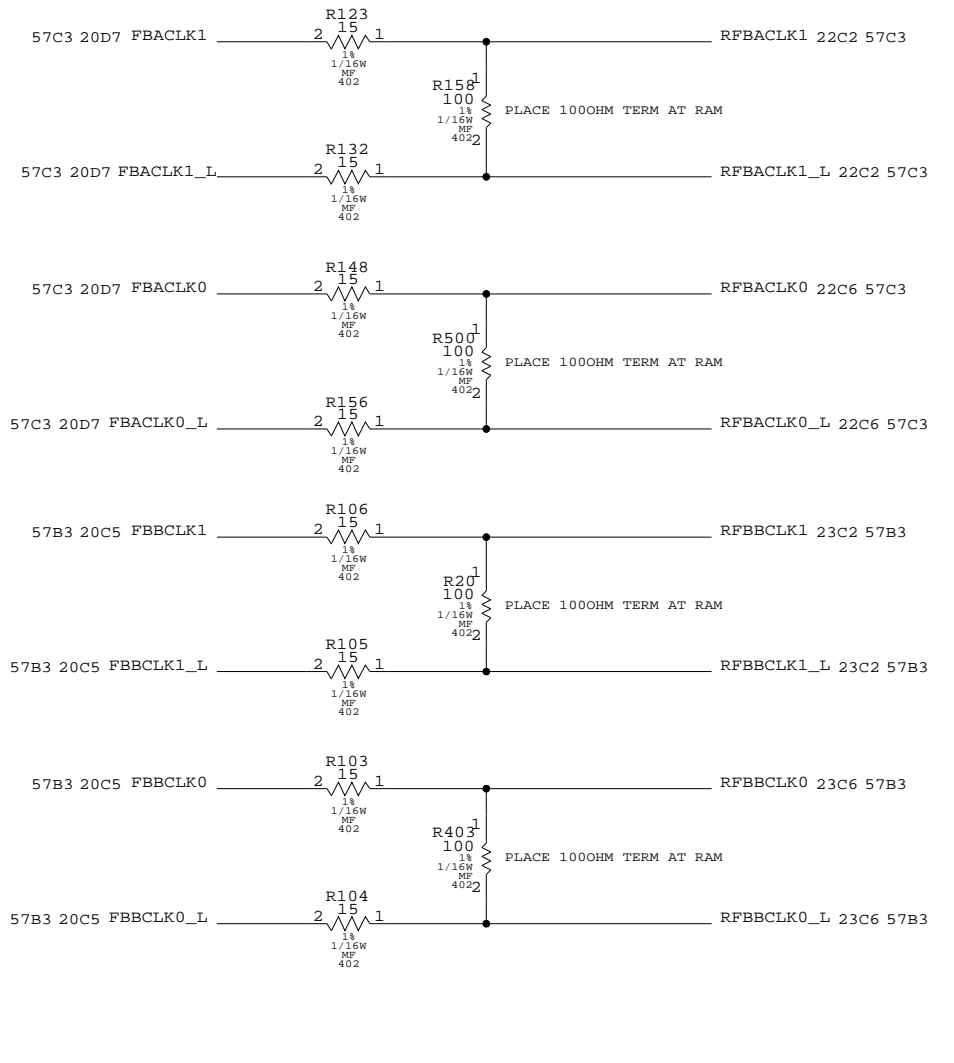
APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-6423	REV. D
	SCALE NONE	SHT 19 OF 74	



PLACE R'S BETWEEN GPU & MEMORY

Table with 3 columns: Component ID (e.g., 57D3 20G8 FBD<0>), Resistor Value (e.g., 15 1), and Resistor Part Number (e.g., 8 RP20). It lists components for rows 8, 7, 6, 5, 4, 3, 2, and 1.

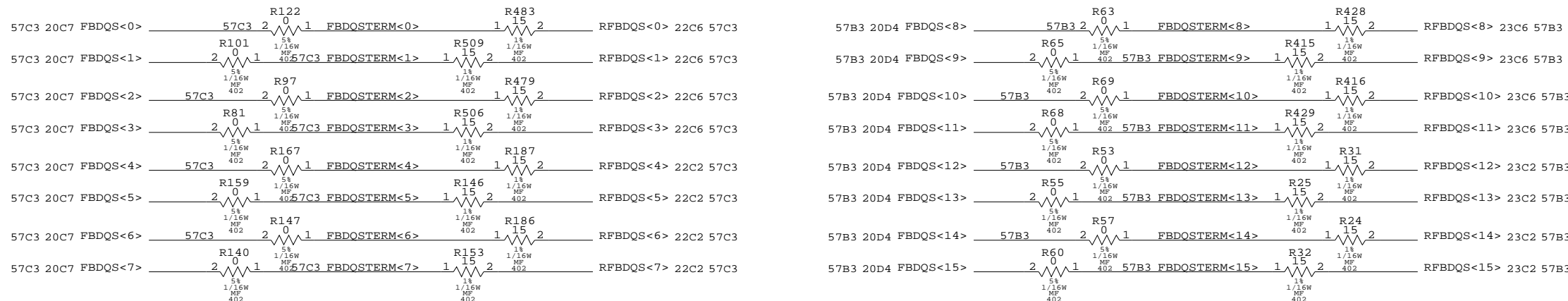
PLACE R'S CLOSE TO GPU



PLACE THESE R CLOSE TO SGRAM

PLACE THESE R CLOSE TO GPU

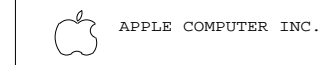
PLACE THESE R CLOSE TO SGRAM

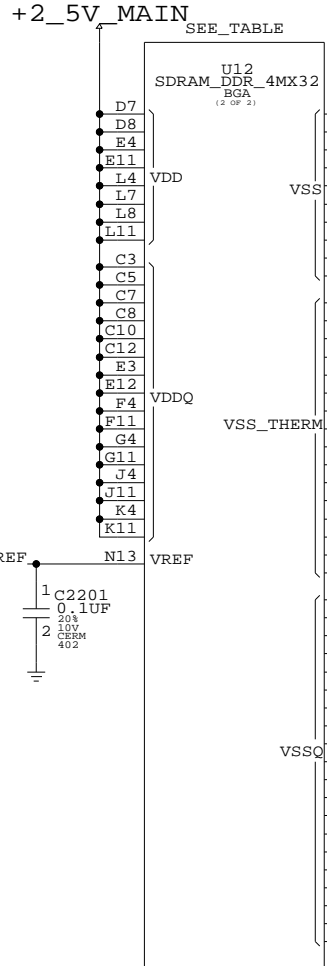
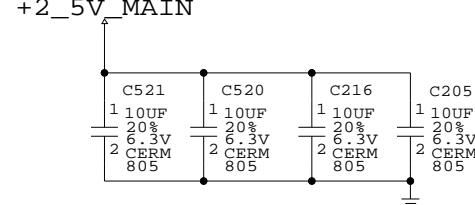
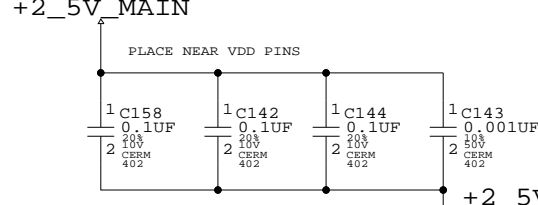
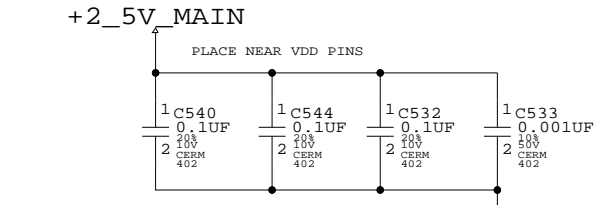
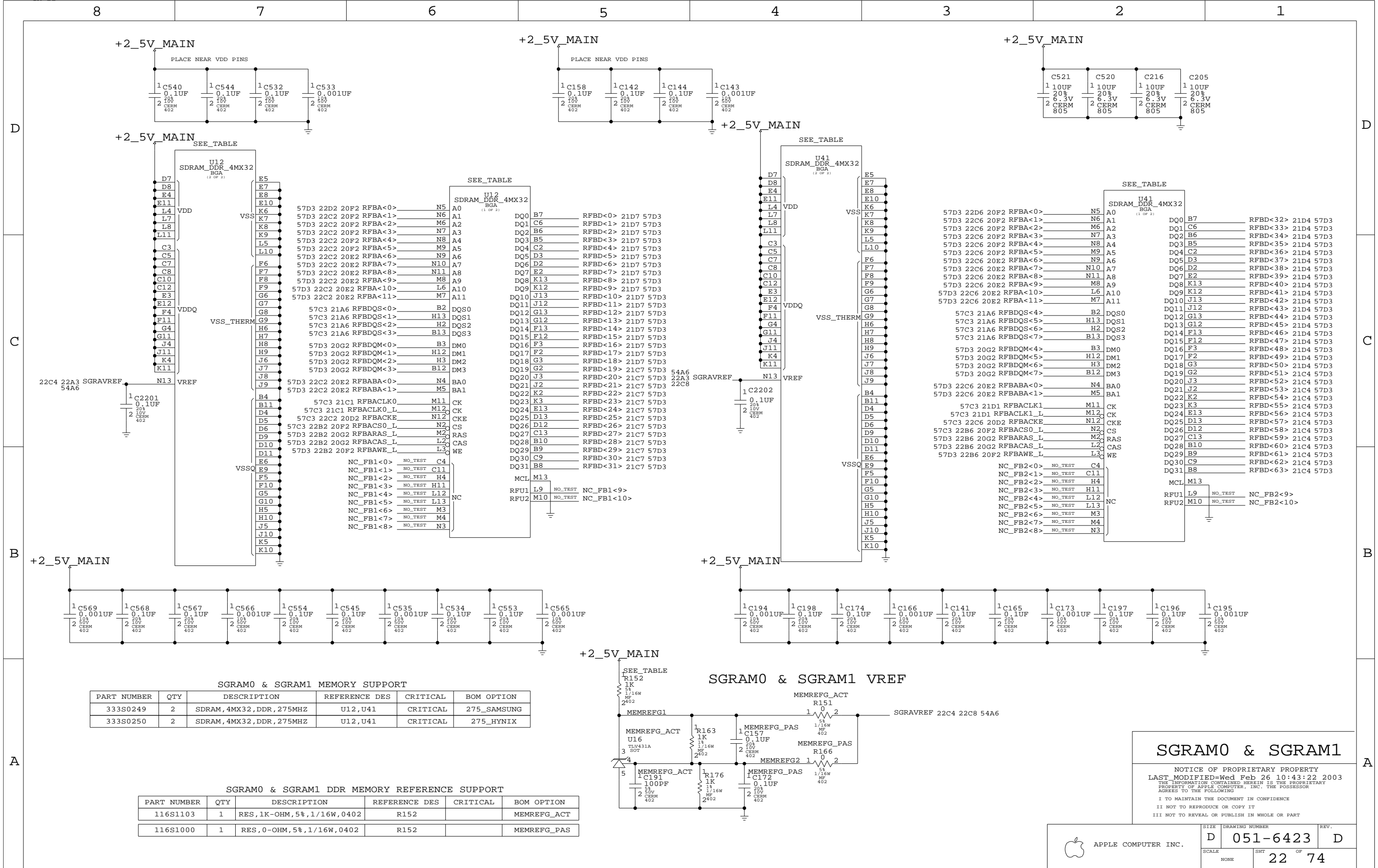


FB TERMINATION

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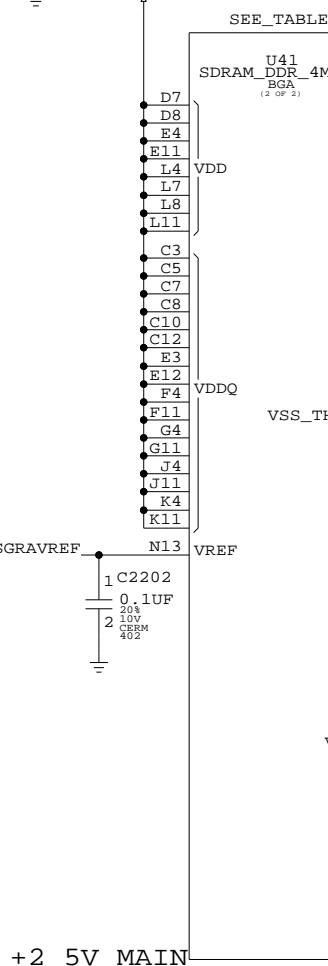
Table with columns: SIZE (D), DRAWING NUMBER (051-6423), REV. (D), SCALE (NONE), SHT (21 OF 74).





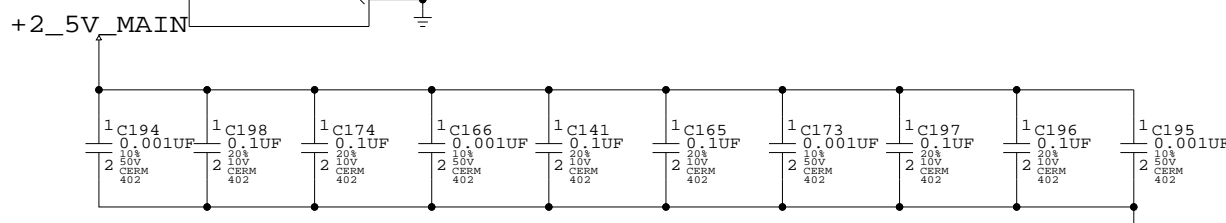
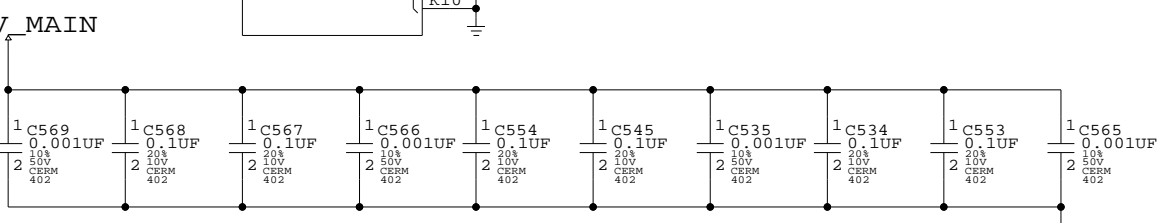
SEE\_TABLE

U12 SDRAM_DDR_4MX32 (1 OF 2)	U12 SDRAM_DDR_4MX32 (2 OF 2)
57D3 22D2 20F2 RFBA<0>	N5 A0
57D3 22C2 20F2 RFBA<1>	N6 A1
57D3 22C2 20F2 RFBA<2>	M6 A2
57D3 22C2 20F2 RFBA<3>	N7 A3
57D3 22C2 20F2 RFBA<4>	N8 A4
57D3 22C2 20F2 RFBA<5>	M9 A5
57D3 22C2 20E2 RFBA<6>	N9 A6
57D3 22C2 20E2 RFBA<7>	N10 A7
57D3 22C2 20E2 RFBA<8>	N11 A8
57D3 22C2 20E2 RFBA<9>	M8 A9
57D3 22C2 20E2 RFBA<10>	L6 A10
57D3 22C2 20E2 RFBA<11>	M7 A11
57C3 21A6 RFBDS<0>	B2 DQS0
57C3 21A6 RFBDS<1>	H13 DQS1
57C3 21A6 RFBDS<2>	H2 DQS2
57C3 21A6 RFBDS<3>	B13 DQS3
57D3 20G2 RFBDM<0>	B3 DM0
57D3 20G2 RFBDM<1>	H12 DM1
57D3 20G2 RFBDM<2>	H3 DM2
57D3 20G2 RFBDM<3>	B12 DM3
57D3 22C2 20E2 RFBABA<0>	N4 BA0
57D3 22C2 20E2 RFBABA<1>	M5 BA1
57C3 21C1 RFBACLK0	M11 CK
57C3 21C1 RFBACLK0_L	M12 CK
57C3 22C2 20D2 RFBACKE	N12 CKE
57C3 22B2 20F2 RFBACS0_L	N2 CS
57D3 22B2 20G2 RFBARAS_L	M2 RAS
57D3 22B2 20G2 RFBACAS_L	L2 CAS
57D3 22B2 20F2 RFBABWE_L	L3 WE
NC_FB1<0>	NO_TEST C4
NC_FB1<1>	NO_TEST C11
NC_FB1<2>	NO_TEST H4
NC_FB1<3>	NO_TEST H11
NC_FB1<4>	NO_TEST L12
NC_FB1<5>	NO_TEST L13
NC_FB1<6>	NO_TEST M3
NC_FB1<7>	NO_TEST M4
NC_FB1<8>	NO_TEST N3
MCL M13	
RFU1 L9	NO_TEST NC_FB1<9>
RFU2 M10	NO_TEST NC_FB1<10>



SEE\_TABLE

U41 SDRAM_DDR_4MX32 (1 OF 2)	U41 SDRAM_DDR_4MX32 (2 OF 2)
57D3 22D6 20F2 RFBA<0>	N5 A0
57D3 22C6 20F2 RFBA<1>	N6 A1
57D3 22C6 20F2 RFBA<2>	M6 A2
57D3 22C6 20F2 RFBA<3>	N7 A3
57D3 22C6 20F2 RFBA<4>	N8 A4
57D3 22C6 20F2 RFBA<5>	M9 A5
57D3 22C6 20E2 RFBA<6>	N9 A6
57D3 22C6 20E2 RFBA<7>	N10 A7
57D3 22C6 20E2 RFBA<8>	N11 A8
57D3 22C6 20E2 RFBA<9>	M8 A9
57D3 22C6 20E2 RFBA<10>	L6 A10
57D3 22C6 20E2 RFBA<11>	M7 A11
57C3 21A6 RFBDS<4>	B2 DQS0
57C3 21A6 RFBDS<5>	H13 DQS1
57C3 21A6 RFBDS<6>	H2 DQS2
57C3 21A6 RFBDS<7>	B13 DQS3
57D3 20G2 RFBDM<4>	B3 DM0
57D3 20G2 RFBDM<5>	H12 DM1
57D3 20G2 RFBDM<6>	H3 DM2
57D3 20G2 RFBDM<7>	B12 DM3
57D3 22C6 20E2 RFBABA<0>	N4 BA0
57D3 22C6 20E2 RFBABA<1>	M5 BA1
57C3 21D1 RFBACLK1	M11 CK
57C3 21D1 RFBACLK1_L	M12 CK
57C3 22C6 20D2 RFBACKE	N12 CKE
57C3 22B6 20F2 RFBACS0_L	N2 CS
57D3 22B6 20G2 RFBARAS_L	M2 RAS
57D3 22B6 20G2 RFBACAS_L	L2 CAS
57D3 22B6 20F2 RFBABWE_L	L3 WE
NC_FB2<0>	NO_TEST C4
NC_FB2<1>	NO_TEST C11
NC_FB2<2>	NO_TEST H4
NC_FB2<3>	NO_TEST H11
NC_FB2<4>	NO_TEST L12
NC_FB2<5>	NO_TEST L13
NC_FB2<6>	NO_TEST M3
NC_FB2<7>	NO_TEST M4
NC_FB2<8>	NO_TEST N3
MCL M13	
RFU1 L9	NO_TEST NC_FB2<9>
RFU2 M10	NO_TEST NC_FB2<10>

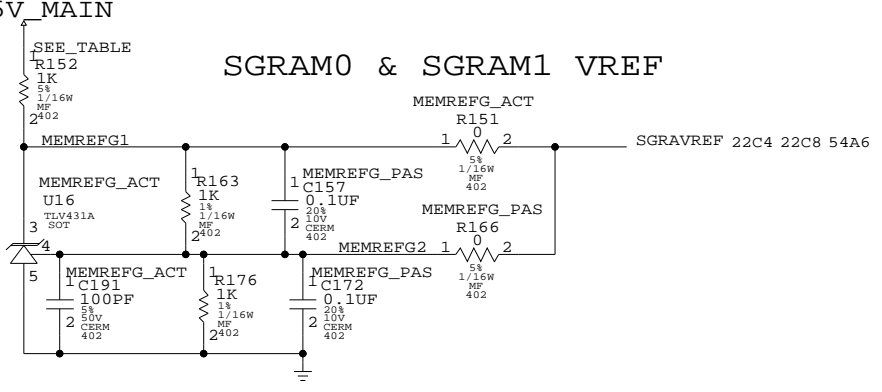


SGRAM0 & SGRAM1 MEMORY SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
333S0249	2	SDRAM, 4MX32, DDR, 275MHZ	U12, U41	CRITICAL	275_SAMSUNG
333S0250	2	SDRAM, 4MX32, DDR, 275MHZ	U12, U41	CRITICAL	275_HYNIX

SGRAM0 & SGRAM1 DDR MEMORY REFERENCE SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
116S1103	1	RES, 1K-OHM, 5%, 1/16W, 0402	R152		MEMREFG_ACT
116S1000	1	RES, 0-OHM, 5%, 1/16W, 0402	R152		MEMREFG_PAS



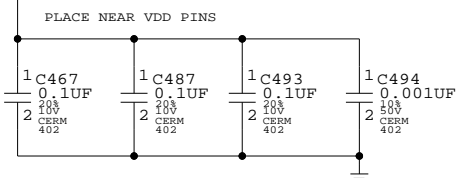
### SGRAM0 & SGRAM1

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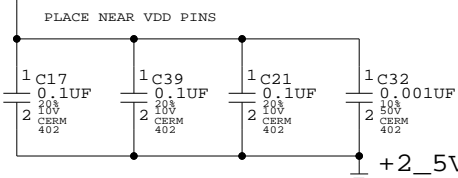
APPLE COMPUTER INC.

SCALE	DRAWING NUMBER	REV.
NONE	051-6423	D
SHT	22	OF 74

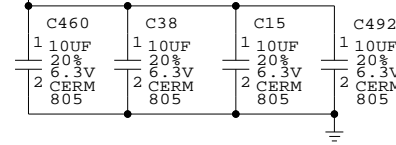
+2\_5V\_MAIN



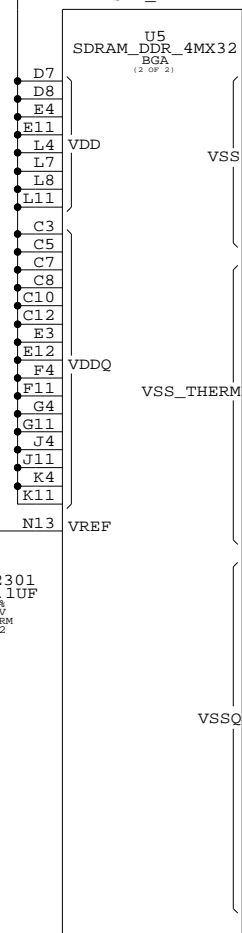
+2\_5V\_MAIN



+2\_5V\_MAIN



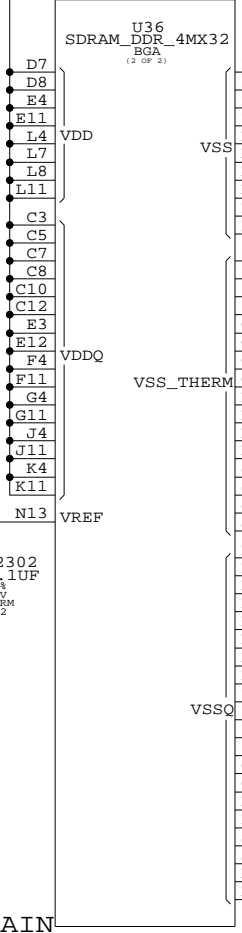
+2\_5V\_MAIN



SEE\_TABLE for U5 SDRAM\_DDR\_4MX32. Lists pin numbers (A0-A11, B0-B12, C0-C3, D0-D3, E0-E3, F0-F3, G0-G3, H0-H3, J0-J3, K0-K3) and their corresponding signals (e.g., RFBD<0>, RFBD<1>, etc.).

SEE\_TABLE for U5 SDRAM\_DDR\_4MX32. Lists pin numbers (B0-B12, C0-C3, D0-D3, E0-E3, F0-F3, G0-G3, H0-H3, J0-J3, K0-K3) and their corresponding signals (e.g., RFBD<4>, RFBD<5>, etc.).

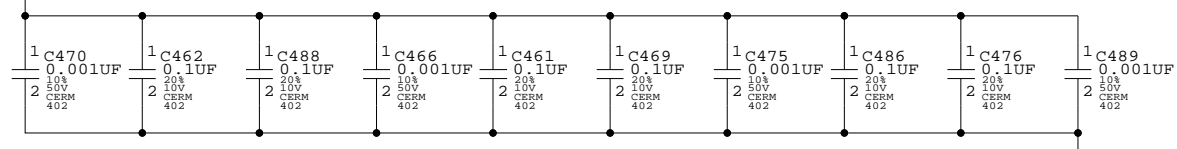
+2\_5V\_MAIN



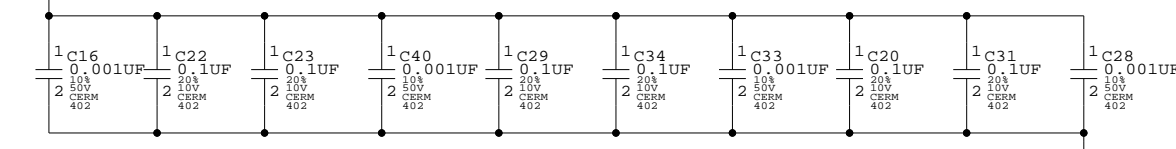
SEE\_TABLE for U36 SDRAM\_DDR\_4MX32. Lists pin numbers (A0-A11, B0-B12, C0-C3, D0-D3, E0-E3, F0-F3, G0-G3, H0-H3, J0-J3, K0-K3) and their corresponding signals (e.g., RFBD<6>, RFBD<7>, etc.).

SEE\_TABLE for U36 SDRAM\_DDR\_4MX32. Lists pin numbers (B0-B12, C0-C3, D0-D3, E0-E3, F0-F3, G0-G3, H0-H3, J0-J3, K0-K3) and their corresponding signals (e.g., RFBD<9>, RFBD<10>, etc.).

+2\_5V\_MAIN



+2\_5V\_MAIN



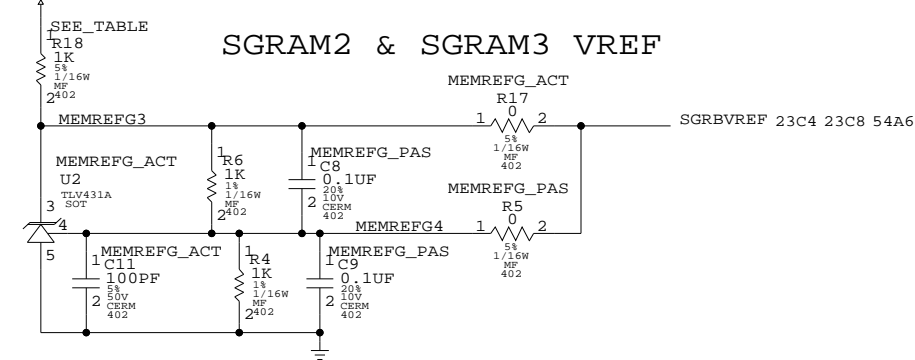
SGRAM0 & SGRAM1 MEMORY SUPPORT

Table with 6 columns: PART NUMBER, QTY, DESCRIPTION, REFERENCE DES, CRITICAL, BOM OPTION. Lists parts 333S0249 and 333S0250.

SGRAM2 & SGRAM3 DDR MEMORY REFERENCE SUPPORT

Table with 6 columns: PART NUMBER, QTY, DESCRIPTION, REFERENCE DES, CRITICAL, BOM OPTION. Lists parts 116S1103 and 116S1000.

+2\_5V\_MAIN



SGRAM2 & SGRAM3

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Apple logo and drawing information: SCALE NONE, SHT 23 OF 74, REV. D, DRAWING NUMBER 051-6423.

D

D

C

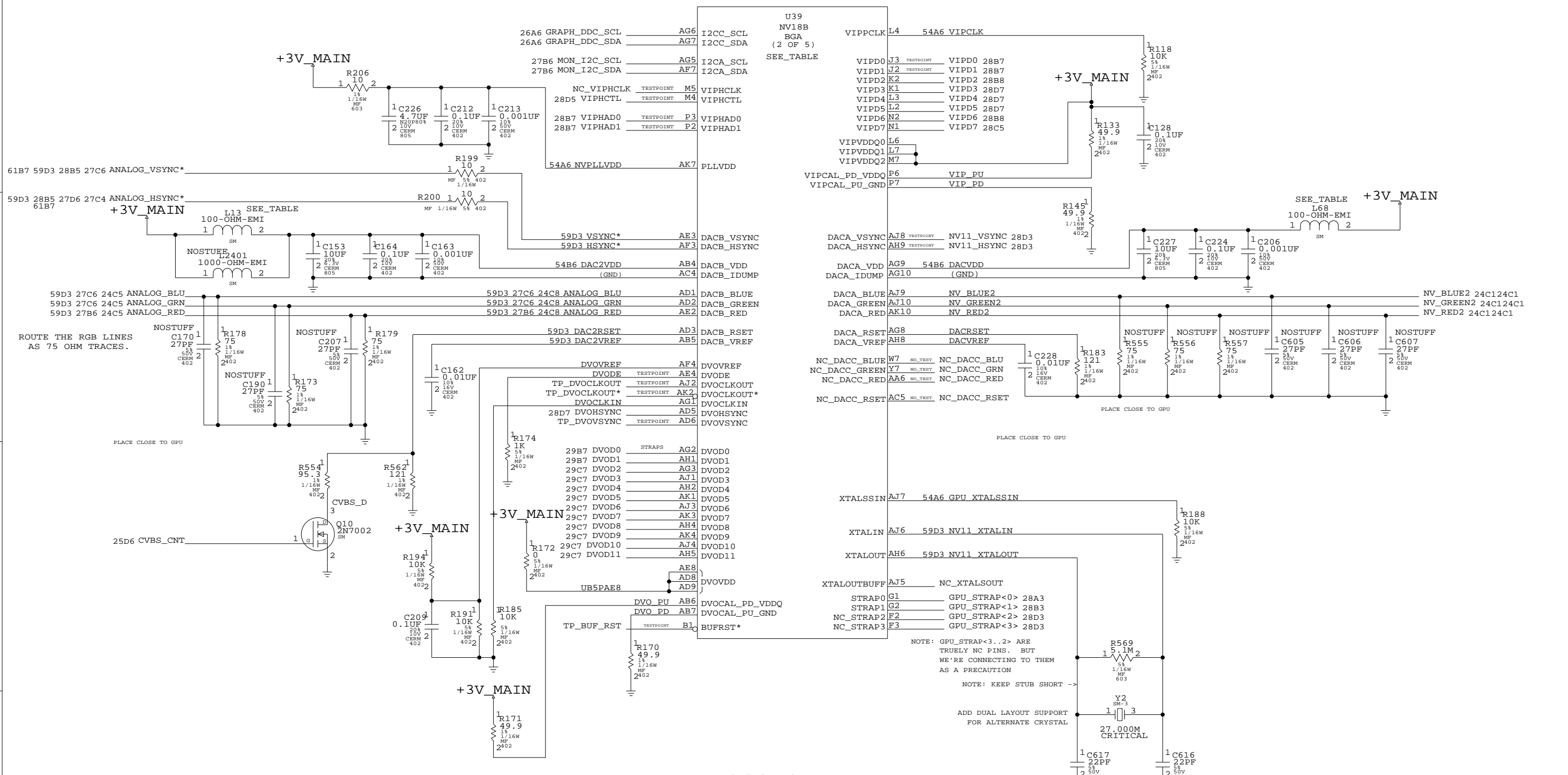
C

B

B

A

A



**NVIDIA ASIC SUPPORT**

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
338S0119	1	IC,NV18B,GRPHCS CTLR,A03	U39		NV18B
338S0112	1	IC,NV31,GRPHCS CTLR	U39		NV31
338S0113	1	IC,NV34,GRPHCS CTLR	U39		NV34
155S0141	1	FLTR,EMI,600 OHMS,.2A,0603	L2401		NOSTUFF
155S0143	2	FLTR,EMI,1000 OHMS,.2A,0805	L13,L68		

**DAC & CLOCKS**

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SCALE	SHT	OF	
NONE	24	74	



D

D

C

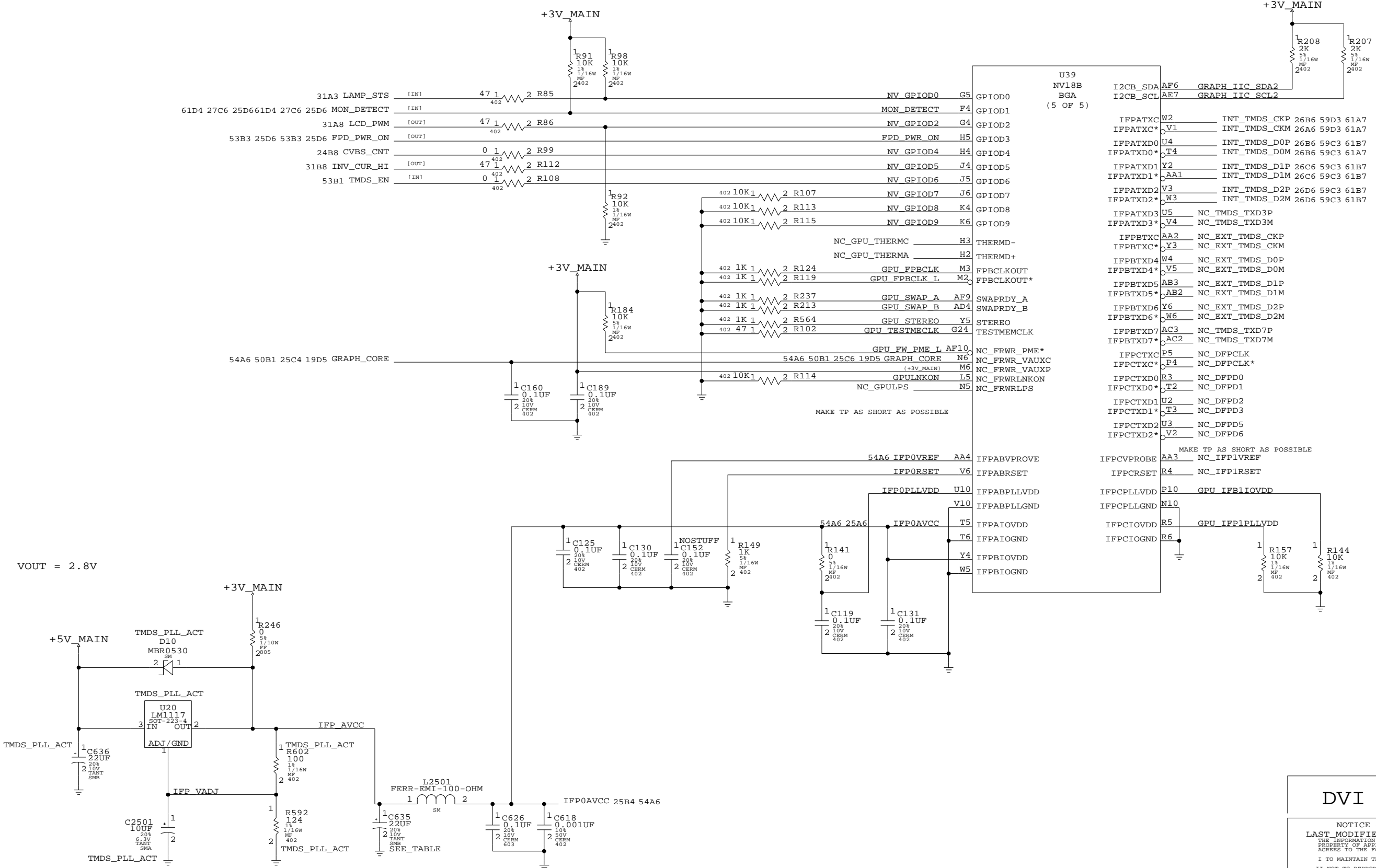
C

B

B

A

A



VOUT = 2.8V

### DVI AND STRAPS

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THE STUFFING OF AN 0805 PACKAGE ONTO C635S LARGER TANT PADS IS CORRECT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
132S1063	1	1UF, 10%, 10V, 0805, CERM	C635		

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6423	D
SCALE	SHT	OF
NONE	25	74

D

C

B

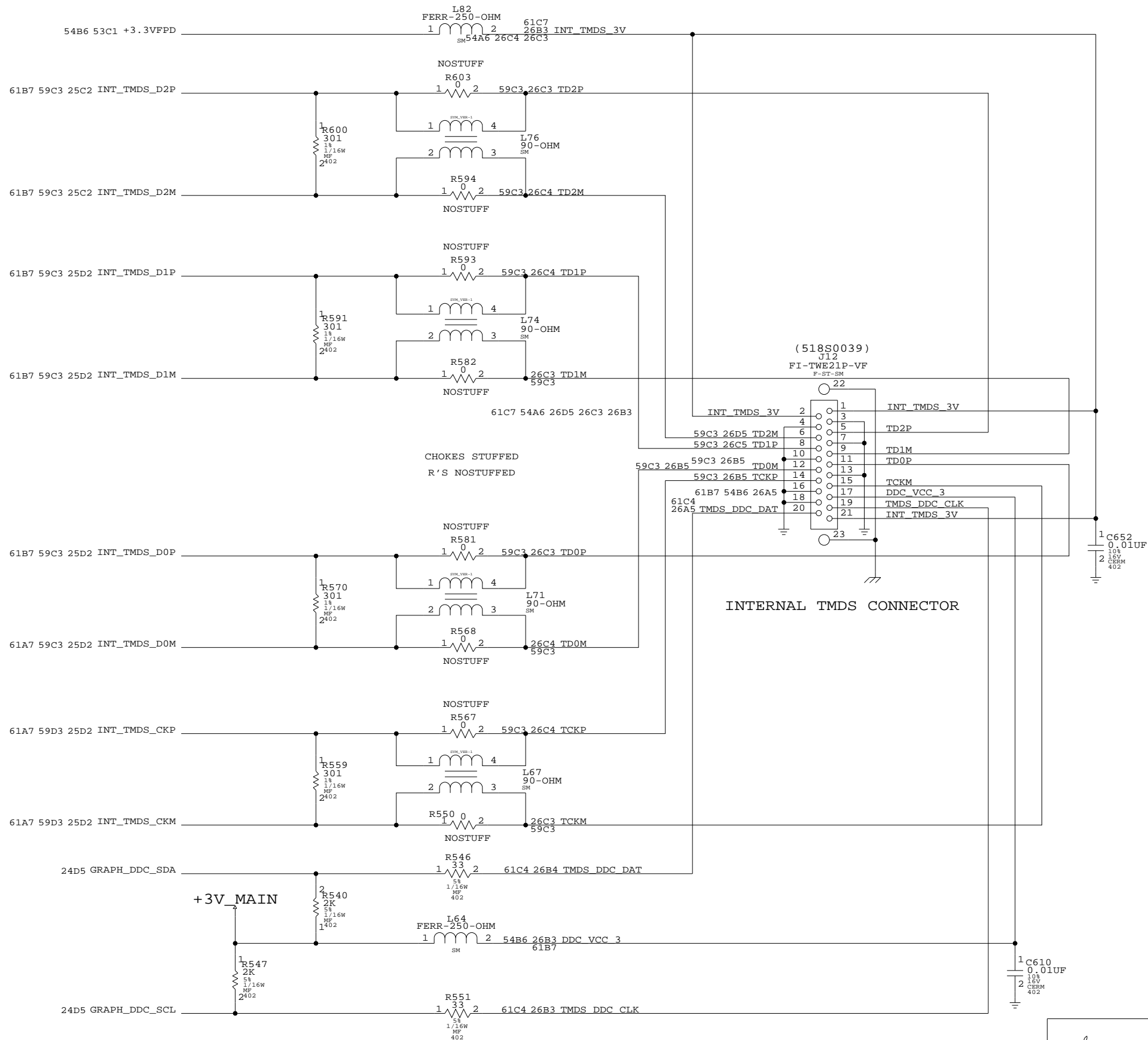
A

D

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B

A



**TMDS**

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	APPLE COMPUTER INC.		SIZE	DRAWING NUMBER	REV.
			D	051-6423	D
		SCALE	NONE	SHT	26 OF 74

D

D

C

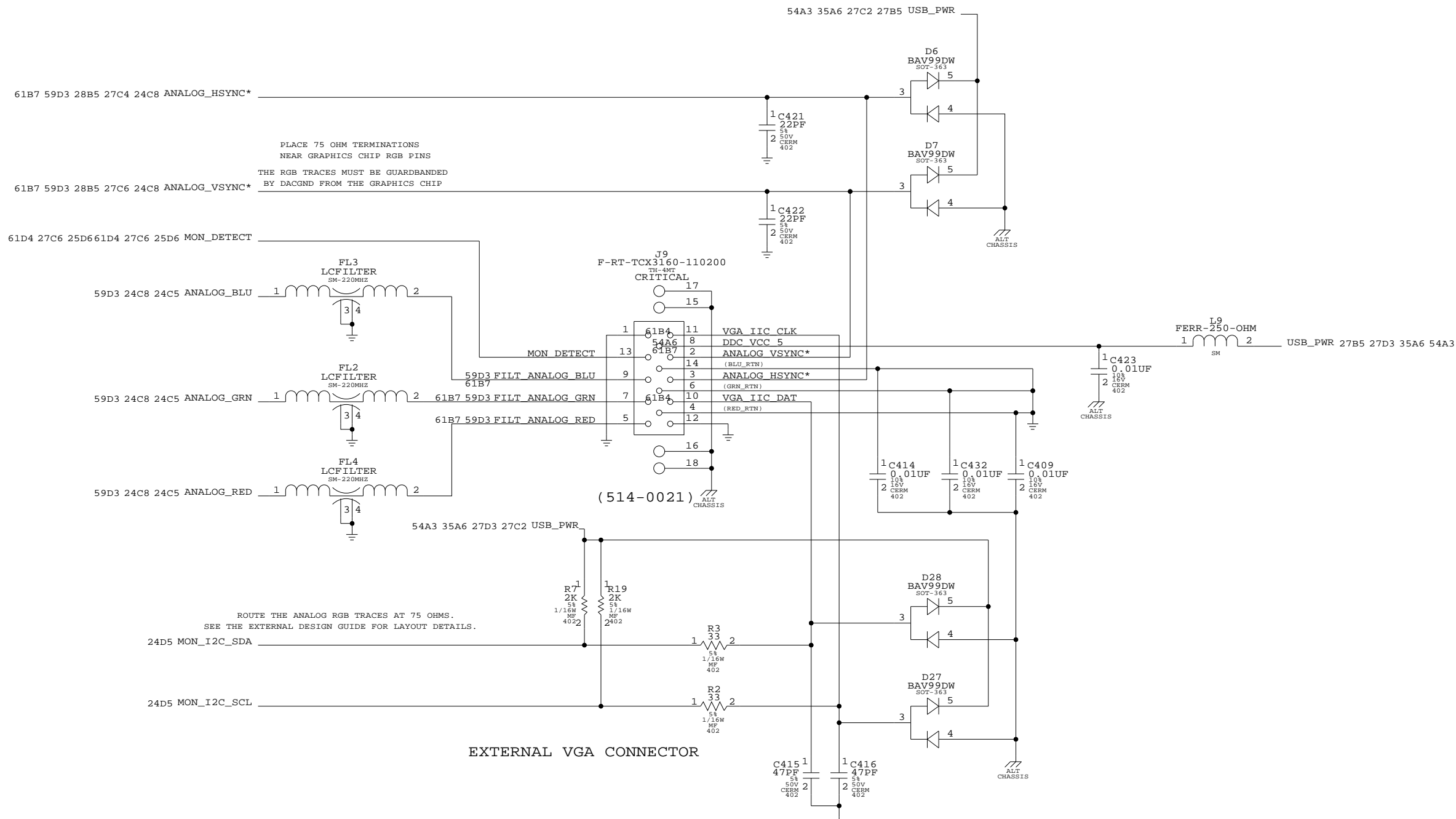
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A



**EXTERNAL VGA**

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6423	D
SCALE	SHT		OF
NONE	27		74

D

D

C

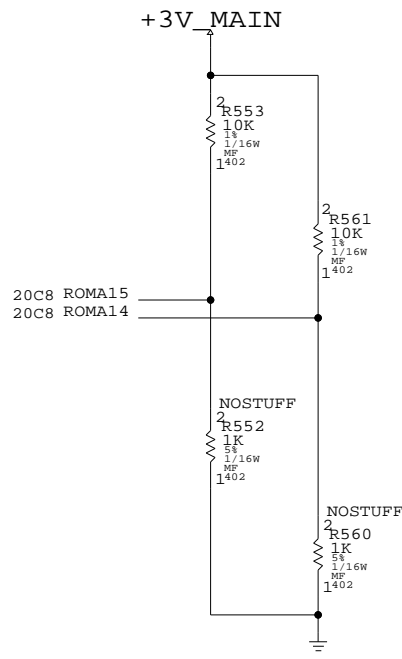
C

B

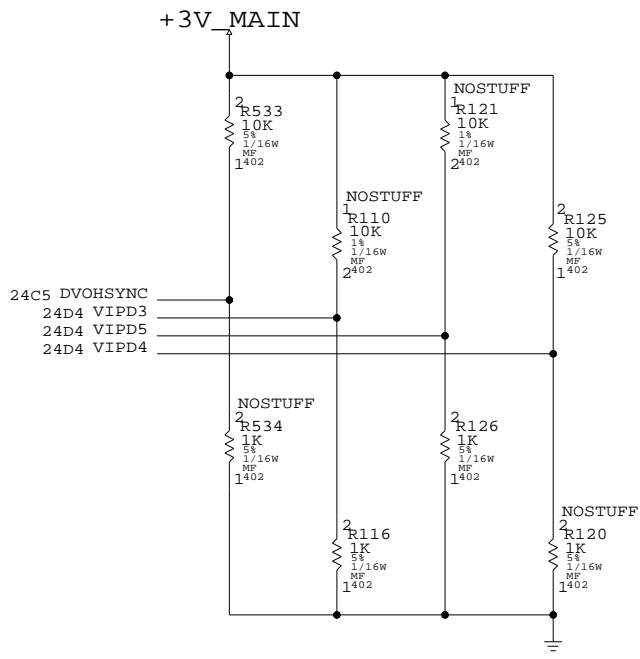
B

A

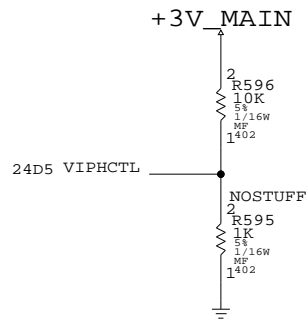
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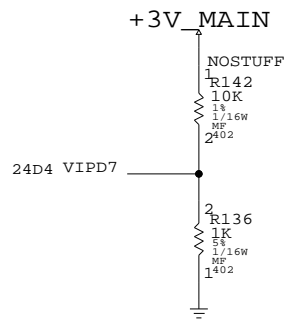
(1) ROM TYPE (OVERRIDDEN IF STRAP1 = 0)  
 [1..0] = [ROMA15,ROMA14]  
 00 = PARALLEL  
 01 = SERIAL AT25F  
 10 = SERIAL SST45VF  
 \* 11 = SERIAL FUTURE



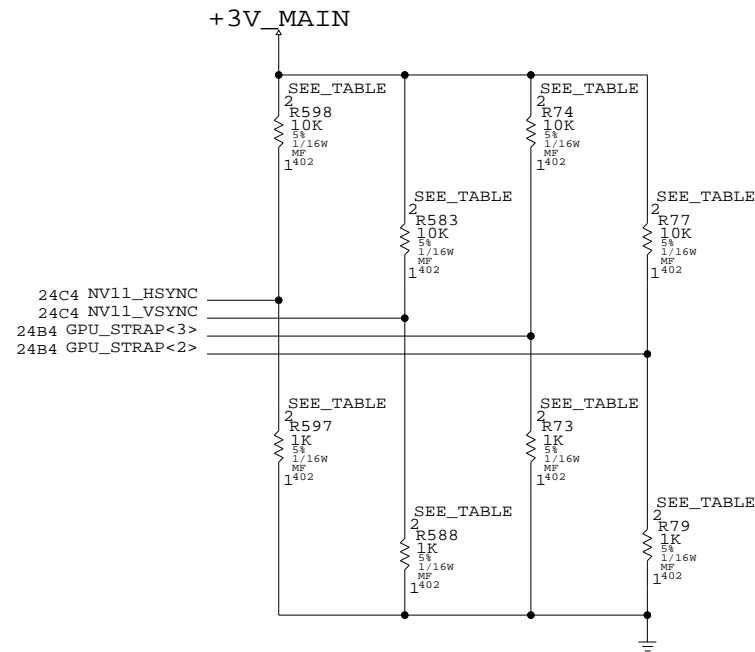
(3) PCI DEVICE ID  
 [3..0] = [DVOHSYNC, VIPD3, VIPD5, VIPD4]  
 0010 = 0X112 GEFORCE2 GO  
 0011 = 0X113 QUADRO2 GO  
 0100 = 0X114 NV17M  
 0000 = 0X110 GEFORCE2GO MX (NV11B)  
 \* 1001 = NV18B, NV31, NV34



(5) HOST MODE  
 [0] = [VIPHCTL]  
 0 = PCI MODE  
 \* 1 = AGP MODE

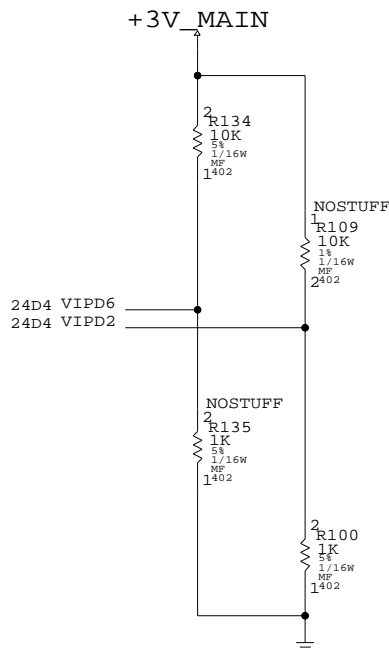


(6) AGP SIDEBAND  
 [0] = [VIPD7]  
 \* 0 = ENABLE AGP SIDEBAND  
 1 = DISABLE AGP SIDEBAND

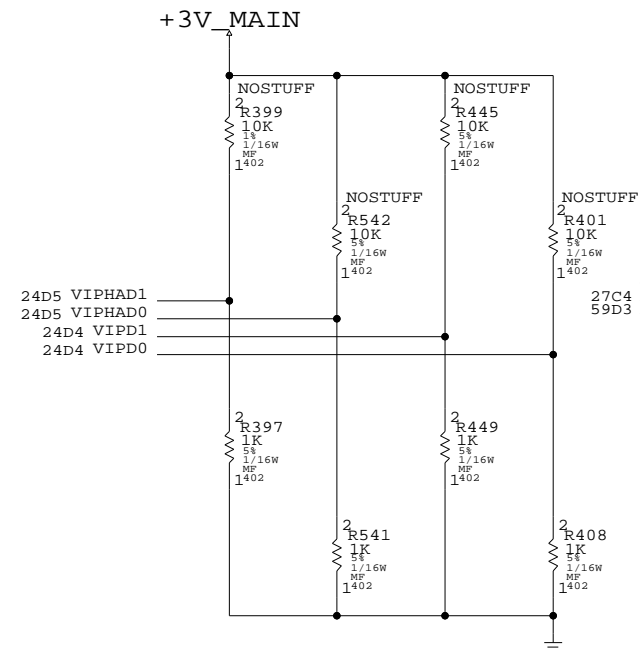


(8) FRAME BUFFER MEMORY TYPE  
 [3..0] = [NV11\_HSYNC, NV11\_VSYNC, GPU\_STRAP<3>, GPU\_STRAP<2>]  
 1111 = 222MHZ  
 1101 = 275MHZ SAMSUNG  
 1100 = 275MHZ HYNIX

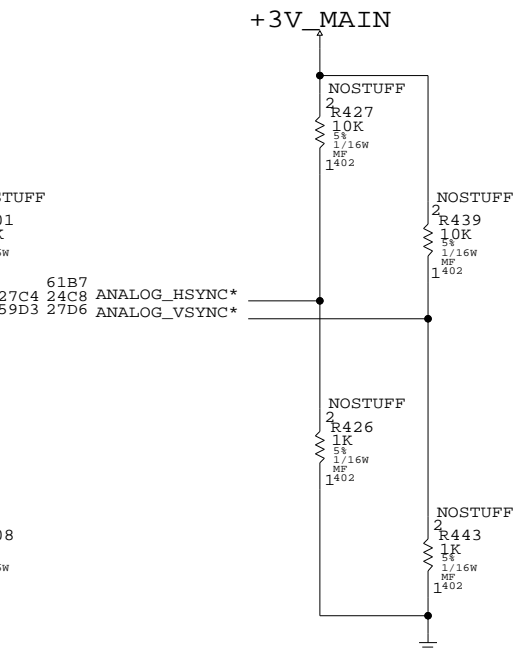
PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
116S1104	3	RES, 10KOHM, 5%, 0402	R598, R583, R77		275_SAMSUNG
116S1103	1	RES, 1KOHM, 5%, 0402	R73		275_SAMSUNG
116S1104	2	RES, 10KOHM, 5%, 0402	R598, R583		275_HYNIX
116S1103	2	RES, 1KOHM, 5%, 0402	R73, R79		275_HYNIX



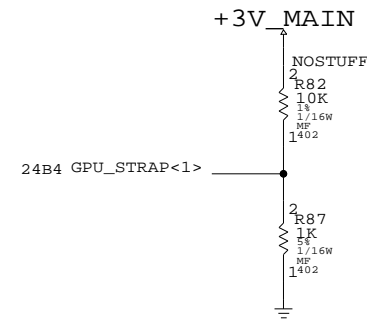
(2) CRYSTAL FREQUENCY SELECT  
 [1..0] = [VIPD6, VIPD2]  
 00 = 13.5MHZ  
 01 = 14.38MHZ  
 \* 10 = 27MHZ  
 11 = {UNDEFINED}



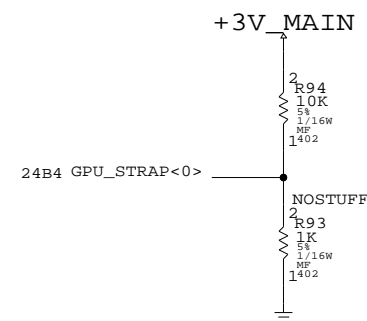
(4) USER DEFINED STRAPS  
 [3..0] = [VIPHAD1, VIPHAD0, VIPD1, VIPD0]  
 THESE BITS ARE UNDEFINED BUT THEY  
 MUST BE KEPT LOW DURING RESET



(7) TV MODE  
 [1..0] = [ANALOG\_HSYNC\*, ANALOG\_VSYNC\*]  
 00 = SECAM  
 01 = NTSC  
 10 = PAL  
 \* 11 = DISABLED  
 (THESE RESISTORS ARE ALL NOSTUFF)



(9) SUB-VENDOR  
 [0] = [GPU\_STRAP<1>]  
 0 = SYSTEM BIOS (VENDOR & SUBSYSTEM ID=0X0000)  
 \* 1 = ADAPTER CARD VGA BIOS (VENDOR & SUBSYSTEM ID=0X54-0X57)



(10) PCI ADDRESS BUS  
 [0] = [GPU\_STRAP<0>]  
 0 = REVERSED  
 \* 1 = NORMAL

### NVIDIA STRAPS 1

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SIZE	DRAWING NUMBER	REV.
D	051-6423	D
SCALE	SHT	OF
NONE	28	74

D

D

C

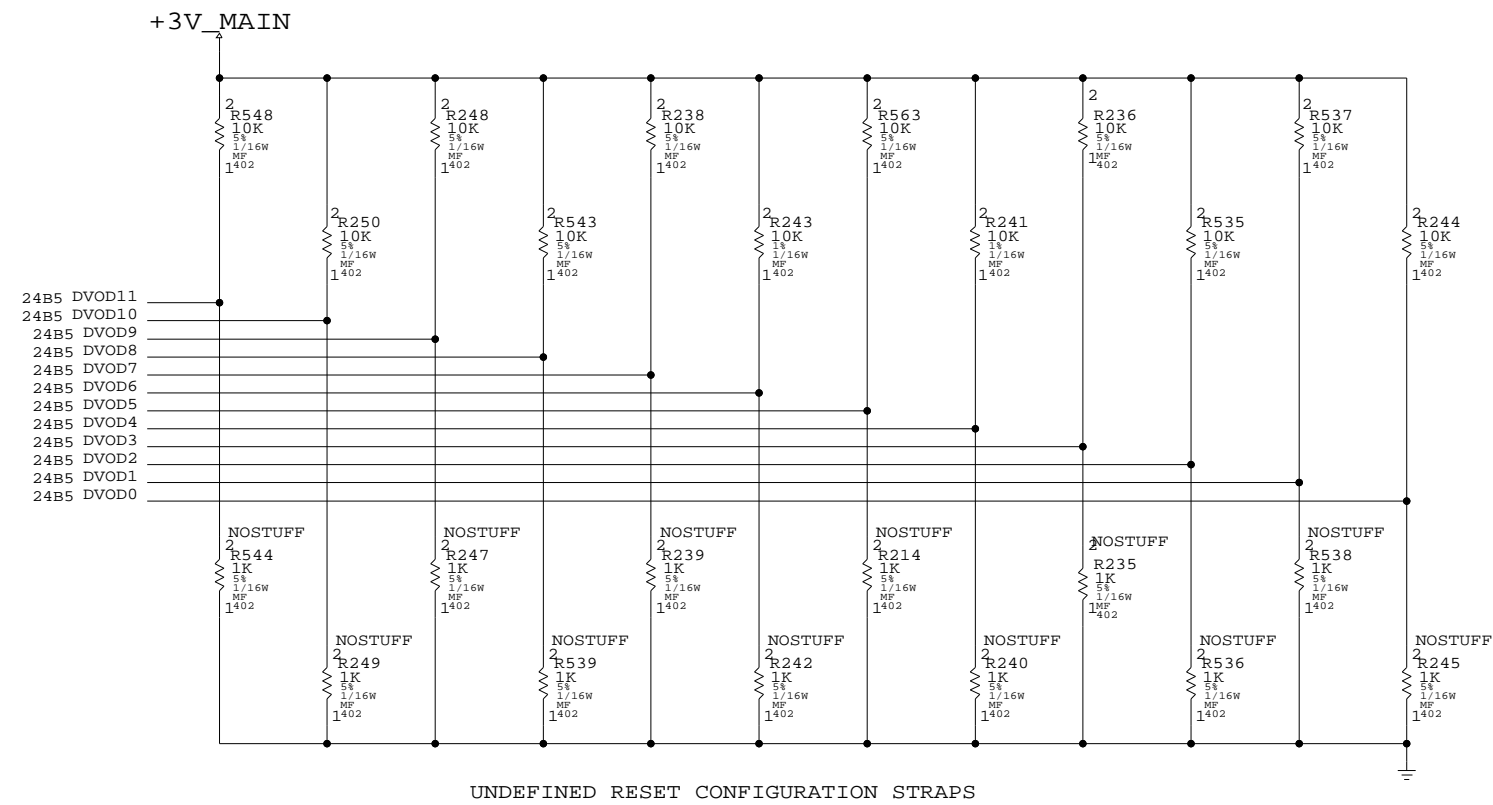
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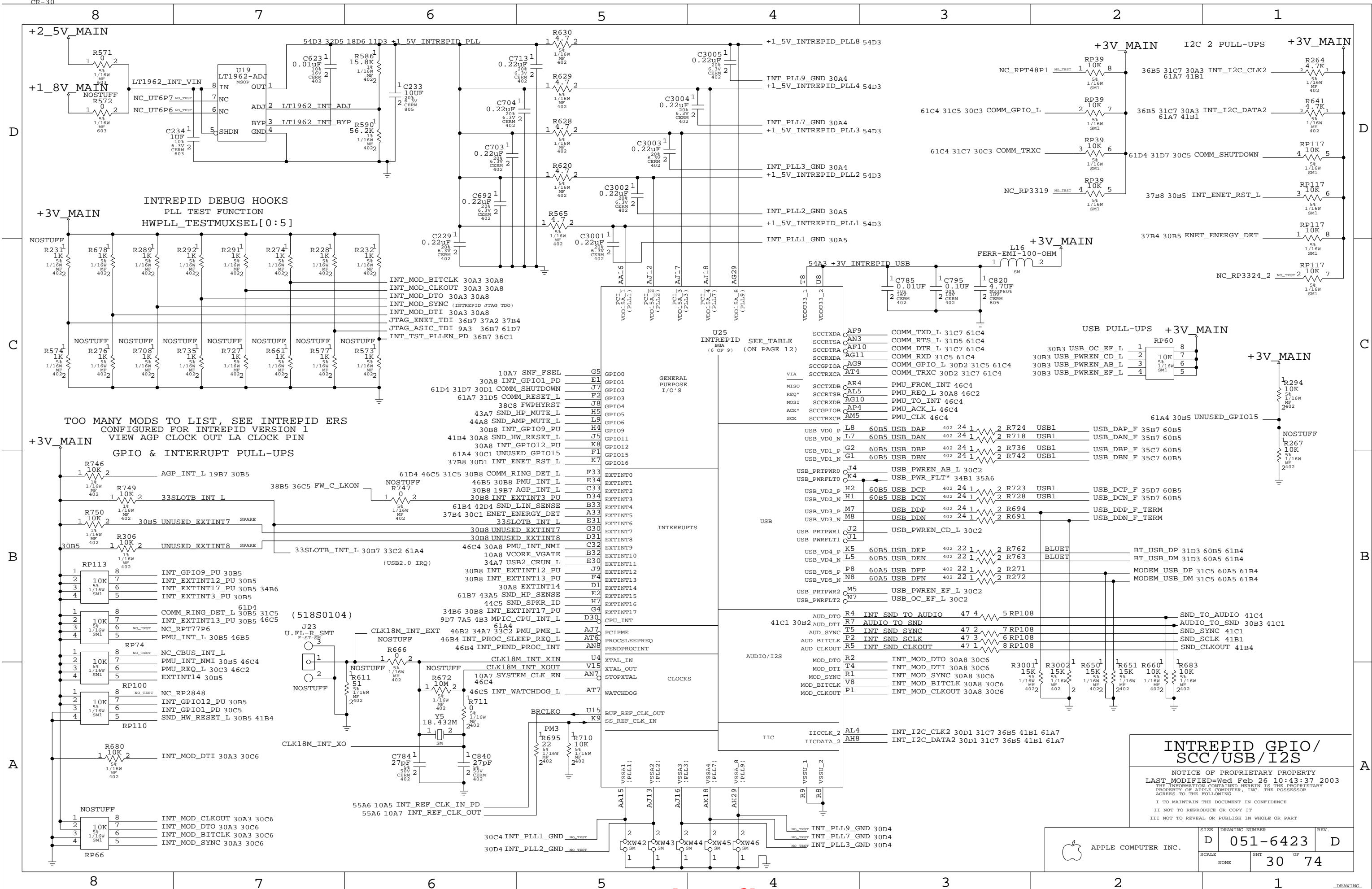
A



### NVIDIA STRAPS 2

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	SIZE	DRAWING NUMBER	REV.
	D	051-6423	D
SCALE	SHT		OF
NONE	29		74

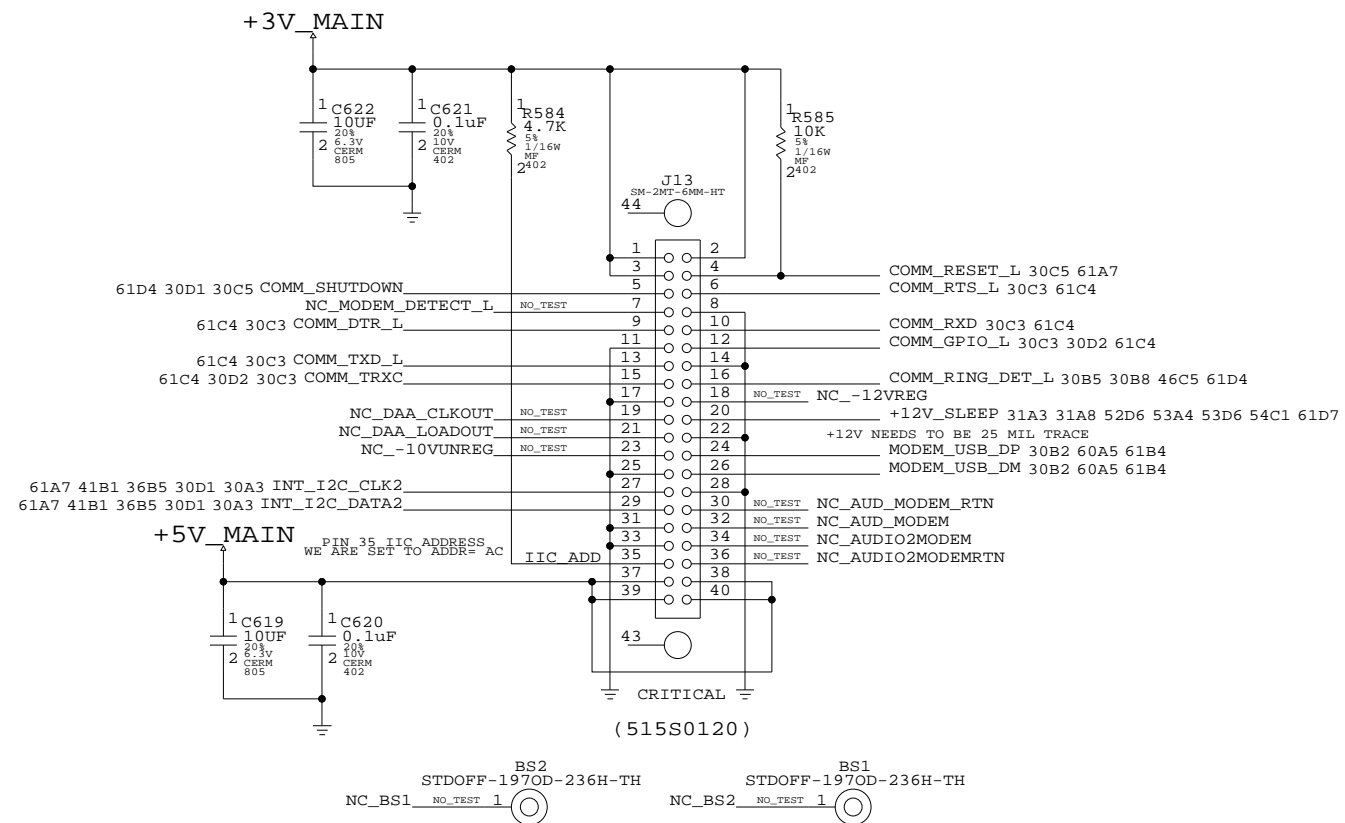


**INTREPID GPIO/  
SCC/USB/I2S**

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APPLE COMPUTER INC.	SIZE: <b>D</b> DRAWING NUMBER: <b>051-6423</b> REV.: <b>D</b>	SCALE: NONE    SHT: <b>30</b> OF <b>74</b>
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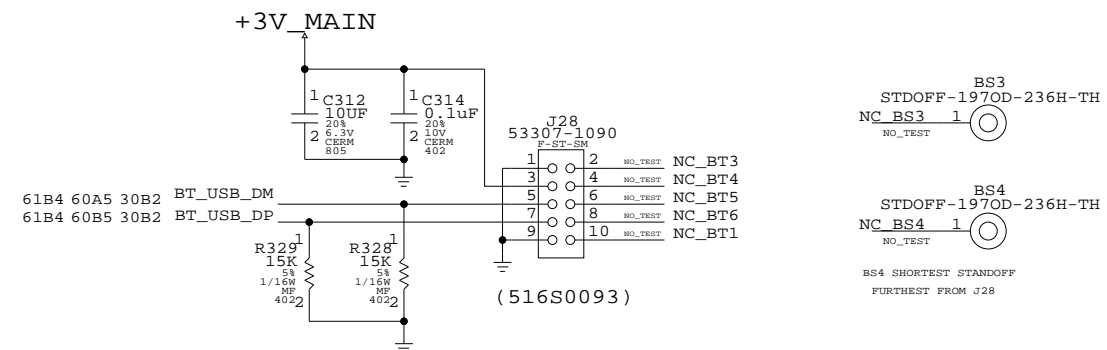
### MODEM BOARD CONNECTOR (DASH II)



MODEM STANDOFF SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
860-1034	2	STDOFF-19709-236H-TH	BS1, BS2		

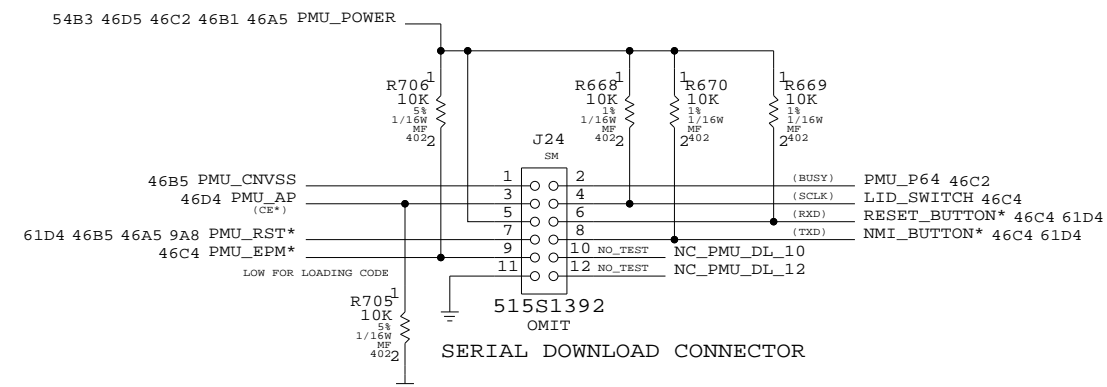
### BLUETOOTH CONNECTOR



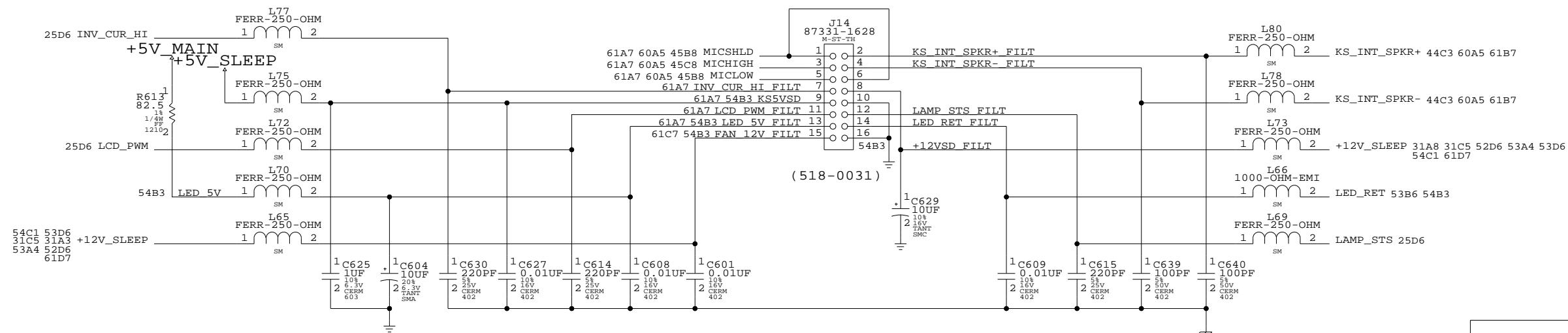
BLUETOOTH CARD MOUNTING HARDWARE SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
860-0170	1	STDOFF, BLUETOOTH, SHORT	BS4		
860-0171	1	STDOFF, BLUETOOTH, LONG	BS3		

### SERIAL DOWNLOAD INTERFACE



### 'KITCHEN SINK' CONNECTOR (MICROPHONE, INTERNAL SPEAKER CONNECTIONS INVERTER, LCD, LED & FAN POWER)



### MODEM, BLUETOOTH, & SERIAL DOWNLOAD

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	D	051-6423	D
SCALE	SHT	OF	
NONE	31	74	

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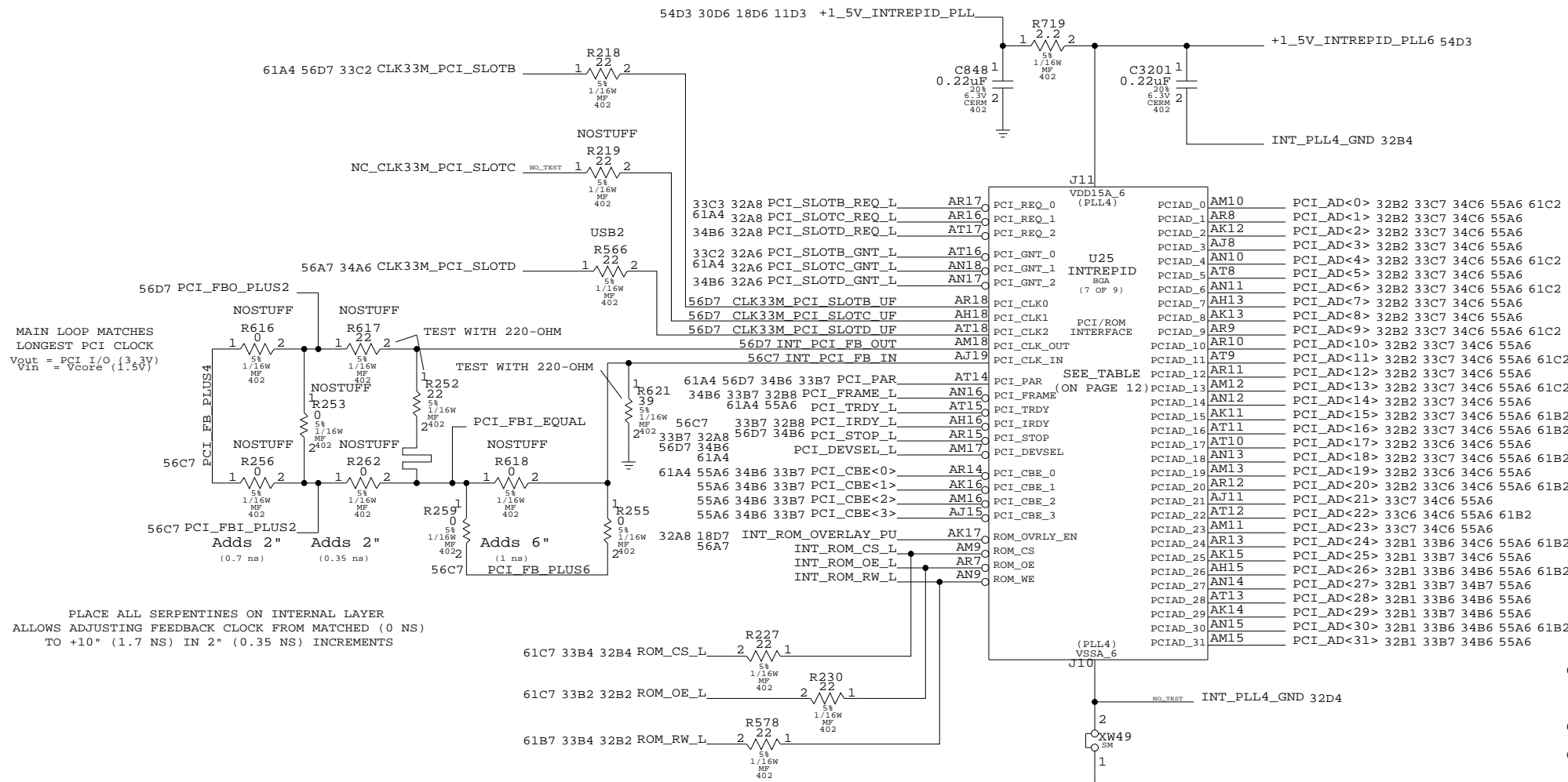
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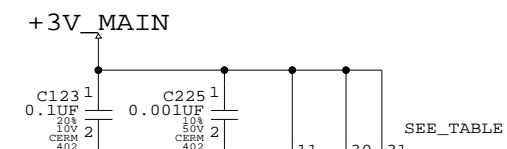


MAIN LOOP MATCHES  
LONGEST PCI CLOCK  
V<sub>OUT</sub> = PCI I/O (3.3V)  
V<sub>IN</sub> = V<sub>core</sub> (1.5V)

PLACE ALL SERPENTINES ON INTERNAL LAYER  
ALLOWS ADJUSTING FEEDBACK CLOCK FROM MATCHED (0 NS)  
TO +10" (1.7 NS) IN 2" (0.35 NS) INCREMENTS

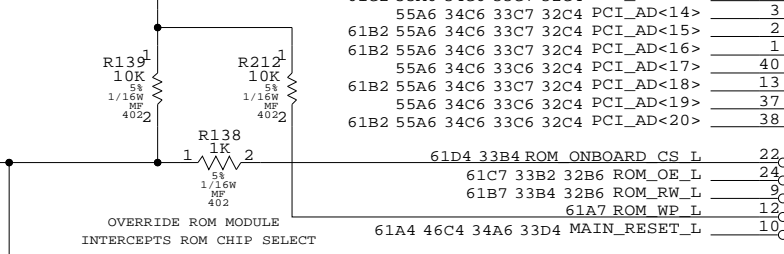
J11

VDD15A_6 (PLL4)	PCIAD_0	AM10	PCI_AD<0>	32B2	33C7	34C6	55A6	61C2
PCI_REQ_0	PCIAD_1	AR8	PCI_AD<1>	32B2	33C7	34C6	55A6	
PCI_REQ_1	PCIAD_2	AK12	PCI_AD<2>	32B2	33C7	34C6	55A6	
PCI_REQ_2	PCIAD_3	AJ8	PCI_AD<3>	32B2	33C7	34C6	55A6	
PCI_GNT_0	PCIAD_4	AN10	PCI_AD<4>	32B2	33C7	34C6	55A6	61C2
PCI_GNT_1	PCIAD_5	AT8	PCI_AD<5>	32B2	33C7	34C6	55A6	
PCI_GNT_2	PCIAD_6	AN11	PCI_AD<6>	32B2	33C7	34C6	55A6	61C2
PCI_CLK0	PCIAD_7	AH13	PCI_AD<7>	32B2	33C7	34C6	55A6	
PCI_CLK1	PCIAD_8	AK13	PCI_AD<8>	32B2	33C7	34C6	55A6	
PCI_CLK2	PCIAD_9	AR9	PCI_AD<9>	32B2	33C7	34C6	55A6	61C2
PCI_CLK_OUT	PCIAD_10	AR10	PCI_AD<10>	32B2	33C7	34C6	55A6	
PCI_CLK_IN	PCIAD_11	AT9	PCI_AD<11>	32B2	33C7	34C6	55A6	61C2
PCI_PAR	PCIAD_12	AR11	PCI_AD<12>	32B2	33C7	34C6	55A6	
PCI_FRAME	PCIAD_13	AM12	PCI_AD<13>	32B2	33C7	34C6	55A6	61C2
PCI_TRDY_L	PCIAD_14	AN12	PCI_AD<14>	32B2	33C7	34C6	55A6	
PCI_IRDY	PCIAD_15	AK11	PCI_AD<15>	32B2	33C7	34C6	55A6	61B2
PCI_STOP	PCIAD_16	AT11	PCI_AD<16>	32B2	33C7	34C6	55A6	61B2
PCI_DEVSEL	PCIAD_17	AT10	PCI_AD<17>	32B2	33C6	34C6	55A6	
PCI_CBE_0	PCIAD_18	AN13	PCI_AD<18>	32B2	33C7	34C6	55A6	61B2
PCI_CBE_1	PCIAD_19	AM13	PCI_AD<19>	32B2	33C6	34C6	55A6	
PCI_CBE_2	PCIAD_20	AR12	PCI_AD<20>	32B2	33C6	34C6	55A6	61B2
PCI_CBE_3	PCIAD_21	AT11	PCI_AD<21>	33C7	34C6	55A6		
ROM_OVRLY_EN	PCIAD_22	AT12	PCI_AD<22>	33C6	34C6	55A6	61B2	
ROM_CS	PCIAD_23	AM11	PCI_AD<23>	33C7	34C6	55A6		
ROM_OE	PCIAD_24	AR13	PCI_AD<24>	32B1	33B6	34C6	55A6	61B2
ROM_WE	PCIAD_25	AK15	PCI_AD<25>	32B1	33B7	34C6	55A6	
	PCIAD_26	AH15	PCI_AD<26>	32B1	33B6	34B6	55A6	61B2
	PCIAD_27	AN14	PCI_AD<27>	32B1	33B7	34B7	55A6	
	PCIAD_28	AT13	PCI_AD<28>	32B1	33B6	34B6	55A6	
	PCIAD_29	AK14	PCI_AD<29>	32B1	33B7	34B6	55A6	
	PCIAD_30	AN15	PCI_AD<30>	32B1	33B6	34B6	55A6	61B2
	PCIAD_31	AM15	PCI_AD<31>	32B1	33B7	34B6	55A6	

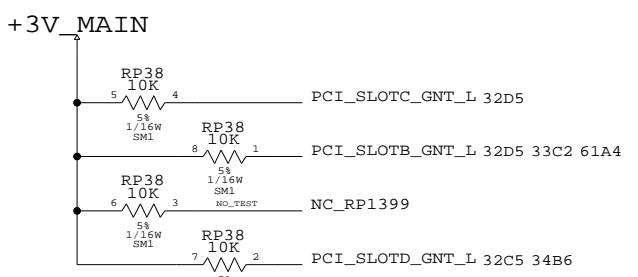


INTREPID PCI INTERFACE

+3V MAIN



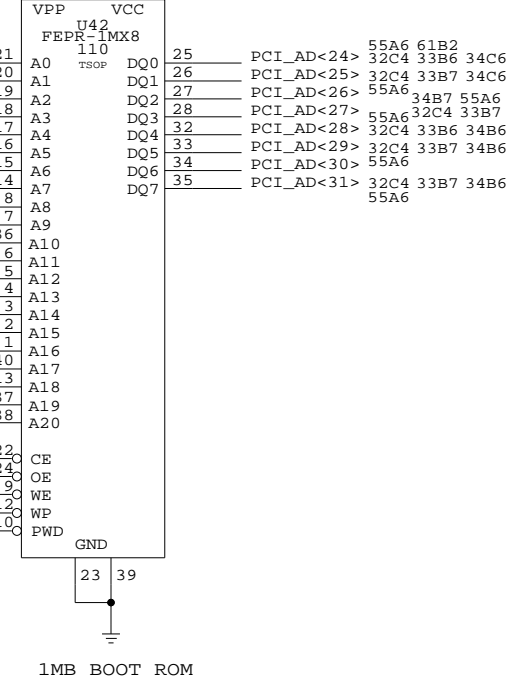
+3V MAIN



FLASH BOOT ROM SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
341T1107	1	IC, FLASH, PROD, ROM, PRGMD	U42		
341S1169	1	IC, FEPR, FLASH ROM, PROD	U42		ROM_PRGMD PROD
009-6420	1	IC, FEPR, FLASH ROM, PROD	U42		ROM_IMG PROD
341S1225	1	IC, FLASH, BOOT ROM, Q26, 4.5.8B2	U42		ROM_PRGMD PROD
009-6076	1	CODE, BOOTROM, Q26, PVT, 4.5.8B2	U42		ROM_IMG PROD
335S0350	1	IC, FLASH ROM, 1MB, BLANK	U42		OMIT

61C2 55A6 34C6 33C7 32D4	PCI_AD<0>	21		25	PCI_AD<24>	55A6 61B2
55A6 34C6 33C7 32D4	PCI_AD<1>	20	A0	26	PCI_AD<25>	32C4 33B6 34C6
55A6 34C6 33C7 32D4	PCI_AD<2>	19	A1	27	PCI_AD<26>	55A6 34B7 55A6
55A6 34C6 33C7 32D4	PCI_AD<3>	18	A2	28	PCI_AD<27>	55A6 32C4 33B7
61C2 55A6 34C6 33C7 32D4	PCI_AD<4>	17	A3	29	PCI_AD<28>	32C4 33B6 34B6
55A6 34C6 33C7 32D4	PCI_AD<5>	16	A4	30	PCI_AD<29>	32C4 33B7 34B6
61C2 55A6 34C6 33C7 32C4	PCI_AD<6>	15	A5	31	PCI_AD<30>	55A6
55A6 34C6 33C7 32C4	PCI_AD<7>	14	A6	32	PCI_AD<31>	32C4 33B7 34B6
55A6 34C6 33C7 32C4	PCI_AD<8>	8	A7	33		55A6
61C2 55A6 34C6 33C7 32C4	PCI_AD<9>	7	A8	34		
55A6 34C6 33C7 32C4	PCI_AD<10>	36	A9	35		
61C2 55A6 34C6 33C7 32C4	PCI_AD<11>	6	A10	36		
55A6 34C6 33C7 32C4	PCI_AD<12>	5	A11	37		
61C2 55A6 34C6 33C7 32C4	PCI_AD<13>	4	A12	38		
55A6 34C6 33C7 32C4	PCI_AD<14>	3	A13	39		
61B2 55A6 34C6 33C7 32C4	PCI_AD<15>	2	A14	40		
55A6 34C6 33C7 32C4	PCI_AD<16>	1	A15	41		
61B2 55A6 34C6 33C7 32C4	PCI_AD<17>	40	A16	42		
55A6 34C6 33C6 32C4	PCI_AD<18>	13	A17	43		
61B2 55A6 34C6 33C6 32C4	PCI_AD<19>	37	A18	44		
55A6 34C6 33C6 32C4	PCI_AD<20>	38	A19	45		
61B2 55A6 34C6 33C6 32C4	PCI_AD<21>	38	A20	46		



**INTREPID PCI & BOOT ROM**

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SCALE NONE SHT 32 OF 74 REV.



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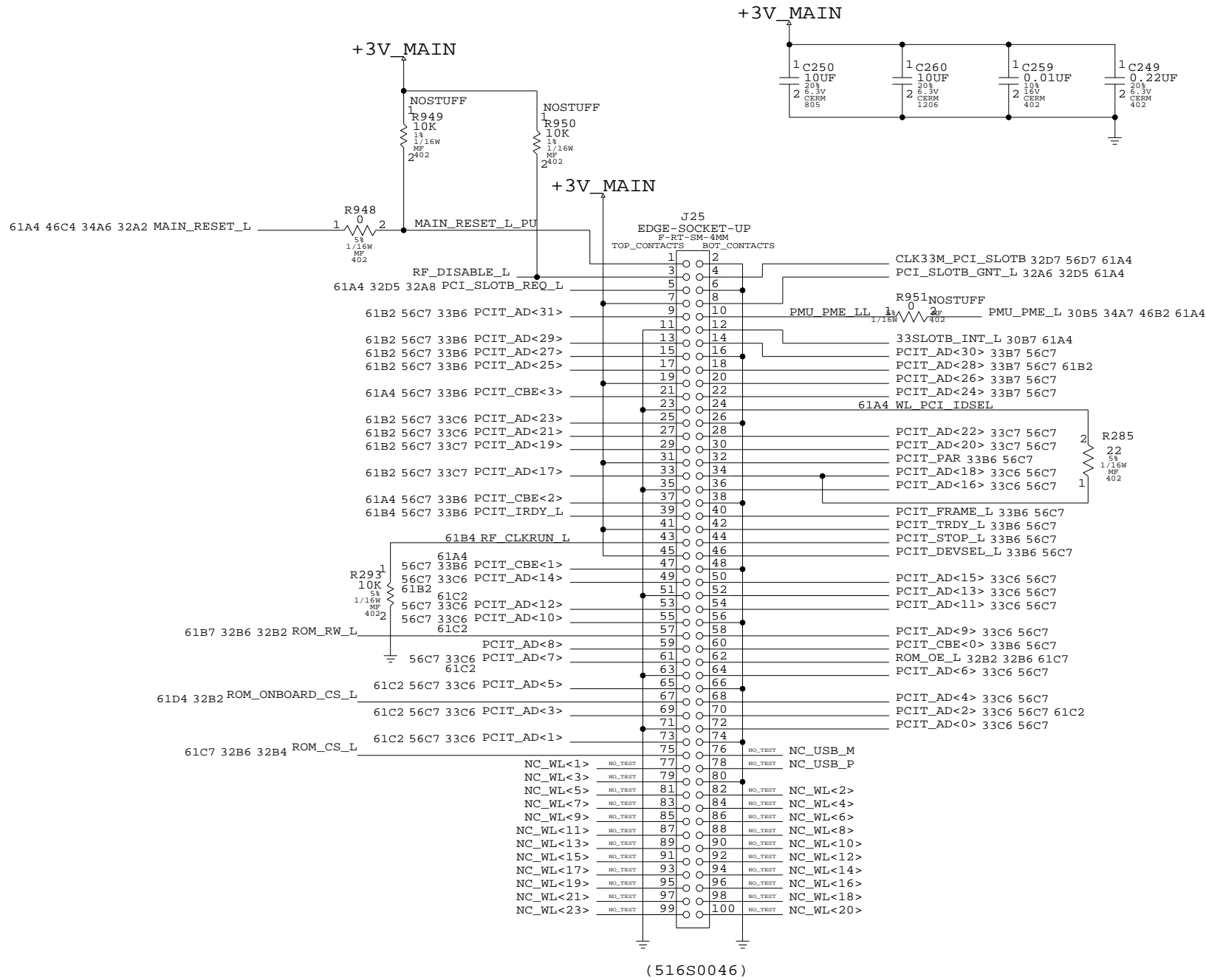
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PLACE RP'S NEAR WIRELESS CONNECTOR

61C2 55A6 34C6 32D4 32B2 PCI_AD<0>	1	8	PCIT_AD<0>	33B2 56C7
55A6 34C6 32D4 32B2 PCI_AD<1>	2	7	PCIT_AD<1>	33B3 56C7 61C2
55A6 34C6 32D4 32B2 PCI_AD<2>	3	6 NO_TEST	PCIT_AD<2>	33B2 56C7 61C2
55A6 34C6 32D4 32B2 PCI_AD<3>	4	5	PCIT_AD<3>	33B3 56C7 61C2
61C2 55A6 34C6 32D4 32B2 PCI_AD<4>	1	8	PCIT_AD<4>	33B2 56C7
55A6 34C6 32D4 32B2 PCI_AD<5>	2	7	PCIT_AD<5>	33B3 56C7 61C2
61C2 55A6 34C6 32C4 32B2 PCI_AD<6>	3	6 NO_TEST	PCIT_AD<6>	33B2 56C7
55A6 34C6 32C4 32B2 PCI_AD<7>	4	5	PCIT_AD<7>	33B3 56C7 61C2
55A6 34C6 32C4 32B2 PCI_AD<8>	1	8	PCIT_AD<8>	33B3 56C7 61C2
61C2 55A6 34C6 32C4 32B2 PCI_AD<9>	2	7 NO_TEST	PCIT_AD<9>	33B2 56C7
55A6 34C6 32C4 32B2 PCI_AD<10>	3	6	PCIT_AD<10>	33B3 56C7 61C2
61C2 55A6 34C6 32C4 32B2 PCI_AD<11>	4	5 NO_TEST	PCIT_AD<11>	33B2 56C7
55A6 34C6 32C4 32B2 PCI_AD<12>	1	8	PCIT_AD<12>	33B3 56C7 61C2
61C2 55A6 34C6 32C4 32B2 PCI_AD<13>	2	7 NO_TEST	PCIT_AD<13>	33B2 56C7
55A6 34C6 32C4 32B2 PCI_AD<14>	3	6	PCIT_AD<14>	33C3 56C7 61B2
61B2 55A6 34C6 32C4 32B2 PCI_AD<15>	4	5 NO_TEST	PCIT_AD<15>	33C2 56C7
61B2 55A6 34C6 32C4 32B2 PCI_AD<16>	1	8	PCIT_AD<16>	33C2 56C7
61B2 56C7 33C3 PCIT_AD<17>	2	7	PCI_AD<17>	32B2 32C4 34C6 55A6
61B2 55A6 34C6 32C4 32B2 PCI_AD<18>	3	6	PCIT_AD<18>	33C2 56C7
61B2 56C7 33C3 PCIT_AD<19>	4	5	PCI_AD<19>	32B2 32C4 34C6 55A6
55A6 34C6 32C4 PCI_AD<23>	1	8	PCIT_AD<23>	33C3 56C7 61B2
55A6 34C6 32C4 PCI_AD<21>	2	7	PCIT_AD<21>	33C3 56C7 61B2
56C7 33C2 PCIT_AD<22>	3	6	PCI_AD<22>	32C4 34C6 55A6 61B2
56C7 33C2 PCIT_AD<20>	4	5	PCI_AD<20>	32B2 32C4 34C6 55A6 61B2
55A6 34B7 32C4 32B1 PCI_AD<27>	1	8	PCIT_AD<27>	33C3 56C7 61B2
55A6 34C6 32C4 32B1 PCI_AD<25>	2	7	PCIT_AD<25>	33C3 56C7 61B2
56C7 33C2 PCIT_AD<26>	3	6	PCI_AD<26>	32B1 32C4 34B6 55A6 61B2
56C7 33C2 PCIT_AD<24>	4	5	PCI_AD<24>	32B1 32C4 34C6 55A6 61B2
61B2 56C7 33C2 PCIT_AD<28>	1	8	PCI_AD<28>	32B1 32C4 34B6 55A6
56C7 33C2 PCIT_AD<30>	2	7	PCI_AD<30>	32B1 32C4 34B6 55A6 61B2
55A6 34B6 32C4 32B1 PCI_AD<31>	3	6	PCIT_AD<31>	33C3 56C7 61B2
55A6 34B6 32C4 32B1 PCI_AD<29>	4	5	PCIT_AD<29>	33C3 56C7 61B2
61A4 56D7 34B6 32C5 PCI_PAR	1	8	PCIT_PAR 329	33C2 56C7
61A4 55A6 34B6 32C5 32B8 PCI_FRAME_L	2	7 NO_TEST	PCIT_FRAME_L	33C2 56C7
61A4 56D7 34B6 32C5 32B8 PCI_TRDY_L	3	6 NO_TEST	PCIT_TRDY_L	33C2 56C7
56D7 34B6 32C5 32B8 PCI_IRDY_L	4	5 NO_TEST	PCIT_IRDY_L	33C3 56C7 61B4
61A4 56D7 34B6 32C5 32A8 PCI_STOP_L	1	8	PCIT_STOP_L	33C2 56C7
61A4 56D7 34B6 32C5 32A8 PCI_DEVSEL_L	2	7 NO_TEST	PCIT_DEVSEL_L	33C2 56C7
55A6 34B6 32C5 PCI_CBE<1>	3	6	PCIT_CBE<1>	33C3 56C7 61A4
61A4 55A6 34B6 32C5 PCI_CBE<0>	4	5 NO_TEST	PCIT_CBE<0>	33B2 56C7
55A6 34B6 32C5 PCI_CBE<2>	1	8	PCIT_CBE<2>	33C3 56C7 61A4
55A6 34B6 32C5 PCI_CBE<3>	2	7	PCIT_CBE<3>	33C3 56C7 61A4
NC_PCIR0	NO_TEST	3	NO_TEST	NC_PCITR0
NC_PCIR1	NO_TEST	4	NO_TEST	NC_PCITR1



(516S0046)

WIRELESS CARD MOUNTING HARDWARE SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
815-7245	1	WIRELESS CARD GUIDE, J25	J251		
452-0411	2	NUT, HEX, M2 X 1.5H, J25	J252, J253		
452-0412	2	SCREW, M2 X 0.4 X 6.0 L, J25	J254, J255		

WIRELESS PCI

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6423	D
SCALE	NONE	SHT	33 OF 74

D

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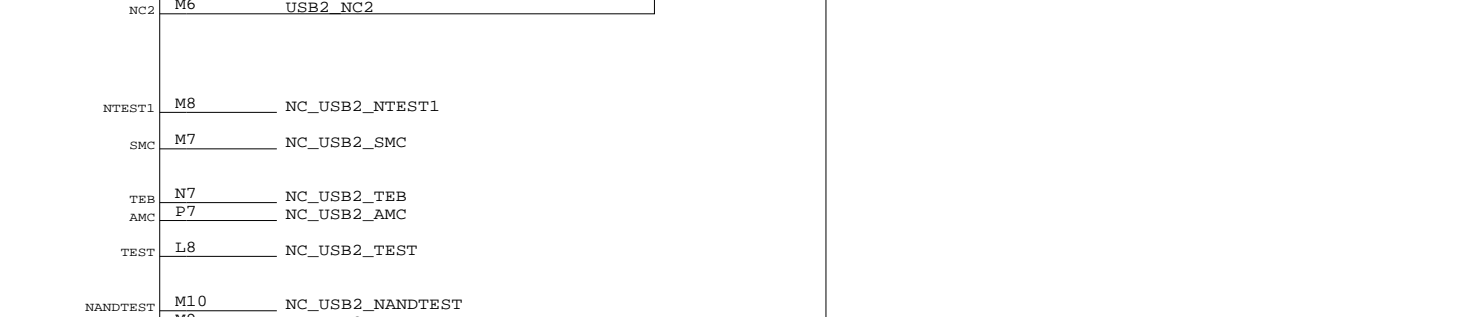
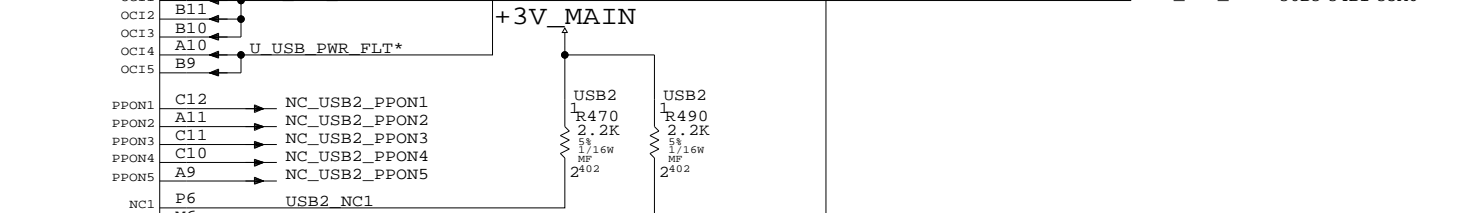
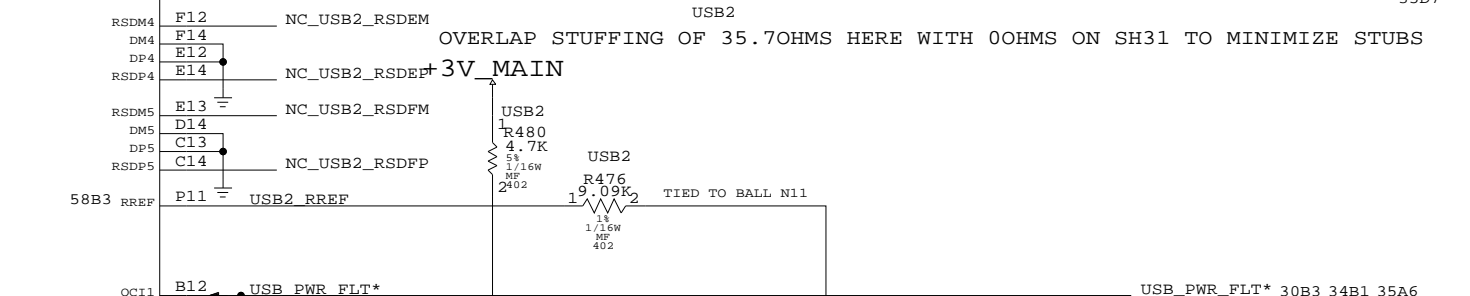
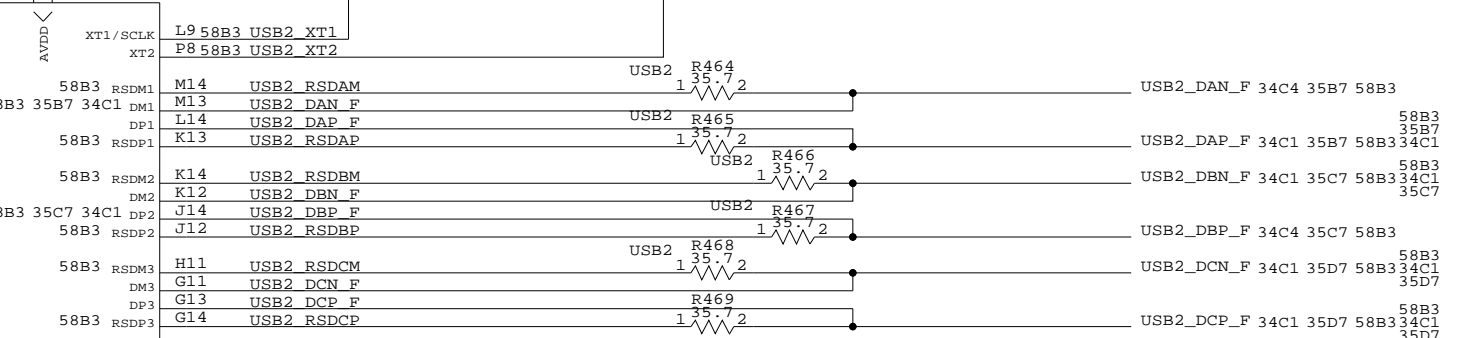
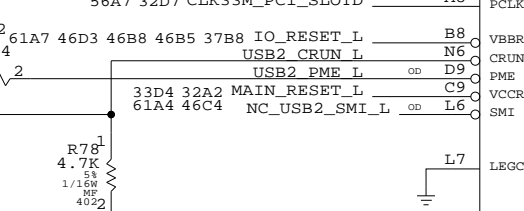
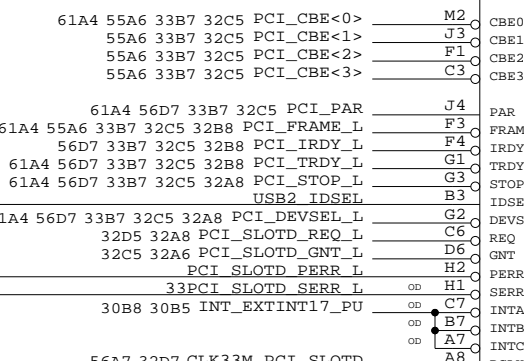
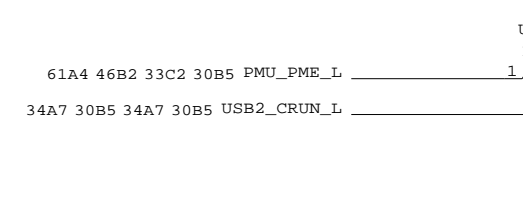
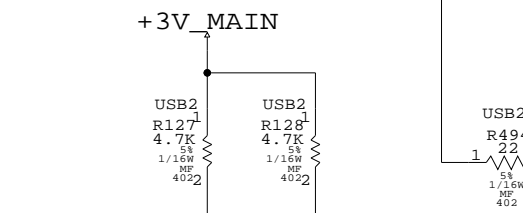
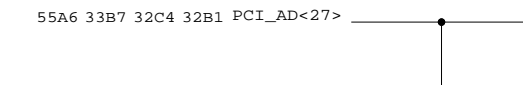
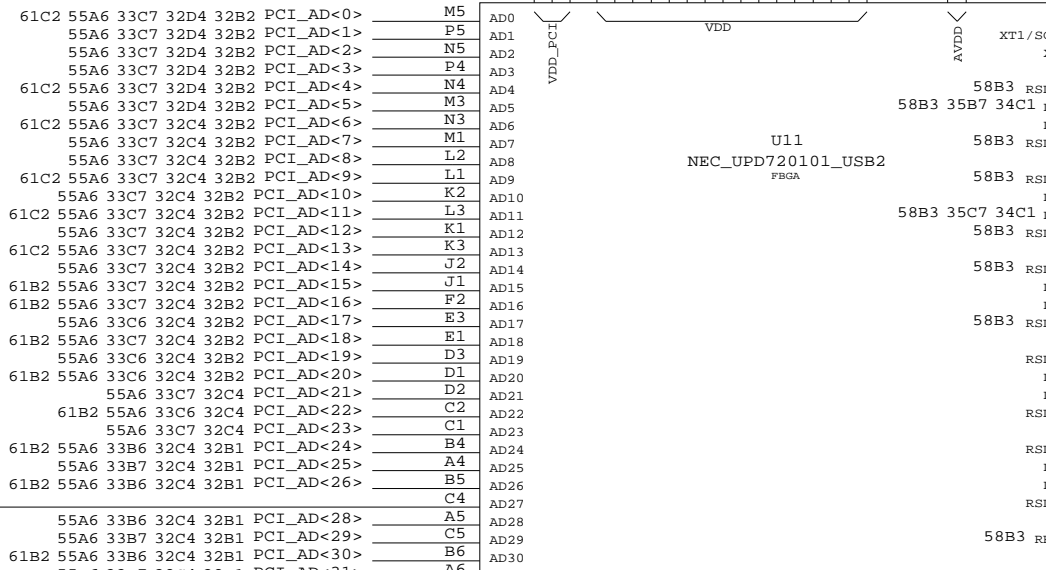
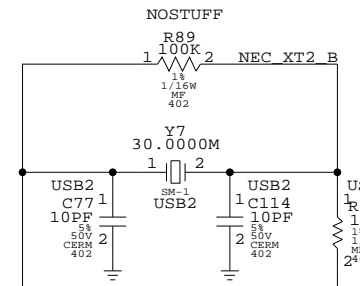
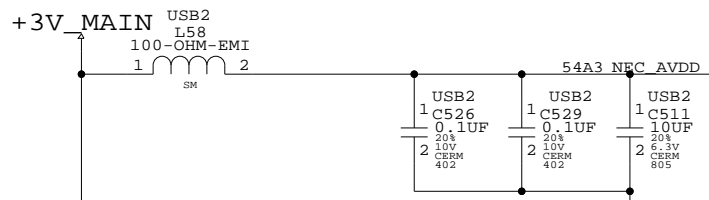
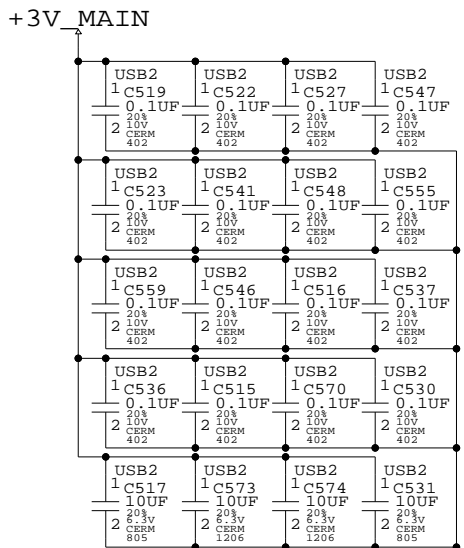
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# USB2 CONTROLLER

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	D	051-6423	D
SCALE	NONE	SHT	34 OF 74

D

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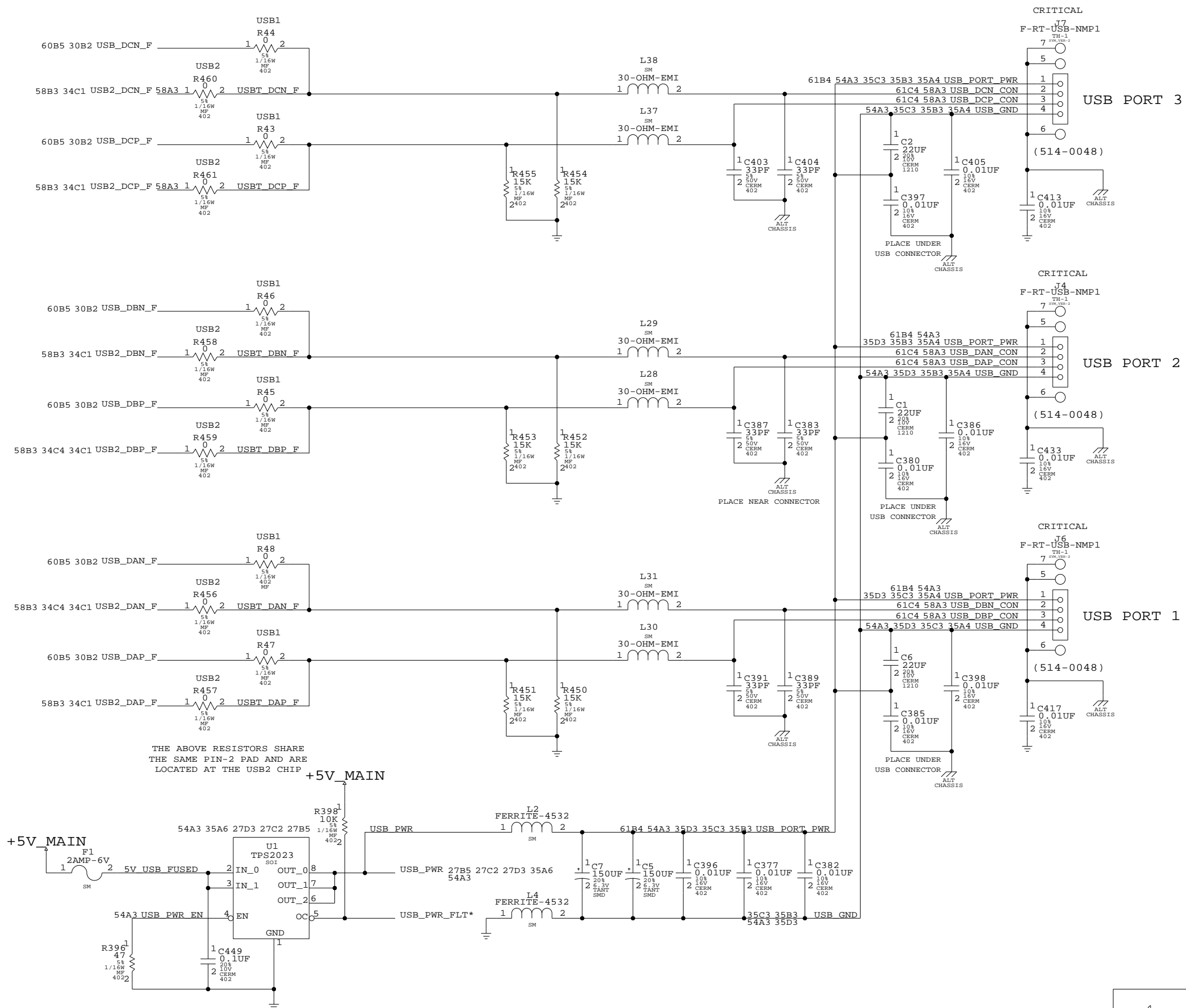
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### USB CONNS & PWR

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	D	051-6423	D
SCALE	SHT		OF
NONE	35		74

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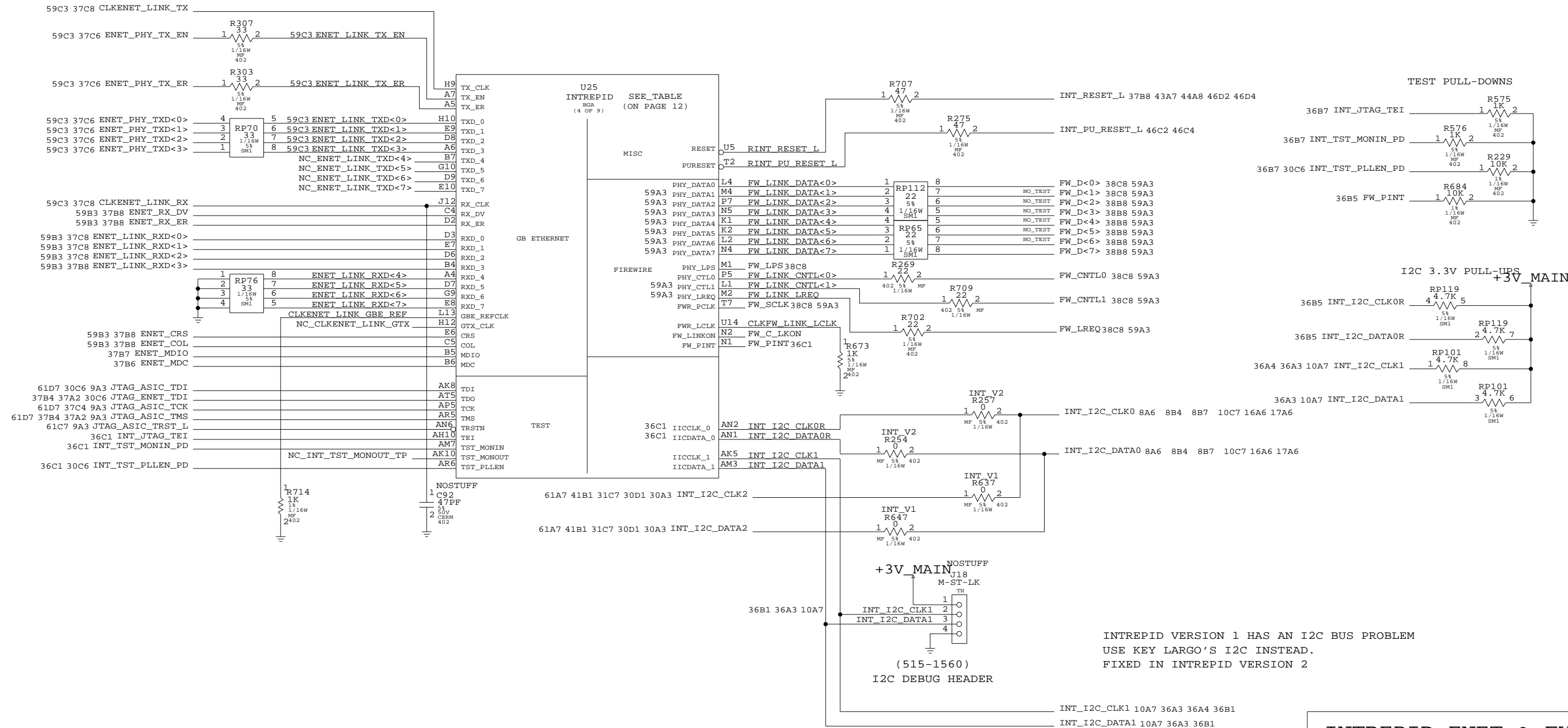
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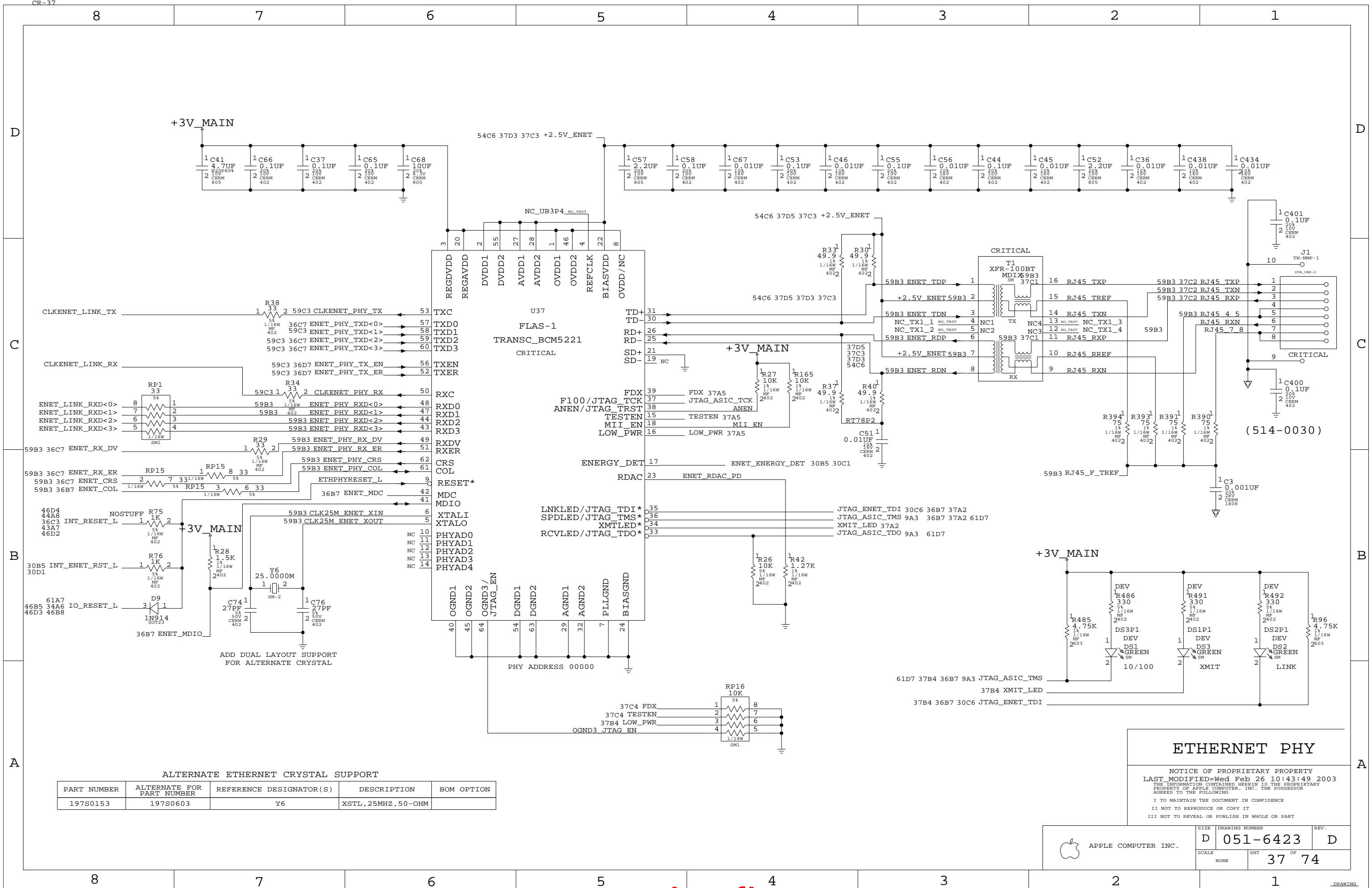
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	NONE	051-6423	D
SCALE		SHT	OF
NONE		36	74



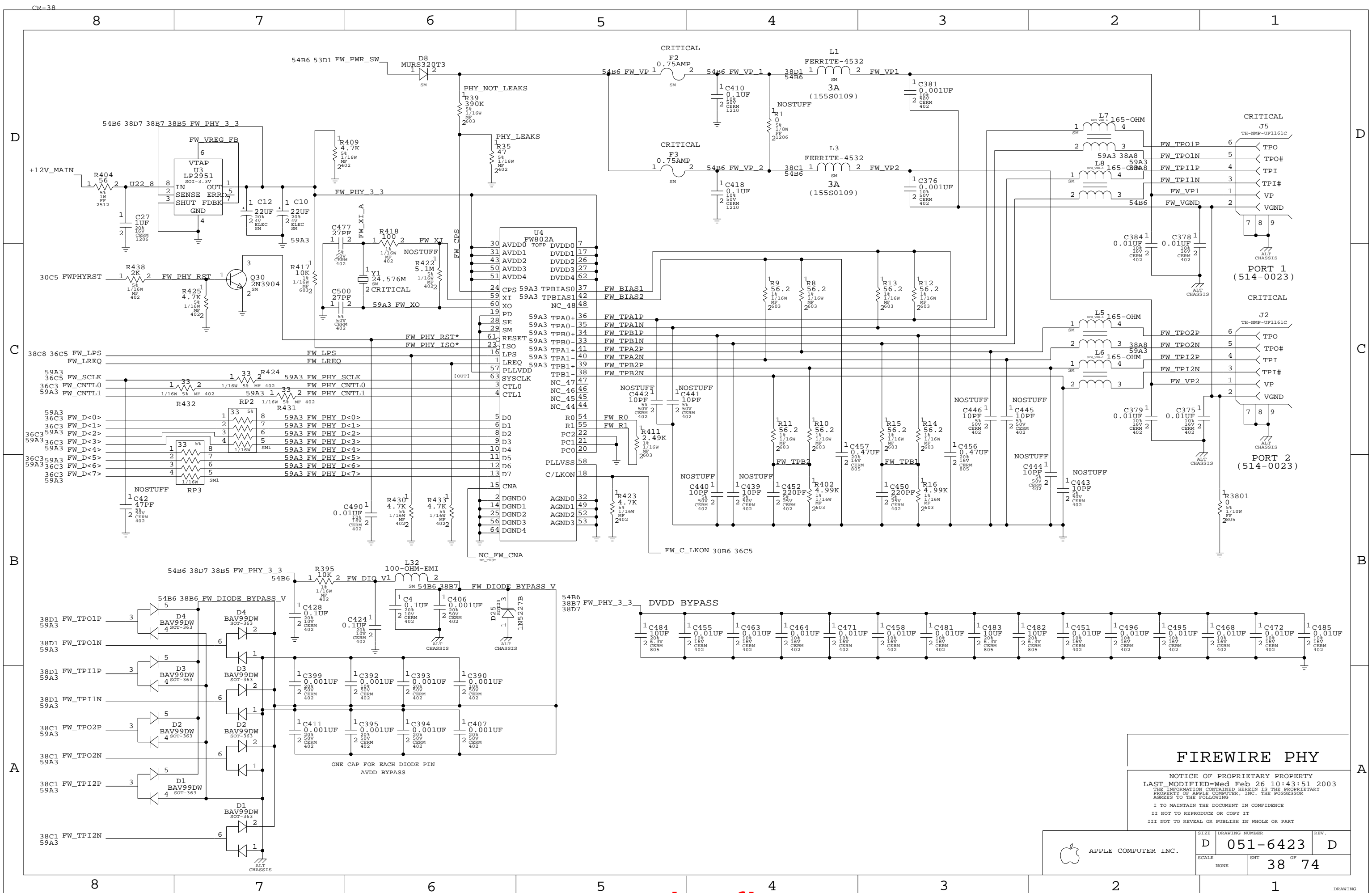
ALTERNATE ETHERNET CRYSTAL SUPPORT

PART NUMBER	ALTERNATE FOR PART NUMBER	REFERENCE DESIGNATOR(S)	DESCRIPTION	BOM OPTION
197S0153	197S0603	Y6	XSTL, 25MHZ, 50-OHM	

ETHERNET PHY

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APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-6423	REV. D
	SCALE NONE	SHT 37 OF 74	



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U25  
INTREPID  
BCA  
(5 OF 9)  
SEE\_TABLE  
(ON PAGE 12)

UATA100

CARDSLOT

54A6 CS\_WAIT

IDE

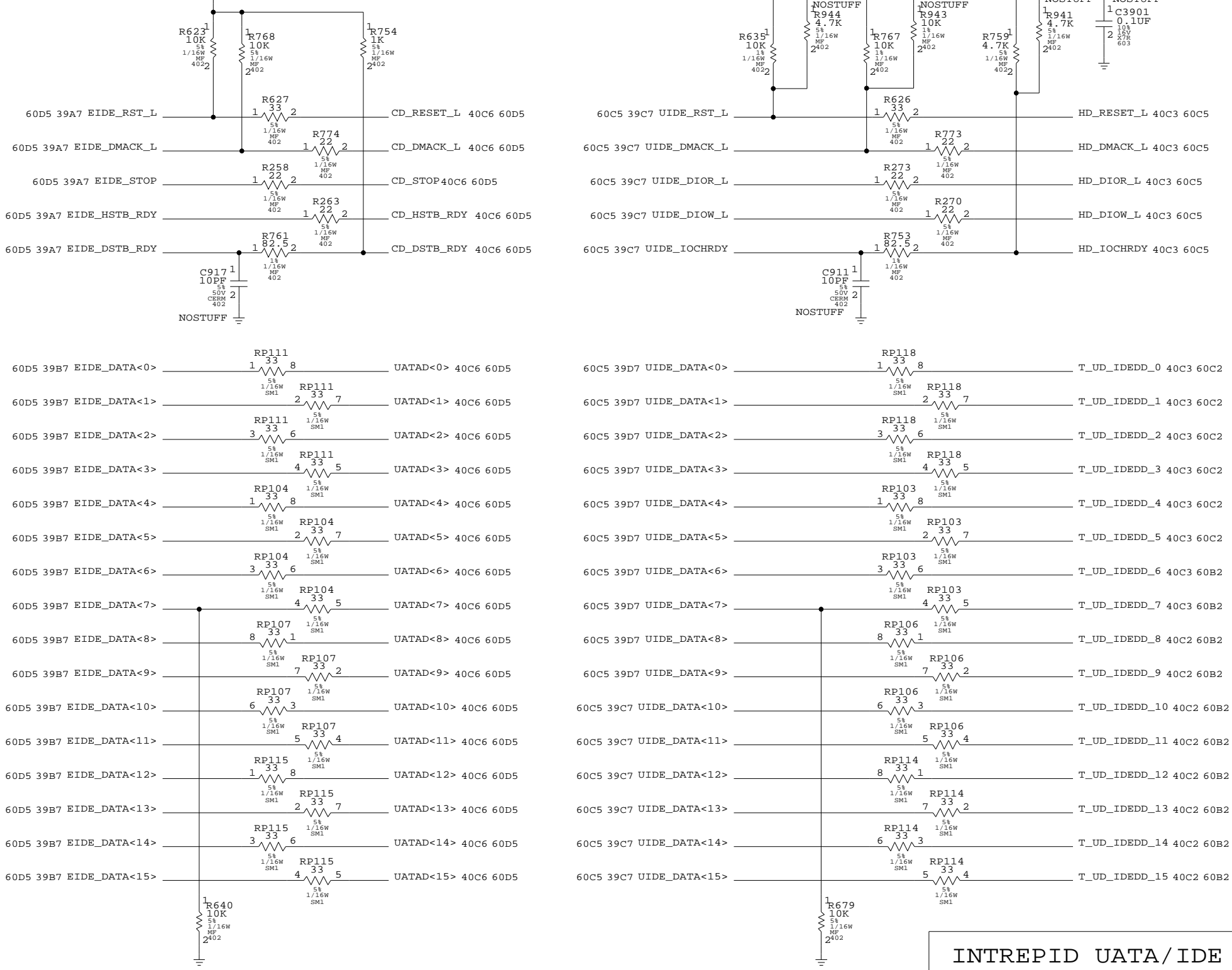
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ATA_D1	T1	UIDE_DATA<1>	39C3 60C5
ATA_D2	U1	UIDE_DATA<2>	39C3 60C5
ATA_D3	U2	UIDE_DATA<3>	39C3 60C5
ATA_D4	V4	UIDE_DATA<4>	39B3 60C5
ATA_D5	V2	UIDE_DATA<5>	39B3 60C5
ATA_D6	W1	UIDE_DATA<6>	39B3 60C5
ATA_D7	V1	UIDE_DATA<7>	39B3 60C5
ATA_D8	W2	UIDE_DATA<8>	39B3 60C5
ATA_D9	W8	UIDE_DATA<9>	39B3 60C5
ATA_D10	W4	UIDE_DATA<10>	39B3 60C5
ATA_D11	W5	UIDE_DATA<11>	39B3 60C5
ATA_D12	Y2	UIDE_DATA<12>	39B3 60C5
ATA_D13	Y1	UIDE_DATA<13>	39A3 60C5
ATA_D14	W7	UIDE_DATA<14>	39A3 60C5
ATA_D15	Y8	UIDE_DATA<15>	39A3 60C5
ATA_A0	Y5	UIDE_ADDR<0>	40B4 60B5
ATA_A1	AB1	UIDE_ADDR<1>	40B4 60B5
ATA_A2	Y7	UIDE_ADDR<2>	40A4 60B5
ATA_VREF	Y15	UIDE_REF	
ATA_RST	Y4	UIDE_RST_L	39D3 60C5
ATA_WR	AA1	UIDE_DIOW_L	39C3 60C5
ATA_RD	AA2	UIDE_DIOR_L	39D3 60C5
ATA_CHRDY	AA5	UIDE_IOCHRDY	39C3 60C5
ATA_CS0	AA4	UIDE_CS1FX_L	40B4 60B5
ATA_CS1	AB2	UIDE_CS3FX_L	40B4 60B5
ATA_DMACK	AC1	UIDE_DMACK_L	39D3 60C5
ATA_DMARQ	AC2	UIDE_DMARQ	40C4 60C5
ATA_INTRQ	AA8	UIDE_INTRQ	40C4 60C5
CS_CE1	AD1	NC_CSLOT_CE1_L	
CS_CE2	AB4	NC_CSLOT_CE2_L	
CS_IORD	AB5	NC_CSLOT_IORD_L	
CS_IOWR	AD2	NC_CSLOT_IOWR_L	
CS_OE	AC4	NC_CSLOT_OE_L	
CS_WE	AE1	NC_CSLOT_WE_L	
54A6 CS_WAIT	AE2	CSLOT_IOWAIT_L	
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IDEDD2	AF1	EIDE_DATA<2>	39C5 60D5
IDEDD3	AG1	EIDE_DATA<3>	39C5 60D5
IDEDD4	AF2	EIDE_DATA<4>	39B5 60D5
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IDEA0	AF5	EIDE_ADDR<0>	40B8 60C5
IDEA1	AE7	EIDE_ADDR<1>	40B8 60C5
IDEA2	AK1	EIDE_ADDR<2>	40A8 60C5
IDEA3	AG5	NC_CSLOT_ADDR<3>	
IDEA4	AH4	NC_CSLOT_ADDR<4>	
IDEA5	AL1	NC_CSLOT_ADDR<5>	
IDEA6	AK2	NC_CSLOT_ADDR<6>	
IDEA7	AH5	NC_CSLOT_ADDR<7>	
IDEA8	AF7	NC_CSLOT_ADDR<8>	
IDEA9	AG7	NC_CSLOT_ADDR<9>	
IDECHRDY	AK4	EIDE_DSTB_RDY	39C5 60D5
IDEC50	AB7	EIDE_CS1FX_L	40B8 60C5
IDEC51	AM1	EIDE_CS3FX_L	40B8 60C5
IDERST	AJ4	EIDE_RST_L	39D5 60D5
IDEWR	AM2	EIDE_STOP	39D5 60D5
IDERD	AL2	EIDE_HSTB_RDY	39C5 60D5
IDEDMACK	AG8	EIDE_DMACK_L	39D5 60D5
IDEDMARQ	AH7	EIDE_DMARQ	40C8 60D5
IDEINTRQ	AA7	EIDE_INTRQ	40C8 60C5

+3V\_MAIN

+5V\_SLEEP

+5V\_SLEEP

3\_6V\_SLEEP 53C1



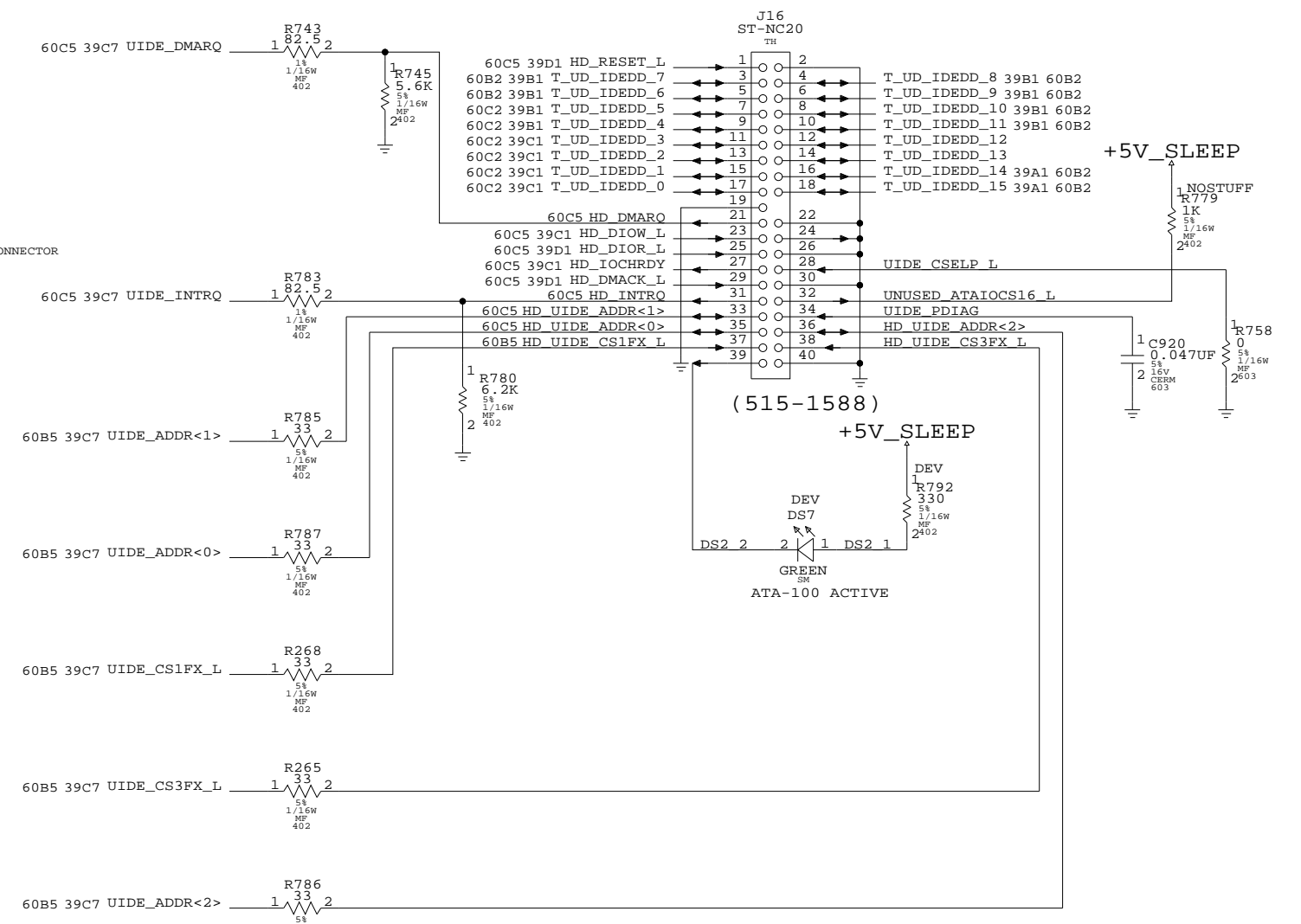
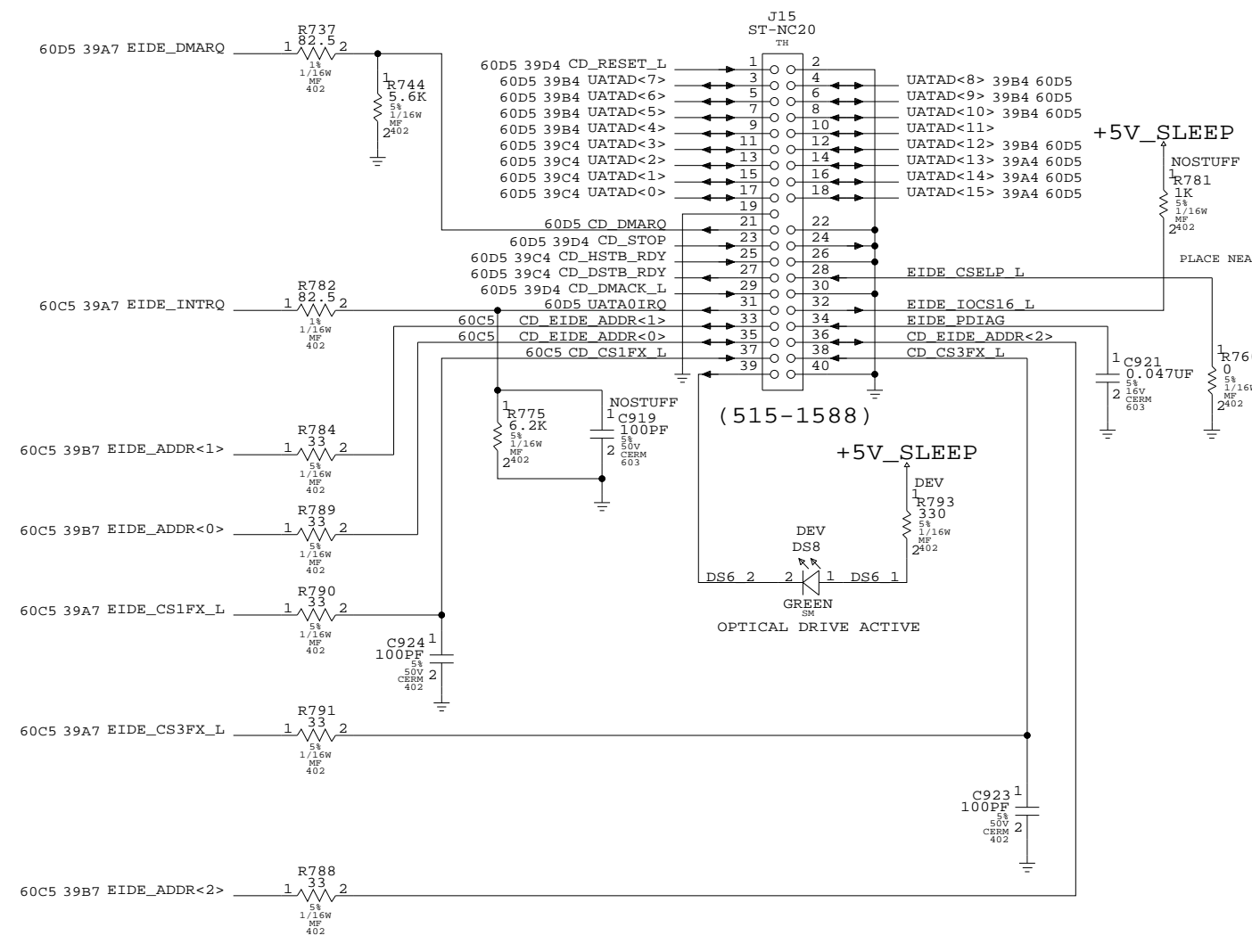
**INTREPID UATA/IDE**

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APPLE COMPUTER INC.		SCALE	DRAWING NUMBER	REV.
D		NONE	051-6423	D
		SHT	39	OF 74

### OPTICAL DRIVE INTERFACE

### ATA-100 INTERFACE

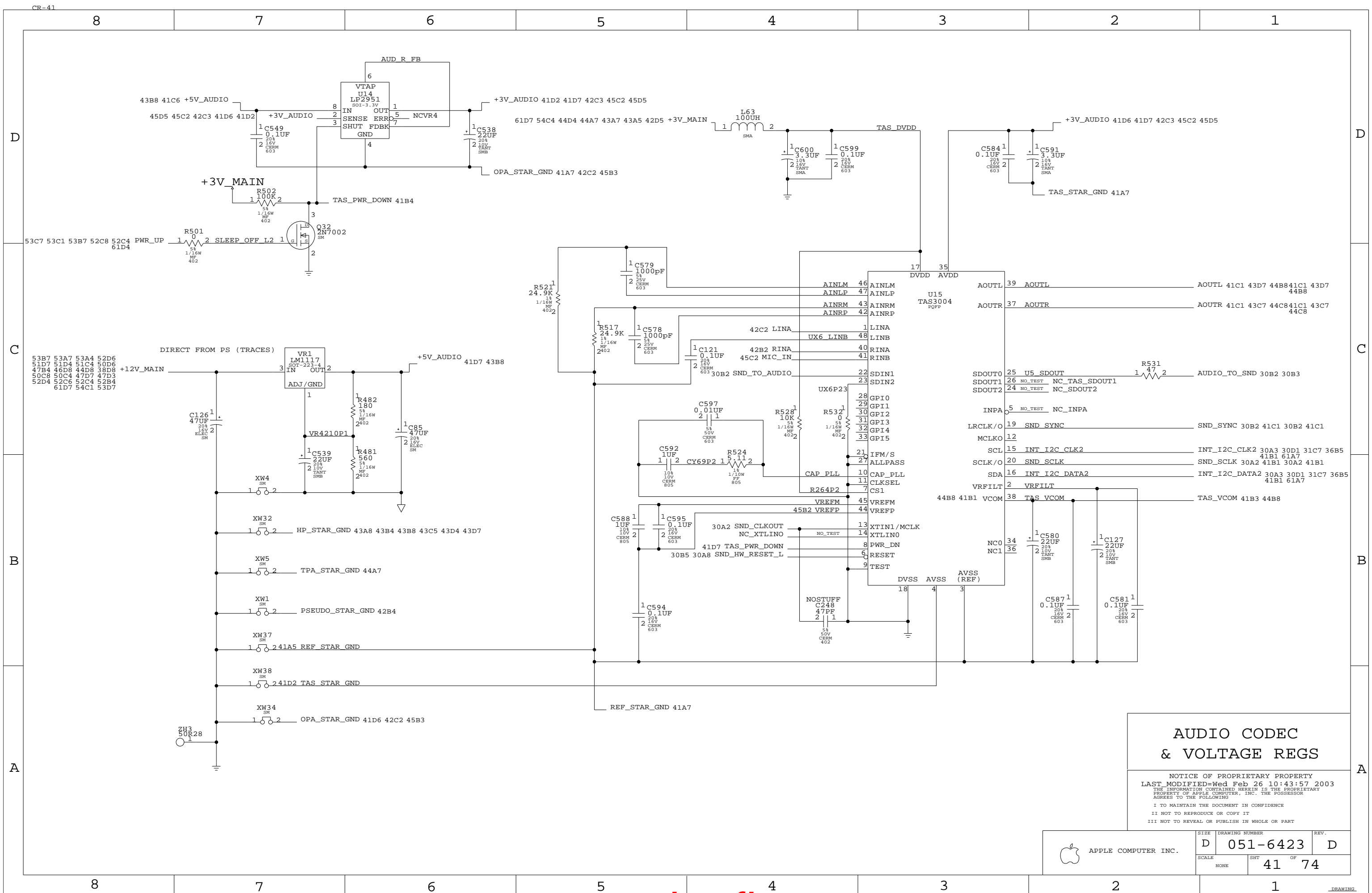


**CD/HD CONS**

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	SCALE	40	74





## AUDIO CODEC & VOLTAGE REGS

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APPLE COMPUTER INC.	SIZE <b>D</b>	DRAWING NUMBER <b>051-6423</b>	REV. <b>D</b>
	SCALE NONE	SHT <b>41</b>	OF <b>74</b>

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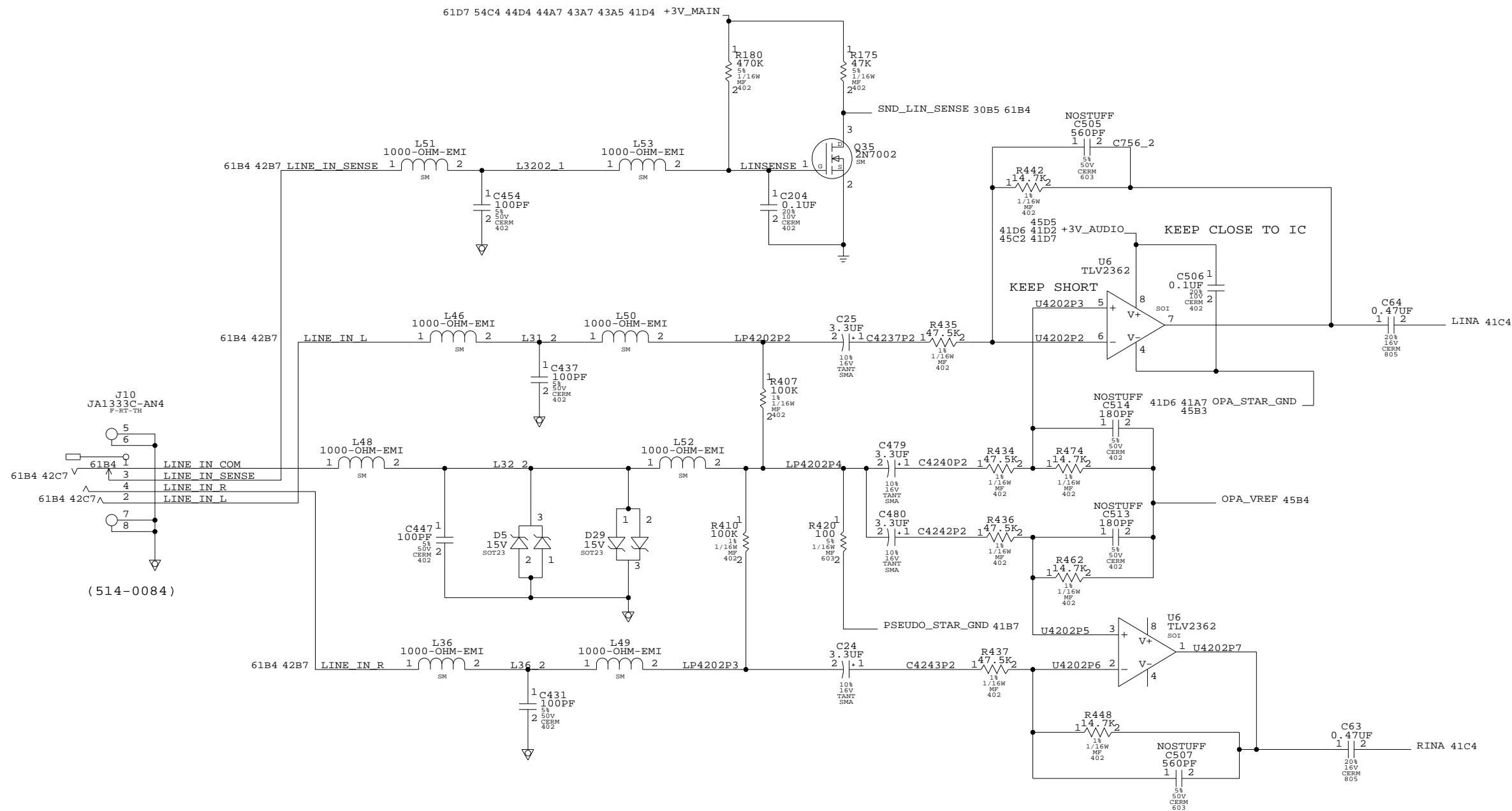
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# LINE IN BUFFER

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	NONE	051-6423	D
		SHT	OF
		42	74

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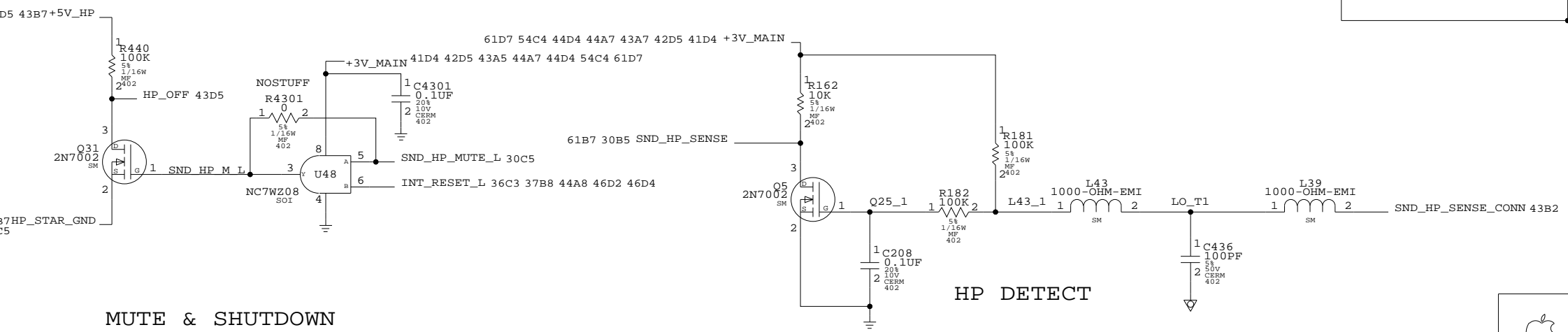
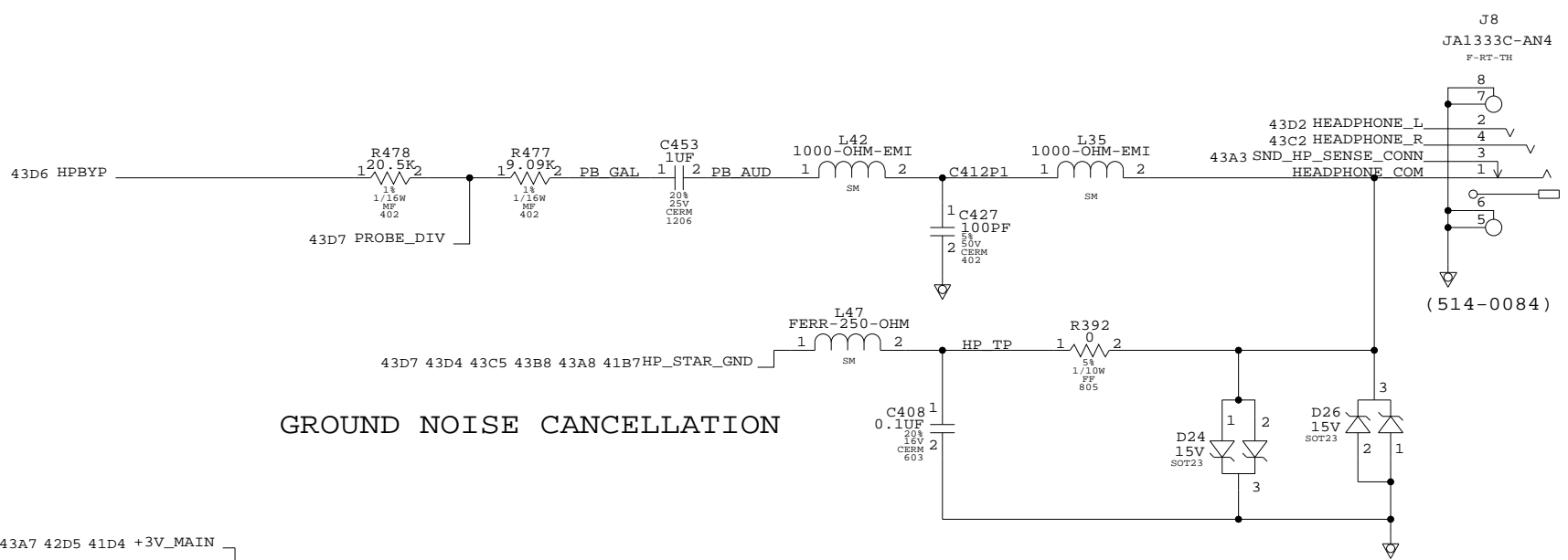
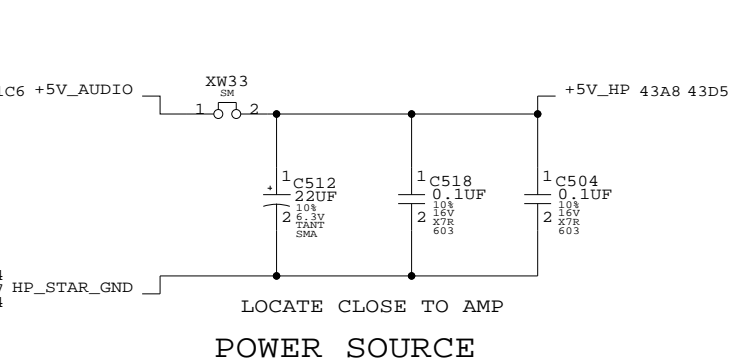
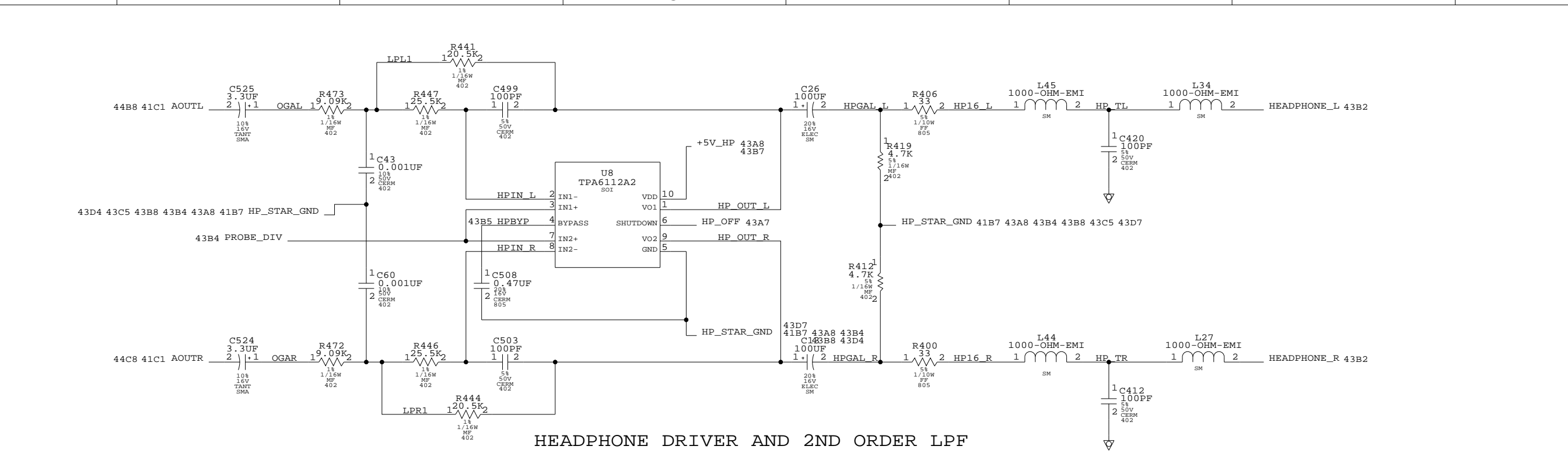
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**HEADPHONE OUT AMP**

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6423	D
SCALE	SHT		OF
NONE	43		74

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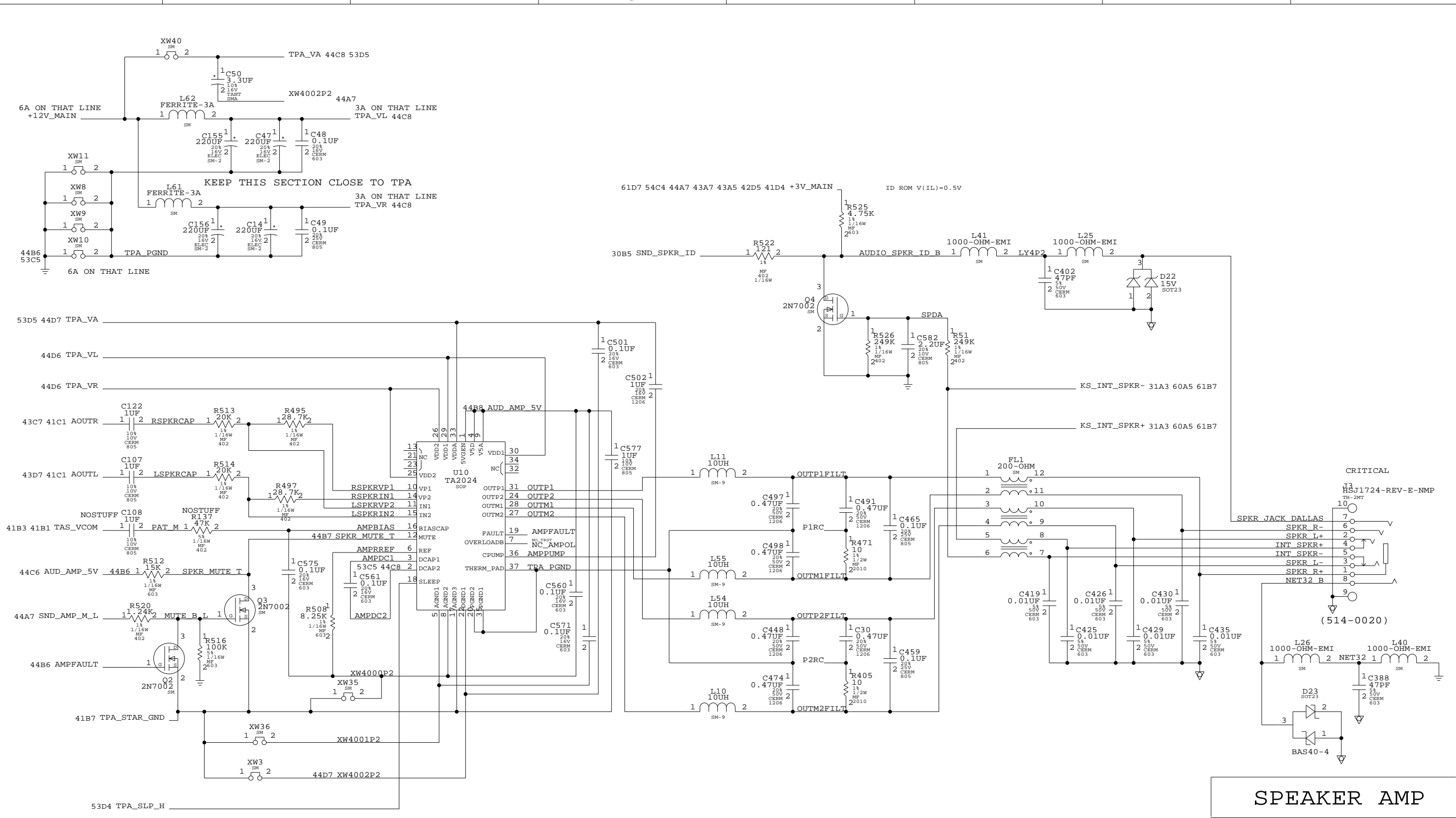
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# SPEAKER AMP

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	SIZE	DRAWING NUMBER	REV.
	D	051-6423	D
SCALE	SHT		OF
NONE	44		74

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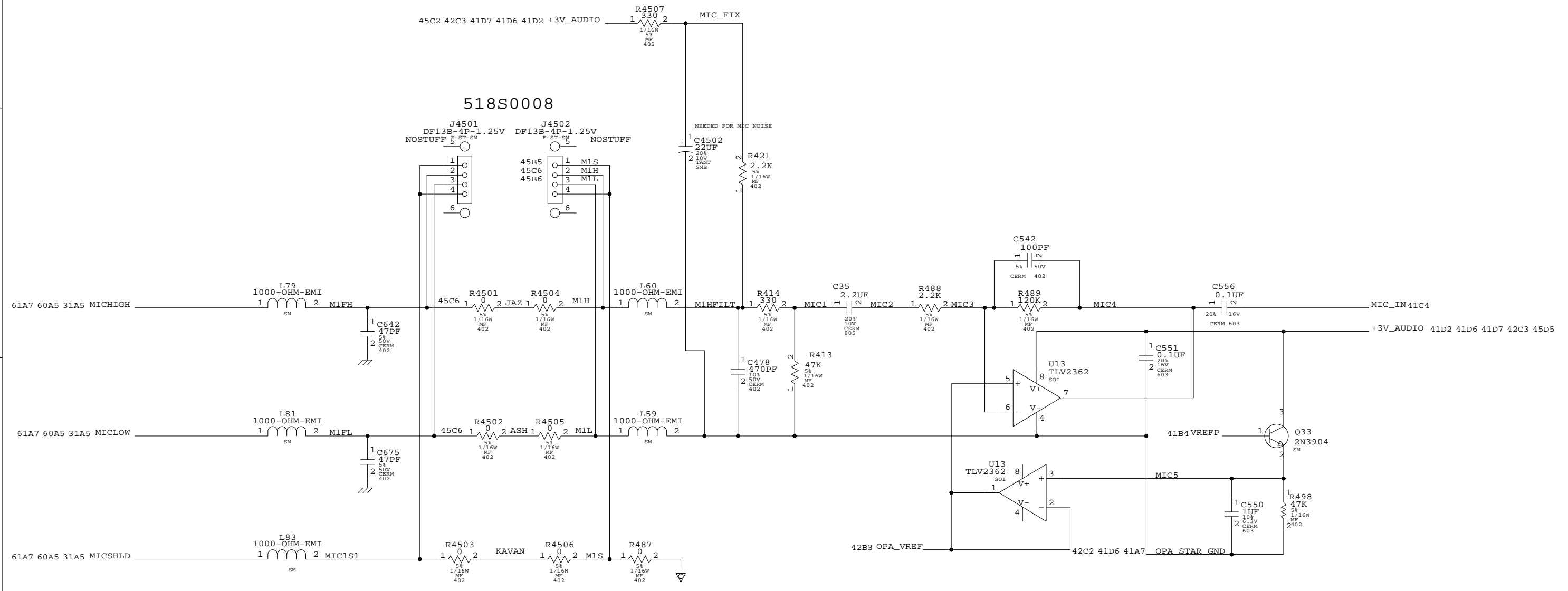
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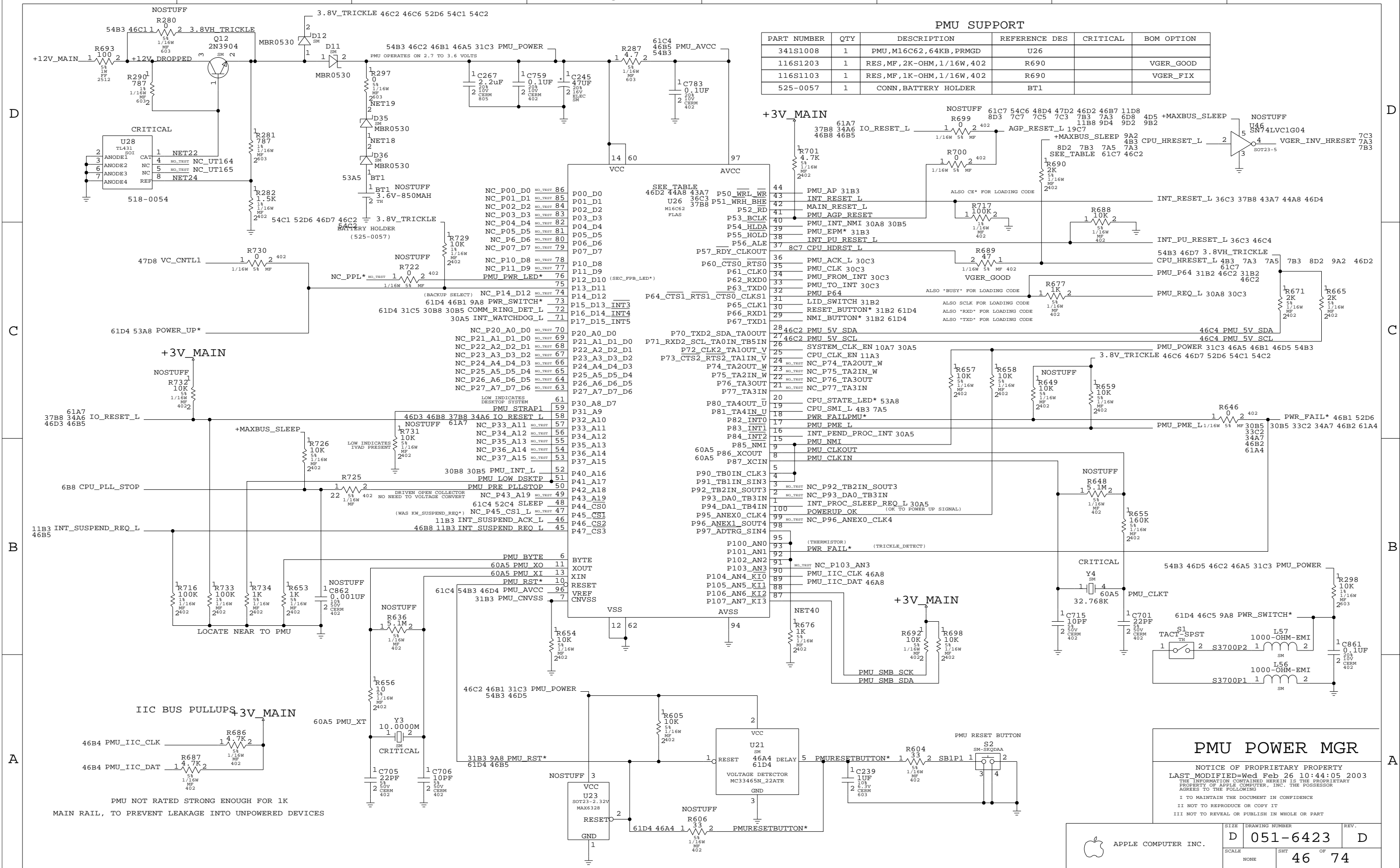
PLACE R4501, R4502 AND R4503 NEAR AUDIO

PLACE R4504, R4505 AND R4506 NEAR KITCHENSINK

# MIC PREAMP

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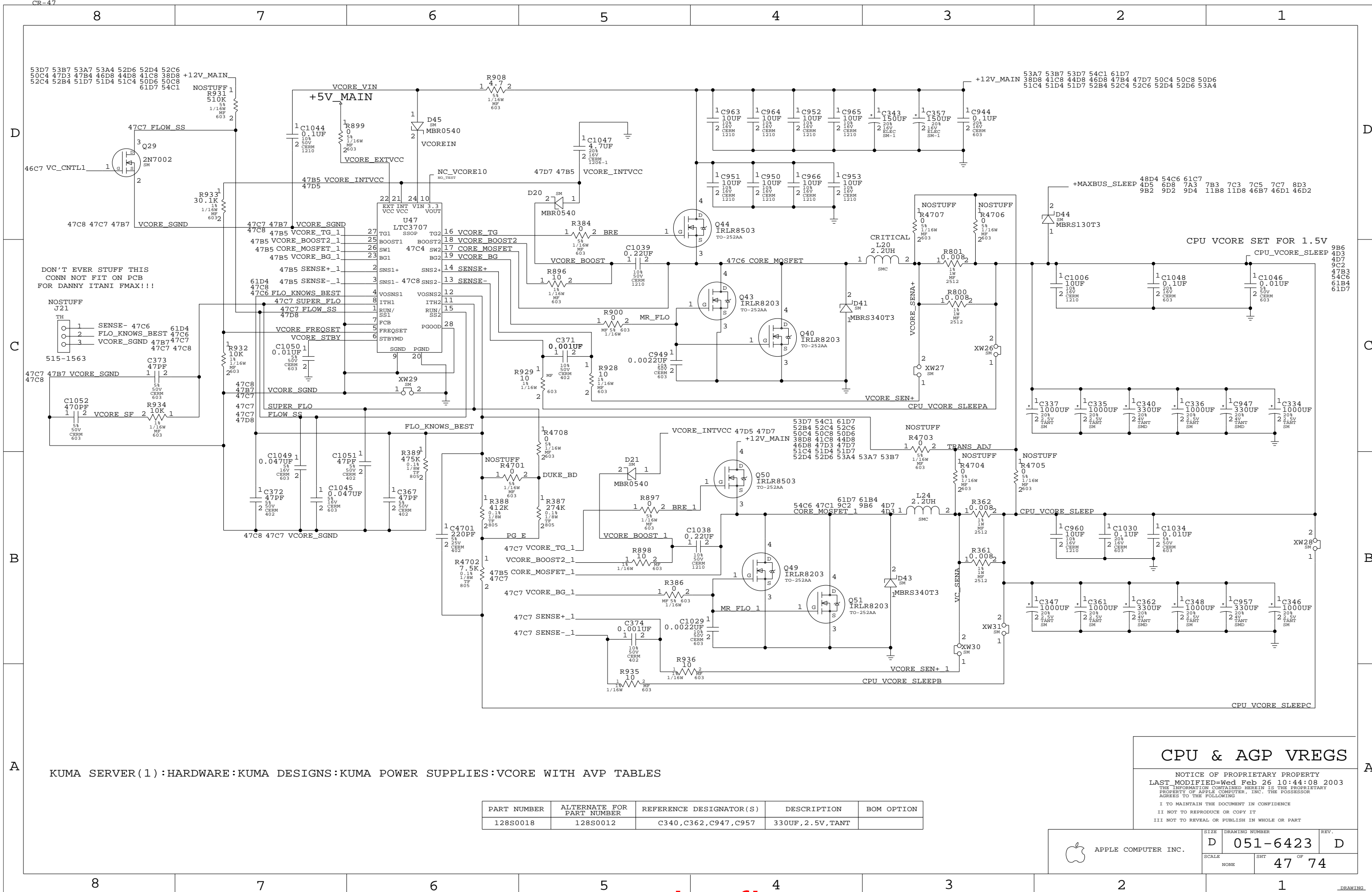
APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6423	D
SCALE	NONE	SHT	45 OF 74



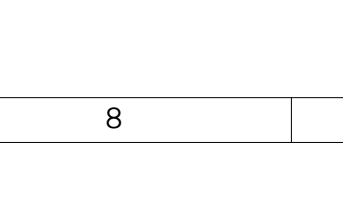
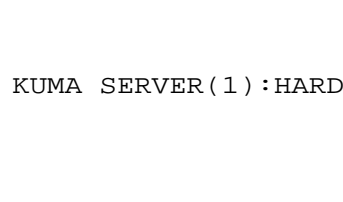
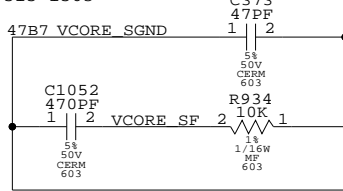
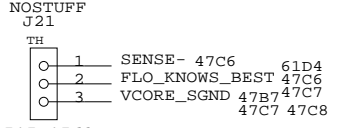
**PMU POWER MGR**

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DON'T EVER STUFF THIS CONN NOT FIT ON PCB FOR DANNY ITANI FMAX!!!



KUMA SERVER(1):HARDWARE:KUMA DESIGNS:KUMA POWER SUPPLIES:VCORE WITH AVP TABLES

PART NUMBER	ALTERNATE FOR PART NUMBER	REFERENCE DESIGNATOR(S)	DESCRIPTION	BOM OPTION
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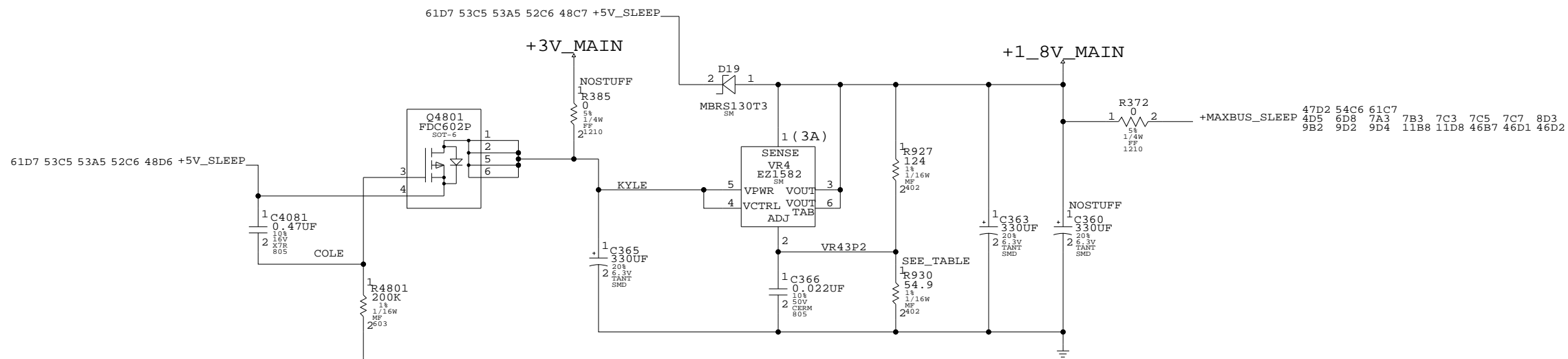
### CPU & AGP VREGS

NOTICE OF PROPRIETARY PROPERTY  
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APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6423	D
SCALE	SHT	REV.
NONE	47 OF 74	

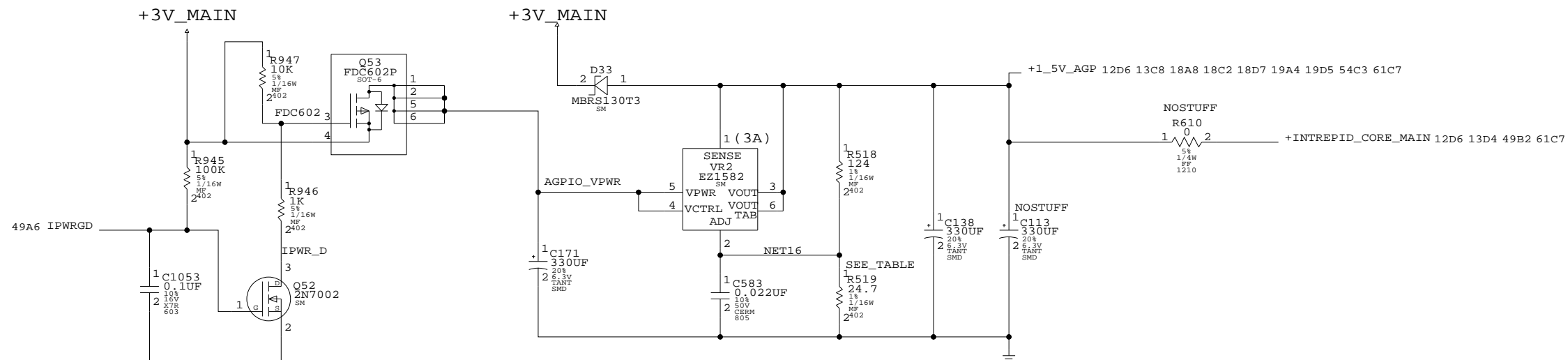
### INTREPID MAXBUS & CPU OVDD POWER CONVERTER (OFF DURING SLEEP)



MAXBUS I/O SUPPLY SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
114S2491	1	RES, FF, 24.9-OHM, 1%	R930		MAXIO_1'50V
114S3481	1	RES, FF, 34.8-OHM, 1%	R930		MAXIO_1'65V
114S4421	1	RES, FF, 44.2-OHM, 1%	R930		MAXIO_1'70V
114S5491	1	RES, FF, 54.9-OHM, 1%	R930		MAXIO_1'80V

### AGP I/O POWER CONVERTER



AGP I/O SUPPLY SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
114S2491	1	RES, FF, 24.9-OHM, 1%	R519		AGPIO_1'50V
114S3481	1	RES, FF, 34.8-OHM, 1%	R519		AGPIO_1'65V
114S4421	1	RES, FF, 44.2-OHM, 1%	R519		AGPIO_1'70V
114S5491	1	RES, FF, 54.9-OHM, 1%	R519		AGPIO_1'80V

### CPU & AGP VREGS

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	SIZE	DRAWING NUMBER	REV.
	NONE	051-6423	D
SCALE		SHT	OF
		48	74



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D

C

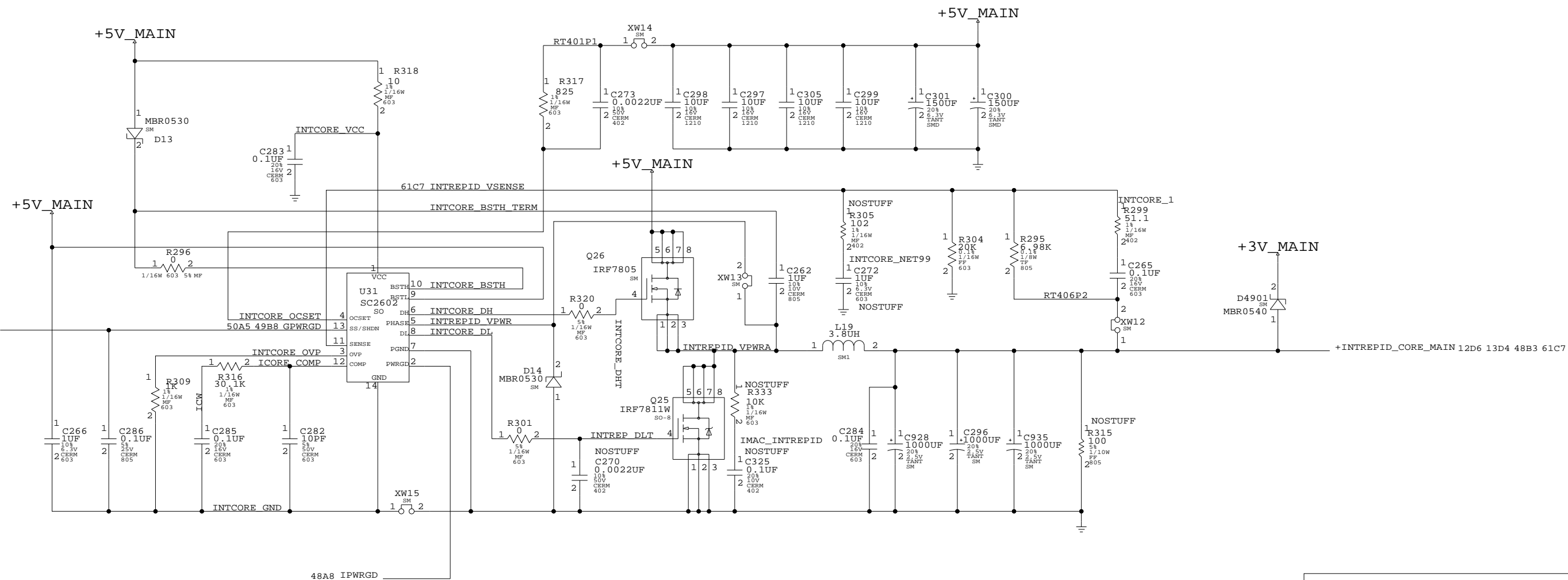
C

B

B

A

A



TABLES FOR INTREPID CORE RESISTOR VALUES TO VOLTAGES ARE LOCATED AT  
 KUMA SERVER(1):HARDWARE:KUMA DESIGNS;KUMA POWER SUPPLIES;ICORE R TOLERANCE

**INTREPID CORE**

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6423	D
SCALE	SHT		OF
NONE	49		74

D

D

C

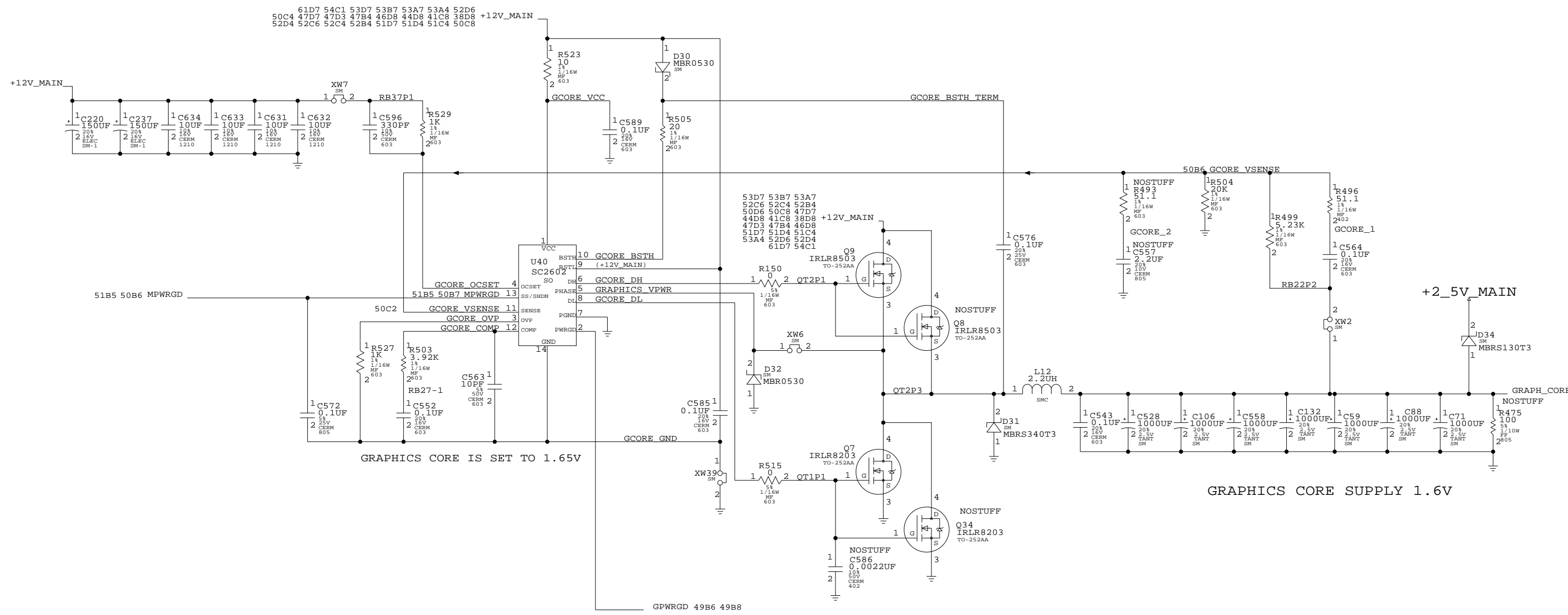
C

B

B

A

A



### GRAPHICS CORE

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SCALE	SHT	OF	
NONE	50	74	

D

D

C

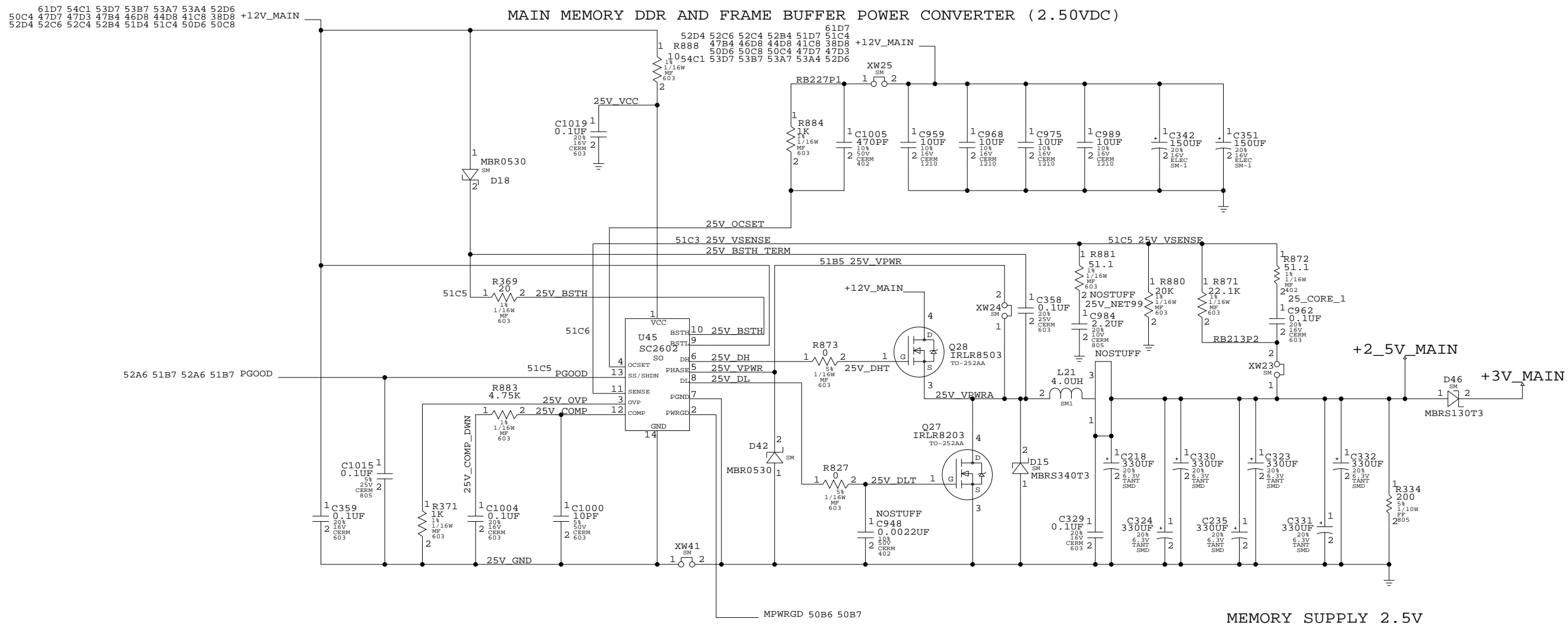
C

B

B

A

A



# MEMORY PS

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6423	D
SCALE	SHT		OF
NONE	51		74

D

D

C

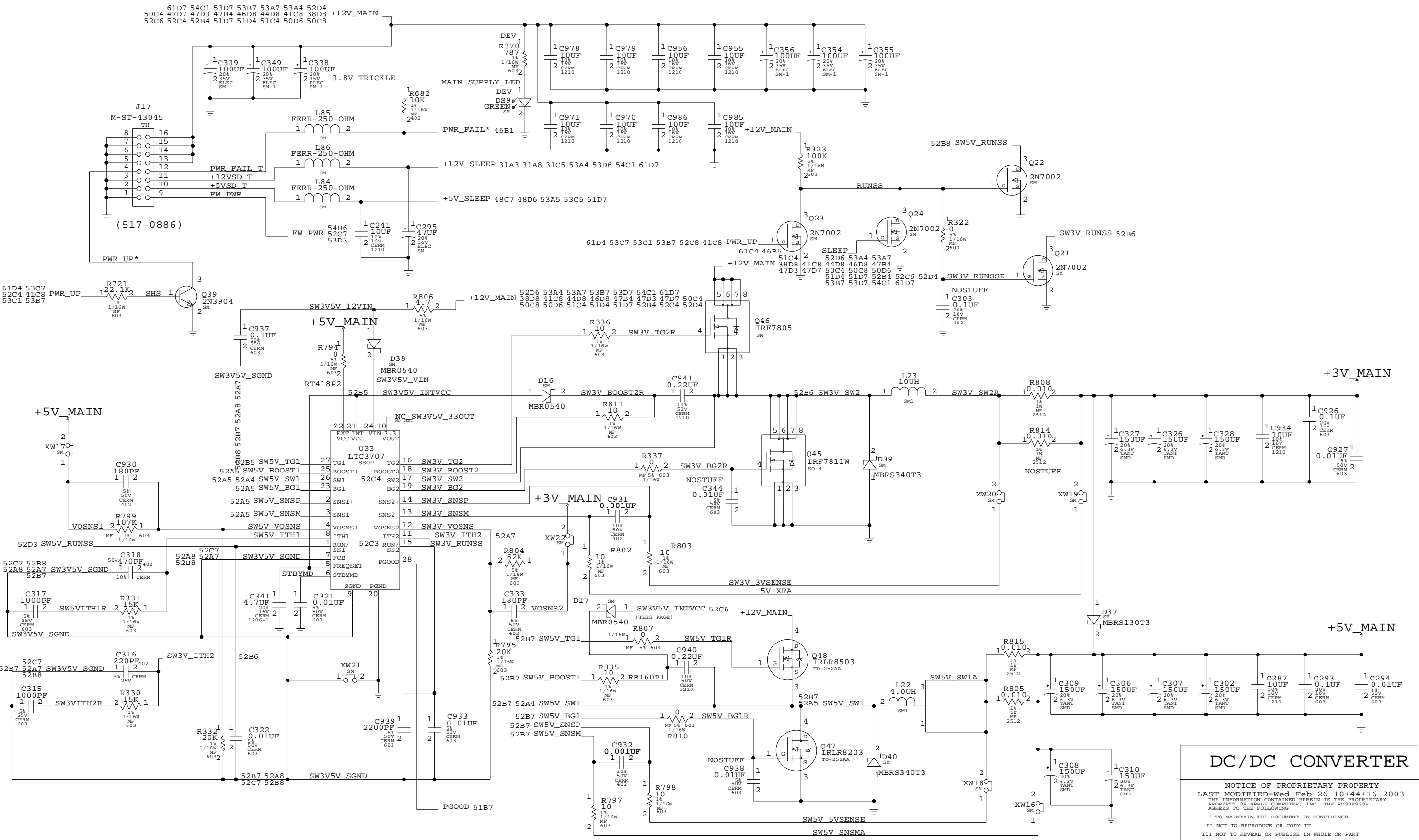
C

B

B

A

A



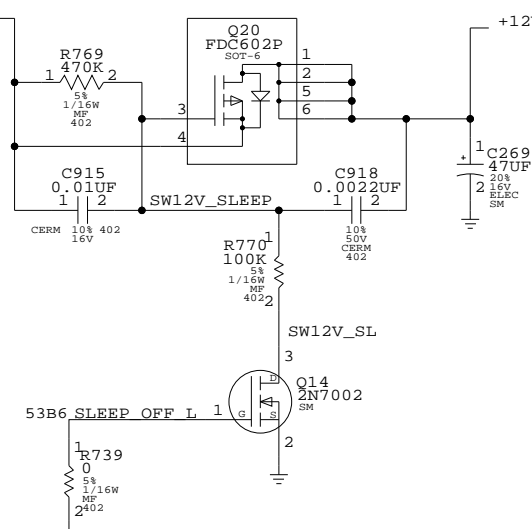
**DC/DC CONVERTER**

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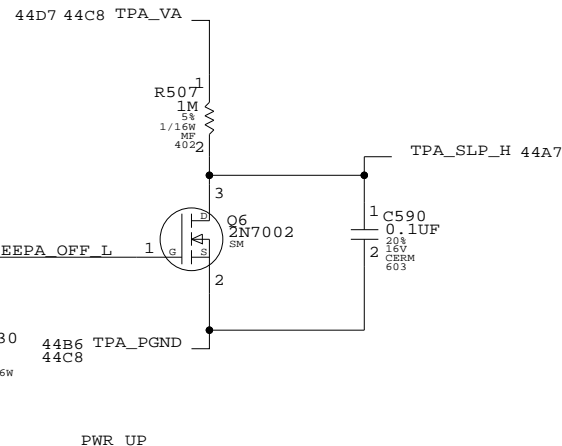
	SCALE	DRAWING NUMBER	REV.
	NONE	D 051-6423	D
		SHT	OF
		52	74

### +12V MAIN POWER SWITCH (OFF DURING SLEEP)

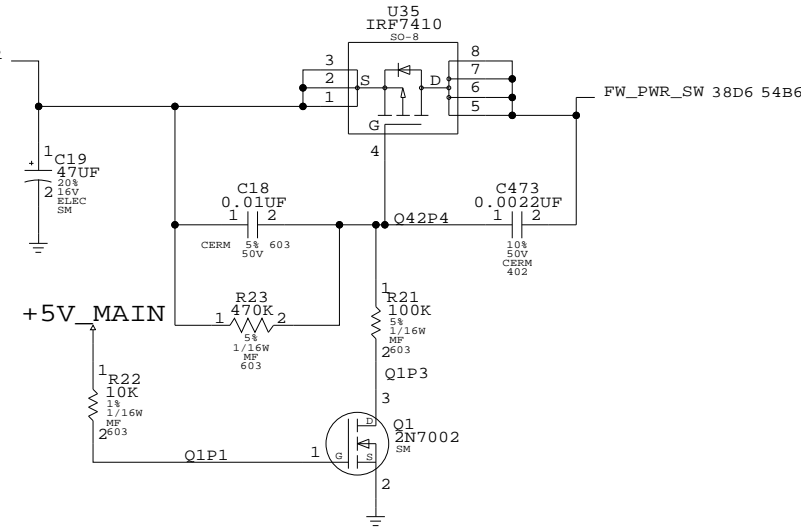
61D7 54C1 53B7  
52C4 52B4 51D7 51D4 51C4  
47B4 46D8 44D8 41C8 38D8  
50D6 50C8 50C4 47D7 47D3  
53A7 53A4 52D6 52D4 52C6



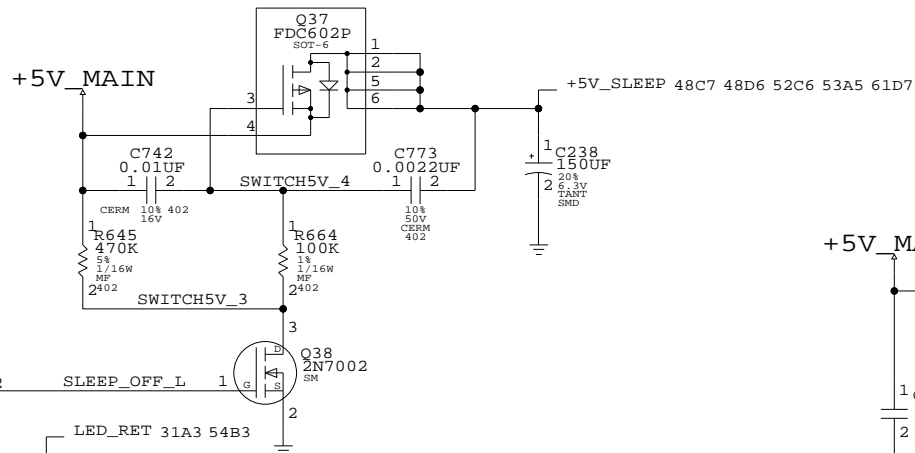
### AUDIO SLEEP SWITCH (OFF DURING SLEEP)



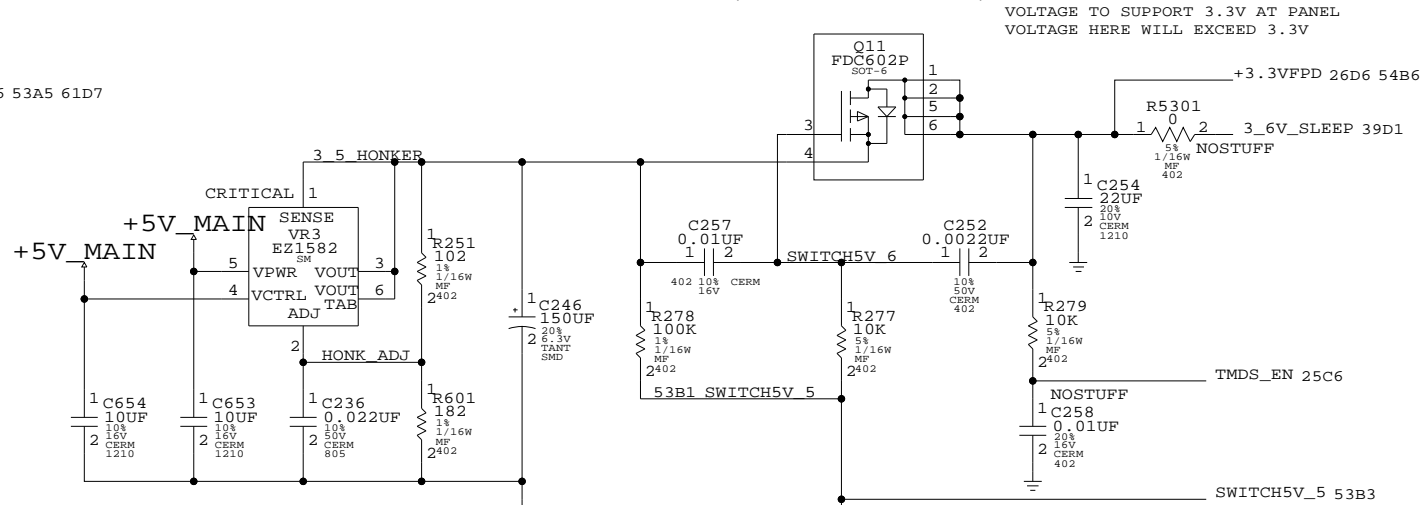
### FIREWIRE POWER SWITCH



### +5V POWER SWITCH (OFF DURING SLEEP)

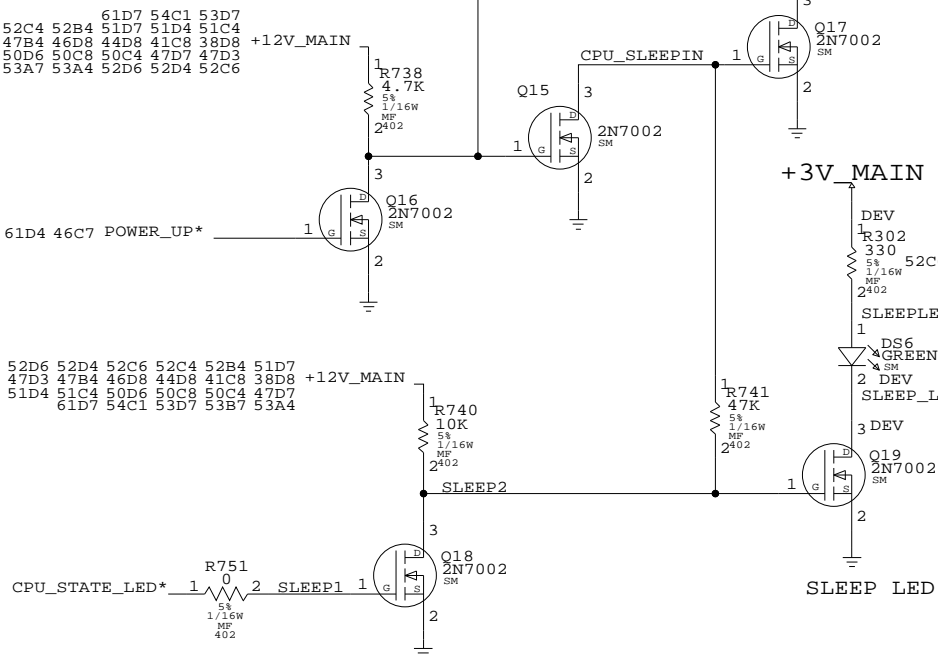


### TMDS POWER CONVERTER & SWITCH (OFF DURING SLEEP)



### +5V POWER, SLEEP & TESTPOINT LEDS (TESTPOINT LED IS USED TO FOR SERVICE AND IF NOT ILLUMINATED, TELLS USER ITS OK TO ADD MEMORY)

61D7 54C1 53D7  
52C4 52B4 51D7 51D4 51C4  
47B4 46D8 44D8 41C8 38D8  
50D6 50C8 50C4 47D7 47D3  
53A7 53A4 52D6 52D4 52C6



52D6 52D4 52C6 52C4 52B4 51D7  
47D3 47B4 46D8 44D8 41C8 38D8  
51D4 51C4 50D6 50C8 50C4 47D7  
61D7 54C1 53D7 53B7 53A4

CPU\_STATE\_LED\*  
SLEEP1

### +5V/+12V, AUDIO FW & TMDS PWR

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SIZE	DRAWING NUMBER	REV.
D	051-6423	D
SCALE	SHT	OF
NONE	53	74

CLOCK POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+2_5V_DDR_AVDD	10	2.5	20
+2_5V_DDR_VDD	10	2.5	20
+3V_INTSS_AVDD	10	3.3	20
+3V_INTSS_VDD	10	3.3	20
PCK2059_VDDI2C	10	2.5	20

10D4  
10D5  
10B7  
10B6  
10D4

CPU POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+MAXBUS_SLEEP	10	1.8	20
CPU_AVDD	10	1.85	20
CPU_VCORE_SLEEP	10	1.85	20

4D5 6D8 7A3 7B3 7C3 7C5 7C7 8D3 9B2 9D2 9D4  
4D3 11B8 11D8 46B7 46D1 46D2 47D2 48D4 61C7  
4D3 4D7 9B6 9C2 47B3 47C1 61B4 61D7

ETHERNET POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+2.5V_ENET	10	2.5	20

37C3 37D3 37D5

FIREWIRE POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
FW_DIO_V	10	3.3	20
FW_DIODE_BYPASS_V	10	3.3	20
FW_PWR	10	24	20
FW_PWR_SW	10	24	20
FW_PHY_3_3	10	3.3	20
FW_VGND	10	0	20
FW_VP	10	12	20
FW_VP1	10	12	20
FW_VP2	10	12	20
FW_VP_1	10	12	20
FW_VP_2	10	12	20

38B6  
38B6 38B7  
52C7 53D3  
38D6 53D1  
38B5 38B7 38D7  
38D1  
38D5  
38D1 38D3  
38C1 38D3  
38D4  
38D4

GRAPHICS POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+3.3VFPD	10	3.6	20
DAC2VDD	10	3.3	20
DACVDD	10	3.3	20
DDC_VCC_3	10	3.3	20
DDC_VCC_5	10	5	20
DDR_VREF	10	1.25	20
IFP0AVCC	10	3.8	20
IFP0VREF	10	3.8	20
INT_TMDS_3V	10	3.6	20
GPU_AGP_VREF	10	0.75	20
GPU_FB_VREF	10	1.25	20
GRAPH_CORE	10	1.6	20
NVPLLVD	10	3.3	20
SGRAVREF	10	1.25	20
SGRBVREF	10	1.25	20

26D6 53C1  
24C5  
24C4  
26A5 26B3 61B7  
27C4 61B7  
16B2 16D1 16D8 17D8  
25A6 25B4  
25B4  
26B3 26C3 26C4 26D5 61C7  
19A3 19A8  
20C8  
19D5 25C4 25C6 50B1  
24D5  
22A3 22C4 22C8  
23A3 23C4 23C8

GPU_50PULLUP	1.5	OFFPA9A5
GPU_50PULLDWN	0	OFFPA9A5
GPU_TMODE	0	OFFPA9A5

GPU_XTALSSIN	0	OFFPA9B4
VIPCLK	0	OFFPA9D4

CSLOT_IOWAIT_L	3.3	OFFPA9B7
EIDE_CSELP_L	0	OFFPA9C6
EIDE_IOCS16_L	5	OFFPA9C6
UIDE_CSELP_L	0	OFFPA9C2
UNUSED_ATAIOCS16_L	5	OFFPA9C2

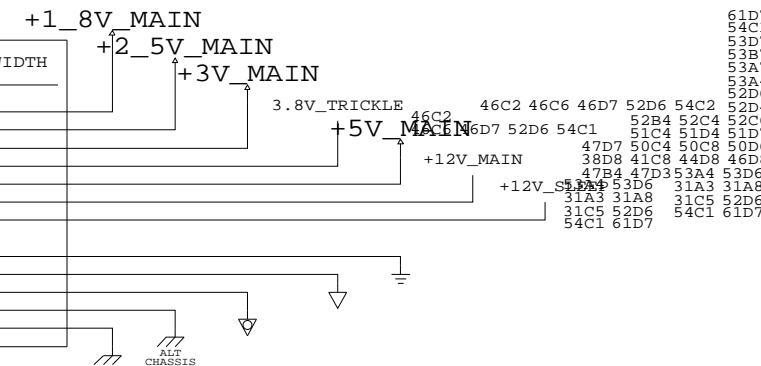
INTREPID POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+1_5V_INTREPID_PLL	10	1.5	20
+1_5V_INTREPID_PLL1	10	1.5	20
+1_5V_INTREPID_PLL2	10	1.5	20
+1_5V_INTREPID_PLL3	10	1.5	20
+1_5V_INTREPID_PLL4	10	1.5	20
+1_5V_INTREPID_PLL5	10	1.5	20
+1_5V_INTREPID_PLL6	10	1.5	20
+1_5V_INTREPID_PLL7	10	1.5	20
+1_5V_INTREPID_PLL8	10	1.5	20
+1_5V_AGP	10	1.5	20
INT_AGP_VREF	10	0.75	20

11D3 18D6 30D6 32D5  
30C4  
30D4  
30D4  
30D4  
18D5  
32D4  
11D2  
30D4  
12D6 13C8 18A8 18C2 18D7 19A4 19D5 48B4 61C7  
18A7 18C6

MAIN POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+1_8V_MAIN	10	1.8	20
+2_5V_MAIN	10	2.5	20
+3V_MAIN	10	3.3	20
3.8V_TRICKLE	10	3.8	20
+5V_MAIN	10	5	20
+12V_MAIN	10	12	20
+12V_SLEEP	10	12	20
GND	10	0	20
AGND	10	0	20
ANALOGGND	10	0	20
ALTCGND	10	0	20
CHGND	10	0	20



PMU POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
3.8VH_TRICKLE	10	3.8	20
PMU_AVCC	10	3.5	20
PMU_POWER	10	3.5	20

46C1 46D7  
46B5 46D4 61C4  
31C3 46A5 46B1 46C2 46D5

SYSTEM POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+12VSD_FILT	10	12	20
FAN_12V_FILT	10	12	20
KSSVSD	10	5	20
LED_5V	10	5	20
LED_5V_FILT	10	5	20
LED_RET	10	0	20
LED_RET_FILT	10	0	20

31A5  
31A5 61C7  
31A5 61A7  
31A8  
31A5 61A7  
31A3 53B6  
31A5 61A7

USB POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+3V_INTREPID_USB	10	3.3	20
NEC_AVDD	10	3.3	20
USB_GND	10	0	20
USB_PORT_PWR	10	5	20
USB_PWR	10	5	20
USB_PWR_EN	10	0	20

30C4  
34D5  
35A4 35B3 35C3 35D3  
35A4 35B3 35C3 35D3  
61B4 27B5 27C2 27D3  
35A6

POWER CONSTRAINTS

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	SCALE	NONE	SHT	54	OF	74

SIG_NAME	RATSNEST_SCHEDULE MATCHED_DELAY	MAX_VIAS	DELAY_RULE	STUB_LENGTH	MIN_NECK_WIDTH NET_SPACING_TYPE	MAX_EXPOSED_LENGTH	NO_TEST	FUNC_TEST	PULSE_PARAM
15C5 15B6 15B3 15A6 14D8 14C8 14B8 MEM_DATA<0..63>	MEM_GROUP0:::150	8	:::1300		3				167 MHZ
17D6 17D4 17C6 17C4 15C8 RAM_DATA_A<0..63>	RAM_GROUP0_A:::180	8	:::1800		3				167 MHZ
15C7 15C4 15B7 15B4 15B2 15A6 15A2 RAM_DATA_B<0..63>	RAM_GROUP0_B:::180	2	:::2400		3				167 MHZ
17B6 17B4 17A6 17A4 16D6 16C6 16B6 MEM_DQS<0..7>	MEM_GROUP0:::180	3	:::1300		3				167 MHZ
16A6 15D7 15D4 15C7 15C4 15B6 15B2 RAM_DQS_A<0..7>	RAM_GROUP0_A:::180	3	:::1700		3				167 MHZ
15C7 15C4 15B7 15B4 15B2 15A6 15A2 RAM_DQS_B<0..7>	RAM_GROUP0_B:::180	2	:::2400		3				167 MHZ
17D6 17C6 17B6 17A6 16D6 16C6 16B6 MEM_DQM<0..7>	MEM_GROUP0:::180	3	:::1300		3				167 MHZ
15C7 15C4 15B7 15B4 15B2 15A6 15A2 RAM_DQM_A<0..7>	RAM_GROUP0_A:::180	3	:::1800		3				167 MHZ
17D4 17C4 17B4 17A4 14C6 14C2 14B2 14A2 RAM_DQM_B<0..7>	RAM_GROUP0_B:::180	2	:::2400		3				167 MHZ
14D6 14D3 14D2 14C3 14C2 14B3 14A3 MEM_ADDR<0..12>	MEM_ADDR:::200	3	:::600						
16B4 14D3 14D1 14C3 14C1 14B3 14A3 RAM_ADDR<0..12>	RAM_ADDR:::1300	4	:::3500	200					
17C6 17C4 17B6 17B4 16B6 14D6 14B3 MEM_BA<0..1>	MEM_ADDR:::1300	3	:::600						
17B6 16B6 16B4 14B3 RAM_BA<0..1>	RAM_ADDR:::1300	4	:::4000	200					
14C6 14C2 14B2 14A2 MEM_CS_L<0..3>	MEM_ADDR:::200	3	:::600		10 MIL SPACING				
16B6 16B4 14C1 14B1 RAM_CS_L<0..1>	RAM_CS_GROUP0:::400	3	:::2000:3500		10 MIL SPACING				
17B4 14B1 14A1 RAM_CS_L<2..3>	RAM_CS_GROUP1:::350	2	:::2000:3500		10 MIL SPACING				
14C6 14A3 MEM_RAS_L	MEM_ADDR:::200	3	:::600						
14C6 14A3 MEM_CAS_L	MEM_ADDR:::200	3	:::600						
14C6 14A3 MEM_WE_L	MEM_ADDR:::280	3	:::600						
17B6 16B4 14A3 RAM_CAS_L	RAM_ADDR:::2000	4	:::4000	200					
17B4 16B4 14A3 RAM_RAS_L	RAM_ADDR:::2000	4	:::4000	200					
17B6 16B6 14A3 RAM_WE_L	RAM_ADDR:::2000	4	:::4000	200					
14C6 14C2 14B6 14B2 14A2 MEM_CKE<0..3>	MEM_ADDR:::200	3	:::600		10 MIL SPACING				
16B6 16B4 14B1 14A1 RAM_CKE<0..1>	RAM_CS_GROUP0:::400	3	:::2500		10 MIL SPACING				
17C6 17C4 14C1 14B1 RAM_CKE<2..3>	RAM_CS_GROUP1:::350	2	:::2500		10 MIL SPACING				
14D5 14B6 MEM_MUXSEL_H<0..1>		3	:::1000						
14D5 14B6 MEM_MUXSEL_L<0..1>		3	:::1000						167 MHZ
15C5 15A3 14D4 MUX_SEL_H		4	:::2000	200					167 MHZ
15C8 15A6 14D4 MUX_SEL_L		4	:::2000	200					167 MHZ
14C5 14B6 SYSCLK_DDRCLK_A0_UF		3	:::500:850		8 MIL SPACING	270			167 MHZ
14C5 14B6 SYSCLK_DDRCLK_A0_L_UF		3	:::500:850		8 MIL SPACING	270			167 MHZ
16D6 14C4 10C2 SYSCLK_DDRCLK_A0_UF	SYSCLK_DDRCLKA0:::100	3	:::2600	200	8 MIL SPACING	270			167 MHZ
16D6 14C4 10C2 SYSCLK_DDRCLK_A0_L_UF	SYSCLK_DDRCLKA0:::100	3	:::2600	200	8 MIL SPACING	270			167 MHZ
14C5 14B6 SYSCLK_DDRCLK_A1_UF		3	:::500:850		8 MIL SPACING	270			167 MHZ
14B6 14B5 SYSCLK_DDRCLK_A1_L_UF		3	:::500:850		8 MIL SPACING	270			167 MHZ
16A4 14C4 10C2 SYSCLK_DDRCLK_A1_UF	SYSCLK_DDRCLKA1:::100	3	:::2600	200	8 MIL SPACING	270			167 MHZ
16A4 14B4 10C2 SYSCLK_DDRCLK_A1_L_UF	SYSCLK_DDRCLKA1:::100	3	:::2600	200	8 MIL SPACING	270			167 MHZ
14B6 14B5 SYSCLK_DDRCLK_A2_UF		3	:::500:850		8 MIL SPACING	270			167 MHZ
14B6 14B5 SYSCLK_DDRCLK_A2_L_UF		3	:::500:850		8 MIL SPACING	270			167 MHZ
14B4 10C5 SYSCLK_DDRCLK_A2_L		3	:::750		8 MIL SPACING	270			167 MHZ
14B6 14B5 SYSCLK_DDRCLK_B0_UF		3	:::500:850		8 MIL SPACING	270			167 MHZ
14B6 14B5 SYSCLK_DDRCLK_B0_L_UF		3	:::500:850		8 MIL SPACING	270			167 MHZ
17B4 14B4 10C2 SYSCLK_DDRCLK_B0_UF	SYSCLK_DDRCLKB0:::100	3	:::3500	200	8 MIL SPACING	270			167 MHZ
17B4 14B4 10C2 SYSCLK_DDRCLK_B0_L_UF	SYSCLK_DDRCLKB0:::100	3	:::3500	200	8 MIL SPACING	270			167 MHZ
14B6 14A5 SYSCLK_DDRCLK_B1_UF		3	:::500:850		8 MIL SPACING	270			167 MHZ
14B6 14A5 SYSCLK_DDRCLK_B1_L_UF		3	:::500:850		8 MIL SPACING	270			167 MHZ
17D6 14A4 10C2 SYSCLK_DDRCLK_B1_UF	SYSCLK_DDRCLKB1:::100	3	:::3500	200	8 MIL SPACING	270			167 MHZ
17D6 14A4 10C2 SYSCLK_DDRCLK_B1_L_UF	SYSCLK_DDRCLKB1:::100	3	:::3500	200	8 MIL SPACING	270			167 MHZ
14B6 14A5 SYSCLK_DDRCLK_B2_UF		3	:::500:850		8 MIL SPACING	270			167 MHZ
14B6 14A5 SYSCLK_DDRCLK_B2_L_UF		3	:::500:850		8 MIL SPACING	270			167 MHZ
17A6 14A4 10B2 SYSCLK_DDRCLK_B2_UF	SYSCLK_DDRCLKB2:::100	3	:::3500	200	8 MIL SPACING	270			167 MHZ
17A6 14A4 10B2 SYSCLK_DDRCLK_B2_L_UF	SYSCLK_DDRCLKB2:::100	3	:::3500	200	8 MIL SPACING	270			167 MHZ
10C5 DDR_CLK_VDD_2		3	:::1000			400			167 MHZ
10C3 DDRCLK_A0_UF		3	:::400:500		8 MIL SPACING	270			167 MHZ
10C3 DDRCLK_A1_UF		3	:::400:500		8 MIL SPACING	270			167 MHZ
10C3 DDRCLK_A2_UF		3	:::400:500		8 MIL SPACING	270			167 MHZ
10C3 DDRCLK_B0_UF		3	:::400:500		8 MIL SPACING	270			167 MHZ
10C3 DDRCLK_B1_UF		3	:::400:500		8 MIL SPACING	270			167 MHZ
10C3 DDRCLK_B2_UF		3	:::400:500		8 MIL SPACING	270			167 MHZ
10B3 DDRCLK_A0_L_UF		3	:::400:500		8 MIL SPACING	270			167 MHZ
10B3 DDRCLK_A1_L_UF		8	:::400:500		8 MIL SPACING	270			167 MHZ
10B3 DDRCLK_A2_L_UF		3	:::400:500		8 MIL SPACING	270			167 MHZ
10B3 DDRCLK_B0_L_UF		3	:::400:500		8 MIL SPACING	270			167 MHZ
10B3 DDRCLK_B1_L_UF		3	:::400:500		8 MIL SPACING	270			167 MHZ
10B3 DDRCLK_B2_L_UF		3	:::400:500		8 MIL SPACING	270			167 MHZ
10B3 DDR_FBO		3	:::500		8 MIL SPACING	270			167 MHZ
10B3 DDR_FBO_L		3	:::500		8 MIL SPACING	270			167 MHZ
10B5 DDR_FBIN		8	:::1550:1615		8 MIL SPACING	400			167 MHZ
10B5 DDR_FBIN_L		8	:::1600:1650		8 MIL SPACING	270			167 MHZ
30A6 10A7 INT_REF_CLK_OUT		3	:::1500		10 MIL SPACING	270			49.92 MHZ
INT_REF_CLK_OUT_UF		8	:::1500		10 MIL SPACING	270			49.92 MHZ
10A6 CLKGEN_OUT_1		3	:::500		10 MIL SPACING	270			66.56 MHZ
61C2 30A6 10A5 INT_REF_CLK_IN_PD		8	:::2500		10 MIL SPACING	270			66.56 MHZ
33C7 33C6 33B7 33B6 32D4 32C4 32B2 32B1 PCI_AD<31..0>	MIN_DAISSY_CHAIN	6	:::6000:8000	500					33 MHZ
61B2 34C6 34B7 34B6 61A4 34B6 33B7 32C5 PCI_CBE<3..0>	MIN_DAISSY_CHAIN	6	:::6000:8000	500					33 MHZ
61A4 34B6 33B7 32C5 32B8 PCI_FRAME_L	MIN_DAISSY_CHAIN	7	:::6000:8000	500					33 MHZ

DIGITAL SIGNAL CONSTRAINTS

SIGNAL CONSTRAINTS

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APPLE COMPUTER INC.	SIZE	D	DRAWING NUMBER	051-6423	REV.	D
	SCALE	NONE	SHT	55	OF	74

D

D

C

C

B

B

A

A

SIG_NAME	RATSNEST_SCHEDULE	MATCHED_DELAY	MAX_VIAS	DELAY_RULE	STUB_LENGTH	NET_SPACING_TYPE	MAX_EXPOSED_LENGTH	DIFFERENTIAL_PAIR	FUNC_TEST	PULSE_PARAM
34B6 33B7 32C5 32B8 PCI_IRDY_L	MIN_DAI	6	6	:6000:8000	500					33 MHZ
61A4 34B6 33B7 32C5 32B8 PCI_TRDY_L	MIN_DAI	6	6	:6000:8000	500					33 MHZ
61A4 34B6 33B7 32C5 32A8 PCI_DEVSEL_L	MIN_DAI	6	6	:6000:8000	500					33 MHZ
61A4 34B6 33B7 32C5 32A8 PCI_STOP_L	MIN_DAI	6	6	:6000:8000	500					33 MHZ
61A4 34B6 33B7 32C5 PCI_PAR	MIN_DAI	6	6	:6000:8000	500					33 MHZ
32C5 CLK33M_PCI_SLOTB_UF		3	3	:600:1000	200		450			33 MHZ
32C5 CLK33M_PCI_SLOTC_UF		3	3	:600:1200	200		450			33 MHZ
32C5 CLK33M_PCI_SLOTD_UF		3	3	:600:1000	200		450			33 MHZ
61A4 33C2 32D7 CLK33M_PCI_SLOTB		5	5	:3000:4000	200	10 MIL SPACING	450			33 MHZ
32C5 INT_PCI_FB_OUT		4	4	:::1000	200		450			33 MHZ
32C7 PCI_FBO_PLUS2		4	4	:::200	200		450			33 MHZ
32C8 PCI_FB_PLUS4		4	4	:1900:2000	200		450			33 MHZ
32C7 PCI_FBI_PLUS2		4	4	:1900:2000	200		450			33 MHZ
32C7 PCI_FBI_EQUAL		4	4	:2000:3000	200		450			33 MHZ
32C7 PCI_FB_PLUS6		4	4	:5900:6000	200		450			33 MHZ
32C5 INT_PCI_FB_IN		4	4	:::1080	200		450			33 MHZ
33C7 33C6 33C3 33C2 33B7 33B6 33B3 33B2 PCIT_AD<31..0>		3	3	:::1000						33 MHZ
61C2 61B2 61A4 33C3 33B6 33B2 PCIT_CBE<31..0>		3	3	:::1000						33 MHZ
33C2 33B6 PCIT_FRAME_L		3	3	:::1000						33 MHZ
61B4 33C3 33B6 PCIT_IRDY_L		3	3	:::1000						33 MHZ
33C2 33B6 PCIT_TRDY_L		3	3	:::1000						33 MHZ
33C2 33B6 PCIT_DEVSEL_L		3	3	:::1000						33 MHZ
33C2 33B6 PCIT_STOP_L		3	3	:::1000						33 MHZ
33C2 33B6 PCIT_PAR		3	3	:::1000						33 MHZ
19D8 19C8 18C4 AGP_AD<0..15>	AGP_GROUP0:::280	5	5	:::4500						266 MHZ
19C8 18B4 AGP_CBE<0..1>	AGP_GROUP0:::330	5	5	:::4500						266 MHZ
19B8 18B3 18A4 AGP_AD_STB<0>	AGP_GROUP0:::330	4	4	:::4400	200	8 MIL SPACING	500	AGP_ADSTBDP0		133 MHZ
19B8 18D1 18A4 AGP_AD_STB_L<0>	AGP_GROUP0:::330	4	4	:::4400	200	8 MIL SPACING	500	AGP_ADSTBDP0		133 MHZ
19C8 18C4 18B4 AGP_AD<16..31>	AGP_GROUP0:::280	5	5	:::4500						266 MHZ
19C8 18B4 AGP_CBE<2..3>	AGP_GROUP0:::280	5	5	:::4500						266 MHZ
19B8 18B3 18A4 AGP_AD_STB<1>	AGP_GROUP0:::280	4	4	:::4400	200	8 MIL SPACING	500	AGP_ADSTBDP1		133 MHZ
19B8 18D1 18A4 AGP_AD_STB_L<1>	AGP_GROUP0:::330	4	4	:::4400	200	8 MIL SPACING	500	AGP_ADSTBDP1		133 MHZ
19B8 18C3 18B4 AGP_FRAME_L		5	5	:4000:4500						66 MHZ
19B8 18C3 18B4 AGP_IRDY_L		5	5	:4000:4500						66 MHZ
19B8 18B4 18B3 AGP_TRDY_L		5	5	:4000:4500						66 MHZ
19B8 18C3 18B4 AGP_DEVSEL_L		5	5	:4000:4500						66 MHZ
19B8 18B4 18B3 AGP_STOP_L		5	5	:4000:4500						66 MHZ
19B8 18B4 AGP_PAR		5	5	:4000:4500						66 MHZ
19A8 18C1 18B4 18B1 18A4 AGP_SBA<0..7>	AGP_GROUP99:::200	5	5	:::4500				AGP_SBSTBB		
19A8 18D1 18A4 AGP_SB_STB_L	AGP_GROUP99:::200	5	5	:::4500				AGP_SBSTBB		
19B7 18B1 18A4 AGP_ST<0..2>		5	5	:4500:5000						
19B8 18B3 18A4 AGP_PIPE_L		5	5	:4000:4500						
19B8 18B3 18A4 AGP_RBF_L		5	5	:4000:4500						
19B7 18C4 18C3 AGP_REQ_L		5	5	:4500:5000						
19B7 18C4 18C3 AGP_GNT_L		5	5	:4500:5000						
19B8 18B1 18A6 AGP_WBF_L		5	5	:4000:4500						
19A8 18D3 18D1 18C6 AGP_BUSY_L		5	5	:4500:5000						
19A8 18D3 18C6 STOP_AGP_L		5	5	:4500:5000						
19D7 19C7 GPU_AGP_AD<0..15>	GPU_AGP_GROUP0:::100	3	3	:::600						266 MHZ
19C7 GPU_AGP_CBE<0..1>	GPU_AGP_GROUP0:::100	3	3	:::600						266 MHZ
19B7 AGP_AD_STB_GPUUF<0>	GPU_AGP_STB0:::50	3	3	:::450		8 MIL SPACING	500	GPU_ADSTBDP0		133 MHZ
19B7 AGP_AD_STB_L_GPUUF<0>	GPU_AGP_STB0:::50	3	3	:::800		8 MIL SPACING	500	GPU_ADSTBDP0		133 MHZ
19C7 GPU_AGP_AD<16..31>	GPU_AGP_GROUP1:::100	3	3	:::600						266 MHZ
19C7 GPU_AGP_CBE<2..3>	GPU_AGP_GROUP1:::100	3	3	:::600						266 MHZ
19B7 AGP_AD_STB_GPUUF<1>	GPU_AGP_STB1:::50	3	3	:::800		8 MIL SPACING	500	GPU_ADSTBDP1		133 MHZ
19B7 AGP_AD_STB_L_GPUUF<1>	GPU_AGP_STB1:::50	3	3	:::800		8 MIL SPACING	500	GPU_ADSTBDP1		133 MHZ
19B7 GPU_AGP_FRAME_L		3	3	:300:600						66 MHZ
19B7 GPU_AGP_IRDY_L		3	3	:300:600						66 MHZ
19B7 GPU_AGP_TRDY_L		3	3	:300:600						66 MHZ
19B7 GPU_AGP_DEVSEL_L		3	3	:300:600						66 MHZ
19B7 GPU_AGP_STOP_L		3	3	:300:600						66 MHZ
19B7 GPU_AGP_PAR		3	3	:300:600						66 MHZ
19A7 GPU_AGP_SBA<0..7>	GPU_AGP_SBSTB:::50	3	3	:300:600				GPU_SBSTBB		
19A7 GPU_AGP_SB_STB_L	GPU_AGP_SBSTB:::50	3	3	:300:600				GPU_SBSTBB		
19B7 GPU_AGP_PIPE_L		3	3	:300:600						
19B7 GPU_AGP_RBF_L		3	3	:300:600						
18C6 CLK66M_GPU_UF		3	3	:1000:1100		10 MIL SPACING	250			66 MHZ
19C7 18D8 CLK66M_GPU_AGP		4	4	:3700:3900	200	10 MIL SPACING	250			66 MHZ
18C6 INT_AGP_FB_OUT		4	4	:1400:1500	200		250			66 MHZ
18B7 AGP_FBO_EQUAL		4	4	:900:1080	200		250			66 MHZ
18B8 AGP_FB_PLUS2		4	4	:1900:2000	200		250			66 MHZ
18C7 AGP_FBI_EQUAL		4	4	:::200	200		250			66 MHZ
18C6 INT_AGP_FB_IN		4	4	:::1200	200		250			66 MHZ
32C5 32A8 18D7 INT_ROM_OVERLAY_PU		3	3	:600:800		10 MIL SPACING	250			66 MHZ
61A7 58B3 18C7 11B4 9B4 INT_ANALYZER_CLK		2	2	:::2800		8 MIL SPACING	250			66 MHZ
34A6 32D7 CLK33M_PCI_SLOTD		4	4	:3000:3500	200	8 MIL SPACING	250			33 MHZ

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6423	D
SCALE	NONE	SHT	56 OF 74



DIGITAL SIGNALS

GROUP	SIG_NAME	MATCHED_DELAY	MAX VIAS	DELAY_RULE	STUB_LENGTH	NET_SPACING_TYPE	MAX EXPOSED LENGTH	PULSE PARAM	RATSNEST_SCHEDULE
	FBD<0..63>	GPU_FBDDATA_A:::225	4	:::800				300 MHZ	20E8 20F8 20G8 21C5 21C8 21D5 21D8
	FBDD<0..63>	RAM_FBDDATA_A:::300	4	:::1000				300 MHZ	21C4 21C7 21D4 21D7 22B1 22B5 22C1 22C5
	FBDQM<0..7>	GPU_FBDQM_A:::200	4	:::800				300 MHZ	20D8 20G3
	FBDDQM<0..7>	RAM_FBDQM_A:::200	4	:::1000				300 MHZ	20G2 22C2 22C6
	FBA<0..12>	GPU_FBADDR_A:::200	4	:::700				300 MHZ	20C8 20D8 20E3 20F3
	FBBA<0..11>	RAM_FBADDR_A:::530	4	:::2400	2350			300 MHZ	20E2 20F2 22C2 22C6 22D2 22D6
	FBABA<0..1>	GPU_FBADDR_A:::200	4	:::600				300 MHZ	20C8 20E3
	FBABA<0..1>	RAM_FBADDR_A:::530	5	:::2400	50			300 MHZ	20E2 22C2 22C6
	FBRAS L	GPU_FBCNTL_A:::200	4	:::400				300 MHZ	20C8 20G3
	FBRAS L	RAM_FBCNTL_A:::200	4	:::400				300 MHZ	20C8 20G3
	FBAWE L	GPU_FBCNTL_A:::200	4	:::400				300 MHZ	20C8 20F3
	FBAWE L	RAM_FBCNTL_A:::200	4	:::400				300 MHZ	20C8 20F3
	FBCS0 L	GPU_FBCNTL_A:::200	4	:::400				300 MHZ	20C8 20F3
	FBCS0 L	RAM_FBCNTL_A:::200	4	:::400				300 MHZ	20C8 20F3
	FBACKE	GPU_FBCNTL_A:::200	5	:::400	100			300 MHZ	20D3 20D7
	FBACKE	RAM_FBCNTL_A:::200	5	:::400	100			300 MHZ	20D3 20D7
	FBRAS L	GPU_FBCNTL_A:::350	5	:::2700	50			300 MHZ	20G2 22B2 22B6
	FBRAS L	RAM_FBCNTL_A:::350	5	:::2700	50			300 MHZ	20G2 22B2 22B6
	FBAWE L	GPU_FBCNTL_A:::500	5	:::2700	50			300 MHZ	20F2 22B2 22B6
	FBAWE L	RAM_FBCNTL_A:::500	5	:::2700	50			300 MHZ	20F2 22B2 22B6
	FBCS0 L	GPU_FBCNTL_A:::350	5	:::2700	50			300 MHZ	20F2 22B2 22B6
	FBCS0 L	RAM_FBCNTL_A:::350	5	:::2700	50			300 MHZ	20F2 22B2 22B6
	FBACKE	GPU_FBDQS_A:::100	3	:::350				300 MHZ	20D2 22C2 22C6
	FBACKE	RAM_FBDQS_A:::100	3	:::350				300 MHZ	20C7 21A8
	FBDQSTERM<0..7>	FB_DQSTERM_A:::50	3	:::1500		10 MIL SPACING		300 MHZ	21A7
	FBDQSTERM<0..7>	RAM_FBDQSTERM_A:::50	3	:::1500		10 MIL SPACING		300 MHZ	21A7
	FBDQS<0..7>	GPU_FBDQS_A:::55	3	:::150				300 MHZ	21A6 22C2 22C6
	FBDQS<0..7>	RAM_FBDQS_A:::55	3	:::150				300 MHZ	21A6 22C2 22C6
	FBCLK0	GPU_FBCLK_A:::50	3	:::150			200	300 MHZ	20D7 21C3
	FBCLK0	RAM_FBCLK_A:::50	3	:::150			200	300 MHZ	20D7 21C3
	FBCLK0_L	GPU_FBCLK_A:::50	3	:::150			200	300 MHZ	20D7 21C3
	FBCLK0_L	RAM_FBCLK_A:::50	3	:::150			200	300 MHZ	20D7 21C3
	FBCLK1	GPU_FBCLK_A:::50	3	:::150			200	300 MHZ	20D7 21D3
	FBCLK1	RAM_FBCLK_A:::50	3	:::150			200	300 MHZ	20D7 21D3
	FBCLK1_L	GPU_FBCLK_A:::50	3	:::150			200	300 MHZ	20D7 21D3
	FBCLK1_L	RAM_FBCLK_A:::50	3	:::150			200	300 MHZ	20D7 21D3
	FBCLK1	GPU_FBCLK_A:::80	3	:::2500			200	300 MHZ	21D1 22C2
	FBCLK1	RAM_FBCLK_A:::80	3	:::2500			200	300 MHZ	21D1 22C2
	FBCLK1_L	GPU_FBCLK_A:::80	3	:::2500			200	300 MHZ	21D1 22C2
	FBCLK1_L	RAM_FBCLK_A:::80	3	:::2500			200	300 MHZ	21D1 22C2
	FBCLK0	GPU_FBCLK_A:::70	3	:::2500			200	300 MHZ	21C1 22C6
	FBCLK0	RAM_FBCLK_A:::70	3	:::2500			200	300 MHZ	21C1 22C6
	FBCLK0_L	GPU_FBCLK_A:::70	3	:::2500			200	300 MHZ	21C1 22C6
	FBCLK0_L	RAM_FBCLK_A:::70	3	:::2500			200	300 MHZ	21C1 22C6
	FBD<64..127>	GPU_FBDDATA_B:::225	4	:::800				300 MHZ	20E5 20F5 20G5 21B5 21B8 21C5 21C8
	FBDD<64..127>	RAM_FBDDATA_B:::325	4	:::1000				300 MHZ	21B4 21B7 21C4 21C7 23B1 23B5 23C1 23C5
	FBDQM<8..15>	GPU_FBDQM_B:::120	4	:::800				300 MHZ	20C3 20D3 20D5
	FBDDQM<8..15>	RAM_FBDQM_B:::120	4	:::1000				300 MHZ	20C2 20D2 23C2 23C6
	FBA<0..12>	GPU_FBADDR_B:::120	4	:::600				300 MHZ	20A3 20B3 20C3 20C5 20D5
	FBBA<0..11>	RAM_FBADDR_B:::370	5	:::2400	50			300 MHZ	20B2 20C2 23C2 23C6 23D2 23D6
	FBABA<0..1>	GPU_FBADDR_B:::120	4	:::600				300 MHZ	20A3 20C5
	FBABA<0..1>	RAM_FBADDR_B:::370	5	:::2400	50			300 MHZ	20A2 23C2 23C6
	FBRAS L	GPU_FBCNTL_B:::120	4	:::400				300 MHZ	20C3 20D4
	FBRAS L	RAM_FBCNTL_B:::120	4	:::400				300 MHZ	20C3 20D4
	FBAWE L	GPU_FBCNTL_B:::120	4	:::400				300 MHZ	20C3 20D4
	FBAWE L	RAM_FBCNTL_B:::120	4	:::400				300 MHZ	20C3 20D4
	FBCS0 L	GPU_FBCNTL_B:::120	4	:::400				300 MHZ	20C3 20C4
	FBCS0 L	RAM_FBCNTL_B:::120	4	:::400				300 MHZ	20C3 20C4
	FBACKE	GPU_FBCNTL_B:::120	5	:::400	100			300 MHZ	20A3 20C4
	FBACKE	RAM_FBCNTL_B:::120	5	:::400	100			300 MHZ	20A3 20C4
	FBRAS L	GPU_FBCNTL_B:::2000	5	:::3500	3550			300 MHZ	20C2 23B2 23B6
	FBRAS L	RAM_FBCNTL_B:::2000	5	:::3500	3550			300 MHZ	20C2 23B2 23B6
	FBAWE L	GPU_FBCNTL_B:::2000	5	:::3500	3550			300 MHZ	20C2 23B2 23B6
	FBAWE L	RAM_FBCNTL_B:::2000	5	:::3500	3550			300 MHZ	20C2 23B2 23B6
	FBCS0 L	GPU_FBCNTL_B:::2000	5	:::3500	3550			300 MHZ	20C2 23B2 23B6
	FBCS0 L	RAM_FBCNTL_B:::2000	5	:::3500	3550			300 MHZ	20C2 23B2 23B6
	FBACKE	GPU_FBCNTL_B:::2000	5	:::3500	3550			300 MHZ	20A2 23C2 23C6
	FBACKE	RAM_FBCNTL_B:::2000	5	:::3500	3550			300 MHZ	20A2 23C2 23C6
	FBDQS<8..15>	GPU_FBDQS_B:::100	3	:::350		10 MIL SPACING		300 MHZ	20D4 21A5
	FBDQS<8..15>	RAM_FBDQS_B:::100	3	:::350		10 MIL SPACING		300 MHZ	20D4 21A5
	FBDQSTERM<8..15>	FB_FBDQSTERM_B:::60	3	:::1500		10 MIL SPACING		300 MHZ	21A4
	FBDQSTERM<8..15>	RAM_FBDQSTERM_B:::60	3	:::1500		10 MIL SPACING		300 MHZ	21A4
	FBDQS<8..15>	GPU_FBDQS_B:::50	3	:::150				300 MHZ	21A3 23C2 23C6
	FBDQS<8..15>	RAM_FBDQS_B:::50	3	:::150				300 MHZ	21A3 23C2 23C6
	FBCLK0	GPU_FBCLK_B:::50	3	:::150			200	300 MHZ	20C5 21B3
	FBCLK0	RAM_FBCLK_B:::50	3	:::150			200	300 MHZ	20C5 21B3
	FBCLK0_L	GPU_FBCLK_B:::50	3	:::150			200	300 MHZ	20C5 21B3
	FBCLK0_L	RAM_FBCLK_B:::50	3	:::150			200	300 MHZ	20C5 21B3
	FBCLK1	GPU_FBCLK_B:::50	3	:::150			200	300 MHZ	20C5 21C3
	FBCLK1	RAM_FBCLK_B:::50	3	:::150			200	300 MHZ	20C5 21C3
	FBCLK1_L	GPU_FBCLK_B:::50	3	:::150			200	300 MHZ	20C5 21C3
	FBCLK1_L	RAM_FBCLK_B:::50	3	:::150			200	300 MHZ	20C5 21C3
	FBCLK1	GPU_FBCLK_B:::90	4	:::2500			200	300 MHZ	21C1 23C2
	FBCLK1	RAM_FBCLK_B:::90	4	:::2500			200	300 MHZ	21C1 23C2
	FBCLK1_L	GPU_FBCLK_B:::90	4	:::2500			200	300 MHZ	21B1 23C2
	FBCLK1_L	RAM_FBCLK_B:::90	4	:::2500			200	300 MHZ	21B1 23C2
	FBCLK0	GPU_FBCLK_B:::90	3	:::2500			200	300 MHZ	21B1 23C6
	FBCLK0	RAM_FBCLK_B:::90	3	:::2500			200	300 MHZ	21B1 23C6
	FBCLK0_L	GPU_FBCLK_B:::90	3	:::2500			200	300 MHZ	21B1 23C6
	FBCLK0_L	RAM_FBCLK_B:::90	3	:::2500			200	300 MHZ	21B1 23C6

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NONE	57		74

DIGITAL SIGNALS

GROUP	SIG_NAME	MATCHED_DELAY	MAX_VIAS	DELAY_RULE	STUB_LENGTH	NET_SPACING_TYPE	NO_TEST	PULSE_PARAM	MAX_EXPOSED_LENGTH		
MAXBUS	#GPU_ADDR<0..31>	CPU_ADDR_GROUP:::71250	:::5000		250			166 MHZ		4B7 4C7 9B7 9B8 9C5 9C6 9C7 9C8 11C3 11D3 11D8	
	#GPU_DATA<0..63>	CPU_DATA_GROUP:::7400	:::5000		1550			166 MHZ		5A4 5B4 5C4 5D4 9C5 9C6 9C7 9C8 9D5 9D6 9D7 9D8	
	#GPU_BR L	CPU_CNTL_GROUP:::7100	:::5000		250	10 MIL SPACING		166 MHZ		4D7 7C7 9C5 11D3 11B1 11B5 11B8 11C1 11C5 11D1 11D5	
	#GPU_BG L	CPU_CNTL_GROUP:::7100	:::5000		250	10 MIL SPACING		166 MHZ		4D7 7B7 9B5 11D3	
	#GPU_TS L	CPU_CNTL_GROUP:::7000	:::5000		250	10 MIL SPACING		166 MHZ		4D7 7C7 9C7 11D3	
	#GPU_TT<0..4>	CPU_CNTL_GROUP:::7300	:::5000		250	10 MIL SPACING		166 MHZ		4B7 7A7 9B5 9B6 9C5 9C6 11B3	
	#GPU_TBST L	CPU_CNTL_GROUP:::7100	:::5000		250	10 MIL SPACING		166 MHZ		4B7 7B7 9B5 11B3	
	#GPU_TSIZ<0..2>	CPU_CNTL_GROUP:::7260	:::5000		250	10 MIL SPACING		166 MHZ		4B7 9B6 9B7 9C6 11B3	
	#GPU_ARTRY L	CPU_CNTL_GROUP:::7000	:::5000		250	10 MIL SPACING		166 MHZ		4A7 7C7 9B8 11B3	
	#GPU_AACK L	CPU_CNTL_GROUP:::7000	:::5000		250	10 MIL SPACING		166 MHZ		4A7 7B7 9B6 11B3	
	#GPU_GBL L	CPU_CNTL_GROUP:::7000	:::1000		250	10 MIL SPACING		166 MHZ		4B8 9B6	
	#GPU_INT_GBL L	CPU_CNTL_GROUP:::5100	:::3500		250	10 MIL SPACING		166 MHZ		4B8 7B7 11C3	
	#GPU_CI L	CPU_CNTL_GROUP:::7000	:::5000		250	10 MIL SPACING		166 MHZ		4A7 7A7 9C6 11C3	
	#GPU_HIT L	CPU_CNTL_GROUP:::7000	:::5000		250	10 MIL SPACING		166 MHZ		4A7 7C7 9B8 11B3	
	#GPU_DBG L	CPU_CNTL_GROUP:::7000	:::3500		250	10 MIL SPACING		166 MHZ		4C3 7B7 9C8 11B1	
	#GPU_DRDY L	CPU_CNTL_GROUP:::5100	:::500		250	10 MIL SPACING		166 MHZ		4C2 7B7 9B6 11B1	
	#GPU_WT L	CPU_CNTL_GROUP:::7000	:::5000		250	10 MIL SPACING		166 MHZ		4B7 7A7 9B6 11B3	
	#GPU_DRDY L UF		2	:::5000		250	10 MIL SPACING		166 MHZ		4C3
	#GPU_DTI<0..2>	CPU_CNTL_GROUP:::7150	:::5000		250	10 MIL SPACING		166 MHZ		4C3 9B5 9B7 11A1	
	#GPU_TA L	CPU_CNTL_GROUP:::7100	:::5000		250	10 MIL SPACING		166 MHZ		4C3 7C7 9C5 11A1	
	#GPU_TEA L	CPU_CNTL_GROUP:::7100	:::5000		250	10 MIL SPACING		166 MHZ		4C3 7B7 9C6 11A1	
	#GPU_OREO L	CPU_CNTL_GROUP:::7000	:::5000		250	10 MIL SPACING		166 MHZ		4C3 7D5 9B7 11B3	
#GPU_OACK L	CPU_CNTL_GROUP:::1000	:::4000		250	10 MIL SPACING		166 MHZ		4C3 9B5 11B3		
#SYSCLK CPU UF		2	:::150				166 MHZ	315	11A3		
#SYSCLK CPU		4	:::2200	2400	200	10 MIL SPACING		166 MHZ	315	4D2 11A4	
#INT CPU FB OUT		3	:::1000		200			166 MHZ	315	11B3	
#GPU_FBO PLUS1		3	:::200		200			166 MHZ	315	11A4	
#GPU_FBI PLUS1		3	:::1400	1500	200			166 MHZ	315	11A4	
#GPU_FB MINUS3		4	:::900	1000	200			166 MHZ	315	11A4	
#INT CPU FB IN		4	:::1000		200			166 MHZ	315	11B3	
#GPU_FB PLUS2		3	:::900	1000				166 MHZ	315	11A5	
#GPU_FB PLUS3		3	:::2900	3000				166 MHZ	315	11A4	
#INT_ANALYZER_CLK		3	:::300					166 MHZ		9B4 11B4 18C7 56A7 61A7	
#SYSCLK LA		2	:::2000					166 MHZ		9B3 9D8	
#INT_CLOCK_OUT		3	:::3000					166 MHZ		9C4	
MIN_LINE_WIDTH											
#USB2_XT1		3	:::1000		100	10 MIL SPACING		30 MHZ		34C4	
#USB2_XT2 B		3	:::1000		100	10 MIL SPACING		30 MHZ			
#USB2_XT2		3	:::100		100	10 MIL SPACING		30 MHZ		34C4	
#USB2_RREF		2	:::100							34B4	
#USB2_RSDAM	USB2_RSDA:::20	2	:::500			8 MIL SPACING	3.5	480 MHZ		34C4	
#USB2_RSDAP	USB2_RSDA:::20	2	:::500			8 MIL SPACING	3.5	480 MHZ		34C4	
#USB2_RSDBM	USB2_RSDB:::20	2	:::500			8 MIL SPACING	3.5	480 MHZ		34C4	
#USB2_RSDBP	USB2_RSDB:::20	2	:::500			8 MIL SPACING	3.5	480 MHZ		34C4	
#USB2_RSDCM	USB2_RSDC:::20	2	:::500			8 MIL SPACING	3.5	480 MHZ		34C4	
#USB2_RSDCP	USB2_RSDC:::20	2	:::500			8 MIL SPACING	3.5	480 MHZ		34C4	
#USB2_DAN F	USB2_DMA:::30	3	:::500		50	8 MIL SPACING	3.5	480 MHZ	USB2_DMA_DP	MIN_DAI5Y_CHAIN 34C1 34C4 35B7	
#USB2_DAP F	USB2_DMA:::30	3	:::500		50	8 MIL SPACING	3.5	480 MHZ	USB2_DMA_DP	MIN_DAI5Y_CHAIN 34C1 35B7	
#USB2_DBN F	USB2_DMB:::20	3	:::500		50	8 MIL SPACING	3.5	480 MHZ	USB2_DMB_DP	MIN_DAI5Y_CHAIN 34C1 35C7	
#USB2_DBP F	USB2_DMB:::20	3	:::500		50	8 MIL SPACING	3.5	480 MHZ	USB2_DMB_DP	MIN_DAI5Y_CHAIN 34C1 34C4 35C7	
#USB2_DCN F	USB2_DMC:::20	3	:::500		50	8 MIL SPACING	3.5	480 MHZ	USB2_DMC_DP	MIN_DAI5Y_CHAIN 34C1 35D7	
#USB2_DCP F	USB2_DMC:::20	3	:::500		50	8 MIL SPACING	3.5	480 MHZ	USB2_DMC_DP	MIN_DAI5Y_CHAIN 34C1 35D7	
#USBT_DAN F	USB2_DMAT:::60	4	:::3000		2000	8 MIL SPACING	3.5	480 MHZ	USB2_DMAT_DP	MIN_DAI5Y_CHAIN 35B6	
#USBT_DAP F	USB2_DMAT:::60	4	:::3000		2000	8 MIL SPACING	3.5	480 MHZ	USB2_DMAT_DP	MIN_DAI5Y_CHAIN 35B6	
#USBT_DBN F	USB2_DMBT:::60	4	:::3000		2000	8 MIL SPACING	3.5	480 MHZ	USB2_DMBT_DP	MIN_DAI5Y_CHAIN 35C6	
#USBT_DBP F	USB2_DMBT:::60	4	:::3000		2000	8 MIL SPACING	3.5	480 MHZ	USB2_DMBT_DP	MIN_DAI5Y_CHAIN 35C6	
#USBT_DCN F	USB2_DMCT:::60	4	:::3000		2000	8 MIL SPACING	3.5	480 MHZ	USB2_DMCT_DP	MIN_DAI5Y_CHAIN 35D6	
#USBT_DCP F	USB2_DMCT:::60	4	:::3000		2000	8 MIL SPACING	3.5	480 MHZ	USB2_DMCT_DP	MIN_DAI5Y_CHAIN 35D6	
#USB_DAN CON	USB2_CONA:::30	2	:::750		50	8 MIL SPACING	3.5	480 MHZ	USB2_CONA_DP	MIN_DAI5Y_CHAIN 35C3 61C4	
#USB_DAP CON	USB2_CONA:::30	2	:::750		50	8 MIL SPACING	3.5	480 MHZ	USB2_CONA_DP	MIN_DAI5Y_CHAIN 35C3 61C4	
#USB_DBN CON	USB2_CONB:::30	2	:::750		50	8 MIL SPACING	3.5	480 MHZ	USB2_CONB_DP	MIN_DAI5Y_CHAIN 35B3 61C4	
#USB_DBP CON	USB2_CONB:::30	2	:::750		50	8 MIL SPACING	3.5	480 MHZ	USB2_CONB_DP	MIN_DAI5Y_CHAIN 35B3 61C4	
#USB_DCN CON	USB2_CONC:::30	2	:::750		50	8 MIL SPACING	3.5	480 MHZ	USB2_CONC_DP	MIN_DAI5Y_CHAIN 35D3 61C4	
#USB_DCP CON	USB2_CONC:::30	2	:::750		50	8 MIL SPACING	3.5	480 MHZ	USB2_CONC_DP	MIN_DAI5Y_CHAIN 35D3 61C4	

SIGNAL CONSTRAINTS

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6423	D
SCALE	NONE	SHT	58 OF 74

DIGITAL SIGNALS

GROUP	SIG_NAME	MATCHED_DELAY	MAX_VIAS	DELAY_RULE	STUB_LENGTH	NET_SPACING_TYPE	MIN_LINE_WIDTH	MAX_EXPOSED_LENGTH	PULSE_PARAM
NEW	FLVSYNC*		3	:::1000					24C5
STUFF	FLANALOG_VSYNC*		4	:::3500					24C8 27C6 28B5 61B7
HERE	FLHSYNC*		3	:::1000					24C5
	FLANALOG_HSYNC*		5	:::3500					24C8 27C4 27D6 28B5 61B7
	FLANALOG_BLU		4	:::4000	200	10 MIL SPACING	5.8		24C5 24C8 27C6
	FLANALOG_GRN		4	:::4000	200	10 MIL SPACING	5.8		24C5 24C8 27C6
	FLANALOG_RED		4	:::4000	200	10 MIL SPACING	5.8		24C5 24C8 27B6
	FLILT_ANALOG_RED		2	:::500		10 MIL SPACING	5.8		27C5 61B7
	FLILT_ANALOG_GRN		2	:::500		10 MIL SPACING	5.8		27C5 61B7
	FLILT_ANALOG_BLU		2	:::500		10 MIL SPACING	5.8		27C5 61B7
	FLDAC2RSET			:::1000		10 MIL SPACING			24C6
	FLDAC2VREF			:::1000		10 MIL SPACING			24C5
	FLNV11_XTALIN		4	:::1000	100	8 MIL SPACING		27 MHZ	24B4
	FLNV11_XTALOUT		4	:::1000	100	8 MIL SPACING		27 MHZ	24B4
	FLNT_TMDS_CKP	TMDS:::120	3		50	8 MIL SPACING	TMDS_CLK		25D2 26B6 61A7
	FLNT_TMDS_CKM	TMDS:::120	3		50	8 MIL SPACING	TMDS_CLK		25D2 26A6 61A7
	FLNT_TMDS_D0P	TMDS:::120	3		50	8 MIL SPACING	TMDS_D0		25D2 26B6 61B7
	FLNT_TMDS_D0M	TMDS:::120	3		50	8 MIL SPACING	TMDS_D0		25D2 26B6 61A7
	FLNT_TMDS_D1P	TMDS:::120	3		50	8 MIL SPACING	TMDS_D1		25D2 26C6 61B7
	FLNT_TMDS_D1M	TMDS:::120	3		50	8 MIL SPACING	TMDS_D1		25D2 26C6 61B7
	FLNT_TMDS_D2P	TMDS:::120	3		50	8 MIL SPACING	TMDS_D2		25C2 26D6 61B7
	FLNT_TMDS_D2M	TMDS:::120	3		50	8 MIL SPACING	TMDS_D2		25C2 26D6 61B7
	FLCKP	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_CLK		26B5 26C4
	FLCKM	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_CLK		26A5 26C3
	FLD0P	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_D0		26B5 26C3
	FLD0M	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_D0		26B5 26C4
	FLD1P	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_D1		26C4 26C5
	FLD1M	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_D1		26C3 26C5
	FLD2P	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_D2		26C3 26D5
	FLD2M	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_D2		26C4 26D5
	FLNET_LINK_TX_EN		4	:::1000				25 MHZ	36D6
	FLNET_LINK_TX_ER		4	:::1000				25 MHZ	36D6
	FLNET_LINK_TXD<0..3>		4	:::1000				25 MHZ	36C6
	FLNET_PHY_TX_EN		4	:::5600				25 MHZ	36D7 37C6
	FLNET_PHY_TX_ER		4	:::5600				25 MHZ	36D7 37C6
	FLNET_PHY_TXD<0..3>		4	:::4600:5600				25 MHZ	36C7 37C6
	FLGLKENET_LINK_TX		4	:::4600:5600				25 MHZ	36D7 37C8
	FLGLKENET_PHY_TX		4	:::1000				25 MHZ	37C6
	FLGLKENET_LINK_RX		4	:::4600:5600				25 MHZ	36C7 37C8
	FLGLKENET_PHY_RX		4	:::1000				25 MHZ	37C6
	FLNET_PHY_RXD<0..3>		4	:::1000				25 MHZ	37B6 37C6
	FLNET_PHY_RX_DV		4	:::1000				25 MHZ	37B6
	FLNET_PHY_RX_ER		4	:::1000				25 MHZ	37B6
	FLNET_PHY_CRS		4	:::1000				25 MHZ	37B6
	FLNET_PHY_COL		4	:::1000				25 MHZ	37B6
	FLNET_LINK_RXD<0..3>		4	:::4600:5600				25 MHZ	36C7 37B8 37C8
	FLNET_CRS		4	:::4600:5600				25 MHZ	36C7 37B8
	FLNET_COL		4	:::4600:5600				25 MHZ	36B7 37B8
	FLNET_RX_DV		4	:::4600:5600				25 MHZ	36C7 37B8
	FLNET_RX_ER		4	:::4600:5600				25 MHZ	36C7 37B8
	FLGLK25M_ENET_XIN		3	:::1000	100	8 MIL SPACING		25 MHZ	37B6
	FLGLK25M_ENET_XOUT		3	:::1000	100	8 MIL SPACING		25 MHZ	37B6
	FLNET_TDP	ETHTD:::70	3	:::4000	3150	10 MIL SPACING	ETH_TXD	100 MHZ	37C3
	FLNET_TDN	ETHTD:::70	3	:::4000	3150	10 MIL SPACING	ETH_TXD	100 MHZ	37C3
	FLNET_RDP	ETHRD:::70	3	:::4000	3150	10 MIL SPACING	ETH_RXD	100 MHZ	37C3
	FLNET_RDN	ETHRD:::70	3	:::4000	3150	10 MIL SPACING	ETH_RXD	100 MHZ	37C3
	FLRJ45_TXP	RJTXD:::70	2	:::750		2KV ISO	RJ45_TXD	100 MHZ	37C1 37C2
	FLRJ45_TXN	RJTXD:::70	2	:::750		2KV ISO	RJ45_TXD	100 MHZ	37C1 37C2
	FLRJ45_RXP	RJRXD:::70	2	:::750		2KV ISO	RJ45_RXD	100 MHZ	37C1 37C2
	FLRJ45_RXN	RJRXD:::70	2	:::750		2KV ISO	RJ45_RXD	100 MHZ	37C1 37C2
	FLRJ45_TREF					2KV ISO			37C2
	FLRJ45_RREF					2KV ISO			37C2
	FLRJ45_4_5					2KV ISO			37C1
	FLRJ45_7_8					2KV ISO			37C1
	FLRJ45_F_TREF					2KV ISO			37B2
	FLFW_LINK_DATA<0..7>		4	:::1000					36C5
	FLFW_LINK_CNTL<0..1>		4	:::1000				49.152 MHZ	36C5
	FLFW_LINK_LREQ		4	:::1000				49.152 MHZ	36C5
	FLFW_SCLK		4	:::3500:4500				49.152 MHZ	36C5 38C8
	FLFW_D<0..7>		4	:::3700:4700				49.152 MHZ	36C3 38B8 38C8
	FLFW_CNTL0		4	:::3700:4700				49.152 MHZ	36C3 38C8
	FLFW_CNTL1		4	:::3700:4700				49.152 MHZ	36C3 38C8
	FLFW_LREQ		4	:::3700:4700				49.152 MHZ	36C3 38C8
	FLFW_PHY_SCLK		4	:::500				49.152 MHZ	38C7
	FLFW_PHY_CNTL0		4	:::1000				49.152 MHZ	38C7
	FLFW_PHY_CNTL1		4	:::1000				49.152 MHZ	38C7
	FLFW_PHY_D<0..7>		4	:::1000				49.152 MHZ	38B7 38C7
	FLFW_XI		3	:::1000	100	8 MIL SPACING		24.576 MHZ	38C6
	FLFW_XO		3	:::1000	100	8 MIL SPACING		24.576 MHZ	38C6
	FLFW_BIAS1								38C5
	FLFW_BIAS2								38C5
	FLFW_TPA1P	FWTPA1:::50	3	:::1220	5000		FW TPA1	400 MHZ	38C5
	FLFW_TPA1N	FWTPA1:::50	3	:::1220	5000		FW TPA1	400 MHZ	38C5
	FLFW_TPB1P	FWTPB1:::50	3	:::1220	5000		FW TPB1	400 MHZ	38C5
	FLFW_TPB1N	FWTPB1:::50	3	:::1220	5000		FW TPB1	400 MHZ	38C5
	FLFW_TPA2P	FWTPA2:::50	3	:::1220	5000		FW TPA2	400 MHZ	38C5
	FLFW_TPA2N	FWTPA2:::50	3	:::1220	5000		FW TPA2	400 MHZ	38C5
	FLFW_TPB2P	FWTPB2:::50	3	:::1220	5000		FW TPB2	400 MHZ	38C5
	FLFW_TPB2N	FWTPB2:::50	3	:::1220	5000		FW TPB2	400 MHZ	38C5
	FLFW_TPO1P	FWTPO1:::50	3	:::1220	5000		FW TPO1	400 MHZ	38D1
	FLFW_TPO1N	FWTPO1:::50	3	:::1220	5000		FW TPO1	400 MHZ	38A8 38D1
	FLFW_TPI1P	FWTPL1:::50	3	:::1220	5000		FW TPL1	400 MHZ	38A8 38D1
	FLFW_TPI1N	FWTPL1:::50	3	:::1220	5000		FW TPL1	400 MHZ	38A8 38D1
	FLFW_TPO2P	FWTPO2:::50	3	:::1220	5000		FW TPO2	400 MHZ	38A8 38C1
	FLFW_TPO2N	FWTPO2:::50	3	:::1220	5000		FW TPO2	400 MHZ	38A8 38C1
	FLFW_TPI2P	FWTPL2:::50	3	:::1220	5000		FW TPL2	400 MHZ	38A8 38C1
	FLFW_TPI2N	FWTPL2:::50	3	:::1220	5000		FW TPL2	400 MHZ	38A8 38C1

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SIZE	DRAWING NUMBER	REV.
D	051-6423	D
SCALE	SHT	OF
NONE	59	74

DIGITAL SIGNALS (CONT'D)

GROUP	SIG_NAME	MATCHED_DELAY	MAX_VIAS	DELAY_RULE	STUB_LENGTH	NET_SPACING_TYPE	MAX_EXPOSED_LENGTH	PULSE_PARAM
CD DRIVE BUS	EIDE_RST_L			:::3500:5500				33 MHZ 39A7 39D5
	EIDE_DMACK_L			:::3500:5500				33 MHZ 39A7 39D5
	EIDE_STOP			:::5500				33 MHZ 39A7 39D5
	EIDE_HSTB_RDY			:::5500				33 MHZ 39A7 39C5
	EIDE_DSTB_RDY			:::3500:5500				33 MHZ 39A7 39C5
	EIDE_DATA<0..15>			:::3500:5500				33 MHZ 39A5 39B5 39B7 39C5
	GP_RESET_L			:::1000				33 MHZ 39D4 40C6
	GP_DMACK_L			:::4000				33 MHZ 39D4 40C6
	GP_STOP			:::5000				33 MHZ 39D4 40C6
	GP_HSTB_RDY			:::5000				33 MHZ 39C4 40C6
	GP_DSTB_RDY			:::1000				33 MHZ 39C4 40C6
	UATAD<0..15>			:::1000				33 MHZ 39A4 39B4 39C4 40C6
	GP_DMARQ			:::1000				33 MHZ 40C6
	EIDE_DMARQ			:::3500:5500				33 MHZ 39A7 40C8
	UATA0IRO			:::1000				33 MHZ 40C6
	EIDE_INTRO			:::3500:5500				33 MHZ 39A7 40C8
	CD_EIDE_ADDR<0..2>			:::1000				33 MHZ 40C6
	EIDE_ADDR<0..2>			:::3500:5500				33 MHZ 39B7 40A8 40B8
	CD_CS1FX_L			:::1000				33 MHZ 40C6
	EIDE_CS1FX_L			:::3500:5500				33 MHZ 39A7 40B8
	CD_CS3FX_L			:::1000				33 MHZ 40C6
	EIDE_CS3FX_L			:::3500:5500				33 MHZ 39A7 40B8
HD DRIVE BUS	UIDE_RST_L	HD_DATA:::5500		:::100:6000	500			100 MHZ 39C7 39D3
	UIDE_DMACK_L	HD_DATA:::5500		:::100:6000	500			100 MHZ 39C7 39D3
	UIDE_DIOR_L	HD_DATA:::5500		:::100:6000				100 MHZ 39C7 39D3
	UIDE_DIOW_L	HD_DATA:::5500		:::6000				100 MHZ 39C3 39C7
	UIDE_IOCHRDY	HD_DATA:::5500		:::100:6000	500			100 MHZ 39C3 39C7
	UIDE_DATA<0..15>	HD_DATA:::5500		:::100:6000				100 MHZ 39A3 39B3 39C3 39C7 39D7
	HD_RESET_L			:::1000				100 MHZ 39D1 40C3
	HD_DMACK_L			:::5500				100 MHZ 39D1 40C3
	HD_DIOR_L			:::5500				100 MHZ 39D1 40C3
	HD_DIOW_L			:::5500				100 MHZ 39C1 40C3
	HD_IOCHRDY			:::1000				100 MHZ 39C1 40C3
	HD_DMARQ			:::1000				100 MHZ 40C3
	UIDE_DMARQ	HD_DATA:::5500		:::100:6000				100 MHZ 39C7 40C4
	HD_INTRO			:::1000	500			100 MHZ 40C3
	UIDE_INTRO	HD_DATA:::5500		:::100:6000				100 MHZ 39C7 40C4
	HD_UIDE_ADDR<0..2>			:::1000				100 MHZ 40C2 40C3
	UIDE_ADDR<0..2>	HD_DATA:::5500		:::100:6000				100 MHZ 39C7 40A4 40B4
	HD_UIDE_CS1FX_L			:::6000				100 MHZ 40C3
	UIDE_CS1FX_L	HD_DATA:::5500		:::6000				100 MHZ 39C7 40B4
	HD_UIDE_CS3FX_L			:::6000				100 MHZ 40C2
	UIDE_CS3FX_L	HD_DATA:::5500		:::6000				100 MHZ 39C7 40B4
	CLK_18M_INT_XOUT		3	:::1000	100	8 MIL SPACING		18.432 MHZ 60B5
	CLK_18M_INT_XOUT		3	:::1000	100	8 MIL SPACING		18.432 MHZ 60B5
	CLK_18M_INT_XOUT		3	:::200	50	8 MIL SPACING		18.432 MHZ 60B5
	USB_DAP	USBA:::500						30B3
	USB_DAN	USBA:::500						30B3
	USB_DAP_F	USBA_F:::500			100			30B2 35B7
	USB_DAN_F	USBA_F:::500			100			30B2 35B7
	USB_DBP	USBB:::500						30B3
	USB_DBN	USBB:::500						30B3
	USB_DBP_F	USBB_F:::500			100			30B2 35C7
	USB_DBN_F	USBB_F:::500			100			30B2 35C7
	USB_DCP	USBC:::500						30B3
	USB_DCN	USBC:::500						30B3
	USB_DCP_F	USBC_F:::500			100			30B2 35D7
	USB_DCN_F	USBC_F:::500			100			30B2 35D7
	USB_DEP	USBE:::500						30B3
	USB_DEN	USBE:::500						30B3
	RT_USB_DP	USBE_F:::500			100			30B2 31D3 61B4
	RT_USB_DM	USBE_F:::500			100			30B2 31D3 61B4
	USB_DFP	USBF:::500						30B3
	USB_DFN	USBF:::500						30B3
	MODEM_USB_DP	USBF_F:::500			100			30B2 31C5 61B4
	MODEM_USB_DM	USBF_F:::500			100			30B2 31C5 61B4
	EMU_XO		3	:::1000	100	8 MIL SPACING		10 MHZ 46B5
	EMU_XI		3	:::1000	100	8 MIL SPACING		10 MHZ 46B5
	EMU_XT		3	:::300	50	8 MIL SPACING		10 MHZ 46A6
	EMU_CLKOUT		3	:::1000	100	8 MIL SPACING		32.768 MHZ 46B4
	EMU_CLKIN		3	:::1000	100	8 MIL SPACING		32.768 MHZ 46B4
	EMU_CLKT		3	:::300	50	8 MIL SPACING		32.768 MHZ 46B2
	MICSHLD					10 MIL SPACING		31A5 45B8 61A7
	MICHIGH					10 MIL SPACING		31A5 45C8 61A7
	MICLOW					10 MIL SPACING		31A5 45B8 61A7
	KS_INT_SPKR+					10 MIL SPACING		31A3 44C3 61B7
	KS_INT_SPKR-					10 MIL SPACING		31A3 44C3 61B7

SIG_NAME	DELAY_RULE	PULSE_PARAM
T_UD_IDEDD_0	:::1000	100 MHZ 39C1 40C3
T_UD_IDEDD_1	:::1000	100 MHZ 39C1 40C3
T_UD_IDEDD_2	:::1000	100 MHZ 39C1 40C3
T_UD_IDEDD_3	:::1000	100 MHZ 39C1 40C3
T_UD_IDEDD_4	:::1000	100 MHZ 39B1 40C3
T_UD_IDEDD_5	:::1000	100 MHZ 39B1 40C3
T_UD_IDEDD_6	:::1000	100 MHZ 39B1 40C3
T_UD_IDEDD_7	:::1000	100 MHZ 39B1 40C3
T_UD_IDEDD_8	:::1000	100 MHZ 39B1 40C2
T_UD_IDEDD_9	:::1000	100 MHZ 39B1 40C2
T_UD_IDEDD_10	:::1000	100 MHZ 39B1 40C2
T_UD_IDEDD_11	:::1000	100 MHZ 39B1 40C2
T_UD_IDEDD_12	:::1000	100 MHZ 39B1 40C2
T_UD_IDEDD_13	:::1000	100 MHZ 39A1 40C2
T_UD_IDEDD_14	:::1000	100 MHZ 39A1 40C2
T_UD_IDEDD_15	:::1000	100 MHZ 39A1 40C2

SIGNAL CONSTRAINTS

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	D	051-6423	D
SCALE	NONE	SHT	60 OF 74

FUNC\_TEST

+1_8V_MAIN	FUNC_TEST	OFFF54C4
+12V_MAIN	FUNC_TEST	53B7 53D7 54C1
+12V_SLEEP	FUNC_TEST	OFFF54D8 41C8 44D8 46D8 47B4 47D3 47D7 50C4 50C8 50D6
+12V_SLEEPS	FUNC_TEST	OFFF51C4 51D4 51D7 52B4 52C4 52C6 52D4 52D6 53A4 53A7
+12V_SLEEPSA	FUNC_TEST	OFFF31A3 31A8 31C5 52D6 53A4 53D6 54C1
+5V_MAIN	FUNC_TEST	OFFPAGE
+5V_SLEEP	FUNC_TEST	OFFF54C4
+2_5V_MAIN	FUNC_TEST	OFFF48C7 48D6 52C6 53A5 53C5
+3V_MAIN	FUNC_TEST	OFFF32A3 54C4
CPU_VCORE_SLEEP	FUNC_TEST	OFFF41D4 42D5 43A5 43A7 44A7 44D4 54C4
JTAG_ASIC_TCK	FUNC_TEST	OFFF41D3 4D7 9B6 9C2 47B3 47C1 54C6 61B4
JTAG_ASIC_TDI	FUNC_TEST	OFFF41A3 36B7 37C4
JTAG_ASIC_TDO	FUNC_TEST	OFFF41A3 30C6 36B7
JTAG_ASIC_TMS	FUNC_TEST	OFFF41A3 37B4
JTAG_ASIC_TRST_L	FUNC_TEST	OFFF41A3 36B7 37A2 37B4
INT_TMDS_3V	FUNC_TEST	OFFF41A3 36B7
+1_5V_AGP	FUNC_TEST	OFFF26B3 26C3 26C4 26D5 54A6
FAN_12V_FILT	FUNC_TEST	OFFF12D6 13C8 18A8 18C2 18D7 19A4 19D5 48B4 54C3
+INTREPID_CORE_MAIN	FUNC_TEST	OFFF11A5 54B3
INTREPID_VSENSE	FUNC_TEST	OFFF12D6 13D4 48B3 49B2
OVDG_ADJ	FUNC_TEST	OFFF49C6
CPU_CHKSTP_OUT_L	FUNC_TEST	OFFPAGE
CPU_CHKSTP_IN_L	FUNC_TEST	OFFF41B3 7B5 9A2 9D6
CPU_HRESET_L	FUNC_TEST	OFFF41B3 7B5
JTAG_CPU_TCK	FUNC_TEST	OFFF41B3 7A3 7A5 7B3 8D2 9A2 46C2 46D2
JTAG_CPU_TDI	FUNC_TEST	OFFF41C3 7D5 8C2 9A2
JTAG_CPU_TDO	FUNC_TEST	OFFF41C3 7A5 8D2 9A2
JTAG_CPU_TMS	FUNC_TEST	OFFF41C3 9A2
JTAG_CPU_TRST_L	FUNC_TEST	OFFF41C3 7A5 8D2 9A2
+MAXBUS_SLEEP	FUNC_TEST	OFFF41C3 7C5 8D2 9A2
ROM_CS_L	FUNC_TEST	OFFF41D5 6D8 7A3 7B3 7C3 7C5 7C7 8D3 9B2 9D2
ROM_OE_L	FUNC_TEST	OFFF9D4 11B8 11D8 46B7 46D1 46D2 47D2 48D4 54C6
ROM_RW_L	FUNC_TEST	OFFF42B4 32B6 33B4
DDC_VCC_3	FUNC_TEST	OFFF42B2 32B6 33B4
DDC_VCC_5	FUNC_TEST	OFFF26A5 26B3 54B6
SND_HP_SENSE	FUNC_TEST	OFFF27C4 54A6
ANALOG_HSYNC*	FUNC_TEST	OFFF40B5 43A5
ANALOG_VSYNC*	FUNC_TEST	OFFF24C8 27C4 27D6 28B5 59D3
FILT_ANALOG_BLU	FUNC_TEST	OFFF24C8 27C6 28B5 59D3
FILT_ANALOG_RED	FUNC_TEST	OFFF27C5 59D3
FILT_ANALOG_GRN	FUNC_TEST	OFFF27C5 59D3
GND	FUNC_TEST	OFFPAGE
KS_INT_SPKR+	FUNC_TEST	OFFF41A3 44C3 60A5
KS_INT_SPKR-	FUNC_TEST	OFFF41A3 44C3 60A5
INT_TMDS_D2P	FUNC_TEST	OFFF25C2 26D6 59C3
INT_TMDS_D2M	FUNC_TEST	OFFF25C2 26D6 59C3
INT_TMDS_D1P	FUNC_TEST	OFFF25D2 26C6 59C3
INT_TMDS_D1M	FUNC_TEST	OFFF25D2 26C6 59C3
INT_TMDS_D0P	FUNC_TEST	OFFF25D2 26B6 59C3
INT_TMDS_D0M	FUNC_TEST	OFFF25D2 26B6 59C3
INT_TMDS_CKMP	FUNC_TEST	OFFF25D2 26B6 59D3
INT_TMDS_CKM	FUNC_TEST	OFFF25D2 26A6 59D3
INV_CUR_HI_FILT	FUNC_TEST	OFFF31A5
IO_RESET_L	FUNC_TEST	OFFF44A6 37B8 46B5 46B8 46D3
KS5VSD	FUNC_TEST	OFFF31A5 54B3
INT_I2C_CLK2	FUNC_TEST	OFFF40A3 30D1 31C7 36B5 41B1
INT_I2C_DATA2	FUNC_TEST	OFFF40A3 30D1 31C7 36B5 41B1
INT_ANALYZER_CLK	FUNC_TEST	OFFF40B4 11B4 18C7 56A7 58B3
LAMP_STS_FILT	FUNC_TEST	OFFF31A5
LCD_PWM_FILT	FUNC_TEST	OFFF31A5
LED_5V_FILT	FUNC_TEST	OFFF31A5 54B3
LED_RET_FILT	FUNC_TEST	OFFF31A5 54A3
MICSHLD	FUNC_TEST	OFFF31A5 45B8 60A5
MICHIGH	FUNC_TEST	OFFF31A5 45C8 60A5
MICLOW	FUNC_TEST	OFFF31A5 45B8 60A5
COMM_RESET_L	FUNC_TEST	OFFF40C5 31D5
IIC_ADD	FUNC_TEST	OFFF31C6
ROM_WP_L	FUNC_TEST	OFFF32A2

FUNC\_TEST

COMM_SHUTDOWN	FUNC_TEST	OFFF30C5 30D1 31D7
MON_DETECT	FUNC_TEST	OFFF25D6 27C6
FLO_KNOWS_BEST	FUNC_TEST	OFFF47C6 47C7 47C8
NMI_BUTTON*	FUNC_TEST	OFFF31B2 46C4
PWR_SWITCH*	FUNC_TEST	OFFF46A8 46B1 46C5 61D4
PMU_RST*	FUNC_TEST	OFFF46A8 31B3 46A5 46B5
PMURESETBUTTON*	FUNC_TEST	OFFF46A4
PWR_SWITCH*	FUNC_TEST	OFFF46A8 46B1 46C5 61D4
PWR_UP	FUNC_TEST	OFFF41C8 52C4 52C8 53B7 53C1 53C7
POWER_UP*	FUNC_TEST	OFFF46C7 53A8
RESET_BUTTON*	FUNC_TEST	OFFF31B2 46C4
COMM_RING_DET_L	FUNC_TEST	OFFF30B5 30B8 31C5 46C5
ROM_ONBOARD_CS_L	FUNC_TEST	OFFF32B2 33B4
COMM_DTR_L	FUNC_TEST	OFFF30C3 31C7
COMM_TXD_L	FUNC_TEST	OFFF30C3 31C7
COMM_TRXC	FUNC_TEST	OFFF30C3 30D2 31C7
COMM_RTS_L	FUNC_TEST	OFFF30C3 31D5
COMM_RXD	FUNC_TEST	OFFF30C3 31C5
COMM_GPIO_L	FUNC_TEST	OFFF30C3 30D2 31C5
SLEEP	FUNC_TEST	OFFF46B5 52C4
CPU_SRESET_L	FUNC_TEST	OFFF41B3 7A5 9A2
PMU_AVCC	FUNC_TEST	OFFF46B5 46D4 54B3
TMDS_DDC_CLK	FUNC_TEST	OFFF26A5 26B3
TMDS_DDC_DAT	FUNC_TEST	OFFF26A5 26B4
USB_DCN_CON	FUNC_TEST	OFFF35D3 58A3
USB_DCP_CON	FUNC_TEST	OFFF35D3 58A3
USB_DBN_CON	FUNC_TEST	OFFF35B3 58A3
USB_DBP_CON	FUNC_TEST	OFFF35B3 58A3
USB_DAN_CON	FUNC_TEST	OFFF35C3 58A3
USB_DAP_CON	FUNC_TEST	OFFF35C3 58A3
BT_USB_DP	FUNC_TEST	OFFF30B2 31D3 60B5
BT_USB_DM	FUNC_TEST	OFFF30B2 31D3 60A5
MODEM_USB_DP	FUNC_TEST	OFFF30B2 31C5 60A5
MODEM_USB_DM	FUNC_TEST	OFFF30B2 31C5 60A5
USB_PORT_PWR	FUNC_TEST	OFFF35A4 35B3 35C3 35D3 54A3
VGA_IIC_CLK	FUNC_TEST	OFFF27C4
VGA_IIC_DAT	FUNC_TEST	OFFF27C4
CPU_VCORE_SLEEP	FUNC_TEST	OFFF41B3 4D7 9B6 9C2 47B3 47C1 54C6 61D7
LINE_IN_COM	FUNC_TEST	OFFF42B7
LINE_IN_R	FUNC_TEST	OFFF42B6 42B7
LINE_IN_SENSE	FUNC_TEST	OFFF42B7 42C7
LINE_IN_L	FUNC_TEST	OFFF42B7 42C7
SND_LIN_SENSE	FUNC_TEST	OFFF30B5 42D4
OUT_R	FUNC_TEST	OFFPAGE
LINEOUT_COMM2	FUNC_TEST	OFFPAGE
LINE_OUT_L	FUNC_TEST	OFFPAGE
PCIT_IRDY_L	FUNC_TEST	OFFF33B6 33C3 56C7
RF_CLKRUN_L	FUNC_TEST	OFFF30C3
NC_RF_DISABLE_L	FUNC_TEST	OFFPAGE
PCI_DEVSEL_L	FUNC_TEST	OFFF32A8 32C5 33B7 34B6 56D7
PCI_STOP_L	FUNC_TEST	OFFF32A8 32C5 33B7 34B6 56D7
PCI_TRDY_L	FUNC_TEST	OFFF32B8 32C5 33B7 34B6 56D7
PCI_FRAME_L	FUNC_TEST	OFFF32B8 32C5 33B7 34B6 55A6
PCI_PAR	FUNC_TEST	OFFF32C5 33B7 34B6 56D7
WL_PCI_IDSEL	FUNC_TEST	OFFF30C2
33SLOTB_INT_L	FUNC_TEST	OFFF30B7 33C2
PMU_PME_L	FUNC_TEST	OFFF30B5 33C2 34A7 46B2
PCI_SLOTB_GNT_L	FUNC_TEST	OFFF32A6 32D5 33C2
CLK33M_PCI_SLOTB	FUNC_TEST	OFFF32D7 33C2 56D7
PCI_SLOTB_REQ_L	FUNC_TEST	OFFF32A8 32D5 33C3
MAIN_RESET_L	FUNC_TEST	OFFF32A2 33D4 34A6 46C4
PCI_CBE<0>	FUNC_TEST	OFFF32C5 33B7 34B6 55A6
PCIT_CBE<1>	FUNC_TEST	OFFF33B6 33C3 56C7
PCIT_CBE<2>	FUNC_TEST	OFFF33B6 33C3 56C7
PCIT_CBE<3>	FUNC_TEST	OFFF33B6 33C3 56C7
UNUSED_GPIO15	FUNC_TEST	OFFF30B5 30C1


FUNC\_TEST

PCI_AD<0>	FUNC_TEST	OFFF32B2 32D4 33C7 34C6 55A6
PCIT_AD<1>	FUNC_TEST	OFFF32B3 33C6 56C7
PCIT_AD<2>	FUNC_TEST	OFFF32B3 33C6 56C7
PCIT_AD<3>	FUNC_TEST	OFFF32B3 33C6 56C7
PCI_AD<4>	FUNC_TEST	OFFF32B2 32D4 33C7 34C6 55A6
PCI_AD<5>	FUNC_TEST	OFFF32B3 33C6 56C7
PCI_AD<6>	FUNC_TEST	OFFF32B2 32C4 33C7 34C6 55A6
PCIT_AD<7>	FUNC_TEST	OFFF32B3 33C6 56C7
PCIT_AD<8>	FUNC_TEST	OFFF32B3 33C6 56C7
PCI_AD<9>	FUNC_TEST	OFFF32B2 32C4 33C7 34C6 55A6
PCIT_AD<10>	FUNC_TEST	OFFF32B3 33C6 56C7
PCI_AD<11>	FUNC_TEST	OFFF32B2 32C4 33C7 34C6 55A6
PCIT_AD<12>	FUNC_TEST	OFFF32B3 33C6 56C7
PCI_AD<13>	FUNC_TEST	OFFF32B2 32C4 33C7 34C6 55A6
PCIT_AD<14>	FUNC_TEST	OFFF32C3 33C6 56C7
PCI_AD<15>	FUNC_TEST	OFFF32B2 32C4 33C7 34C6 55A6
PCI_AD<16>	FUNC_TEST	OFFF32B2 32C4 33C7 34C6 55A6
PCIT_AD<17>	FUNC_TEST	OFFF32C3 33C7 56C7
PCI_AD<18>	FUNC_TEST	OFFF32B2 32C4 33C7 34C6 55A6
PCIT_AD<19>	FUNC_TEST	OFFF32C3 33C7 56C7
PCI_AD<20>	FUNC_TEST	OFFF32B2 32C4 33C6 34C6 55A6
PCIT_AD<21>	FUNC_TEST	OFFF32C3 33C6 56C7
PCI_AD<22>	FUNC_TEST	OFFF32C4 33C6 34C6 55A6
PCIT_AD<23>	FUNC_TEST	OFFF33C3 33C6 56C7
PCI_AD<24>	FUNC_TEST	OFFF32B1 32C4 33B6 34C6 55A6
PCIT_AD<25>	FUNC_TEST	OFFF33B6 33C3 56C7
PCI_AD<26>	FUNC_TEST	OFFF32B1 32C4 33B6 34B6 55A6
PCIT_AD<27>	FUNC_TEST	OFFF33B6 33C3 56C7
PCIT_AD<28>	FUNC_TEST	OFFF33B7 33C2 56C7
PCIT_AD<29>	FUNC_TEST	OFFF33B6 33C3 56C7
PCI_AD<30>	FUNC_TEST	OFFF32B1 32C4 33B6 34B6 55A6
PCIT_AD<31>	FUNC_TEST	OFFF33B6 33C3 56C7

CONSTRAINT TABLES

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SCALE	NONE	SHT	61	OF	74	REV.	D
APPLE COMPUTER INC.							D

	8	7	6	5	4	3	2	1													
	<p>*** Signal Cross-Reference ***  --- for the entire design ---</p> <p>+1_5V_AGP 12D6 13C8 18A8 18C2 18D7 19A4 19D5  48B4 54C3 61C7</p> <p>+1_5V_INTREPID_PLL 11D3 18D6 30D6 32D5 54D3</p> <p>+1_5V_INTREPID_PLL1 30C4 54D3</p> <p>+1_5V_INTREPID_PLL2 30D4 54D3</p> <p>+1_5V_INTREPID_PLL3 30D4 54D3</p> <p>+1_5V_INTREPID_PLL4 30D4 54D3</p> <p>+1_5V_INTREPID_PLL5 18D5 54D3</p> <p>+1_5V_INTREPID_PLL6 32D4 54D3</p> <p>+1_5V_INTREPID_PLL7 11D2 54D3</p> <p>+1_5V_INTREPID_PLL8 30D4 54D3</p> <p>+1_8V_MAIN 54C4 61D7</p> <p>+2_5V_ENET 37C3 37D3 37D5 54C6</p> <p>+2_5V_DDR_AVDD 10D4 54D6</p> <p>+2_5V_DDR_VDD 10D5 54D6</p> <p>+2_5V_MAIN 32A3 54C4 61D7</p> <p>+3.3VFPD 26D6 53C1 54B6</p> <p>+3V_AUDIO 41D2 41D6 41D7 42C3 45C2 45D5</p> <p>+3V_INTREPID_USB 30C4 54A3</p> <p>+3V_INTSS_AVDD 10B7 54D6</p> <p>+3V_INTSS_VDD 10B6 54D6</p> <p>+3V_MAIN 41D4 42D5 43A5 43A7 44A7 44D4 54C4  61D7</p> <p>+5VSD_T 52D7</p> <p>+5V_AUDIO 41C6 41D7 43B8</p> <p>+5V_HP 43A8 43B7 43D5</p> <p>+5V_MAIN 54C4 61D7</p> <p>+5V_SLEEP 48C7 48D6 52C6 53A5 53C5 61D7</p> <p>+12VSD_FILT 31A5 54B3</p> <p>+12VSD_T 52D7</p> <p>+12V_DROPPED 46D8</p> <p>+12V_MAIN 38D8 41C8 44D8 46D8 47B4 47D3 47D7  50C4 50C8 50D6 51C4 51D4 51D7 52B4  52C4 52C6 52D4 52D6 53A4 53A7 53B7  53D7 54C1 61D7</p> <p>+12V_SLEEP 31A3 31A8 31C5 52D6 53A4 53D6 54C1  61D7</p> <p>+12V_SLEEPA 61D7</p> <p>+INTREPID_CORE_MAIN 12D6 13D4 48B3 49B2 61C7</p> <p>+MAXBUS_SLEEP 4D5 6D8 7A3 7B3 7C3 7C5 7C7 8D3 9B2  9D2 9D4 11B8 11D8 46B7 46D1 46D2  47D2 48D4 54C6 61C7</p> <p>3.8VH_TRICKLE 46C1 46D7 54B3</p> <p>3.8V_TRICKLE 46C2 46C6 46D7 52D6 54C1 54C2</p> <p>3_5_HONKER 53C4</p> <p>3_6V_SLEEP 39D1 53C1</p> <p>5V_USB_FUSED 35A7</p> <p>5V_XRA 52B4</p> <p>25V_BSTH 51C5 51C6</p> <p>25V_BSTH_TERM 51C5</p> <p>25V_COMP 51B6</p> <p>25V_COMP_DWN 51B6</p> <p>25V_DH 51B5</p> <p>25V_DHT 51B5</p> <p>25V_DL 51B5</p> <p>25V_DLT 51B4</p> <p>25V_GND 51B6</p> <p>25V_NET99 51C3</p> <p>25V_OCSET 51C5</p> <p>25V_OVP 51B6</p> <p>25V_VCC 51C6</p> <p>25V_VPWR 51B5 51C5</p> <p>25V_VPWRA 51B4</p> <p>25V_VSENSE 51C3 51C5</p> <p>25_CORE_1 51C3</p> <p>33PCI_SLOTD_SERR_L 34B6</p> <p>33SLOTB_INT_L 30B7 33C2 61A4</p> <p>2059_IIC_CLK 10C5</p> <p>2059_IIC_DAT 10C5</p> <p>A0_IIC_MCK 10A4 10C5</p> <p>A1_IIC_MCK 10A4 10C5</p> <p>AGND 54C4</p> <p>AGPIO_VPWR 48B6</p> <p>AGP_AD&lt;31..0&gt; 18B4 18C4 19C8 19D8 56C7</p> <p>AGP_AD_STB&lt;1..0&gt; 18A4 18B3 19B8 56C7</p> <p>AGP_AD_STB_GPUUF&lt;1..0&gt; 19B7 56B7</p> <p>AGP_AD_STB_L&lt;1..0&gt; 18A4 18D1 19B8 56C7</p> <p>AGP_AD_STB_L_GPUUF&lt;1..0&gt; 19B7 56B7</p>		<p>AGP_BUSY_L 18C6 18D1 18D3 19A8 56B7</p> <p>AGP_CBE&lt;3..0&gt; 18B4 19C8 56C7</p> <p>AGP_DEVSEL_L 18B4 18C3 19B8 56C7</p> <p>AGP_FBI_EQUAL 18C7 56A7</p> <p>AGP_FBO_EQUAL 18B7 56A7</p> <p>AGP_FB_PLUS2 18B8 56A7</p> <p>AGP_FRAME_L 18B4 18C3 19B8 56C7</p> <p>AGP_GNT_L 18C3 18C4 19B7 56B7</p> <p>AGP_INT_L 19B7 30B5 30B8</p> <p>AGP_IRDY_L 18B4 18C3 19B8 56C7</p> <p>AGP_PAR 18B4 19B8 56B7</p> <p>AGP_PIPE_L 18A4 18B3 19B8 56B7</p> <p>AGP_PLLVDD 19C5</p> <p>AGP_RBF_L 18A4 18B3 19B8 56B7</p> <p>AGP_REQ_L 18C3 18C4 19B7 56B7</p> <p>AGP_RESET_L 19C7 46D3</p> <p>AGP_SBA&lt;7..0&gt; 18A4 18B1 18B4 18C1 19A8 56B7</p> <p>AGP_SB_STB 18A4 18B3 19A8 56B7</p> <p>AGP_SB_STB_L 18A4 18D1 19A8 56B7</p> <p>AGP_ST&lt;2..0&gt; 18A4 18B1 19B7 56B7</p> <p>AGP_STOP_L 18B3 18B4 19B8 56C7</p> <p>AGP_TRDY_L 18B3 18B4 19B8 56C7</p> <p>AGP_WBF_L 18A6 18B1 19B8 56B7</p> <p>AINLM 41C4</p> <p>AINLP 41C4</p> <p>AINRM 41C4</p> <p>AINRP 41C4</p> <p>ALTCHGND 54C4</p> <p>AMPBIAS 44B6</p> <p>AMPDC1 44B6</p> <p>AMPDC2 44B6</p> <p>AMPPFAULT 44B6 44B8</p> <p>AMPPUMP 44B6</p> <p>AMPRREF 44B6</p> <p>ANALOGGND 54C4</p> <p>ANALOG_BLU 24C5 24C8 27C6 59D3</p> <p>ANALOG_GRN 24C5 24C8 27C6 59D3</p> <p>ANALOG_HSYNC* 24C8 27C4 27D6 28B5 59D3 61B7</p> <p>ANALOG_RED 24C5 24C8 27B6 59D3</p> <p>ANALOG_VSYNC* 24C8 27C6 28B5 59D3 61B7</p> <p>ANEN 37C4</p> <p>AOUTL 41C1 43D7 44B8</p> <p>AOUTR 41C1 43C7 44C8</p> <p>ASH 45B6</p> <p>AUDIO_SPKR_ID_B 44C4</p> <p>AUDIO_TO_SND 30B2 30B3 41C1</p> <p>AUD_AMP_5V 44B8 44C6</p> <p>AUD_R_FB 41D6</p> <p>BB_MISO 8B4 8B6</p> <p>BB_MOSI 8B4 8B5 8B6</p> <p>BB_SCK 8B4 8B6</p> <p>BFR_HRESET_L 8B4 8C6</p> <p>BRCLKO 30A5</p> <p>BRE 47C5</p> <p>BRE_1 47B5</p> <p>BT1 46D6 53A5</p> <p>BT1_LED 53A5</p> <p>BT_USB_DM 30B2 31D3 60A5 61B4</p> <p>BT_USB_DP 30B2 31D3 60B5 61B4</p> <p>C412P1 43B3</p> <p>C756_2 42D3</p> <p>C4237P2 42C4</p> <p>C4240P2 42C4</p> <p>C4242P2 42B4</p> <p>C4243P2 42B4</p> <p>CAP_PLL 41B4</p> <p>CD_CS1FX_L 40C6 60C5</p> <p>CD_CS3FX_L 40C6 60C5</p> <p>CD_DMACK_L 39D4 40C6 60D5</p> <p>CD_DMARQ 40C6 60D5</p> <p>CD_DSTB_RDY 39C4 40C6 60D5</p> <p>CD_EIDE_ADDR&lt;2..0&gt; 40C6 60C5</p> <p>CD_HSTB_RDY 39C4 40C6 60D5</p> <p>CD_RESET_L 39D4 40C6 60D5</p> <p>CD_STOP 39D4 40C6 60D5</p> <p>CHGND 54C4</p> <p>CLK18M_INT_EXT 30B6</p> <p>CLK18M_INT_XIN 30A5</p> <p>CLK18M_INT_XO 30A6</p> <p>CLK18M_INT_XOUT 30A5</p> <p>CLK25M_ENET_XIN 37B6 59B3</p>		<p>CLK25M_ENET_XOUT 37B6 59B3</p> <p>CLK33M_PCI_SLOTB 32D7 33C2 56D7 61A4</p> <p>CLK33M_PCI_SLOTB_UF 32C5 56D7</p> <p>CLK33M_PCI_SLOTC_UF 32C5 56D7</p> <p>CLK33M_PCI_SLOTD 32D7 34A6 56A7</p> <p>CLK33M_PCI_SLOTD_UF 32C5 56D7</p> <p>CLK66M_GPU_AGP 18D8 19C7 56A7</p> <p>CLK66M_GPU_UF 18C6 56A7</p> <p>CLKENET_LINK_GBE_REF 36C6</p> <p>CLKENET_LINK_RX 36C7 37C8 59C3</p> <p>CLKENET_LINK_TX 36D7 37C8 59C3</p> <p>CLKENET_PHY_RX 37C6 59C3</p> <p>CLKENET_PHY_TX 37C6 59C3</p> <p>CLKFW_LINK_LCLK 36C5</p> <p>CLKGEN_OUT_1 10A6 55A6</p> <p>CLK_18M_INT_XOUT 60B5</p> <p>COLE 48C7</p> <p>COMM_DTR_L 30C3 31C7 61C4</p> <p>COMM_GPIO_L 30C3 30D2 31C5 61C4</p> <p>COMM_RESET_L 30C5 31D5 61A7</p> <p>COMM_RING_DET_L 30B5 30B8 31C5 46C5 61D4</p> <p>COMM_RTS_L 30C3 31D5 61C4</p> <p>COMM_RXD 30C3 31C5 61C4</p> <p>COMM_SHUTDOWN 30C5 30D1 31D7 61D4</p> <p>COMM_TRXC 30C3 30D2 31C7 61C4</p> <p>COMM_TXD_L 30C3 31C7 61C4</p> <p>CORE_MOSFET 47C4 47C6</p> <p>CORE_MOSFET_1 47B5 47C7</p> <p>CPU_AACK_L 4A7 7B7 9B6 11B3 58C3</p> <p>CPU_ADDR&lt;31..0&gt; 4B7 4C7 9B7 9B8 9C5 9C6 9C7 9C8  11C3 11D3 58D3</p> <p>CPU_ARTRY_L 4A7 7C7 9B8 11B3 58C3</p> <p>CPU_AVDD 4D3 54C6</p> <p>CPU_BG_L 4D7 7B7 9B5 11D3 58D3</p> <p>CPU_BR_L 4D7 7C7 9C5 11D3 58D3</p> <p>CPU_BUS_VSEL 4D3 7C4</p> <p>CPU_CHKSTP_IN_L 4B3 7B5 61C7</p> <p>CPU_CHKSTP_OUT_L 4B3 7B5 9A2 9D6 61C7</p> <p>CPU_CI_L 4A7 7A7 9C6 11C3 58C3</p> <p>CPU_CLK_EN 11A3 46C4</p> <p>CPU_DATA&lt;63..0&gt; 5A4 5B4 5C4 5D4 9C5 9C6 9C7 9C8 9D5  9D6 9D7 9D8 11B1 11B5 11B8 11C1 11C5  11D1 11D5 11D8 58D3</p> <p>CPU_DBG_L 4C3 7B7 9C8 11B1 58C3</p> <p>CPU_DRDY_L 4C2 7B7 9B6 11B1 58C3</p> <p>CPU_DRDY_L_UF 4C3 58C3</p> <p>CPU_DTI&lt;2..0&gt; 4C3 9B5 9B7 11A1 58C3</p> <p>CPU_EDTI 4C3 7C5</p> <p>CPU_EMODE0_L 4B3 7A4</p> <p>CPU_EMODE1_L 4B3 7A4</p> <p>CPU_FBI_PLUS1 11A4 58C3</p> <p>CPU_FBO_PLUS1 11A4 58C3</p> <p>CPU_FB_MINUS3 11A4 58C3</p> <p>CPU_FB_PLUS2 11A5 58C3</p> <p>CPU_FB_PLUS3 11A4 58B3</p> <p>CPU_GBL_L 4B8 9B6 58C3</p> <p>CPU_HDRST_L 8C7 46C4</p> <p>CPU_HIT_L 4A7 7C7 9B8 11B3 58C3</p> <p>CPU_HRESET_L 4B3 7A3 7A5 7B3 8D2 9A2 46C2 46D2  61C7</p> <p>CPU_INT_GBL_L 4B8 7B7 11C3 58C3</p> <p>CPU_L1TSTCLK 4C3 7A4</p> <p>CPU_L2TSTCLK 4C3 7C4</p> <p>CPU_LSSD_MODE 4C3 7B5</p> <p>CPU_MCP_L 4B3 7B5</p> <p>CPU_PLL_CFG&lt;3..0&gt; 4C3 4D3 6C6 9A8</p> <p>CPU_PLL_CFGEXT 4C3 6C6 9A8</p> <p>CPU_PLL_STOP 6B8 46B8</p> <p>CPU_PMONIN_L 4B3 7C5</p> <p>CPU_PULLDOWN 4A3 4D7 7C5</p> <p>CPU_PULLUP 4A3 7A5</p> <p>CPU_QACK_L 4C3 9B5 11B3 58C3</p> <p>CPU_QREQ_L 4C3 7D5 9B7 11B3 58C3</p> <p>CPU_SHD0_L 4A7 7B5</p> <p>CPU_SHD1_L 4A7 7B5</p> <p>CPU_SLEEPIN 53B7</p> <p>CPU_SMI_L 4B3 7A5 46C4</p> <p>CPU_SRESET_L 4B3 7A5 9A2 61C4</p> <p>CPU_STATE_LED* 46C4 53A8</p> <p>CPU_TA_L 4C3 7C7 9C5 11A1 58C3</p> <p>CPU_TBEN 4C3 7C5 11A3</p>		<p>CPU_TBST_L 4B7 7B7 9B5 11B3 58D3</p> <p>CPU_TEA_L 4C3 7B7 9C6 11A1 58C3</p> <p>CPU_TSIZ&lt;2..0&gt; 4B7 9B6 9B7 9C6 11B3 58D3</p> <p>CPU_TS_L 4D7 7C7 9C7 11D3 58D3</p> <p>CPU_TT&lt;4..0&gt; 4B7 7A7 9B5 9B6 9C5 9C6 11B3 58D3</p> <p>CPU_VCORE_SLEEP 4D3 4D7 9B6 9C2 47B3 47C1 54C6 61B4  61D7</p> <p>CPU_VCORE_SLEEPA 47C3</p> <p>CPU_VCORE_SLEEPA 47C3</p> <p>CPU_VCORE_SLEEPA 47C3</p> <p>CPU_VCORE_SLEEPC 47A1</p> <p>CPU_WT_L 4B7 7A7 9B6 11B3 58C3</p> <p>CSLOT_IOWAIT_L 39B7 54A6</p> <p>CVBS_CNT 24B8 25D6</p> <p>CVBS_D 24B7</p> <p>CY69P2 41B5</p> <p>DAC2RSET 24C6 59D3</p> <p>DAC2VDD 24C5 54B6</p> <p>DAC2VREF 24C5 59D3</p> <p>DACRSET 24C4</p> <p>DACVDD 24C4 54B6</p> <p>DACVREF 24C4</p> <p>DDC_VCC_3 26A5 26B3 54B6 61B7</p> <p>DDC_VCC_5 27C4 54A6 61B7</p> <p>DDRCLK_A0_L_UF 10B3 55B6</p> <p>DDRCLK_A0_UF 10C3 55B6</p> <p>DDRCLK_A1_L_UF 10B3 55B6</p> <p>DDRCLK_A1_UF 10C3 55B6</p> <p>DDRCLK_A2_L_UF 10B3 55B6</p> <p>DDRCLK_A2_UF 10C3 55B6</p> <p>DDRCLK_B0_L_UF 10B3 55A6</p> <p>DDRCLK_B0_UF 10C3 55B6</p> <p>DDRCLK_B1_L_UF 10B3 55A6</p> <p>DDRCLK_B1_UF 10C3 55B6</p> <p>DDRCLK_B2_L_UF 10B3 55A6</p> <p>DDRCLK_B2_UF 10C3 55B6</p> <p>DDR_CK_CE 10B5</p> <p>DDR_CLK_VDD_2 10C5 55B6</p> <p>DDR_FBIN 10B5 55A6</p> <p>DDR_FBIN_L 10B5 55A6</p> <p>DDR_FBO 10B3 55A6</p> <p>DDR_FBO_L 10B3 55A6</p> <p>DDR_VREF 16B2 16D1 16D8 17D8 54A6</p> <p>DS1P1 37B2</p> <p>DS2P1 37B1</p> <p>DS2_1 40B2</p> <p>DS2_2 40B2</p> <p>DS3P1 37B2</p> <p>DS6_1 40B6</p> <p>DS6_2 40B6</p> <p>DUKE_BD 47B5</p> <p>DVOCLOCKIN 24C5</p> <p>DVD0 24B5 29B7</p> <p>DVD01 24B5 29B7</p> <p>DVD02 24B5 29C7</p> <p>DVD03 24B5 29C7</p> <p>DVD04 24B5 29C7</p> <p>DVD05 24B5 29C7</p> <p>DVD06 24B5 29C7</p> <p>DVD07 24B5 29C7</p> <p>DVD08 24B5 29C7</p> <p>DVD09 24B5 29C7</p> <p>DVD010 24B5 29C7</p> <p>DVD011 24B5 29C7</p> <p>DVODE 24C5</p> <p>DVOHSYNC 24C5 28D7</p> <p>DVOVREF 24C5</p> <p>DVO_PD 24B5</p> <p>DVO_PU 24B5</p> <p>EEPR_WP 8A6</p> <p>EIDE_ADDR&lt;2..0&gt; 39B7 40A8 40B8 60C5</p> <p>EIDE_CS1FX_L 39A7 40B8 60C5</p> <p>EIDE_CS3FX_L 39A7 40B8 60C5</p> <p>EIDE_CSELP_L 40C6 54A6</p> <p>EIDE_DATA&lt;15..0&gt; 39A5 39B5 39B7 39C5 60D5</p> <p>EIDE_DMACK_L 39A7 39D5 60D5</p> <p>EIDE_DMARQ 39A7 40C8 60D5</p> <p>EIDE_DSTB_RDY 39A7 39C5 60D5</p> <p>EIDE_HSTB_RDY 39A7 39C5 60D5</p> <p>EIDE_INTRQ 39A7 40C8 60C5</p> <p>EIDE_IOCS16_L 40C6 54A6</p> <p>EIDE_PDIAG 40C6</p>		<p>NOTICE OF PROPRIETARY PROPERTY</p> <p>THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING</p> <p>I TO MAINTAIN THE DOCUMENT IN CONFIDENCE</p> <p>II NOT TO REPRODUCE OR COPY IT</p> <p>III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART</p>												
	8	7	6	5	4	3	2	1													
			<table border="1"> <tr> <td>SIZE</td> <td>DRAWING NUMBER</td> <td>REV.</td> </tr> <tr> <td>D</td> <td>051-6423</td> <td>D</td> </tr> </table>		SIZE	DRAWING NUMBER	REV.	D	051-6423	D	<table border="1"> <tr> <td>SCALE</td> <td>SHT</td> <td>OF</td> </tr> <tr> <td>NONE</td> <td>62</td> <td>74</td> </tr> </table>		SCALE	SHT	OF	NONE	62	74			
SIZE	DRAWING NUMBER	REV.																			
D	051-6423	D																			
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	8	7	6	5	4	3	2	1
D	EIDE_RST_L 39A7 39D5 60D5 EIDE_STOP 39A7 39D5 60D5 ENET_COL 36B7 37B8 59B3 ENET_CRIS 36C7 37B8 59B3 ENET_ENERGY_DET 30B5 30C1 37B4 ENET_LINK_RXD<7..0> 36C6 36C7 37B8 37C8 59B3 ENET_LINK_TXD<3..0> 36C6 59C3 ENET_LINK_TX_EN 36D6 59C3 ENET_LINK_TX_ER 36D6 59C3 ENET_MDC 36B7 37B6 ENET_MDIO 36B7 37B7 ENET_PHY_COL 37B6 59B3 ENET_PHY_CRIS 37B6 59B3 ENET_PHY_RXD<3..0> 37B6 37C6 59B3 ENET_PHY_RX_DV 37B6 59B3 ENET_PHY_RX_ER 37B6 59B3 ENET_PHY_TXD<3..0> 36C7 37C6 59C3 ENET_PHY_TX_EN 36D7 37C6 59C3 ENET_PHY_TX_ER 36D7 37C6 59C3 ENET_RDAC_PD 37B5 ENET_RDN 37C3 59B3 ENET_RDP 37C3 59B3 ENET_RX_DV 36C7 37B8 59B3 ENET_RX_ER 36C7 37B8 59B3 ENET_TDN 37C3 59B3 ENET_TDP 37C3 59B3 ETHPHYRESET_L 37B6 EXTINT14 30A8 30B5 FAN_12V_FILT 31A5 54B3 61C7 FBA<12..0> 20C8 20D8 20E3 20F3 57D3 FBABA<1..0> 20C8 20E3 57D3 FBACAS_L 20C8 20G3 57D3 FBACKE 20D3 20D7 57D3 FBACLK0 20D7 21C3 57C3 FBACLK0_L 20D7 21C3 57C3 FBACLK1 20D7 21D3 57C3 FBACLK1_L 20D7 21D3 57C3 FBACSO_L 20C8 20F3 57D3 FBARAS_L 20C8 20G3 57D3 FBAWE_L 20C8 20F3 57D3 FBBA<12..0> 20A3 20B3 20C3 20C5 20D5 57C3 FBBBA<1..0> 20A3 20C5 57C3 FBBCAS_L 20C3 20D4 57C3 FBBCKE 20A3 20C4 57B3 FBBCLK0 20C5 21B3 57B3 FBBCLK0_L 20C5 21B3 57B3 FBBCLK1 20C5 21C3 57B3 FBBCLK1_L 20C5 21B3 57B3 FBBCS0_L 20C3 20C4 57C3 FBBRAS_L 20C3 20D4 57C3 FBBWE_L 20C3 20D4 57C3 FBCAL_CLK_GND 20A5 20D7 FBCAL_PD_VDDQ 20D7 FBCAL_PU_GND 20A5 20D7 FBCAL_TERM_GND 20A5 20D7 FBD<127..0> 20E5 20E8 20F5 20F8 20G5 20G8 21B5 21B8 21C5 21C8 21D5 21D8 57C3 57D3 FBDQM<15..0> 20C3 20D3 20D5 20D8 20G3 57C3 57D3 FBDQS<15..0> 20C7 20D4 21A5 21A8 57B3 57C3 FBDQSTERM<15..0> 21A4 21A7 57B3 57C3 FB_DLLVDD 20C6 20D7 FDC602 48B7 FDX 37A5 37C4 FILT_ANALOG_BLU 27C5 59D3 61B7 FILT_ANALOG_GRN 27C5 59D3 61B7 FILT_ANALOG_RED 27C5 59D3 61B7 FIX_RESET_L 8B6 8C4 FLOW_SS 47C7 47D8 FLO_KNOWS_BEST 47C6 47C7 47C8 61D4 FPD_PWR_ON 25D6 53B3 FPD_PWR_ON_T 53B3 FWPHYRST 30C5 38C8 FW_BIAS1 38C5 59A3 FW_BIAS2 38C5 59A3 FW_CNTRL0 36C3 38C8 59A3 FW_CNTRL1 36C3 38C8 59A3 FW_CPS 38C6 FW_C_LKON 30B6 36C5 38B5 FW_D<7..0> 36C3 38B8 38C8 59A3 FW_DIODE_BYPASS_V 38B6 38B7 54B6 FW_DIO_V 38B6 54B6	FW_LINK_CNTRL<1..0> 36C5 59A3 FW_LINK_DATA<7..0> 36C5 59A3 FW_LINK_LREQ 36C5 59A3 FW_LPS 36C5 38C8 FW_LREQ 36C3 38C8 59A3 FW_PHY_3_3 38B5 38B7 38D7 54B6 FW_PHY_CNTRL0 38C7 59A3 FW_PHY_CNTRL1 38C7 59A3 FW_PHY_D<7..0> 38B7 38C7 59A3 FW_PHY_ISO* 38C6 FW_PHY_RST 38C8 FW_PHY_RST* 38C6 FW_PHY_SCLK 38C7 59A3 FW_PINT 36B5 36C1 FW_PWR 52C7 53D3 54B6 FW_PWR_SW 38D6 53D1 54B6 FW_R0 38C5 FW_R1 38C5 FW_SCLK 36C5 38C8 59A3 FW_TPA1N 38C5 59A3 FW_TPA1P 38C5 59A3 FW_TPA2N 38C5 59A3 FW_TPA2P 38C5 59A3 FW_TPB1 38B3 FW_TPB1N 38C5 59A3 FW_TPB1P 38C5 59A3 FW_TPB2 38B4 FW_TPB2N 38C5 59A3 FW_TPB2P 38C5 59A3 FW_TPI1N 38A8 38D1 59A3 FW_TPI1P 38A8 38D1 59A3 FW_TPI2N 38A8 38C1 59A3 FW_TPI2P 38A8 38C1 59A3 FW_TPOLN 38A8 38D1 59A3 FW_TPOLP 38B8 38D1 59A3 FW_TPO2N 38A8 38C1 59A3 FW_TPO2P 38A8 38C1 59A3 FW_VGND 38D1 54B6 FW_VP 38D5 54B6 FW_VP1 38D1 38D3 54B6 FW_VP2 38C1 38D3 54B6 FW_VP_1 38D4 54B6 FW_VP_2 38D4 54B6 FW_VREG_FB 38D7 FW_XI 38C6 59A3 FW_XI_A 38C6 FW_XO 38C6 59A3 GCORE_1 50C2 GCORE_2 50C3 GCORE_BSTH 50C5 GCORE_BSTH_TERM 50C3 GCORE_COMP 50B6 GCORE_DH 50B5 GCORE_DL 50B5 GCORE_GND 50B5 GCORE_OCSET 50B6 GCORE_OVP 50B6 GCORE_VCC 50C6 GCORE_VSENSE 50B6 50C2 GPULLNKN 25C4 GPU_50PULLDWN 19A5 54A6 GPU_50PULLUP 19A5 54A6 GPU_AGP_AD<31..0> 19C7 19D7 56B7 GPU_AGP_CBE<3..0> 19C7 56B7 GPU_AGP_DEVSEL_L 19B7 56B7 GPU_AGP_FRAME_L 19B7 56B7 GPU_AGP_IRDY_L 19B7 56B7 GPU_AGP_PAR 19B7 56B7 GPU_AGP_PIPE_L 19B7 56A7 GPU_AGP_RBF_L 19B7 56A7 GPU_AGP_SBA<7..0> 19A7 56A7 GPU_AGP_SB_STB 19A7 56A7 GPU_AGP_SB_STB_L 19A7 56A7 GPU_AGP_STOP_L 19B7 56B7 GPU_AGP_TRDY_L 19B7 56B7 GPU_AGP_VREF 19A3 19A8 54A6 GPU_AGP_VREF_H 18A8 GPU_AGP_VREF_L 18A8 GPU_AGP_VREF_X 19A3 GPU_AGP_VREF_Y 19A3 GPU_AGP_WBF_L 19B7	GPU_FB_VREF 20C8 54A6 GPU_FPBCLK 25C4 GPU_FPBCLK_L 25C4 GPU_FW_PME_L 25C4 GPU_IFB1IOVDD 25B2 GPU_IFP1PLLVD 25B2 GPU_MBDL 19A7 GPU_STEREO 25C4 GPU_STRAP<3..0> 24B4 28A3 28B3 28D3 GPU_SWAP_A 25C4 GPU_SWAP_B 25C4 GPU_TESTMECLK 25C4 GPU_TMODE 19A5 54A6 GPU_XTALSSIN 24B4 54A6 GPWRGD 49B6 49B8 50A5 GRAPHICS_VPWR 50B5 GRAPH_CORE 19D5 25C4 25C6 50B1 54A6 GRAPH_DDC_SCL 24D5 26A6 GRAPH_DDC_SDA 24D5 26A6 GRAPH_IIC_SCL2 25D2 GRAPH_IIC_SDA2 25D2 HD_DIOR_L 39D1 40C3 60C5 HD_DIOW_L 39C1 40C3 60C5 HD_DMACK_L 39D1 40C3 60C5 HD_DMARQ 40C3 60C5 HD_INTRQ 40C3 60C5 HD_IOCHRQ 39C1 40C3 60C5 HD_RESET_L 39D1 40C3 60C5 HD_UIDE_ADDR<2..0> 40C2 40C3 60C5 HD_UIDE_CS1FX_L 40C3 60B5 HD_UIDE_CS3FX_L 40C2 60B5 HEADPHONE_COM 43B2 HEADPHONE_L 43B2 43D2 HEADPHONE_R 43B2 43C2 HONK_ADJ 53B4 HP16_L 43D4 HP16_R 43C4 HPBYP 43B5 43D6 HPGAL_L 43D4 HPGAL_R 43C4 HPIN_L 43D6 HPIN_R 43D6 HP_OFF 43A7 43D5 HP_OUT_L 43D5 HP_OUT_R 43D5 HP_STAR_GND 41B7 43A8 43B4 43B8 43C5 43D4 43D7 HP_TL 43D3 HP_TP 43B3 HP_TR 43C3 HRST_REF 8B4 8B7 HSYNC* 24C5 59D3 ICORE_COMP 49B6 ICW 49B7 IFP0AVCC 25A6 25B4 54A6 IFP0PLLVD 25B4 IFPORSET 25B4 IFP0VREF 25B4 54A6 IFP_AVCC 25A7 IFP_VADJ 25A8 IIC_A<2..0> 8A6 8A7 IIC_ADD 31C6 61A7 IMAC_INTREPID 49B4 INTCORE_1 49B3 INTCORE_BSTH 49B6 INTCORE_BSTH_TERM 49B6 INTCORE_DH 49B6 INTCORE_DHT 49B5 INTCORE_DL 49B6 INTCORE_GND 49A7 INTCORE_NET99 49B4 INTCORE_OCSET 49B6 INTCORE_OVP 49B6 INTCORE_VCC 49C7 INTREPID_ACS_REF 11A3 INTREPID_VPWR 49B6 INTREPID_VPWR_A 49B4 INTREPID_VSENSE 49C6 61C7 INTREP_DLT 49B5 INT_AGPPVT 18C6 INT_AGP_FB_IN 18C6 56A7 INT_AGP_FB_OUT 18C6 56A7	INT_AGP_VREF 18A7 18C6 54C3 INT_ANALYZER_CLK 9B4 11B4 18C7 56A7 58B3 61A7 INT_ANALYZER_CLKA 11B3 INT_CLOCK_OUT 9C4 58B3 INT_CPU_FB_IN 11B3 58C3 INT_CPU_FB_OUT 11B3 58C3 INT_ENET_RST_L 30B5 30D1 37B8 INT_EXTINT3_PU 30B5 30B8 INT_EXTINT12_PU 30B5 30B8 INT_EXTINT13_PU 30B5 30B8 INT_EXTINT17_PU 30B5 30B8 34B6 INT_GPI01_PD 30A8 30C5 INT_GPI09_PU 30B5 30B8 INT_GPI012_PU 30A8 30B5 INT_I2C_CLK0 8A6 8B4 8B7 10C7 16A6 17A6 36B3 INT_I2C_CLK0R 36B5 36C1 INT_I2C_CLK1 10A7 36A3 36A4 36B1 INT_I2C_CLK2 30A3 30D1 31C7 36B5 41B1 61A7 INT_I2C_DATA0 8A6 8B4 8B7 10C7 16A6 17A6 36B3 INT_I2C_DATA0R 36B5 36C1 INT_I2C_DATA1 10A7 36A3 36B1 INT_I2C_DATA2 30A3 30D1 31C7 36B5 41B1 61A7 INT_JTAG_TEI 36B7 36C1 INT_MEM_REF 14B6 INT_MOD_BITCLK 30A3 30A8 30C6 INT_MOD_CLKOUT 30A3 30A8 30C6 INT_MOD_DTI 30A3 30A8 30C6 INT_MOD_DTO 30A3 30A8 30C6 INT_MOD_SYNC 30A3 30A8 30C6 INT_PCI_FB_IN 32C5 56C7 INT_PCI_FB_OUT 32C5 56D7 INT_PEND_PROC_INT 30A5 46B4 INT_PLL1_GND 30A5 30C4 INT_PLL2_GND 30A5 30D4 INT_PLL3_GND 30A4 30D4 INT_PLL4_GND 32B4 32D4 INT_PLL5_GND 18A5 18D5 INT_PLL6_GND 11A2 11D3 INT_PLL7_GND 30A4 30D4 INT_PLL9_GND 30A4 30D4 INT_PROC_SLEEP_REQ_L 30A5 46B4 INT_PU_RESET_L 36C3 46C2 46C4 INT_REF_CLK_IN_PD 10A5 30A6 55A6 INT_REF_CLK_OUT 10A7 30A6 55A6 INT_REF_CLK_OUT_UF 55A6 INT_RESET_L 36C3 37B8 43A7 44A8 46D2 46D4 INT_ROM_CS_L 32C5 INT_ROM_OE_L 32C5 INT_ROM_OVERLAY_PU 18D7 32A8 32C5 56A7 INT_ROM_RW_L 32C5 INT_SND_CLKOUT 30A3 INT_SND_SCLK 30A3 INT_SND_SYNC 30B3 INT_SND_TO_AUDIO 30B3 INT_SPKR+ 44B1 INT_SPKR- 44B1 INT_SUSPEND_ACK_L 11B3 46B5 INT_SUSPEND_REQ_L 11B3 46B5 46B8 INT_TMDS_3V 26B3 26C3 26C4 26D5 54A6 61C7 INT_TMDS_CKM 25D2 26A6 59D3 61A7 INT_TMDS_CKP 25D2 26B6 59D3 61A7 INT_TMDS_DOM 25D2 26B6 59C3 61A7 INT_TMDS_D0P 25D2 26B6 59C3 61B7 INT_TMDS_DIM 25D2 26C6 59C3 61B7 INT_TMDS_D1P 25D2 26C6 59C3 61B7 INT_TMDS_D2M 25C2 26D6 59C3 61B7 INT_TMDS_D2P 25C2 26D6 59C3 61B7 INT_TST_MONIN_PD 36B7 36C1 INT_TST_PLEN_PD 30C6 36B7 36C1 INT_WATCHDOG_L 30A5 46C5 INV_BFR_HRST 8D6 INV_CUR_HI 25D6 31B8 INV_CUR_HI_FILT 31A5 61A7 IO_RESET_L 34A6 37B8 46B5 46B8 46D3 61A7 IPWRGD 48A8 49A6 IPWR_D 48A7 JAZ 45C6 JTAG_ASIC_TCK 9A3 36B7 37C4 61D7 JTAG_ASIC_TDI 9A3 30C6 36B7 61D7 JTAG_ASIC_TDO 9A3 37B4 61D7 JTAG_ASIC_TMS 9A3 36B7 37A2 37B4 61D7	NOTICE OF PROPRIETARY PROPERTY THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. 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A	8	7	6	5	4	3	2	1

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D	JTAG_ASIC_TRST_L 9A3 36B7 61C7 JTAG_CPU_TCK 4C3 7D5 8C2 9A2 61C7 JTAG_CPU_TDI 4C3 7A5 8D2 9A2 61C7 JTAG_CPU_TDO 4C3 9A2 61C7 JTAG_CPU_TMS 4C3 7A5 8D2 9A2 61C7 JTAG_CPU_TRST_L 4C3 7C5 8D2 9A2 61C7 JTAG_ENET_TDI 30C6 36B7 37A2 37B4 KAVAN 45B6 KSSVSD 31A5 54B3 61A7 KS_INT_SPKR+ 31A3 44C3 60A5 61B7 KS_INT_SPKR+_FILT 31A5 KS_INT_SPKR- 31A3 44C3 60A5 61B7 KS_INT_SPKR-_FILT 31A5 KYLE 48C6 L31_2 42C6 L32_2 42B6 L36_2 42B6 L43_1 43A4 L3202_1 42C6 LAMP_STS 25D6 31A3 LAMP_STS_FILT 31A5 61A7 LCD_PWM 25D6 31A8 LCD_PWM_FILT 31A5 61A7 LED_5V 31A8 54B3 LED_5V_FILT 31A5 54B3 61A7 LED_RET 31A3 53B6 54B3 LED_RET_FILT 31A5 54A3 61A7 LED_ROMCS 32A3 LED_ROMCS_L 32A4 LED_ROMCS_LIGHT 32A3 LID_SWITCH 31B2 46C4 LINA 41C4 42C2 LINEOUT_COMM2 61B4 LINE_IN_COM 42B7 61B4 LINE_IN_L 42B7 42C7 61B4 LINE_IN_R 42B6 42B7 61B4 LINE_IN_SENSE 42B7 42C7 61B4 LINE_OUT_L 61B4 LINSENSE 42C5 LOW_PWR 37A5 37B4 LO_T1 43A4 LP4202P2 42C5 LP4202P3 42B5 LP4202P4 42B5 LPL1 43D6 LPR1 43C6 LSPKRCAP 44B8 LSPKRIN2 44B6 LSPKRVP2 44B6 LT1962_INT_ADJ 30D7 LT1962_INT_BYP 30D7 LT1962_INT_VIN 30D7 LY4P2 44C3 M1FH 45C7 M1FL 45B7 M1H 45C6 M1HFILT 45C5 M1L 45B6 45C6 M1S 45B5 45C6 MAIN_RESET_L 32A2 33D4 34A6 46C4 61A4 MAIN_RESET_L_PU 33D3 MAIN_SUPPLY_LED 52D6 MEMI_VREF 14A6 MEMREFG1 22A5 MEMREFG2 22A4 MEMREFG3 23A5 MEMREFG4 23A4 MEMREFI1 14A8 MEMREFI2 14A7 MEMREFM1 16B3 MEMREFM2 16B2 MEMREFN1 20B8 MEMREFN2 20B7 MEM_ADDR<12..0> 14A3 14B3 14C2 14C3 14D2 14D3 14D6 55D6 MEM_BA<1..0> 14B3 14D6 55D6 MEM_CAS_L 14A3 14C6 55C6 MEM_CKE<3..0> 14A2 14B2 14B6 14C2 14C6 55C6 MEM_CS_L<3..0> 14A2 14B2 14C2 14C6 55D6 MEM_DATA<63..0> 14B8 14C8 14D8 15A6 15B3 15B6 15C5 15C8 55D6	MEM_DQM<7..0> 14C6 15A6 15B3 15C5 15C8 55D6 MEM_DQS<7..0> 14C6 15A6 15B3 15C5 15C8 55D6 MEM_MUXSEL_H<1..0> 14B6 14D5 55C6 MEM_MUXSEL_L<1..0> 14B6 14D5 55C6 MEM_RAS_L 14A3 14C6 55C6 MEM_WE_L 14A3 14C6 55C6 MIC1 45C4 MIC1S1 45B7 MIC2 45C4 MIC3 45C4 MIC4 45C3 MIC5 45B3 MICHIGH 31A5 45C8 60A5 61A7 MICLOW 31A5 45B8 60A5 61A7 MICSHLD 31A5 45B8 60A5 61A7 MIC_FIX 45D5 MIC_IN 41C4 45C2 MIC_TDO 8B3 MII_EN 37C4 MODEM_USB_DM 30B2 31C5 60A5 61B4 MODEM_USB_DP 30B2 31C5 60A5 61B4 MON_DETECT 25D6 27C6 61D4 MON_I2C_SCL 24D5 27B6 MON_I2C_SDA 24D5 27B6 MPIC_CPU_INT_L 4B3 7A5 9D7 30B5 MPWRGD 50B6 50B7 51B5 MR_FLO 47C5 MR_FLO_1 47B4 MUTE_B_L 44B8 MUX_SEL_H 14D4 15A3 15C5 55C6 MUX_SEL_L 14D4 15A6 15C8 55C6 M_SPD_WP 17A7 M_VDDID 17A7 NCV4 41D6 NC_-10VUNREG 31C6 NC_-12VREG 31C6 NC_28512P4 10A7 NC_28512P14 10A7 NC_28512P17 10A7 NC_AMPOL 44B6 NC_ATXTAL1 8C4 NC_ATXTAL2 8B4 NC_AUDIO2MODEM 31C6 NC_AUDIO2MODEMRTN 31C6 NC_AUD_MODEM 31C6 NC_AUD_MODEM_RTN 31C6 NC_BIGDIMM9 17D6 NC_BIGDIMM10 17D6 NC_BIGDIMM44 17C6 NC_BIGDIMM45 17B6 NC_BIGDIMM47 17B6 NC_BIGDIMM49 17B6 NC_BIGDIMM51 17B6 NC_BIGDIMM71 17A6 NC_BIGDIMM101 17D4 NC_BIGDIMM102 17D4 NC_BIGDIMM103 17D4 NC_BIGDIMM113 17C4 NC_BIGDIMM134 17C4 NC_BIGDIMM135 17C4 NC_BIGDIMM140 17B4 NC_BIGDIMM142 17B4 NC_BIGDIMM144 17B4 NC_BIGDIMM163 17A4 NC_BIGDIMM167 17A4 NC_BIGDIMM173 17A4 NC_BS1 31B7 NC_BS2 31B6 NC_BS3 31D1 NC_BS4 31D1 NC_BT1 31D2 NC_BT3 31D2 NC_BT4 31D2 NC_BT5 31D2 NC_BT6 31D2 NC_CBUS_INT_L 30A8 NC_CLK33M_PCI_SLOT 32D7 NC_CLKENET_LINK_GTX 36C6 NC_CPUAP<4..0> 4B7 NC_CPUCRUD<89..0> 5A7 5B7 5C7 5D7 NC_CPUUD<7..0> 5A4	NC_CPU_CLKOUT 4D3 NC_CSLOT_ADDR<9..3> 39A7 39B7 NC_CSLOT_CE1_L 39C7 NC_CSLOT_CE2_L 39C7 NC_CSLOT_TORD_L 39C7 NC_CSLOT_TOWR_L 39C7 NC_CSLOT_OE_L 39B7 NC_CSLOT_WE_L 39B7 NC_DAA_CLKOUT 31C6 NC_DAA_LOADOUT 31C6 NC_DACC_BLU 24C4 NC_DACC_GRN 24C4 NC_DACC_RED 24C4 NC_DACC_RSET 24C4 NC_DDRCLK1 10C3 NC_DDRCLK2 10C3 NC_DDRCLK3 10C3 NC_DDRCLK4 10C3 NC_DDRCLK5 10C3 NC_DDRCLK6 10C3 NC_DDRCLK7 10C3 NC_DDRCLK8 10C3 NC_DDRCLK9 10C3 NC_DDRCLK10 10C3 NC_DDRCLK11 10B3 NC_DDRCLK12 10B3 NC_DDRCLK13 10B3 NC_DDRCLK14 10B3 NC_DDRCLK_A2 10C2 NC_DDRCLK_A2_L 10C2 NC_DFPCLK 25C2 NC_DFPCLK* 25C2 NC_DFPD0 25C2 NC_DFPD1 25C2 NC_DFPD2 25C2 NC_DFPD3 25C2 NC_DFPD5 25B2 NC_DFPD6 25B2 NC_ENET_LINK_TXD<7..4> 36C6 NC_EXT_TMDS_CKM 25C2 NC_EXT_TMDS_CKP 25C2 NC_EXT_TMDS_DOM 25C2 NC_EXT_TMDS_D0P 25C2 NC_EXT_TMDS_D1M 25C2 NC_EXT_TMDS_D1P 25C2 NC_EXT_TMDS_D2M 25C2 NC_EXT_TMDS_D2P 25C2 NC_FB1<10..0> 22B5 22B6 NC_FB2<10..0> 22B1 22B2 NC_FB3<10..0> 23B5 23B6 NC_FB4<10..0> 23B1 23B2 NC_FBACS1_L 20C8 NC_FBBCS1_L 20C4 NC_FBDQS_L<15..0> 20C7 20D4 NC_FMAX7 9A8 NC_FMAX8 9A8 NC_FW_CNA 38B6 NC_GPU<4..0> 19A6 NC_GPULFS 25C4 NC_GPU_DBI_LO 19B7 NC_GPU_INTB_L 19B7 NC_GPU_THERMA 25C4 NC_GPU_THERMC 25C4 NC_IFP1RSET 25B2 NC_IFP1VREF 25B2 NC_INPA 41C2 NC_INT_TST_MONOUT_TP 36B6 NC_JTAG7 9A3 NC_JTAG10 9A2 NC_LCENABLE 9A3 NC_MODEM_DETECT_L 31C6 NC_NVAGP_TDI 19A5 NC_NVAGP_TDO 19A5 NC_NVAGP_TMS 19A5 NC_P00_D0 46D5 NC_P01_D1 46D5 NC_P02_D2 46C5 NC_P03_D3 46C5 NC_P04_D4 46C5 NC_P05_D5 46C5 NC_P6_D6 46C5	NC_P07_D7 46C5 NC_P10_D8 46C5 NC_P11_D9 46C5 NC_P14_D12 46C5 NC_P20_A0_D0 46C5 NC_P21_A1_D1_D0 46C5 NC_P22_A2_D2_D1 46C5 NC_P23_A3_D3_D2 46C5 NC_P24_A4_D4_D3 46C5 NC_P25_A5_D5_D4 46C5 NC_P26_A6_D6_D5 46C5 NC_P27_A7_D7_D6 46C5 NC_P33_A11 46B5 NC_P34_A12 46B5 NC_P35_A13 46B5 NC_P36_A14 46B5 NC_P37_A15 46B5 NC_P43_A19 46B5 NC_P45_CS1_L 46B5 NC_P74_TA2OUT_W 46C4 NC_P75_TA2IN_W 46C4 NC_P76_TA3OUT 46C4 NC_P77_TA3IN 46C4 NC_P92_TB2IN_SOUT3 46B4 NC_P93_DA0_TB3IN 46B4 NC_P96_ANEX0_CLK4 46B4 NC_P103_AN3 46B4 NC_PCIR0 33B7 NC_PCIR1 33B7 NC_PCITR0 33B6 NC_PCITR1 33B6 NC_PD0 8B3 NC_PMON_OUT_L 4B3 NC_PMU_DL_10 31B2 NC_PMU_DL_12 31B2 NC_PPL* 46C6 NC_RESET_BUTTON_L 9A8 NC_RFBA<12..12> 20E2 NC_RFBBA<12..12> 20A2 NC_RF_DISABLE_L 61A4 NC_ROMCS_L 20C8 NC_RP1PIN4 18B3 NC_RP1399 32A6 NC_RP2848 30A8 NC_RP3319 30D2 NC_RP3324_2 30C1 NC_RPT48P1 30D2 NC_RPT77P6 30B8 NC_SDOUT2 41C2 NC_SODIMM71 16C6 NC_SODIMM72 16C5 NC_SODIMM73 16C6 NC_SODIMM74 16C5 NC_SODIMM77 16C6 NC_SODIMM78 16C5 NC_SODIMM79 16C6 NC_SODIMM80 16C5 NC_SODIMM83 16C6 NC_SODIMM84 16C5 NC_SODIMM85 16C6 NC_SODIMM86 16C5 NC_SODIMM89 16C6 NC_SODIMM91 16C6 NC_SODIMM97 16B6 NC_SODIMM98 16B5 NC_SODIMM123 16B6 NC_SODIMM124 16B5 NC_SODIMM199 16A6 NC_SODIMM200 16A5 NC_SODIMM201 16D5 NC_SODIMM202 16A6 NC_SW3V5V_33OUT 52C6 NC_SYSCLK_DDRCLK_A2 14B4 NC_TAS_SDOUT1 41C2 NC_TESTMODE 9A3 NC_TMDS_TXD3M 25C2 NC_TMDS_TXD7M 25C2 NC_TMDS_TXD7P 25C2 NC_TX1_1 37C3 NC_TX1_2 37C3					
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SCALE	NONE	SHT	OF
		64	74



	8	7	6	5	4	3	2	1												
D	<p>NC_TX1_3 37C2  NC_TX1_4 37C2  NC_UB3P4 37D5  NC_USB2_AMC 34A4  NC_USB2_NANDTEST 34A4  NC_USB2_NTTEST1 34A4  NC_USB2_PPON1 34B4  NC_USB2_PPON2 34B4  NC_USB2_PPON3 34B4  NC_USB2_PPON4 34B4  NC_USB2_PPON5 34B4  NC_USB2_RSDEM 34C4  NC_USB2_RSDEP 34C4  NC_USB2_RSDFM 34C4  NC_USB2_RSDFP 34B4  NC_USB2_SMC 34A4  NC_USB2_SMI_L 34A6  NC_USB2_SRCLK 34A4  NC_USB2_SRDTA 34A4  NC_USB2_SRMOD 34A4  NC_USB2_TEB 34A4  NC_USB2_TEST 34A4  NC_USB_M 33B2  NC_USB_P 33B2  NC_UT6P6 30D8  NC_UT6P7 30D8  NC_UT164 46D7  NC_UT165 46D7  NC_VCORE10 47D6  NC_VIPHCLK 24D5  NC_VTT&lt;11..0&gt; 20F7 20G7  NC_WL&lt;23..1&gt; 33B2 33B3  NC_XTALSOUT 24B4  NC_XTLINO 41B4  NEC_AVDD 34D5 54A3  NEC_XT2_B 34D3  NET16 48A5  NET18 46D6  NET19 46D6  NET22 46D8  NET24 46D8  NET32 44B1  NET32_B 44B1  NET40 46B4  NMI_BUTTON* 31B2 46C4 61D4  NV11_HSYNC 24C4 28D3  NV11_VSYNC 24C4 28D3  NV11_XTALIN 24B4 59D3  NV11_XTALOUT 24B4 59D3  NVAGP_TCLK 19A5  NVAGP_TRST_L 19A5  NVPLLVD 24D5 54A6  NV_BLUE2 24C1  NV_GPIOD0 25D4  NV_GPIOD2 25D4  NV_GPIOD4 25D4  NV_GPIOD5 25D4  NV_GPIOD6 25C4  NV_GPIOD7 25C4  NV_GPIOD8 25C4  NV_GPIOD9 25C4  NV_GREEN2 24C1  NV_RED2 24C1  OGAL 43D7  OGAR 43C7  OGND3_JTAG_EN 37A5  OPA_STAR_GND 41A7 41D6 42C2 45B3  OPA_VREF 42B3 45B4  OUTM1 44B6  OUTM1FILT 44B4  OUTM2 44B6  OUTM2FILT 44A4  OUTP1 44B6  OUTP1FILT 44B4  OUTP2 44B6  OUTP2FILT 44B4  OUT_R 61B4  OVDD_ADJ 61C7  P1RC 44B4  P2RC 44B4  PAT_M 44B8</p>	<p>PB_AUD 43B4  PB_GAL 43B4  PCIT_AD&lt;31..0&gt; 33B2 33B3 33B6 33B7 33C2 33C3 33C6  33C7 56C7 61B2 61C2  PCIT_CBE&lt;31..0&gt; 33B2 33B6 33C3 56C7 61A4  PCIT_DEVSEL_L 33B6 33C2 56C7  PCIT_FRAME_L 33B6 33C2 56C7  PCIT_IRDY_L 33B6 33C3 56C7 61B4  PCIT_PAR 33B6 33C2 56C7  PCIT_STOP_L 33B6 33C2 56C7  PCIT_TRDY_L 33B6 33C2 56C7  PCI_AD&lt;31..0&gt; 32B1 32B2 32C4 32D4 33B6 33B7 33C6  33C7 34B6 34B7 34C6 55A6 61B2 61C2  PCI_CBE&lt;3..0&gt; 32C5 33B7 34B6 55A6 61A4  PCI_DEVSEL_L 32A8 32C5 33B7 34B6 56D7 61A4  PCI_FBI_EQUAL 32C7 56C7  PCI_FBI_PLUS2 32C7 56C7  PCI_FBO_PLUS2 32C7 56D7  PCI_FB_PLUS4 32C8 56C7  PCI_FB_PLUS6 32C7 56C7  PCI_FRAME_L 32B8 32C5 33B7 34B6 55A6 61A4  PCI_IRDY_L 32B8 32C5 33B7 34B6 56D7  PCI_PAR 32C5 33B7 34B6 56D7 61A4  PCI_SLOTB_GNT_L 32A6 32D5 33C2 61A4  PCI_SLOTB_REQ_L 32A8 32D5 33C3 61A4  PCI_SLOTC_GNT_L 32A6 32D5  PCI_SLOTC_REQ_L 32A8 32D5  PCI_SLOTD_GNT_L 32A6 32C5 34B6  PCI_SLOTD_PERR_L 34B6  PCI_SLOTD_REQ_L 32A8 32D5 34B6  PCI_STOP_L 32A8 32C5 33B7 34B6 56D7 61A4  PCI_TRDY_L 32B8 32C5 33B7 34B6 56D7 61A4  PCK2059_VDDI2C 10D4 54D6  PGOOD 51B7 52A6  PG_E 47B6  PMURESETBUTTON* 46A4 61D4  PMU_5V_SCL 46C2 46C4  PMU_5V_SDA 46C2 46C4  PMU_ACK_L 30C3 46C4  PMU_AGP_RESET 46C4  PMU_AP 31B3 46D4  PMU_AVCC 46B5 46D4 54B3 61C4  PMU_BYTE 46B5  PMU_CLK 30C3 46C4  PMU_CLKIN 46B4 60A5  PMU_CLKOUT 46B4 60A5  PMU_CLKT 46B2 60A5  PMU_CNVSS 31B3 46B5  PMU_EPM* 31B3 46C4  PMU_FROM_INT 30C3 46C4  PMU_IIC_CLK 46A8 46B4  PMU_IIC_DAT 46A8 46B4  PMU_INT_L 30B5 30B8 46B5  PMU_INT_NMI 30A8 30B5 46C4  PMU_LOW_DSKTP 46B5  PMU_NMI 46B4  PMU_P64 31B2 46C2  PMU_PME_L 30B5 33C2 34A7 46B2 61A4  PMU_PME_LL 33C2  PMU_POWER 31C3 46A5 46B1 46C2 46D5 54B3  PMU_PRE_PLLSTOP 46B5  PMU_PWR_LED* 46C5  PMU_REQ_L 30A8 30C3 46C2  PMU_RST* 9A8 31B3 46A5 46B5 61D4  PMU_SMB_SCK 46A3  PMU_SMB_SDA 46A3  PMU_STRAP1 46C5  PMU_TO_INT 30C3 46C4  PMU_XI 46B5 60A5  PMU_XO 46B5 60A5  PMU_XT 46A6 60A5  POWERUP_OK 46B4  POWER_UP* 46C7 53A8 61D4  PRE_HRESET_L 8B4 8D5  PROBE_DIV 43B4 43D7  PSEUDO_STAR_GND 41B7 42B4  PWR_FAIL* 46B1 52D6  PWR_FAILPMU* 46B4  PWR_FAIL_T 52D7  PWR_LED 53A4  PWR_SWITCH* 9A8 46B1 46C5 61D4</p>	<p>PWR_UP 41C8 52C4 52C8 53B7 53C1 53C7 61D4  PWR_UP* 52C8  Q1P1 53C3  Q1P3 53C2  Q25_1 34A5  Q42P4 53D2  QT1P1 50B4  QT2P1 50B4  QT2P3 50B4  R264P2 41B4  RAM_ADDR&lt;12..0&gt; 14A3 14B3 14C1 14C3 14D1 14D3 16B4  16B6 17B4 17B6 17C4 17C6 55D6  RAM_BA&lt;1..0&gt; 14B3 16B4 16B6 17B6 55D6  RAM_CAS_L 14A3 16B4 17B6 55C6  RAM_CKE&lt;3..0&gt; 14A1 14B1 14C1 16B4 16B6 17C4 17C6  55C6  RAM_CS_L&lt;3..0&gt; 14A1 14B1 14C1 16B4 16B6 17B4 55C6  RAM_DATA_A&lt;63..0&gt; 15B2 15B6 15C2 15C4 15C7 15D4  15D7 16A4 16A6 16B4 16B6 16C4 16C6  16D4 16D6 55D6  RAM_DATA_B&lt;63..0&gt; 15A2 15A6 15B2 15B4 15B7 15C4  15C7 17A4 17A6 17B4 17B6 17C4 17C6  17D4 17D6 55D6  RAM_DQM_A&lt;7..0&gt; 15A6 15B2 15B6 15C4 15C7 15D4 15D7  16A4 16B4 16C4 16D4 55D6  RAM_DQM_B&lt;7..0&gt; 15A2 15A6 15B2 15B4 15B7 15C4 15C7  17A4 17B4 17C4 17D4 55D6  RAM_DQS_A&lt;7..0&gt; 15B2 15B6 15C4 15C7 15D4 15D7 16A6  16B6 16C6 16D6 55D6  RAM_DQS_B&lt;7..0&gt; 15A2 15A6 15B2 15B4 15B7 15C4 15C7  17A6 17B6 17C6 17D6 55D6  RAM_RAS_L 14A3 16B4 17B4 55C6  RAM_SA0 17A4  RAM_WE_L 14A3 16B6 17B6 55C6  RB22P2 50B2  RB27-1 50B6  RB37P1 50C6  RB160P1 52A5  RB213P2 51C3  RB227P1 51C5  REF_STAR_GND 41A5 41A7  RESET_BUTTON* 31B2 46C4 61D4  RFBA&lt;11..0&gt; 20E2 20F2 22C2 22C6 22D2 22D6 57D3  RFBABA&lt;1..0&gt; 20E2 22C2 22C6 57D3  RFBACAS_L 20G2 22B2 22B6 57D3  RFBACKE 20D2 22C2 22C6 57C3  RFBACLK0 21C1 22C6 57C3  RFBACLK0_L 21C1 22C6 57C3  RFBACLK1 21D1 22C2 57C3  RFBACLK1_L 21D1 22C2 57C3  RFBACS0_L 20F2 22B2 22B6 57C3  RFBARAS_L 20G2 22B2 22B6 57D3  RFBARE_L 20F2 22B2 22B6 57D3  RFBBA&lt;11..0&gt; 20B2 20C2 23C2 23C6 23D2 23D6 57C3  RFBBA&lt;1..0&gt; 20A2 23C2 23C6 57C3  RFBBCAS_L 20C2 23B2 23B6 57B3  RFBBCKE 20A2 23C2 23C6 57B3  RFBCLK0 21B1 23C6 57B3  RFBCLK0_L 21B1 23C6 57B3  RFBCLK1 21C1 23C2 57B3  RFBCLK1_L 21B1 23C2 57B3  RFBBCS0_L 20C2 23B2 23B6 57B3  RFBBRAS_L 20C2 23B2 23B6 57B3  RFBWE_L 20C2 23B2 23B6 57B3  RFB&lt;127..0&gt; 21B4 21B7 21C4 21C7 21D4 21D7 22B1  22B5 22C1 22C5 23B1 23B5 23C1 23C5  57C3 57D3  RFB&lt;15..0&gt; 20C2 20D2 20G2 22C2 22C6 23C2 23C6  57C3 57D3  RFB&lt;15..0&gt; 21A3 21A6 22C2 22C6 23C2 23C6 57B3  57C3  RF_CLKRUN_L 33C3 61B4  RF_DISABLE_L 33C3  RINA 41C4 42B2  RINT_PU_RESET_L 36C5  RINT_RESET_L 36C5  RJ45_4_5 37C1 59B3  RJ45_7_8 37C1 59B3  RJ45_F_TREF 37B2 59B3  RJ45_RREF 37C2 59B3  RJ45_RXN 37C1 37C2 59B3</p>	<p>RJ45_RXP 37C1 37C2 59B3  RJ45_TREF 37C2 59B3  RJ45_TXN 37C1 37C2 59B3  RJ45_TXP 37C1 37C2 59B3  ROMA14 20C8 28D8  ROMA15 20C8 28D8  ROM_CS_L 32B4 32B6 33B4 61C7  ROM_OE_L 32B2 32B6 33B2 61C7  ROM_ONBOARD_CS_L 32B2 33B4 61D4  ROM_RW_L 32B2 32B6 33B4 61B7  ROM_WP_L 32A2 61A7  RSPKRCAP 44C8  RSPKRIN1 44B6  RSPKRV1 44B6  RT78P2 37C4  RT737P1 53A5  RT401P1 49C5  RT406P2 49B3  RT418P2 52C7  RUNLED1 53A5  RUNSS 52D4  S3700P1 46A1  S3700P2 46A1  SB1P1 46A3  SENSE+ 47C6  SENSE+_1 47B5 47C7  SENSE- 47C6 47C8  SENSE-_1 47B5 47C7  SGRAVREF 22A3 22C4 22C8 54A6  SGRBVREF 23A3 23C4 23C8 54A6  SHS 52C8  SLEEP 46B5 52C4 61C4  SLEEP1 53A8  SLEEP2 53A7  SLEEPA_OFF_L 53C5  SLEEPLD_TERM 53A6  SLEEP_LED_BD 53A6  SLEEP_OFF_L 53B6 53C7  SLEEP_OFF_L2 41C7  SND_AMP_MUTE_L 30C5 44A8  SND_AMP_M_L 44A7 44B8  SND_CLKOUT 30A2 41B4  SND_HP_MUTE_L 30C5 43A7  SND_HP_M_L 43A7  SND_HP_SENSE 30B5 43A5 61B7  SND_HP_SENSE_CONN 43A3 43B2  SND_HW_RESET_L 30A8 30B5 41B4  SND_LIN_SENSE 30B5 42D4 61B4  SND_SCLK 30A2 41B1  SND_SPKR_ID 30B5 44C5  SND_SYNC 30B2 41C1  SND_TO_AUDIO 30B2 41C4  SNF_FSEL 10A7 30C5  SPDA 44C3  SPKR_JACK_DALLAS 44B1  SPKR_L+ 44B1  SPKR_L- 44B1  SPKR_MUTE_T 44B6 44B7  SPKR_R+ 44B1  SPKR_R- 44B1  SSCG_LOCK 10A7  STBYMD 52B7  STOP_AGP_L 18C6 18D3 19A8 56B7  SUPER_FLO 47C7  SW3V5V_12VIN 52C7  SW3V5V_INTVCC 52B5 52C6  SW3V5V_SGND 52A7 52A8 52B7 52B8 52C7  SW3V5V_VIN 52C6  SW3VITH2R 52A8  SW3V_3VSENSE 52B4  SW3V_BG2 52B6  SW3V_BG2R 52B5  SW3V_BOOST2 52B6  SW3V_BOOST2R 52C5  SW3V_ITH2 52A7 52B6  SW3V_RUNSS 52B6 52C3  SW3V_RUNSSR 52C3  SW3V_SNSM 52B6  SW3V_SNSP 52B6  SW3V_SW2 52B6 52C4  SW3V_SW2A 52C3</p>	<p>NOTICE OF PROPRIETARY PROPERTY  THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY  PROPERTY OF APPLE COMPUTER, INC. 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A	8	7	6	5	4	3	2	1												
							 APPLE COMPUTER INC.	<table border="1"> <tr> <td>SIZE</td> <td>DRAWING NUMBER</td> <td>REV.</td> </tr> <tr> <td>D</td> <td>051-6423</td> <td>D</td> </tr> <tr> <td>SCALE</td> <td>SHT</td> <td>OF</td> </tr> <tr> <td>NONE</td> <td>65</td> <td>74</td> </tr> </table>	SIZE	DRAWING NUMBER	REV.	D	051-6423	D	SCALE	SHT	OF	NONE	65	74
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6423	D
SCALE	NONE	SHT OF	66 74

	8	7	6	5	4	3	2	1																	
	<p>*** Unit Cross-Reference ***  --- for the entire design --</p> <p>BS1 PCB_STANDOFF 31B5  BS2 PCB_STANDOFF 31B6  BS3 PCB_STANDOFF 31D1  BS4 PCB_STANDOFF 31D1  BT1 BATTERY 46D6  C1 CAP 35C3  C2 CAP 35D3  C3 CAP 37B1  C4 CAP 38B6  C5 CAP_P 35A5  C6 CAP 35B3  C7 CAP_P 35A5  C8 CAP 23A4  C9 CAP 23A4  C10 CAP_P 38D7  C11 CAP 23A5  C12 CAP_P 38D7  C13 CAP_P 43C4  C14 CAP_P 44C7  C15 CAP 23D2  C16 CAP 23B4  C17 CAP 23D5  C18 CAP 53D2  C19 CAP_P 53D3  C20 CAP 23B2  C21 CAP 23D5  C22 CAP 23B4  C23 CAP 23B4  C24 CAP_P 42B4  C25 CAP_P 42C4  C26 CAP_P 43D4  C27 CAP 38D8  C28 CAP 23B1  C29 CAP 23B3  C30 CAP 44B4  C31 CAP 23B2  C32 CAP 23D4  C33 CAP 23B2  C34 CAP 23B3  C35 CAP 45C4  C36 CAP 37D2  C37 CAP 37D7  C38 CAP 23D2  C39 CAP 23D5  C40 CAP 23B3  C41 CAP 37D7  C42 CAP 38B8  C43 CAP 43D6  C44 CAP 37D3  C45 CAP 37D2  C46 CAP 37D4  C47 CAP_P 44D7  C48 CAP 44D7  C49 CAP 44C7  C50 CAP_P 44D7  C51 CAP 37B3  C52 CAP 37D2  C53 CAP 37D4  C54 CAP 20B8  C55 CAP 37D3  C56 CAP 37D3  C57 CAP 37D5  C58 CAP 37D5  C59 CAP_P 50B2  C60 CAP 43C6  C61 CAP 20B6  C62 CAP 20D6  C63 CAP 42B2  C64 CAP 42C2  C65 CAP 37D6  C66 CAP 37D7  C67 CAP 37D4  C68 CAP 37D6  C69 CAP 20B5  C70 CAP 20B6  C71 CAP_P 50B1  C72 CAP 20H7  C73 CAP 20H7</p>		<p>C74 CAP 37B7  C75 CAP 20H5  C76 CAP 37B7  C77 CAP 34D3  C78 CAP 20H6  C79 CAP 20H4  C80 CAP 20H2  C81 CAP 20H7  C82 CAP 20H4  C83 CAP 20H3  C84 CAP 20H8  C85 CAP_P 41C6  C86 CAP 20H8  C87 CAP 19B3  C88 CAP_P 50B1  C89 CAP 20H2  C90 CAP 20H7  C91 CAP 19B4  C92 CAP 36B6  C93 CAP 19C4  C94 CAP 19C3  C95 CAP 20H5  C96 CAP 20H2  C97 CAP 19B3  C98 CAP 20H6  C99 CAP 19D2  C100 CAP 19D3  C101 CAP 19D3  C102 CAP 19C3  C103 CAP 19D3  C104 CAP 19D4  C105 CAP 19B3  C106 CAP_P 50B2  C107 CAP 44B8  C108 CAP 44B8  C109 CAP 19D3  C110 CAP 19D2  C111 CAP 19B4  C112 CAP 19B2  C113 CAP_P 48A4  C114 CAP 34D3  C115 CAP 20H3  C116 CAP 19B3  C117 CAP 19D4  C118 CAP 19C3  C119 CAP 25B4  C120 CAP 19C3  C121 CAP 41C4  C122 CAP 44C8  C123 CAP 32C2  C124 CAP 20H1  C125 CAP 25B5  C126 CAP_P 41C7  C127 CAP_P 41B2  C128 CAP 24D3  C129 CAP 19B3  C130 CAP 25B5  C131 CAP 25B4  C132 CAP_P 50B2  C133 CAP 20H6  C134 CAP 19D4  C135 CAP 19C3  C136 CAP 19B3  C137 CAP 19B4  C138 CAP_P 48A5  C139 CAP 19D2  C140 CAP 19C2  C141 CAP 22B3  C142 CAP 22D5  C143 CAP 22D4  C144 CAP 22D5  C145 CAP 20H2  C146 CAP 19D1  C147 CAP 19C3  C148 CAP 19C4  C149 CAP 19C4  C150 CAP 19C4  C151 CAP 19C3  C152 CAP 25B5  C153 CAP 24C7  C154 CAP 10B6</p>		<p>C155 CAP_P 44D7  C156 CAP_P 44C7  C157 CAP 22A4  C158 CAP 22D5  C159 CAP 20H5  C160 CAP 25C6  C161 CAP 19C4  C162 CAP 24C6  C163 CAP 24C6  C164 CAP 24C7  C165 CAP 22B3  C166 CAP 22B3  C167 CAP 20H5  C168 CAP 19C4  C169 CAP 19C3  C170 CAP 24C7  C171 CAP_P 48A6  C172 CAP 22A4  C173 CAP 22B2  C174 CAP 22B4  C175 CAP 20H3  C176 CAP 19D2  C177 CAP 19B4  C178 CAP 19D4  C179 CAP 19B3  C180 CAP 19D3  C181 CAP 19B2  C182 CAP 19B2  C183 CAP 19D2  C184 CAP 19B2  C185 CAP 19D3  C186 CAP 19B4  C187 CAP 19B3  C188 CAP 19B1  C189 CAP 25C5  C190 CAP 24C7  C191 CAP 22A5  C192 CAP 19D5  C193 CAP 19B5  C194 CAP 22B4  C195 CAP 22B1  C196 CAP 22B2  C197 CAP 22B2  C198 CAP 22B4  C199 CAP 20H3  C200 CAP 19D4  C201 CAP 19D4  C202 CAP 19D3  C203 CAP 19D3  C204 CAP 42C5  C205 CAP 22D2  C206 CAP 24C2  C207 CAP 24C7  C208 CAP 43A5  C209 CAP 24B6  C210 CAP 16A3  C211 CAP 16A2  C212 CAP 24D6  C213 CAP 24D6  C214 CAP 19A3  C215 CAP 19A3  C216 CAP 22D2  C217 CAP 19C1  C218 CAP_P 51B3  C219 CAP 19C2  C220 CAP_P 50C8  C221 CAP 16B3  C222 CAP 18A8  C223 CAP 19C2  C224 CAP 24C2  C225 CAP 32C2  C226 CAP 24D6  C227 CAP 24C3  C228 CAP 24C3  C229 CAP 30C6  C230 CAP 15D7  C231 CAP 13C8  C232 CAP 13C8  C233 CAP 30D6  C234 CAP 30D7  C235 CAP_P 51B3</p>		<p>C236 CAP 53B3  C237 CAP_P 50C8  C238 CAP_P 53C5  C239 CAP 46A4  C240 CAP 13B8  C241 CAP 52C6  C242 CAP 16C3  C243 CAP 16C2  C244 CAP 16C3  C245 CAP_P 46D5  C246 CAP_P 53B3  C247 CAP 10D4  C248 CAP 41B4  C249 CAP 33D1  C250 CAP 33D2  C251 CAP 10D7  C252 CAP 53B2  C253 CAP 10D7  C254 CAP 53B1  C255 CAP 16C2  C256 CAP 16C1  C257 CAP 53B2  C258 CAP 53B2  C259 CAP 33D1  C260 CAP 33D2  C261 CAP 16C1  C262 CAP 49B4  C263 CAP 16C3  C264 CAP 13C4  C265 CAP 49B3  C266 CAP 49B8  C267 CAP 46D6  C268 CAP 10B8  C269 CAP_P 53D6  C270 CAP 49A5  C271 CAP 13B8  C272 CAP 49B4  C273 CAP 49C5  C274 CAP 10B8  C275 CAP 16C2  C276 CAP 16C2  C277 CAP 13B8  C278 CAP 13C6  C279 CAP 13C5  C280 CAP 13C6  C281 CAP 13C6  C282 CAP 49B6  C283 CAP 49C7  C284 CAP 49B4  C285 CAP 49B7  C286 CAP 49B7  C287 CAP 52A1  C288 CAP 13D8  C289 CAP 13D8  C290 CAP 13D8  C291 CAP 13D8  C292 CAP 13D4  C293 CAP 52A1  C294 CAP 52A1  C295 CAP_P 52C6  C296 CAP_P 49B3  C297 CAP 49C4  C298 CAP 49C5  C299 CAP 49C4  C300 CAP_P 49C3  C301 CAP_P 49C4  C302 CAP_P 52A2  C303 CAP 52C3  C304 CAP 16C2  C305 CAP 49C4  C306 CAP_P 52A2  C307 CAP_P 52A2  C308 CAP_P 52A3  C309 CAP_P 52A3  C310 CAP_P 52A2  C311 CAP 16C1  C312 CAP 31D3  C313 CAP 16C3  C314 CAP 31D3  C315 CAP 52A8  C316 CAP 52A8</p>		<p>NOTICE OF PROPRIETARY PROPERTY  THE INFORMATION CONTAINED HEREIN IS THE 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D	C317 CAP 52B8 C318 CAP 52B8 C319 CAP 16A8 C320 CAP 16C2 C321 CAP 52B7 C322 CAP 52A7 C323 CAP_P 51B3 C324 CAP_P 51B3 C325 CAP 49A4 C326 CAP_P 52B2 C327 CAP_P 52B2 C328 CAP_P 52B2 C329 CAP 51B3 C330 CAP_P 51B3 C331 CAP_P 51B2 C332 CAP_P 51B2 C333 CAP 52B6 C334 CAP_P 47C1 C335 CAP_P 47C2 C336 CAP_P 47C2 C337 CAP_P 47C2 C338 CAP_P 52D7 C339 CAP_P 52D7 C340 CAP_P 47C2 C341 CAP 52B7 C342 CAP_P 51C3 C343 CAP_P 47D3 C344 CAP 52B4 C345 CAP 4B1 C346 CAP_P 47B1 C347 CAP_P 47B2 C348 CAP_P 47B2 C349 CAP_P 52D7 C350 CAP 9D4 C351 CAP_P 51C3 C352 CAP 4B1 C353 CAP 9D4 C354 CAP_P 52D4 C355 CAP_P 52D4 C356 CAP_P 52D4 C357 CAP_P 47D3 C358 CAP 51C4 C359 CAP 51B7 C360 CAP_P 48C4 C361 CAP_P 47B2 C362 CAP_P 47B2 C363 CAP_P 48C5 C364 CAP 9C1 C365 CAP_P 48C6 C366 CAP 48C5 C367 CAP 47B6 C369 CAP 4A1 C370 CAP 4A1 C371 CAP 47C5 C372 CAP 47B7 C373 CAP 47C8 C374 CAP 47B5 C375 CAP 38C1 C376 CAP 38D3 C377 CAP 35A4 C378 CAP 38C2 C379 CAP 38C2 C380 CAP 35C3 C381 CAP 38D3 C382 CAP 35A4 C383 CAP 35C4 C384 CAP 38C2 C385 CAP 35B3 C386 CAP 35C3 C387 CAP 35C4 C388 CAP 44A1 C389 CAP 35B4 C390 CAP 38A6 C391 CAP 35B4 C392 CAP 38A6 C393 CAP 38A6 C394 CAP 38A6 C395 CAP 38A6 C396 CAP 35A4 C397 CAP 35D3 C398 CAP 35B3	C399 CAP 38A7 C400 CAP 37C1 C401 CAP 37D1 C402 CAP 44C3 C403 CAP 35D4 C404 CAP 35D4 C405 CAP 35D3 C406 CAP 38B6 C407 CAP 38A6 C408 CAP 43B3 C409 CAP 27B3 C410 CAP 38D4 C411 CAP 38A7 C412 CAP 43C3 C413 CAP 35D3 C414 CAP 27B4 C415 CAP 27A4 C416 CAP 27A4 C417 CAP 35B3 C418 CAP 38D4 C419 CAP 44B3 C420 CAP 43D3 C421 CAP 27D4 C422 CAP 27C4 C423 CAP 27C3 C424 CAP 38B6 C425 CAP 44B3 C426 CAP 44B2 C427 CAP 43B3 C428 CAP 38B7 C429 CAP 44B2 C430 CAP 44B2 C431 CAP 42B6 C432 CAP 27B3 C433 CAP 35C3 C434 CAP 37D1 C435 CAP 44B2 C436 CAP 43A4 C437 CAP 42C6 C438 CAP 37D2 C439 CAP 38B4 C440 CAP 38B4 C441 CAP 38C5 C442 CAP 38C5 C443 CAP 38B2 C444 CAP 38B2 C445 CAP 38C3 C446 CAP 38C3 C447 CAP 42B6 C448 CAP 44B4 C449 CAP 35A7 C450 CAP 38B3 C451 CAP 38B2 C452 CAP 38B4 C453 CAP 43B4 C454 CAP 42C6 C455 CAP 38B4 C456 CAP 38B3 C457 CAP 38B4 C458 CAP 38B3 C459 CAP 44B4 C460 CAP 23D2 C461 CAP 23B7 C462 CAP 23B8 C463 CAP 38B4 C464 CAP 38B4 C465 CAP 44B4 C466 CAP 23B7 C467 CAP 23D8 C468 CAP 38B1 C469 CAP 23B7 C470 CAP 23B8 C471 CAP 38B4 C472 CAP 38B1 C473 CAP 53D2 C474 CAP 44A4 C475 CAP 23B6 C476 CAP 23B6 C477 CAP 38C7 C478 CAP 45B5 C479 CAP_P 42C4	C480 CAP_P 42B4 C481 CAP 38B3 C482 CAP 38B3 C483 CAP 38B3 C484 CAP 38B5 C485 CAP 38B1 C486 CAP 23B6 C487 CAP 23D7 C488 CAP 23B7 C489 CAP 23B5 C490 CAP 38B6 C491 CAP 44B4 C492 CAP 23D2 C493 CAP 23D7 C494 CAP 23D7 C495 CAP 38B2 C496 CAP 38B2 C497 CAP 44B4 C498 CAP 44B4 C499 CAP 43D6 C500 CAP 38C7 C501 CAP 44C5 C502 CAP 44C5 C503 CAP 43C6 C504 CAP 43B7 C505 CAP 42D3 C506 CAP 42C3 C507 CAP 42B3 C508 CAP 43C6 C509 CAP 20B8 C510 CAP 20B8 C511 CAP 34D5 C512 CAP_P 43B7 C513 CAP 42B3 C514 CAP 42C3 C515 CAP 34C8 C516 CAP 34D8 C517 CAP 34C8 C518 CAP 43B7 C519 CAP 34D8 C520 CAP 22D2 C521 CAP 22D2 C522 CAP 34D8 C523 CAP 34D8 C524 CAP_P 43C7 C525 CAP_P 43D7 C526 CAP 34D5 C527 CAP 34D8 C528 CAP_P 50B3 C529 CAP 34D5 C530 CAP 34C7 C531 CAP 34C7 C532 CAP 22D7 C533 CAP 22D7 C534 CAP 22B6 C535 CAP 22B6 C536 CAP 34C8 C537 CAP 34D7 C538 CAP_P 41D6 C539 CAP_P 41B7 C540 CAP 22D8 C541 CAP 34D8 C542 CAP 45C3 C543 CAP 50B3 C544 CAP 22D7 C545 CAP 22B7 C546 CAP 34D8 C547 CAP 34D7 C548 CAP 34D8 C549 CAP 41D7 C550 CAP 45B2 C551 CAP 45B3 C552 CAP 50B6 C553 CAP 22B6 C554 CAP 22B7 C555 CAP 34D7 C556 CAP 45C2 C557 CAP 50C3 C558 CAP_P 50B2 C559 CAP 34D8 C560 CAP 44B5	C561 CAP 44B6 C562 CAP 17C2 C563 CAP 50B6 C564 CAP 50C2 C565 CAP 22B5 C566 CAP 22B7 C567 CAP 22B7 C568 CAP 22B8 C569 CAP 22B8 C570 CAP 34C8 C571 CAP 44B5 C572 CAP 50B7 C573 CAP 34C8 C574 CAP 34C8 C575 CAP 44B7 C576 CAP 50C3 C577 CAP 44B5 C578 CAP 41C5 C579 CAP 41C5 C580 CAP_P 41B2 C581 CAP 41B2 C582 CAP 44C3 C583 CAP 48A5 C584 CAP 41D3 C585 CAP 50B5 C586 CAP 50A4 C587 CAP 41B2 C588 CAP 41B5 C589 CAP 50C5 C590 CAP 53C4 C591 CAP_P 41D2 C592 CAP 41B5 C593 CAP 17C1 C594 CAP 41B5 C595 CAP 41B5 C596 CAP 50C6 C597 CAP 41C4 C598 CAP 9B2 C599 CAP 41D4 C600 CAP_P 41D4 C601 CAP 31A5 C602 CAP 17C1 C603 CAP 18A8 C604 CAP_P 31A7 C605 CAP 24C2 C606 CAP 24C2 C607 CAP 24C1 C608 CAP 31A6 C609 CAP 31A4 C610 CAP 26A3 C611 CAP 15D7 C612 CAP 15D7 C613 CAP 15D6 C614 CAP 31A6 C615 CAP 31A4 C616 CAP 24A3 C617 CAP 24A3 C618 CAP 25A6 C619 CAP 31C7 C620 CAP 31C7 C621 CAP 31D7 C622 CAP 31D7 C623 CAP 30D7 C624 CAP 17C1 C625 CAP 31A7 C626 CAP 25A6 C627 CAP 31A6 C628 CAP 13C8 C629 CAP_P 31A5 C630 CAP 31A6 C631 CAP 50C7 C632 CAP 50C7 C633 CAP 50C7 C634 CAP 50C7 C635 CAP_P 25A6 C636 CAP_P 25A8 C637 CAP 13C4 C638 CAP 13C8 C639 CAP 31A4 C640 CAP 31A3 C641 CAP 13B8					
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	D	051-6423	D
SCALE	NONE	SHT OF	68 74

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D	C642 CAP 45C7 C643 CAP 13C2 C644 CAP 13B2 C645 CAP 13B1 C646 CAP 13B1 C647 CAP 13C7 C648 CAP 13B7 C649 CAP 13C6 C650 CAP 13C7 C651 CAP 13C5 C652 CAP 26B2 C653 CAP 53B4 C654 CAP 53B4 C655 CAP 13C4 C656 CAP 13C2 C657 CAP 13B3 C658 CAP 13B2 C659 CAP 13B3 C660 CAP 13B1 C661 CAP 13B7 C662 CAP 13B7 C663 CAP 13B6 C664 CAP 13C5 C665 CAP 13C6 C666 CAP 13C5 C667 CAP 13C6 C668 CAP 13A5 C669 CAP 15C5 C670 CAP 15C6 C671 CAP 15C5 C672 CAP 17C1 C673 CAP 13B2 C674 CAP 13B6 C675 CAP 45B7 C676 CAP 13B3 C677 CAP 13B1 C678 CAP 13B3 C679 CAP 13B1 C680 CAP 13C3 C681 CAP 13B3 C682 CAP 13B3 C683 CAP 13C6 C684 CAP 13C7 C685 CAP 13C6 C686 CAP 13C6 C687 CAP 13A6 C688 CAP 13B7 C689 CAP 13C1 C690 CAP 13B2 C691 CAP 13C2 C692 CAP 30D6 C693 CAP 13C7 C694 CAP 13C5 C695 CAP 13C5 C696 CAP 13C5 C697 CAP 13C7 C698 CAP 13B5 C699 CAP 13A7 C700 CAP 13B7 C701 CAP 46B2 C702 CAP 13D4 C703 CAP 30D6 C704 CAP 30D5 C705 CAP 46A6 C706 CAP 46A6 C707 CAP 13B3 C708 CAP 13B3 C709 CAP 13C6 C710 CAP 13C6 C711 CAP 13B6 C712 CAP 13B1 C713 CAP 30D5 C714 CAP 13A5 C715 CAP 46B2 C716 CAP 13B2 C717 CAP 13C3 C718 CAP 13B1 C719 CAP 13D2 C720 CAP 13C3 C721 CAP 13D3 C722 CAP 13A7	C723 CAP 13A6 C724 CAP 13A5 C725 CAP 13B7 C726 CAP 17C2 C727 CAP 13D4 C728 CAP 13C3 C729 CAP 13B3 C730 CAP 13D3 C731 CAP 13D1 C732 CAP 13D1 C733 CAP 13C7 C734 CAP 13D2 C735 CAP 13A7 C736 CAP 13A5 C737 CAP 10D6 C738 CAP 10D7 C739 CAP 10D6 C740 CAP 10B5 C741 CAP 17C2 C742 CAP 53C6 C743 CAP 13B3 C744 CAP 13B1 C745 CAP 13C3 C746 CAP 13D3 C747 CAP 13D1 C748 CAP 13D2 C749 CAP 13D3 C750 CAP 13A6 C751 CAP 13A7 C752 CAP 13A6 C753 CAP 13A6 C754 CAP 13A5 C755 CAP 13A6 C756 CAP 10D5 C757 CAP 10D6 C758 CAP 10D4 C759 CAP 46D5 C760 CAP 18D6 C761 CAP 13B3 C762 CAP 13C1 C763 CAP 13D2 C764 CAP 13D3 C765 CAP 13D3 C766 CAP 13D1 C767 CAP 13B6 C768 CAP 13A6 C769 CAP 10D5 C770 CAP 10D6 C771 CAP 10C6 C772 CAP 10C6 C773 CAP 53C5 C774 CAP 13C1 C775 CAP 13C3 C776 CAP 13B2 C777 CAP 13C1 C779 CAP 13D1 C780 CAP 13D2 C781 CAP 13A5 C782 CAP 13A7 C783 CAP 46D5 C784 CAP 30A6 C785 CAP 30C3 C787 CAP 13D3 C788 CAP 13D3 C789 CAP 13D3 C790 CAP 13D1 C791 CAP 13A6 C792 CAP 13A6 C793 CAP 13B6 C794 CAP 13B5 C795 CAP 30C3 C796 CAP 13C3 C797 CAP 13D3 C798 CAP 13D1 C799 CAP 13D1 C800 CAP 13D2 C801 CAP 13A7 C802 CAP 13A5 C803 CAP 13A6 C804 CAP 10D3 C805 CAP 13B2	C806 CAP 13C2 C807 CAP 13B2 C808 CAP 13D2 C809 CAP 13C3 C810 CAP 13D2 C811 CAP 13D2 C812 CAP 13C2 C813 CAP 13A5 C814 CAP 13A6 C815 CAP 13B6 C816 CAP 13A6 C817 CAP 13B6 C818 CAP 13A5 C819 CAP 17C2 C820 CAP 30C3 C821 CAP 13C1 C822 CAP 13C1 C823 CAP 13D7 C824 CAP 13D6 C825 CAP 13A7 C826 CAP 13A7 C827 CAP 13B5 C828 CAP 13C1 C829 CAP 13C2 C830 CAP 13C3 C831 CAP 13D3 C832 CAP 13D7 C833 CAP 13D3 C834 CAP 13D6 C835 CAP 13B3 C836 CAP 13B6 C837 CAP 13B7 C838 CAP 13A7 C839 CAP 13B6 C840 CAP 30A6 C841 CAP 13D4 C842 CAP 13D6 C843 CAP 13C7 C844 CAP 13D6 C845 CAP 13D6 C846 CAP 13D7 C847 CAP 14A7 C848 CAP 32D5 C849 CAP 13C6 C850 CAP 13B3 C851 CAP 13B3 C852 CAP 13B2 C853 CAP 14A7 C854 CAP 11D3 C855 CAP 13B5 C856 CAP 13A7 C857 CAP 13B6 C858 CAP 15D4 C859 CAP 15D3 C860 CAP 15D4 C861 CAP 46A1 C862 CAP 46B7 C863 CAP 13C2 C864 CAP 17C2 C865 CAP 13B1 C866 CAP 13C3 C867 CAP 13C3 C868 CAP 13C2 C869 CAP 13D5 C870 CAP 13D6 C871 CAP 13D6 C872 CAP 13D5 C873 CAP 13D5 C874 CAP 13D5 C875 CAP 13B3 C876 CAP 13B5 C877 CAP 13B7 C878 CAP 13B7 C879 CAP 13B2 C880 CAP 13B5 C881 CAP 13B3 C882 CAP 13B1 C883 CAP 13B2 C884 CAP 13D6 C885 CAP 13D7 C886 CAP 13D5	C887 CAP 13D7 C888 CAP 13D6 C889 CAP 13D6 C890 CAP 13D7 C891 CAP 13D6 C892 CAP 13C4 C893 CAP 13C1 C894 CAP 13C2 C895 CAP 13D5 C896 CAP 13D5 C897 CAP 13D6 C898 CAP 13D5 C899 CAP 13D5 C900 CAP 13D7 C901 CAP 13D7 C902 CAP 13C7 C903 CAP 13B2 C904 CAP 13B6 C905 CAP 10B7 C906 CAP 10B7 C907 CAP 10B8 C908 CAP 15C2 C909 CAP 15C2 C910 CAP 15C1 C911 CAP 39C2 C912 CAP 10B8 C913 CAP 10B7 C914 CAP 17C2 C915 CAP 53D7 C916 CAP 14A8 C917 CAP 39C5 C918 CAP 53D6 C919 CAP 40C7 C920 CAP 40C1 C921 CAP 40C5 C922 CAP 17C1 C923 CAP 40B5 C924 CAP 40B7 C925 CAP 17C2 C926 CAP 52C1 C927 CAP 52B1 C928 CAP_P 49B4 C929 CAP 16A7 C930 CAP 52B8 C931 CAP 52B5 C932 CAP 52A5 C933 CAP 52A6 C934 CAP 52B1 C935 CAP_P 49B3 C936 CAP 17C2 C937 CAP 52C7 C938 CAP 52A4 C939 CAP 52A6 C940 CAP 52A5 C941 CAP 52C5 C942 CAP 8C3 C943 CAP 17A3 C944 CAP 47D3 C945 CAP 17A2 C946 CAP 8A6 C947 CAP_P 47C1 C948 CAP 51B4 C949 CAP 47C5 C950 CAP 47D4 C951 CAP 47D4 C952 CAP 47D4 C953 CAP 47D4 C954 CAP 4C2 C955 CAP 52D4 C956 CAP 52D5 C957 CAP_P 47B1 C958 CAP 9D4 C959 CAP 51C4 C960 CAP 47B2 C961 CAP 9C2 C962 CAP 51C3 C963 CAP 47D4 C964 CAP 47D4 C965 CAP 47D4 C966 CAP 47D4 C967 CAP 9C1					
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	D	051-6423	D
SCALE	SHT OF		
NONE	69 OF 74		

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D	C968 CAP 51C4 C969 CAP 9D4 C970 CAP 52D5 C971 CAP 52D5 C972 CAP 9C2 C973 CAP 9C1 C974 CAP 9D1 C975 CAP 51C4 C976 CAP 9C2 C977 CAP 9D1 C978 CAP 52D5 C979 CAP 52D5 C980 CAP 9C2 C981 CAP 9C2 C982 CAP 9D2 C983 CAP 9D3 C984 CAP 51C3 C985 CAP 52D4 C986 CAP 52D5 C987 CAP 9D2 C988 CAP 9D3 C989 CAP 51C3 C990 CAP 9C1 C991 CAP 9D1 C992 CAP 9D3 C993 CAP 9C1 C994 CAP 9C2 C995 CAP 9D2 C996 CAP 9A6 C997 CAP 9A5 C998 CAP 9D2 C999 CAP 9D1 C1000 CAP 51B6 C1001 CAP 9A5 C1002 CAP 9A6 C1003 CAP 9D1 C1004 CAP 51B6 C1005 CAP 51C4 C1006 CAP 47C2 C1007 CAP 9C2 C1008 CAP 9B5 C1009 CAP 9A6 C1010 CAP 9B5 C1011 CAP 9B5 C1012 CAP 9A6 C1013 CAP 9D2 C1014 CAP 9D2 C1015 CAP 51B7 C1016 CAP 9C1 C1017 CAP 9A5 C1018 CAP 9A5 C1019 CAP 51C6 C1020 CAP 9C1 C1021 CAP 9C1 C1022 CAP 9B6 C1023 CAP 9A5 C1024 CAP 9A5 C1025 CAP 9B5 C1026 CAP 9D2 C1027 CAP 9B6 C1028 CAP 9A5 C1029 CAP 47B4 C1030 CAP 47B2 C1031 CAP 9A5 C1032 CAP 9C2 C1033 CAP 9C1 C1034 CAP 47B2 C1035 CAP 4D3 C1036 CAP 4D3 C1037 CAP 9C1 C1038 CAP 47B4 C1039 CAP 47C5 C1040 CAP 9C2 C1041 CAP 9D3 C1042 CAP 9C2 C1043 CAP 9D2 C1044 CAP 47D7 C1045 CAP 47B7 C1046 CAP 47C1 C1047 CAP 47D5 C1048 CAP 47C2	C1049 CAP 47B7 C1050 CAP 47C7 C1051 CAP 47B6 C1052 CAP 47C8 C1053 CAP 48A8 C1054 CAP 9B2 C1055 CAP 9B2 C1056 CAP 9B1 C1057 CAP 9B1 C1058 CAP 9B1 C1101 CAP 11D3 C1601 CAP 16D7 C1602 CAP 16D3 C1603 CAP 16D3 C1701 CAP 17D7 C1702 CAP 17D7 C1801 CAP 18D5 C1802 CAP 18A7 C1901 CAP 9A6 C1902 CAP 9A5 C1903 CAP 9A5 C1904 CAP 9A5 C1910 CAP 9A5 C1911 CAP 9B2 C1912 CAP 9B2 C1913 CAP 9B1 C1914 CAP 9B1 C2201 CAP 22C8 C2202 CAP 22C4 C2301 CAP 23C8 C2302 CAP 23C4 C2501 CAP_P 25A8 C3001 CAP 30C5 C3002 CAP 30D5 C3003 CAP 30D5 C3004 CAP 30D4 C3005 CAP 30D4 C3201 CAP 32D4 C3901 CAP 39D1 C4081 CAP 48C7 C4301 CAP 43A6 C4502 CAP_P 45C5 C4701 CAP 47B6 D1 DIODE_DUAL_6P 38A7 D2 DIODE_DUAL_6P 38A7 D3 DIODE_DUAL_6P 38A7 D4 DIODE_DUAL_6P 38B7 D5 ZENER_MMBZ15VDLT1 42B6 D6 DIODE_DUAL_6P 27D4 D7 DIODE_DUAL_6P 27D4 D8 DIODE 38D6 D9 DIODE 37B8 D10 DIODE_SCHOT 25B8 D11 DIODE_SCHOT 46D7 D12 DIODE_SCHOT 46D7 D13 DIODE_SCHOT 49C7 D14 DIODE_SCHOT 49B5 D15 DIODE_SCHOT 51B4 D16 DIODE_SCHOT 52C5 D17 DIODE_SCHOT 52B5 D18 DIODE_SCHOT 51C6 D19 DIODE_SCHOT 48D6 D20 DIODE_SCHOT 47D5 D21 DIODE_SCHOT 47B5 D22 ZENER_MMBZ15VDLT1 44C2 D23 DIODE_SCHOT_3P 44A1 D24 ZENER_MMBZ15VDLT1 43B2 D25 ZENER 38B6 D26 ZENER_MMBZ15VDLT1 43B2 D27 DIODE_DUAL_6P 27B4 D28 DIODE_DUAL_6P 27B4 D29 ZENER_MMBZ15VDLT1 42B5 D30 DIODE_SCHOT 50C5 D31 DIODE_SCHOT 50B3 D32 DIODE_SCHOT 50B5 D33 DIODE_SCHOT 48B6 D34 DIODE_SCHOT 50B1 D35 DIODE_SCHOT 46D6 D36 DIODE_SCHOT 46D6 D37 DIODE_SCHOT 52B2 D38 DIODE_SCHOT 52C6	D39 DIODE_SCHOT 52B4 D40 DIODE_SCHOT 52A4 D41 DIODE_SCHOT 47C4 D42 DIODE_SCHOT 51B5 D43 DIODE_SCHOT 47B3 D44 DIODE_SCHOT 47D2 D45 DIODE_SCHOT 47D6 D46 DIODE_SCHOT 51B2 D4901 DIODE_SCHOT 49B2 DS1 LED 37A2 DS2 LED 37A1 DS3 LED 37A1 DS4 LED 32A3 DS5 LED 53A5 DS6 LED 53A6 DS7 LED 40B2 DS8 LED 40B6 DS9 LED 52D6 DS10 LED 53A3 F1 FUSE 35A7 F2 FUSE 38D5 F3 FUSE 38D5 FL1 FILTER_12P 44B3 FL2 FILTER_LC 27C6 FL3 FILTER_LC 27C6 FL4 FILTER_LC 27B6 J1 CON_RJ45 37C1 J2 CON_FWVERT_SKT 38C1 J3 CON_F8RT_S_TH1 44B1 J4 CON_F4RT_USB_UPRIGHT 35C3 J5 CON_FWVERT_SKT 38D1 J6 CON_F4RT_USB_UPRIGHT 35B3 J7 CON_F4RT_USB_UPRIGHT 35D3 J8 CON_F4RT_S4MT_TH1 43C1 J9 CON_F14RT_D4MT_TH1 27C5 J10 CON_F4RT_S4MT_TH1 42C7 J11 CON_F184ST_DDRDIMM 17D5 J12 CON_F21ST_D2MT_SM 26C3 J13 CON_M40SM_635 31D6 J14 CON_M16ST_D_TH 31B5 J15 CON_M40ST_NC20 40D6 J16 CON_M40ST_NC20 40D2 J17 CON_M16ST_MICROFIT 52D8 J18 CON_M4ST_LCK 36B4 J19 CON_37SM_MTOR 9C5 J20 CON_38SM_MTOR 9D7 J21 CON_M3ST_LCK 47C8 J22 CON_F20SM_KX 9B3 J23 CON_F1ST_S2MT_SM 30B7 J24 CON_M12ST_SM 31B3 J25 CON_F100RT_LP_SM 33D3 J26 CON_F200RT_DDRDIMM_SM2 16D5 J27 CON_F1ST_S2MT_SM 9C3 J28 CON_F10ST_D_SMA 31D2 J29 CON_M6ST_BTRY 8B6 J30 CON_38SM_MTOR 9D5 J31 CON_38SM_MTOR 9C7 J32 CON_F12RT_S2MT_SM 9B8 J4501 CON_F4ST_S2MT_SM 45C6 J4502 CON_F4ST_S2MT_SM 45C6 JAZ1 TP 53A4 L1 IND 38D4 L2 IND 35A5 L3 IND 38D4 L4 IND 35A5 L5 FILTER_4P 38C2 L6 FILTER_4P 38C2 L7 FILTER_4P 38D2 L8 FILTER_4P 38D2 L9 IND 27C2 L10 IND 44A5 L11 IND 44B5 L12 IND 50B3 L13 IND 24C7 L14 IND 10D5 L15 IND 10D7 L16 IND 30C3 L17 IND 10B8 L18 IND 10B8 L19 IND 49B4 L20 IND 47C3	L21 IND_3P 51B4 L22 IND_3P 52A3 L23 IND 52C3 L24 IND 47B3 L25 IND 44C3 L26 IND 44B1 L27 IND 43C3 L28 IND 35C5 L29 IND 35C5 L30 IND 35B5 L31 IND 35B5 L32 IND 38B6 L34 IND 43D3 L35 IND 43B3 L36 IND 42B6 L37 IND 35D5 L38 IND 35D5 L39 IND 43A3 L40 IND 44B1 L41 IND 44C3 L42 IND 43B3 L43 IND 43A4 L44 IND 43C3 L45 IND 43D3 L46 IND 42C6 L47 IND 43B3 L48 IND 42C6 L49 IND 42B5 L50 IND 42C5 L51 IND 42D6 L52 IND 42C5 L53 IND 42D5 L54 IND 44B5 L55 IND 44B5 L56 IND 46A1 L57 IND 46A1 L58 IND 34D6 L59 IND 45B5 L60 IND 45C5 L61 IND 44D7 L62 IND 44D7 L63 IND 41D4 L64 IND 26A5 L65 IND 31A7 L66 IND 31A3 L67 FILTER_4P 26A5 L68 IND 24C2 L69 IND 31A3 L70 IND 31A7 L71 FILTER_4P 26B5 L72 IND 31A7 L73 IND 31A3 L74 FILTER_4P 26C5 L75 IND 31A7 L76 FILTER_4P 26D5 L77 IND 31B7 L78 IND 31A3 L79 IND 45C7 L80 IND 31B3 L81 IND 45B7 L82 IND 26D5 L83 IND 45B7 L84 IND 52D7 L85 IND 52D7 L86 IND 52D7 L2401 IND 24C7 L2501 IND 25A6 Q1 TRA_2N7002 53C2 Q2 TRA_2N7002 44A7 Q3 TRA_2N7002 44B7 Q4 TRA_2N7002 44C4 Q5 TRA_2N7002 43A5 Q6 TRA_2N7002 53C4 Q7 TRA_IRLR8203 50B4 Q8 TRA_IRLR8503 50B4 Q9 TRA_IRLR8503 50C4 Q10 TRA_2N7002 24B7 Q11 TRA_FDC602P 53C2 Q12 TRA_2N3904 46D7 Q13 TRA_2N7002 53B2 Q14 TRA_2N7002 53C7					
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	8	7	6	5	4	3	2	1					
D	Q15	TRA_2N7002	53B7	R43	RES	35D6	R124	RES	25C4	R205	RES	18A8	D
	Q16	TRA_2N7002	53A7	R44	RES	35D6	R125	RES	28D5	R206	RES	24D7	
	Q17	TRA_2N7002	53B6	R45	RES	35C6	R126	RES	28C6	R207	RES	25D2	
	Q18	TRA_2N7002	53A7	R46	RES	35C6	R127	RES	34B8	R208	RES	25D2	
	Q19	TRA_2N7002	53A6	R47	RES	35B6	R128	RES	34B8	R209	RES	16B3	
	Q20	TRA_FDC602P	53D7	R48	RES	35B6	R129	RES	34D3	R210	RES	19B8	
	Q21	TRA_2N7002	52C3	R49	RES	20B7	R130	RES	20D2	R211	RES	18C7	
	Q22	TRA_2N7002	52D3	R50	RES	20B7	R131	RES	20D3	R212	RES	32B3	
	Q23	TRA_2N7002	52C4	R51	RES	44C3	R132	RES	21D2	R213	RES	25C4	
	Q24	TRA_2N7002	52C3	R52	RES	20C5	R133	RES	24D3	R214	RES	29B5	
Q25	TRA_IRF7811W	49B5	R53	RES	21A4	R134	RES	28B8	R215	RES	18C7		
Q26	TRA_IRF7805	49B5	R54	RES	20D3	R135	RES	28A8	R216	RES	18B7		
Q27	TRA_IRLR8203	51B4	R55	RES	21A4	R136	RES	28C5	R217	RES	18D3		
Q28	TRA_IRLR8503	51B4	R56	RES	20D2	R137	RES	44B7	R218	RES	32D6		
Q29	TRA_2N7002	47D8	R57	RES	21A4	R138	RES	32B3	R219	RES	32D6		
Q30	TRA_2N3904	38C7	R58	RES	20D3	R139	RES	32B4	R220	RES	18C7		
Q31	TRA_2N7002	43A8	R59	RES	20C2	R140	RES	21A7	R221	RES	18D1		
Q32	TRA_2N7002	41C7	R60	RES	21A4	R141	RES	25B4	R222	RES	18D3		
Q33	TRA_2N3904	45B2	R61	RES	20A3	R142	RES	28C5	R223	RES	18D1		
Q34	TRA_IRLR8203	50A4	R62	RES	20A2	R143	RES	20G2	R224	RES	18C7		
Q35	TRA_2N7002	42D4	R63	RES	21A4	R144	RES	25B2	R225	RES	18D7		
Q37	TRA_FDC602P	53C6	R64	RES	20D3	R145	RES	24C3	R226	RES	18C7		
Q38	TRA_2N7002	53B6	R65	RES	21A4	R146	RES	21A7	R227	RES	32C6		
Q39	TRA_2N3904	52C7	R66	RES	20D2	R147	RES	21A7	R228	RES	30C7		
Q40	TRA_IRLR8203	47C4	R67	RES	20D2	R148	RES	21C2	R229	RES	36C1		
Q41	TRA_2N7002	8D5	R68	RES	21A4	R149	RES	25B5	R230	RES	32B6		
Q42	TRA_2N7002	8C6	R69	RES	21A4	R150	RES	50C5	R231	RES	30C8		
Q43	TRA_IRLR8203	47C4	R70	RES	20D3	R151	RES	22A4	R232	RES	30C6		
Q44	TRA_IRLR8503	47C4	R71	RES	20D6	R152	RES	22A5	R233	RES	18B3		
Q45	TRA_IRF7811W	52B4	R72	RES	19A5	R153	RES	21A7	R234	RES	18D1		
Q46	TRA_IRF7805	52C4	R73	RES	28C2	R154	RES	20G3	R235	RES	29B5		
Q47	TRA_IRLR8203	52A4	R74	RES	28D2	R155	RES	20G2	R236	RES	29C5		
Q48	TRA_IRLR8503	52A4	R75	RES	37B8	R156	RES	21C2	R237	RES	25C4		
Q49	TRA_IRLR8203	47B4	R76	RES	37B8	R157	RES	25B2	R238	RES	29C6		
Q50	TRA_IRLR8503	47B4	R77	RES	28D2	R158	RES	21D2	R239	RES	29B6		
Q51	TRA_IRLR8203	47B4	R78	RES	34A7	R159	RES	21A7	R240	RES	29B5		
Q52	TRA_2N7002	48A7	R79	RES	28C2	R160	RES	19A5	R241	RES	29C5		
Q53	TRA_FDC602P	48B7	R80	RES	19A5	R161	RES	18D1	R242	RES	29B6		
Q4801	TRA_FDC602P	48D7	R81	RES	21A7	R162	RES	43A5	R243	RES	29C6		
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R5	RES	23A4	R86	RES	25D6	R167	RES	21A7	R248	RES	29C7		
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C													C
B													B
A													A

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	D	051-6423	D
SCALE	NONE	SHT	OF
		71	74

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	R287 RES 46D5		R368 RES 11C6		R450 RES 35B5		R531 RES 41C2	
	R288 RES 14D4		R369 RES 51C6		R451 RES 35B5		R532 RES 41C4	
	R289 RES 30C8		R370 RES 52D6		R452 RES 35C5		R533 RES 28D6	
	R290 RES 46D8		R371 RES 51B6		R453 RES 35C5		R534 RES 28C6	
	R291 RES 30C7		R372 RES 48D4		R454 RES 35D5		R535 RES 29C5	
	R292 RES 30C7		R374 RES 6C6		R455 RES 35D5		R536 RES 29B5	
	R293 RES 33C4		R375 RES 6C6		R456 RES 35B7		R537 RES 29C4	
	R294 RES 30C1		R376 RES 6C6		R457 RES 35B7		R538 RES 29B4	
	R295 RES 49B3		R377 RES 6C6		R458 RES 35C7		R539 RES 29B6	
C	R296 RES 49B7		R378 RES 6C7		R459 RES 35C7		R540 RES 26A6	
	R297 RES 46D6		R379 RES 6C7		R460 RES 35D7		R541 RES 28A6	
	R298 RES 46B1		R380 RES 6C7		R461 RES 35D7		R542 RES 28B6	
	R299 RES 49B3		R381 RES 6C7		R462 RES 42B3		R543 RES 29C6	
	R300 RES 53A5		R382 RES 6C8		R463 RES 20B8		R544 RES 29B7	
	R301 RES 49B5		R383 RES 6C8		R464 RES 34C3		R545 RES 18A8	
	R302 RES 53A6		R384 RES 47C5		R465 RES 34C3		R546 RES 26A5	
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	R304 RES 49B3		R386 RES 47B5		R467 RES 34C2		R548 RES 29C7	
	R305 RES 49B4		R387 RES 47B5		R468 RES 34C3		R549 RES 18A8	
B	R306 RES 30B8		R388 RES 47B6		R469 RES 34C3		R550 RES 26A5	
	R307 RES 36D7		R389 RES 47B6		R470 RES 34B3		R551 RES 26A5	
	R308 RES 9C3		R390 RES 37C1		R471 RES 44B4		R552 RES 28C8	
	R309 RES 49B7		R391 RES 37C2		R472 RES 43C7		R553 RES 28D8	
	R310 RES 11A4		R392 RES 43B3		R473 RES 43D7		R554 RES 24B7	
	R311 RES 4B8		R393 RES 37C2		R474 RES 42C3		R555 RES 24C3	
	R312 RES 4A8		R394 RES 37C2		R475 RES 50B1		R556 RES 24C3	
	R313 RES 9B4		R395 RES 38B7		R476 RES 34B3		R557 RES 24C2	
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	R315 RES 49B3		R397 RES 28A6		R478 RES 43B5		R559 RES 26B6	
A	R316 RES 49B7		R398 RES 35A6		R479 RES 21A6		R560 RES 28C7	
	R317 RES 49C5		R399 RES 28B6		R480 RES 34C3		R561 RES 28D7	
	R318 RES 49C6		R400 RES 43C4		R481 RES 41B6		R562 RES 24B6	
	R319 RES 11A4		R401 RES 28B5		R482 RES 41C6		R563 RES 29C5	
	R320 RES 49B5		R402 RES 38B4		R483 RES 21A6		R564 RES 25C4	
	R321 RES 11A4		R403 RES 21B2		R484 RES 34A7		R565 RES 30D5	
	R322 RES 52C3		R404 RES 38D8		R485 RES 37B2		R566 RES 32D6	
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R342 RES 11C7		R424 RES 38C7		R505 RES 50C5		R586 RES 30D6		
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R358 RES 11A7		R440 RES 43A8		R521 RES 41C5		R604 RES 46A3		
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R362 RES 47B3		R444 RES 43C6		R525 RES 44D4		R608 RES 53A4		
R363 RES 11C6		R445 RES 28B6		R526 RES 44C4		R609 RES 53A4		
R364 RES 11C6		R446 RES 43C6		R527 RES 50B7		R610 RES 48B4		
R365 RES 11C5		R447 RES 43D6		R528 RES 41C4		R611 RES 30A6		
R366 RES 11A7		R448 RES 42B3		R529 RES 50C6		R612 RES 53A4		

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	D	051-6423	D
SCALE	NONE	SHT OF	72 OF 74



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R671	RES	46C1	R752	RES	11A3	R833	RES	11D7	R914	RES	7C4												
R672	RES	30A6	R753	RES	39C2	R834	RES	8C5	R915	RES	7C4												
R673	RES	36B4	R754	RES	39D4	R835	RES	8C5	R916	RES	7B4												
R674	RES	10D3	R755	RES	10B7	R836	RES	8C2	R917	RES	7A4												
R675	RES	10D3	R756	RES	10A7	R837	RES	8B2	R918	RES	7C4												
R676	RES	46B4	R757	RES	11A4	R838	RES	8C2	R919	RES	7B4												
R677	RES	46C2	R758	RES	40C1	R839	RES	8D3	R920	RES	7B4												
R678	RES	30C8	R759	RES	39D2	R840	RES	7C7	R921	RES	7B5												
R679	RES	39A2	R760	RES	40C5	R841	RES	7C7	R922	RES	7A5												
R680	RES	30A8	R761	RES	39C5	R842	RES	7B7	R923	RES	7A5												
R681	RES	14A7	R762	RES	30B3	R843	RES	7A7	R924	RES	7D5												
R682	RES	52D6	R763	RES	30B3	R844	RES	7A7	R925	RES	7C5												
R683	RES	30A2	R764	RES	11B4	R845	RES	7B7	R926	RES	7B4												
R684	RES	36C1	R765	RES	11A4	R846	RES	7B7	R927	RES	48C5												
R685	RES	53B6	R766	RES	11A3	R847	RES	7B7	R928	RES	47C5												
R686	RES	46A7	R767	RES	39D2	R848	RES	7D5	R929	RES	47C5												
R687	RES	46A7	R768	RES	39D5	R849	RES	7B7	R930	RES	48C5												
R688	RES	46C2	R769	RES	53D7	R850	RES	4C2	R931	RES	47D7												
R689	RES	46C3	R770	RES	53D7	R851	RES	7B7	R932	RES	47C7												
R690	RES	46D3	R771	RES	10A7	R852	RES	11D7	R933	RES	47D7												
R691	RES	30B3	R772	RES	11A4	R853	RES	11D7	R934	RES	47C8												
R692	RES	46A3	R773	RES	39D2	R854	RES	11D7	R935	RES	47A5												
R693	RES	46D8	R774	RES	39D4	R855	RES	11B6	R936	RES	47A5												

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SCALE	SHT	OF	
NONE	73		74

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D	R937 RES 9B3 R938 RES 9B3 R939 RES 9A3 R940 RES 9A3 R941 RES 39D1 R943 RES 39D2 R944 RES 39D2 R945 RES 48B7 R946 RES 48B7 R947 RES 48B7 R948 RES 33D4 R949 RES 33D4 R950 RES 33D3 R951 RES 33C2 R3001 RES 30A2 R3002 RES 30A2 R3801 RES 38B1 R4301 RES 43A7 R4401 RES 44A8 R4501 RES 45C6 R4502 RES 45B6 R4503 RES 45B6 R4504 RES 45C6 R4505 RES 45B6 R4506 RES 45B6 R4507 RES 45D5 R4701 RES 47B6 R4702 RES 47B6 R4703 RES 47B3 R4704 RES 47B3 R4705 RES 47B3 R4706 RES 47D3 R4707 RES 47D3 R4708 RES 47B5 R4801 RES 48C7 R5301 RES 53C1 RP1 RPAK4P 37C8 RP2 RPAK4P 38C7 RP3 RPAK4P 38B7 RP4 RPAK4P 21B5 21C5 RP5 RPAK4P 21B5 RP6 RPAK4P 21B5 RP7 RPAK4P 21B5 RP8 RPAK4P 20A2 20A3 20B2 RP9 RPAK4P 20C2 20C2 20C3 20C3 RP10 RPAK4P 21C7 RP11 RPAK4P 21B7 RP12 RPAK4P 21B7 RP13 RPAK4P 21B7 RP14 RPAK4P 21C7 RP15 RPAK4P 37B7 37B8 RP16 RPAK4P 37A4 RP17 RPAK4P 21C7 RP18 RPAK4P 21D7 RP19 RPAK4P 21D7 RP20 RPAK4P 21D7 RP21 RPAK4P 21D7 RP22 RPAK4P 20F2 20F3 20G2 20G3 RP23 RPAK4P 20E2 20E3 RP24 RPAK4P 21C5 RP25 RPAK4P 21C5 21D5 RP26 RPAK4P 21C5 RP27 RPAK4P 21D5 RP28 RPAK4P 21D5 RP29 RPAK4P 21D5 RP30 RPAK4P 19D7 RP31 RPAK4P 19C7 RP32 RPAK4P 19D7 RP33 RPAK4P 19B7 RP34 RPAK4P 19C7 RP35 RPAK4P 19B7 RP36 RPAK4P 19A7 RP37 RPAK4P 18B3 18C3 18C3 RP38 RPAK4P 32A6 RP39 RPAK4P 30D2 RP40 RPAK4P 19D7 RP41 RPAK4P 19C7 RP42 RPAK4P 19D7 RP43 RPAK4P 19C7 RP44 RPAK4P 19C7 RP45 RPAK4P 19B7	RP46 RPAK4P 19B7 RP47 RPAK4P 19A7 RP48 RPAK4P 18B3 RP49 RPAK4P 18B1 18B1 18C1 RP50 RPAK4P 18B1 RP51 RPAK4P 14B1 14C1 RP52 RPAK4P 14A1 14B1 RP53 RPAK4P 10C2 RP54 RPAK4P 33B7 RP55 RPAK4P 10C2 RP56 RPAK4P 33B7 RP57 RPAK4P 10C2 RP58 RPAK4P 33C7 RP59 RPAK4P 33C7 RP60 RPAK4P 30C2 RP61 RPAK4P 33B7 RP62 RPAK4P 14B3 14C3 RP63 RPAK4P 32B8 RP64 RPAK4P 33B7 RP65 RPAK4P 36C4 RP66 RPAK4P 30A8 RP67 RPAK4P 33B7 RP68 RPAK4P 14C3 14D3 RP69 RPAK4P 32A8 RP70 RPAK4P 36C7 RP71 RPAK4P 14C1 14D1 RP72 RPAK4P 33C7 RP73 RPAK4P 33C7 RP74 RPAK4P 30A8 RP75 RPAK4P 33C7 RP76 RPAK4P 36C7 RP77 RPAK4P 33C7 RP78 RPAK4P 7A7 RP79 RPAK4P 7A5 7C5 RP80 RPAK4P 21B7 RP81 RPAK4P 21B7 RP82 RPAK4P 21B7 21C7 21C7 RP83 RPAK4P 20B2 20B3 20B3 RP84 RPAK4P 20B2 20B2 20B3 RP85 RPAK4P 20B3 20C2 20C3 RP86 RPAK4P 21B5 RP87 RPAK4P 21B5 RP88 RPAK4P 21C5 RP89 RPAK4P 21C5 RP90 RPAK4P 21C7 RP91 RPAK4P 21C7 21D7 RP92 RPAK4P 21C7 RP93 RPAK4P 20E2 20E3 RP94 RPAK4P 20F2 20F3 RP95 RPAK4P 20F2 20F2 20F3 RP96 RPAK4P 21C5 RP97 RPAK4P 21D5 RP98 RPAK4P 18B3 18C3 18C3 RP99 RPAK4P 18C1 RP100 RPAK4P 30A8 RP101 RPAK4P 36B1 RP102 RPAK4P 14C4 RP103 RPAK4P 39B2 39C2 RP104 RPAK4P 39B4 39B5 39C5 RP105 RPAK4P 14A4 14B4 RP106 RPAK4P 39B2 RP107 RPAK4P 39B4 39B4 39B5 RP108 RPAK4P 30A3 30B3 RP109 RPAK4P 14A4 14B4 RP110 RPAK4P 30A8 RP111 RPAK4P 39C4 39C4 39C5 39C5 RP112 RPAK4P 36C4 RP113 RPAK4P 30B8 RP114 RPAK4P 39A2 39B2 RP115 RPAK4P 39A4 39A5 39B4 39B5 RP116 RPAK4P 14A3 14B3 RP117 RPAK4P 30C1 30C1 30D1 30D1 RP118 RPAK4P 39C2 RP119 RPAK4P 36C1 S1 SWI_TACT_2P1 46B2 S2 SWI_TACT 46A3 SP1 SPRING_CLIP_1P_EMI 12D2 SP2 SPRING_CLIP_1P_EMI 12D2 SP3 SPRING_CLIP_1P_EMI 12D1 SP4 SPRING_CLIP_1P_EMI 12D1 T1 XFR_100BT_MDIX 37C3	U1 SWI_TPS2023 35A6 U2 SHNTREG_TLV431A 23A5 U3 VREG_LP2951 38D7 U4 FW802A 38C5 U5 SDRAM_DDR_4MX32 23D6 23D7 U6 OPAMP_TLV2362 42B3 42C3 U7 SHNTREG_TLV431A 20B8 U8 AMP_TPA6112A2 43D5 U10 AMP_TA2024 44B6 U11 UPD720101_FBGA 34C5 U12 SDRAM_DDR_4MX32 22D6 22D7 U13 OPAMP_TLV2362 45B3 U14 VREG_LP2951 41D6 U15 TAS3004 41C3 U16 SHNTREG_TLV431A 22A5 U17 SHNTREG_TLV431A 16B3 U18 CBTV4020 15D7 U19 VREG_LT1962 30D7 U20 VREG_LM1117 25A8 U21 VDET_MC33465N_22ATR 46A4 U22 CBTV4020 15B6 U23 MAX6328 46A5 U24 CLK_DR_PCK2059 10C4 U25 INTREPID 11D2 12D5 12D7 14D7 18D5 30C4 32D5  U26 M16C62 46C5 U27 CBTV4020 15D4 U28 VREG_TL431 46D8 U29 CBTV4020 15C2 U30 CLK_GEN_CY28512 10A6 U31 DCDC_SC2602 49B6 U32 SHNTREG_TLV431A 14A8 U33 LTC3707 52B6 U34 APOLLO_MPC7445_360 4C5 5D3 5D6 U35 TRA_IRF7410 53D2 U36 SDRAM_DDR_4MX32 23D2 23D4 U37 TRANSCEIVER_BCM5221 37C5 U38 SN74AUC1G04 32A3 U39 NV18B 19D6 20G5 20G7 24D4 25D3 U40 DCDC_SC2602 50C6 U41 SDRAM_DDR_4MX32 22D2 22D4 U42 FEPR_LMX8 32C2 U43 EEPROM_16KX8_M24128B 8B6 U44 AT90S1200 8C4 U45 DCDC_SC2602 51C5 U46 SN74LVCI604 46D1 U47 LTC3707 47C6 U48 NC7WZ08 43A7 44A7 VR1 VREG_LM1117 41C7 VR2 VREG_EZ1582 48B5 VR3 VREG_EZ1582 53B4 VR4 VREG_EZ1582 48C5 XW1 SHORT 41B7 XW2 SHORT8L25_WITH_ALTS 50B2 XW3 SHORT 44A7 XW4 SHORT 41B7 XW5 SHORT 41B7 XW6 SHORT8L25_WITH_ALTS 50B4 XW7 SHORT8L25_WITH_ALTS 50C7 XW8 SHORT 44D8 XW9 SHORT 44D8 XW10 SHORT 44C8 XW11 SHORT 44D8 XW12 SHORT8L25_WITH_ALTS 49B3 XW13 SHORT8L25_WITH_ALTS 49B4 XW14 SHORT8L25_WITH_ALTS 49C5 XW15 SHORT8L25_WITH_ALTS 49A6 XW16 SHORT8L25_WITH_ALTS 52A3 XW17 SHORT8L25_WITH_ALTS 52B8 XW18 SHORT8L25_WITH_ALTS 52A3 XW19 SHORT8L25_WITH_ALTS 52B2 XW20 SHORT8L25_WITH_ALTS 52B3 XW21 SHORT8L25_WITH_ALTS 52A6 XW22 SHORT8L25_WITH_ALTS 52B5 XW23 SHORT8L25_WITH_ALTS 51B3 XW24 SHORT8L25_WITH_ALTS 51C4 XW25 SHORT8L25_WITH_ALTS 51C4 XW26 SHORT8L25_WITH_ALTS 47C3 XW27 SHORT8L25_WITH_ALTS 47C3 XW28 SHORT8L25_WITH_ALTS 47B1 XW29 SHORT8L25_WITH_ALTS 47C6	XW30 SHORT8L25_WITH_ALTS 47A3 XW31 SHORT8L25_WITH_ALTS 47B3 XW32 SHORT 41B7 XW33 SHORT 43B7 XW34 SHORT 41A7 XW35 SHORT 44A7 XW36 SHORT 44A7 XW37 SHORT 41B7 XW38 SHORT 41A7 XW39 SHORT8L25_WITH_ALTS 50B5 XW40 SHORT 44D7 XW41 SHORT8L25_WITH_ALTS 51B5 XW42 SHORT 30A5 XW43 SHORT 30A5 XW44 SHORT 30A5 XW45 SHORT 30A4 XW46 SHORT 30A4 XW47 SHORT 11A2 XW48 SHORT 18A5 XW49 SHORT 32B5 Y1 CRYSTAL 38C6 Y2 CRYSTAL_4PIN 24A3 Y3 CRYSTAL 46A6 Y4 CRYSTAL_4PIN 46B2 Y5 CRYSTAL 30A6 Y6 CRYSTAL 37B7 Y7 CRYSTAL 34D3 ZH3 MTGHOLE 41A7 ZH4 MTGHOLE 4B2 ZH5 SLOT 4B1 ZH6 SLOT 4B2 ZH7 MTGHOLE 4B1 ZT1 HOLE_VIA 20C2 ZT2 HOLE_VIA 20C2 ZT3 HOLE_VIA 20C2 ZT4 HOLE_VIA 20A2 ZT5 HOLE_VIA 20C2 ZT6 HOLE_VIA 20C2 ZT7 HOLE_VIA 20A2 ZT8 HOLE_VIA 20C2 ZT9 HOLE_VIA 20B2 ZT10 HOLE_VIA 20B2 ZT11 HOLE_VIA 20B2 ZT12 HOLE_VIA 20B2 ZT13 HOLE_VIA 20B2 ZT14 HOLE_VIA 20B2 ZT15 HOLE_VIA 20B2 ZT16 HOLE_VIA 20B2 ZT17 HOLE_VIA 20B2 ZT18 HOLE_VIA 20B2 ZT19 HOLE_VIA 20A2 ZT20 HOLE_VIA 20G2 ZT21 HOLE_VIA 20E2 ZT22 HOLE_VIA 20F2 ZT23 HOLE_VIA 20F2 ZT24 HOLE_VIA 20F2 ZT25 HOLE_VIA 20F2 ZT26 HOLE_VIA 20F2 ZT27 HOLE_VIA 20E2 ZT28 HOLE_VIA 20D2 ZT29 HOLE_VIA 20G2 ZT30 HOLE_VIA 20F2 ZT31 HOLE_VIA 20F2 ZT32 HOLE_VIA 20E2 ZT33 HOLE_VIA 20E2 ZT34 HOLE_VIA 20F2 ZT35 HOLE_VIA 20E2 ZT36 HOLE_VIA 20E2 ZT37 HOLE_VIA 20F2 ZT38 HOLE_VIA 20E2				
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