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- 1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
- 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
- 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ZONE	ECN	DESCRIPTION OF CHANGE	CK APPD DATE	ENG APPD DATE
02		38403	ENGINEERING RELEASED	06/01/05	05

SCHEM, SPRINT, Q16C

06/01/2005

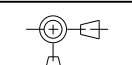
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PAGE	CONTENTS
1	TITLE PAGE AND CONTENTS
2	BACK UP BATTERY
3	CONSTRAINTS
4	COMPONENT LOCATIONS

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PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
051-6846	1	SCHEM, SPRINT, Q16B	SCH1	
820-1819	1	PCBF, SPRINT, Q16B	PCB1	

<p style="text-align: center; font-size: small;">DIMENSIONS ARE IN MILLIMETERS</p> <p>XX : _____</p> <p>X.XX : _____</p> <p>X.XXX : _____</p> <p>ANGLES : _____</p> <p style="text-align: center; font-size: x-small;">DO NOT SCALE DRAWING</p> <div style="text-align: center;">  <p style="font-size: x-small;">THIRD ANGLE PROJECTION</p> </div>	<p>METRIC</p>	<p style="text-align: right; font-weight: bold;">Apple Computer Inc.</p> <p style="font-size: x-small; text-align: center;">NOTICE OF PROPRIETARY PROPERTY</p> <p style="font-size: x-small; text-align: center;">THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING</p> <p style="font-size: x-small; text-align: center;">I TO MAINTAIN THE DOCUMENT IN CONFIDENCE</p> <p style="font-size: x-small; text-align: center;">II NOT TO REPRODUCE OR COPY IT</p> <p style="font-size: x-small; text-align: center;">III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART</p> <p style="text-align: center; font-weight: bold; font-size: large;">SCHEME, SPRINT, Q16C</p> <p style="text-align: right;">DRAWING NUMBER 051-6846 REV. 02</p> <p style="text-align: right; font-size: x-small;">SHT 1 OF 5</p>
<p>DRAPTER <input type="checkbox"/></p> <p>ENG APPD <input type="checkbox"/></p> <p>QA APPD <input type="checkbox"/></p> <p>RELEASE <input type="checkbox"/></p> <p style="font-size: x-small;">MATERIAL/FINISH NOTED AS APPLICABLE</p>	<p>DESIGN CK <input type="checkbox"/></p> <p>MFG APPD <input type="checkbox"/></p> <p>DESIGNER <input type="checkbox"/></p> <p>SCALE NONE <input type="checkbox"/></p> <p>SIZE D</p>	

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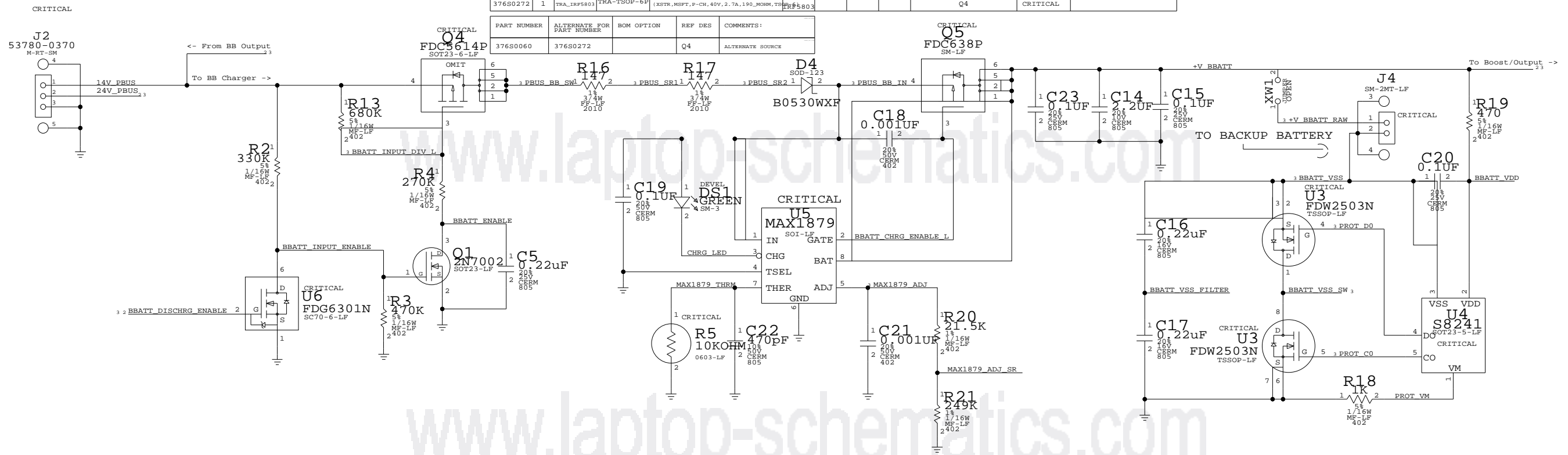
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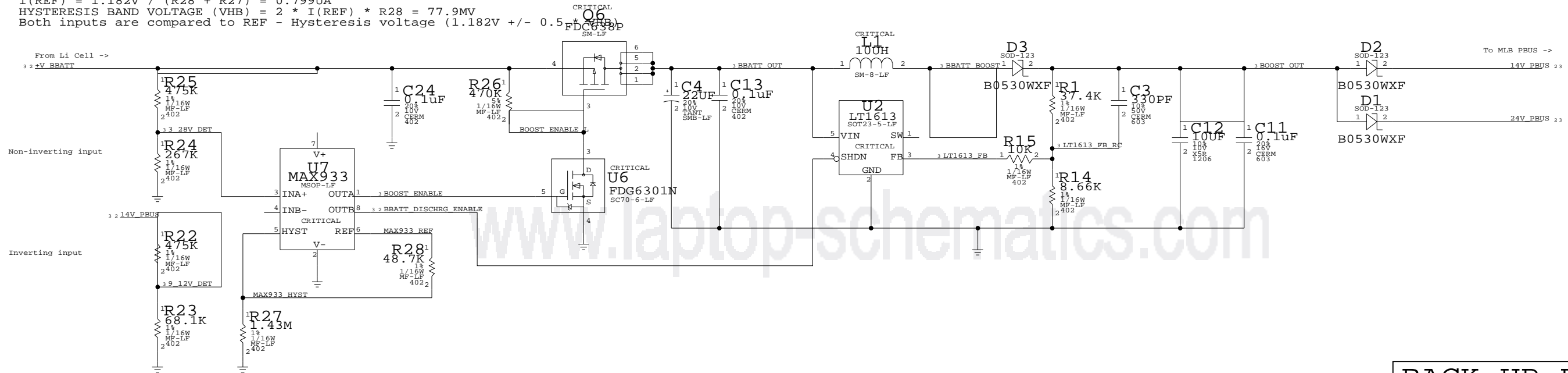
BACKUP BATTERY CHARGER

PART #	QTY	DEVICE	PACKAGE	DESCRIPTION	VALUE	VOLT.	WATT.	TOL.	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
376S0272	1	TRA_IRF5803	TRA-TSOP-6P	(XSTR_MSPT_P-CH_40V_2.7A_190_MOMM_TSM) IRF5803					Q4	CRITICAL	

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS
376S0060	376S0272		Q4	ALTERNATE SOURCE



$I(REF) = 1.182V / (R28 + R27) = 0.799UA$
 HYSTERESIS BAND VOLTAGE (VHB) = $2 * I(REF) * R28 = 77.9mV$
 Both inputs are compared to REF - Hysteresis voltage (1.182V +/- 0.5 * VHB)



BACK UP BATTERY

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6846	02
SCALE	SHT	OF	
NONE	2	5	

REVISION HISTORY

02/21/05 - SCHEMATIC ORIGINATED FROM Q41B 051-6753-A
 02/23/05 - CORRECTED THE NOTE

Power Signals

GROUP	SIG_NAME	VOLTAGE	MIN_LINE_WIDTH	MIN_NECK_WIDTH
BATTERY	24V_FBUS	VOLTAGE=24V	MIN_LINE_WIDTH=0.508MM	MIN_NECK_WIDTH=0.254MM
	14V_FBUS	VOLTAGE=14V	MIN_LINE_WIDTH=0.508MM	MIN_NECK_WIDTH=0.254MM
	PBUS_BB_IN	VOLTAGE=14V	MIN_LINE_WIDTH=0.508MM	MIN_NECK_WIDTH=0.254MM
	PBUS_SR2	VOLTAGE=14V	MIN_LINE_WIDTH=0.508MM	MIN_NECK_WIDTH=0.254MM
	PBUS_SR1	VOLTAGE=14V	MIN_LINE_WIDTH=0.508MM	MIN_NECK_WIDTH=0.254MM
	PBUS_BB_SW	VOLTAGE=14V	MIN_LINE_WIDTH=0.508MM	MIN_NECK_WIDTH=0.254MM
	+V_BBATT	VOLTAGE=4.2V	MIN_LINE_WIDTH=0.508MM	MIN_NECK_WIDTH=0.254MM
	+V_BBATT_RAW	VOLTAGE=4.2V	MIN_LINE_WIDTH=0.508MM	MIN_NECK_WIDTH=0.254MM
	BBATT_VSS	VOLTAGE=0V	MIN_LINE_WIDTH=0.508MM	MIN_NECK_WIDTH=0.254MM
	BBATT_VSS_SW	VOLTAGE=0V	MIN_LINE_WIDTH=0.508MM	MIN_NECK_WIDTH=0.254MM
MAX1879	MAX1879_ADJ	VOLTAGE=1.4V	MIN_LINE_WIDTH=0.203MM	MIN_NECK_WIDTH=0.254MM
	BBATT_INPUT_DIV_L	VOLTAGE=14V	MIN_LINE_WIDTH=0.508MM	MIN_NECK_WIDTH=0.254MM
S8241	PROT_D0	VOLTAGE=4.2V	MIN_LINE_WIDTH=0.203MM	MIN_NECK_WIDTH=0.254MM
	PROT_C0	VOLTAGE=4.2V	MIN_LINE_WIDTH=0.203MM	MIN_NECK_WIDTH=0.254MM
	9_12V_DET	VOLTAGE=1.2V	MIN_LINE_WIDTH=0.203MM	MIN_NECK_WIDTH=0.508MM
MAX933	3_28V_DET	VOLTAGE=1.2V	MIN_LINE_WIDTH=0.203MM	MIN_NECK_WIDTH=0.254MM
	BOOST_ENABLE	VOLTAGE=4.2V	MIN_LINE_WIDTH=0.203MM	MIN_NECK_WIDTH=0.254MM
	BBATT_DISCHRG_ENABLE	VOLTAGE=4.2V	MIN_LINE_WIDTH=0.203MM	MIN_NECK_WIDTH=0.254MM
LT1613	BBATT_OUT	VOLTAGE=4.2V	MIN_LINE_WIDTH=0.508MM	MIN_NECK_WIDTH=0.254MM
	BBATT_BOOST	VOLTAGE=6.5V	MIN_LINE_WIDTH=0.508MM	MIN_NECK_WIDTH=0.254MM
	BOOST_OUT	VOLTAGE=6.5V	MIN_LINE_WIDTH=0.508MM	MIN_NECK_WIDTH=0.254MM
	LT1613_FB	VOLTAGE=1.3V	MIN_LINE_WIDTH=0.203MM	MIN_NECK_WIDTH=0.254MM
	LT1613_FB_RC	VOLTAGE=1.3V	MIN_LINE_WIDTH=0.203MM	MIN_NECK_WIDTH=0.254MM
	GND	VOLTAGE=0V	MIN_LINE_WIDTH=0.508MM	MIN_NECK_WIDTH=0.254MM

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SIGNAL CONSTRAINTS

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6846	02
SCALE	SHT	OF	5
NONE	3	5	5

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*** Signal Cross-Reference for the entire design ***
+V_BBATT      288<- 2D1<- 3D6>
+V_BBATT_BAK  282<- 3D6>
I_1FV_DET     287<- 3C6>
I_1FV_DET     2A7<- 3C6>
I4V_PRES      281<- 286< 2D7<- 3D6>
I4V_PRES      2A1<- 2D7<- 3D6>
BBATT_BOOT    284<- 3C6>
BBATT_CHRG_ENABLE_L  2C4<-
BBATT_DISCHG_ENABLE  286<- 2C7<- 3C6>
BBATT_ENABLE  2C6<-
BBATT_INOUT_DIV_L  2D6<- 3C6>
BBATT_INOUT_ENABLE  2C7<-
BBATT_OUT     285<- 3C6>
BBATT_VDD     2C1<-
BBATT_VSS     2C2<- 3D6>
BBATT_VSS_FILTER  2C2<-
BBATT_VSS_SW  2C2<- 3D6>
BOOT_ENABLE  286<- 3C6>
BOOT_ENABLE_L  286<-
BOOT_OUT     283<- 3C6>
CHRG_LED     2C5<-
LTI613_FB    284<- 3C6>
LTI613_FB_BC  281<- 3C6>
MAX933_HYST  2A7<-
MAX933_REF   2A6<-
MAX1878_ADJ  2C4<- 3C6>
MAX1878_ADJ_SR  2C3<-
MAX1878_THERM  2C5<-
PRES_BM_IN   2D4<- 3D6>
PRES_BM_SW   2D6<- 3D6>
PRES_S01     2D5<- 3D6>
PRES_S02     2D5<- 3D6>
PROT_CO      2C2<- 3C6>
PROT_DS      2C2<- 3C6>
PROT_VH      2C2<-

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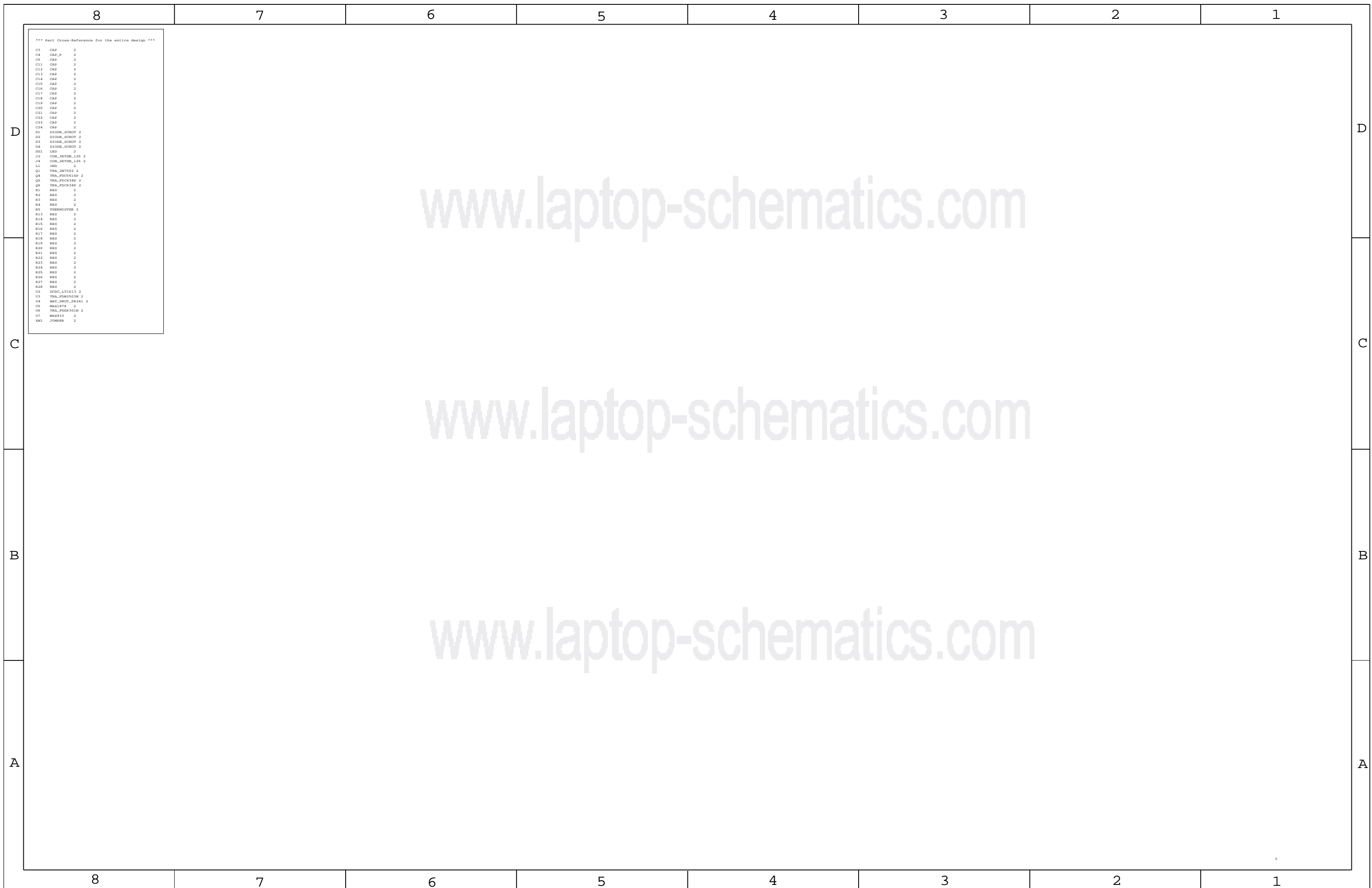
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*** Part Cross-Reference for the entire design ***
C3 CAP 2
C4 CAP_P 2
C5 CAP 2
C11 CAP 2
C12 CAP 2
C13 CAP 2
C14 CAP 2
C15 CAP 2
C16 CAP 2
C17 CAP 2
C18 CAP 2
C19 CAP 2
C20 CAP 2
C21 CAP 2
C22 CAP 2
C23 CAP 2
C24 CAP 2
D1 DIODE_SCHOT 2
D2 DIODE_SCHOT 2
D3 DIODE_SCHOT 2
D4 DIODE_SCHOT 2
D91 LED 2
J2 CON_3P75M_125 2
J4 CON_2P75M_125 2
L1 IND 2
Q1 TRA_2N7002 2
Q4 TRA_PDC614P 2
Q5 TRA_PDC638P 2
Q6 TRA_PDC638P 2
R1 RES 2
R2 RES 2
R3 RES 2
R4 RES 2
R5 THERMISTOR 2
R13 RES 2
R14 RES 2
R15 RES 2
R16 RES 2
R17 RES 2
R18 RES 2
R19 RES 2
R20 RES 2
R21 RES 2
R22 RES 2
R23 RES 2
R24 RES 2
R25 RES 2
R26 RES 2
R27 RES 2
R28 RES 2
U2 DCDC_L71613 2
U3 TRA_PDC653M 2
U4 RAT_PROT_SR241 2
U5 MAX1879 2
U6 TRA_PDC6301M 2
U7 MAX933 2
XM1 JUMPER 2

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