1. All resistance values are in ohms, ±0.1 watt ±/−5%.
2. All capacitance values are in microfarads.
3. All crystals & oscillator values are in hertz.
<table>
<thead>
<tr>
<th><strong>ALTERNATE PARTS</strong></th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>MECHANICAL PARTS</strong></td>
</tr>
<tr>
<td><strong>COMMON</strong></td>
</tr>
<tr>
<td><strong>OPS REQUESTED QUAL PARTS</strong></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>PART#</th>
<th>DESCRIPTION</th>
<th>QTY</th>
<th>REFERENCE DESIGNATOR(S)</th>
</tr>
</thead>
<tbody>
<tr>
<td>353S1461</td>
<td>IC, CPU VREG, OLD DIE, SCREENED PART</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>353S1465</td>
<td>SMC VREF</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>124-0333</td>
<td>C7953, C7954</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>124-0338</td>
<td>FOR SUPPLY</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>124-0339</td>
<td>1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>124-0361</td>
<td>C7577, C7578, C7579, C7956, C7957</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>378S0140</td>
<td>LED601, LED602, LED603</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>378S0141</td>
<td>1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>126S0091, 126S0092</td>
<td>C625 FACTORY SHORTAGE</td>
<td>1</td>
<td></td>
</tr>
</tbody>
</table>

- **Table Items**
- **Common**
- **OPS Requested Qual Parts**

---

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Scale: None

Rev. 12345678

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PLACE R1001 AND R1005 SUCH THAT THEY SHARE ONE PAD
PLACE R1017 AND R1019 SUCH THAT THEY SHARE ONE PAD

PLACE R1002 AND R1018 SUCH THAT THEY SHARE ONE PAD

LAYOUT NOTE:
ROUTE ON SAME LAYER WITH 0.254MM TRACE WIDTH & SPACING.
ADD GND GUARD TRACES FOR CPU_THERMD_P/N

NOTE: SYMBOL SHOULD BE ADT7461A

CPU THERMAL SENSOR

IF CPU T DIODE TO BE READ IN OFF STATE,
THEN THIS SHOULD BE S5

NOTE: CPU THERMD_P/CPU_THERMD_N

CPU_TEMP_SENSOR

APPLE COMPUTER INC.

E051-7199

www.laptop-schematics.com
CPU ITP700FLEX DEBUG SUPPORT

ITP TCK SIGNAL LAYOUT NOTE:
ROUTE THE TCK SIGNAL FROM ITP700FLEX CONNECTOR'S TCK PIN TO CPU'S TCK PIN AND THEN FLOW BACK FROM CPU TCK PIN AND ROUTE BACK TO ITP700FLEX CONNECTOR'S PROG PIN.
SB I2C Busses

PCI CONTROL

SB: SMB HUB AND ALIAS

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DDR2 Vtt Regulator

MemVTT_supply

=PP5V_S0_MEMVTT

=PP1V8_S0_MEMVTT

MEMVTT_VREF

MEMVTT_EN

U3100

VDDQ

Memory Vtt Supply

www.laptop-schematics.com
REMOVED TEST POINTS FROM SHORTTRACE BETWEEN CAP AND CONNECTOR TO IMPROVE SIGNAL INTEGRITY.

PLACE < 0.5 IN FROM BALL OF U2100 MAKE_BASE=TRUE

PLACE C3805-06 CLOSE TO JC901 FOR PP5V_S0_PATA. APPLY A WIDE TRACE SHAPE FROM JC901 TO C3805-06.

NOTE: ATA-2, NOW OBSOLETE

SATA PORT 0 IS NOT USED

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Disk Connectors

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R6306-07 should be placed less than 2.54mm from U6301.

R6303 should be placed less than 2.54mm from U6301.
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**Diagram**

**Fan 0**

- Component labels and connections are present, indicating the circuitry for fan control.

**Fan 1**

- Similar to Fan 0, with specific components labeled for detailed circuit information.

---

**Notes:**

- Added to protect SMC.
- Motor Control
- Fan 0, 1 & System Temp
- SMC_FAN_0_TACH = PP12V_S0_FAN
- SMC_FAN_1_TACH = PP3V3_S0_FAN
- Fan 0, 1 & System Temp
- MIN_LINE_WIDTH=0.5MM
- MIN_NECK_WIDTH=0.25MM

---

**Schematics Source:**

[www.laptop-schematics.com](http://www.laptop-schematics.com)
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