1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ZONE	ECN	DESCRIPTION OF CHANGE	CK APPD / DATE	ENG APPD / DATE
A		418830	PRODUCTION RELEASED	01/06/06?	

SCHEM,LIO/AUDIO,M1

01/06/06

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-6951	1	SCHEM,LIO/AUDIO,M1	SCH1		
820-1699	1	PCBF,LIO/AUDIO,M1	PCB1		

DIMENSIONS ARE IN MILLIMETERS
XX ±
X.XX ±
X.XXX ±
ANGLES ±
DO NOT SCALE DRAWING

THIRD ANGLE PROJECTION

METRIC

DRAFTER		DESIGN CK	
ENG APPD		MFG APPD	
QA APPD		DESIGNER	
RELEASE		SCALE	NONE

MATERIAL/FINISH NOTED AS APPLICABLE

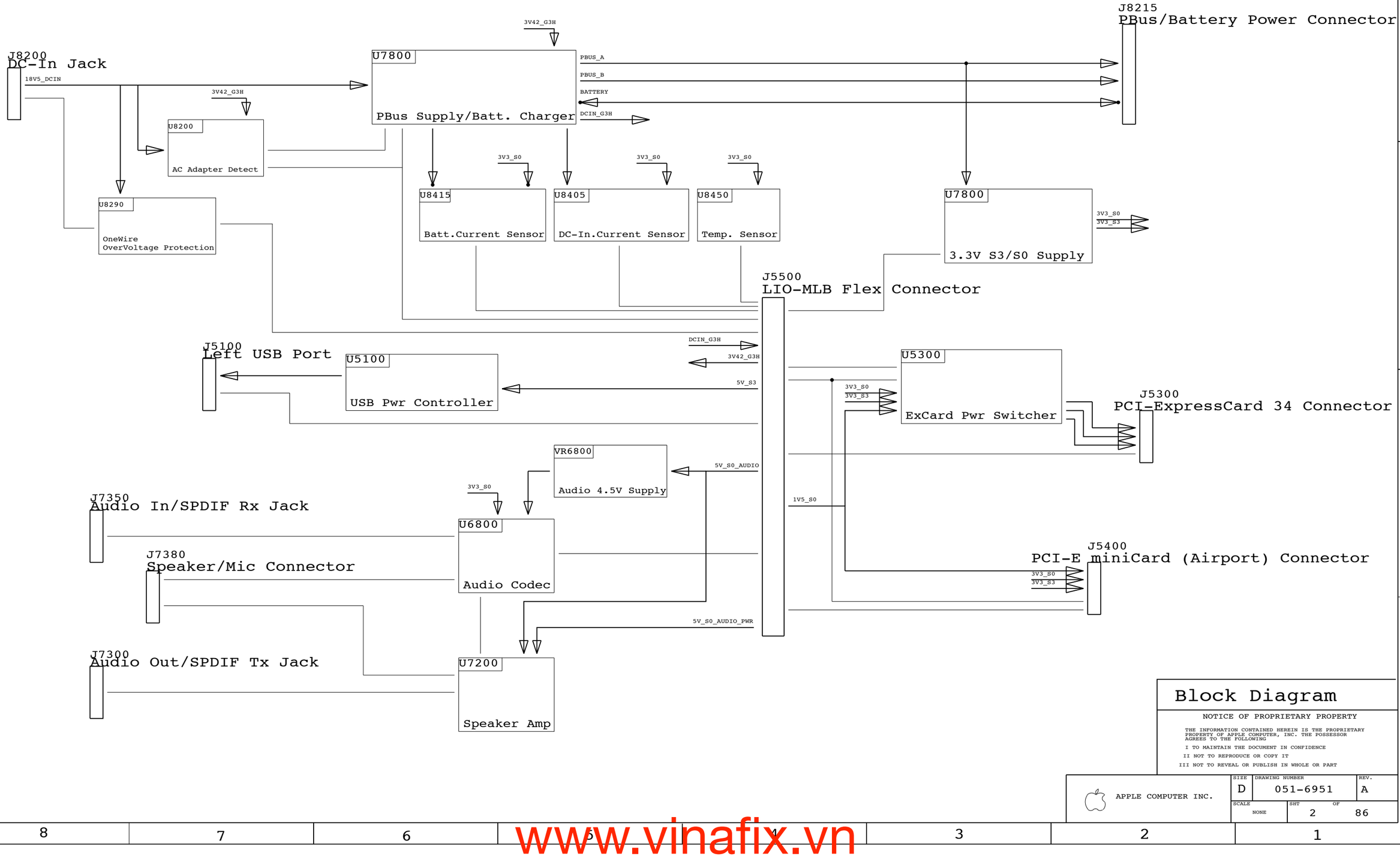
SIZE D

Apple Computer Inc.

TITLE
SCHEM,LIO/AUDIO,M1

DRAWING NUMBER 051-6951 REV. A

SHT 1 OF 86



Block Diagram

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6951	A

SCALE: NONE

SHT 2 OF 86

BOM NUMBER	BOM NAME	BOM OPTIONS
630-7249	PCBA,LIO_AUDIO,M1	COMMON,EEE_TY6,M1_COMMON

BOM GROUP	BOM OPTIONS
M1_COMMON	EXCARD_3CNTL,M1_PARTS,ONEWIRE_DIV,ONEWIRE_PULLUP,ONEWIRE_PU_PROT,ONEWIRE_PWRCTL,STA9220

Bar Code Label / EEE #'s

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
000-0041	1	PLACEHOLDER FOR EEE/CCC INFO	[EEE:TY6]	CRITICAL	EEE_TY6

M1A Parts Reserved for future use

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
514-0306	1	CONN,RCPT,R/A,3.6 CL,BLK	J5100	CRITICAL	M1A_PARTS
514-0307	1	CONN,RCPT,RT ANG,MPM,10A,5P,BLK	J8200	CRITICAL	M1A_PARTS
514-0308	1	CONN,RCPT,OPT-COPPER,SPDIF RX,3.5MM,BLK	J7350	CRITICAL	M1A_PARTS
514-0309	1	CONN,RCPT,OPT-COPPER,SPDIF TX,3.5MM,BLK	J7300	CRITICAL	M1A_PARTS

BOM Configuration

SYNC_MASTER=(Master) SYNC_DATE=(Master)

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6951	A

SCALE	SHT	OF
NONE	4	86

POWER & GROUNDS

14 =PPDCIN_G3H_LIO_CONN
15 =PPVBATT_G3H_LIO_CONN
→ PPDCIN_G3H
MAKE_BASE=TRUE
VOLTAGE=18V
MIN_LINE_WIDTH=0.5mm
MIN_NECK_WIDTH=0.20mm
→ =PPDCIN_G3H 8

14 =PP18V5_G3H_INRUSH
→ PP18V5_G3H_CHGR
MAKE_BASE=TRUE
VOLTAGE=18.5V
MIN_LINE_WIDTH=0.60mm
MIN_NECK_WIDTH=0.20mm
→ =PP18V5_G3H_CHGR 15

15 =PPVBAT_G3H_CHGR_REG
→ PPVBAT_G3H_CHGR_REG
MAKE_BASE=TRUE
VOLTAGE=18.5V
MIN_LINE_WIDTH=1mm
MIN_NECK_WIDTH=0.25mm
→ =PPBUS_G3H_LIO_CONN 14
→ =PPVIN_S3_3V3S3 13

8 =PP3V42_G3H
→ PP3V42_G3H
MAKE_BASE=TRUE
VOLTAGE=3.42V
MIN_LINE_WIDTH=0.5mm
MIN_NECK_WIDTH=0.20mm
→ =PP3V42_G3H_ACIN 14 15
→ =PP3V42_G3H_ONEWIRE 14

8 =PP1V5_S0
→ PP1V5_S0
MAKE_BASE=TRUE
VOLTAGE=1.5V
MIN_LINE_WIDTH=0.5mm
MIN_NECK_WIDTH=0.20mm
→ =PP1V5_S0_MINI 7
→ =PP1V5_S0_EXCARD 6

13 =PP3V3_S3_REG
→ PP3V3_S3_LIO
MAKE_BASE=TRUE
VOLTAGE=3.3V
MIN_LINE_WIDTH=0.50mm
MIN_NECK_WIDTH=0.20mm
→ =PP3V3_S3_MINI 7
→ =PP3V3_S3_EXCARD 6

13 =PP3V3_S0_FET
→ PP3V3_S0_LIO
MAKE_BASE=TRUE
VOLTAGE=3.3V
MIN_LINE_WIDTH=0.30MM
MIN_NECK_WIDTH=0.20MM
→ =PP3V3_S0_MINI 7
→ =PP3V3_S0_AUDIO 9 11 12
→ =PP3V3_S0_PBATTISENS 16
→ =PP3V3_S0_PDCISENS 16
→ =PP3V3_S0_TMPSNSR 16
→ =PP3V3_S0_EXCARD 6

8 =PP5V_S5
→ PP5V_S5
MAKE_BASE=TRUE
VOLTAGE=5V
MIN_LINE_WIDTH=0.5mm
MIN_NECK_WIDTH=0.2mm
→ =PP5V_S5_LTUSB 5
→ =PP5V_S5_3V3S3 13

10 9 8 =PP5V_S0_AUDIO
→ PP5V_S0_AUDIO
MAKE_BASE=TRUE
VOLTAGE=5V
MIN_LINE_WIDTH=0.30 MM
MIN_NECK_WIDTH=0.20 MM

10 8 =PP5V_S0_AUDIO_PWR
→ PP5V_S0_AUDIO_PWR
MAKE_BASE=TRUE
VOLTAGE=5V
MIN_LINE_WIDTH=0.60 MM
MIN_NECK_WIDTH=0.20 MM

11 10 8 =GND_AUDIO_PWR
→ GND_AUDIO_PWR
MAKE_BASE=TRUE
VOLTAGE=0V
MIN_LINE_WIDTH=0.50mm
MIN_NECK_WIDTH=0.20mm

8 =GND_AUDIO
→ GND_AUDIO
MAKE_BASE=TRUE
VOLTAGE=0V
→ =GND_AUDIO_CODEC 9 10 11 12

CHASSIS_GND1
MAKE_BASE=TRUE
VOLTAGE=0V
MIN_LINE_WIDTH=0.50mm
MIN_NECK_WIDTH=0.20mm
→ =GND_CHASSIS_AUDIO_JACK 11

CHASSIS_GND2
MAKE_BASE=TRUE
VOLTAGE=0V
MIN_LINE_WIDTH=0.50mm
MIN_NECK_WIDTH=0.20mm
→ =GND_CHASSIS_PCIE_EXCARD 6
→ =GND_CHASSIS_AUDIO_SPKRCONN 11
→ =GND_CHASSIS_AUDIO_MIC 12

CHASSIS_GND3
MAKE_BASE=TRUE
VOLTAGE=0V
MIN_LINE_WIDTH=0.50mm
MIN_NECK_WIDTH=0.20mm
→ =GND_CHASSIS_DCIN_JACK 14
→ =GND_CHASSIS_LTUSB 5

GND
VOLTAGE=0V
MIN_LINE_WIDTH=0.50mm
MIN_NECK_WIDTH=0.20mm

BATTERY

14 =PPBATTPOS_G3H_LIO_CONN
→ BATT_POS
MAKE_BASE=TRUE
MIN_LINE_WIDTH=0.6mm
MIN_NECK_WIDTH=0.25mm
→ =BATT_POS 15

14 =PPBATTNEG_G3H_LIO_CONN
→ BATT_NEG
MAKE_BASE=TRUE
MIN_LINE_WIDTH=0.6mm
MIN_NECK_WIDTH=0.25mm
→ =BATT_NEG 15

PCI-E EXPRESS CARD 34

8 6 USB2_EXCARD_P
NET_PHYSICAL_TYPE=USB
NET_SPACING_TYPE=USB
→ T20

8 6 USB2_EXCARD_N
NET_PHYSICAL_TYPE=USB
NET_SPACING_TYPE=USB
→ T20

8 6 PCIE_CLK100M_EXCARD_P
NET_PHYSICAL_TYPE=PCIE MAX_EXPOSED_LENGTH=12.5MM
NET_SPACING_TYPE=PCIE
→ T22

8 6 PCIE_CLK100M_EXCARD_N
NET_PHYSICAL_TYPE=PCIE MAX_EXPOSED_LENGTH=12.5MM
NET_SPACING_TYPE=PCIE
→ T23

8 PCIE_CLK100M_EXCARD_UF_P
NET_PHYSICAL_TYPE=PCIE MAX_EXPOSED_LENGTH=12.5MM
NET_SPACING_TYPE=PCIE
→ T21

8 PCIE_CLK100M_EXCARD_UF_N
NET_PHYSICAL_TYPE=PCIE MAX_EXPOSED_LENGTH=12.5MM
NET_SPACING_TYPE=PCIE
→ T21

8 6 PCIE_EXCARD_D2R_P
NET_PHYSICAL_TYPE=PCIE
NET_SPACING_TYPE=PCIE
→ T24

8 6 PCIE_EXCARD_D2R_N
NET_PHYSICAL_TYPE=PCIE
NET_SPACING_TYPE=PCIE
→ T21

8 6 PCIE_EXCARD_R2D_P
NET_PHYSICAL_TYPE=PCIE
NET_SPACING_TYPE=PCIE
→ T21

8 6 PCIE_EXCARD_R2D_N
NET_PHYSICAL_TYPE=PCIE
NET_SPACING_TYPE=PCIE
→ T21

8 6 EXCARD_CLKREQ_L
→ T22

8 6 EXCARD_OC_L
→ T24

8 PCIE_WAKE_L
MAKE_BASE=TRUE
→ PCIE_WAKE_EXCARD_L 6
→ PCIE_WAKE_MINI_L 7

8 LIO_PLT_RESET_L
MAKE_BASE=TRUE
→ PLT_RESET_L 6 7

PCI-E MINICARD

8 7 PCIE_MINI_R2D_P
NET_PHYSICAL_TYPE=PCIE
NET_SPACING_TYPE=PCIE
→ T21

8 7 PCIE_MINI_R2D_N
NET_PHYSICAL_TYPE=PCIE
NET_SPACING_TYPE=PCIE
→ T21

8 7 PCIE_MINI_D2R_P
NET_PHYSICAL_TYPE=PCIE
NET_SPACING_TYPE=PCIE
→ T21

8 7 PCIE_MINI_D2R_N
NET_PHYSICAL_TYPE=PCIE
NET_SPACING_TYPE=PCIE
→ T21

8 7 PCIE_CLK100M_MINI_P
NET_PHYSICAL_TYPE=PCIE MAX_EXPOSED_LENGTH=12.5MM
NET_SPACING_TYPE=PCIE
→ T20

8 7 PCIE_CLK100M_MINI_N
NET_PHYSICAL_TYPE=PCIE MAX_EXPOSED_LENGTH=12.5MM
NET_SPACING_TYPE=PCIE
→ T24

8 PCIE_CLK100M_MINI_UF_P
NET_PHYSICAL_TYPE=PCIE MAX_EXPOSED_LENGTH=12.5MM
NET_SPACING_TYPE=PCIE
→ T24

8 PCIE_CLK100M_MINI_UF_N
NET_PHYSICAL_TYPE=PCIE MAX_EXPOSED_LENGTH=12.5MM
NET_SPACING_TYPE=PCIE
→ T23

7 USB2_MINI_P
NET_PHYSICAL_TYPE=USB
NET_SPACING_TYPE=USB
→ TP_USB2_MINIP

7 USB2_MINI_N
NET_PHYSICAL_TYPE=USB
NET_SPACING_TYPE=USB
→ TP_USB2_MININ

8 7 MINI_CLKREQ_L
→ T22

USB

8 USB2_LT_P
MAKE_BASE=TRUE
NET_PHYSICAL_TYPE=USB
NET_SPACING_TYPE=USB
→ LEFT_USB_P 5

8 USB2_LT_N
MAKE_BASE=TRUE
NET_PHYSICAL_TYPE=USB
NET_SPACING_TYPE=USB
→ LEFT_USB_N 5

8 LTUSB_OC_L
MAKE_BASE=TRUE
→ LEFT_USB_OC_L 5

SMBUS

8 SMBUS_SMC_SCL
MAKE_BASE=TRUE
→ =SMBUS_TMPSNSR_SCL 16

8 SMBUS_SMC_SDA
MAKE_BASE=TRUE
→ =SMBUS_TMPSNSR_SDA 16

8 SMBUS_SB_SCL
MAKE_BASE=TRUE
→ =SMBUS_EXCARD_SCL 6
R0601
0
1 2
5%
1/16W
MF-LF
402
SMBUS_MINI_SCL
MAKE_BASE=TRUE
→ =SMBUS_MINI_SCL 7

8 SMBUS_SB_SDA
MAKE_BASE=TRUE
→ =SMBUS_EXCARD_SDA 6
R0600
0
1 2
5%
1/16W
MF-LF
402
SMBUS_MINI_SDA
MAKE_BASE=TRUE
→ =SMBUS_MINI_SDA 7

POWER MANAGEMENT

8 LIO_S3_EN
MAKE_BASE=TRUE
→ =LEFT_USB_PWRON 5
→ =LIO_P3V3S3_EN 13

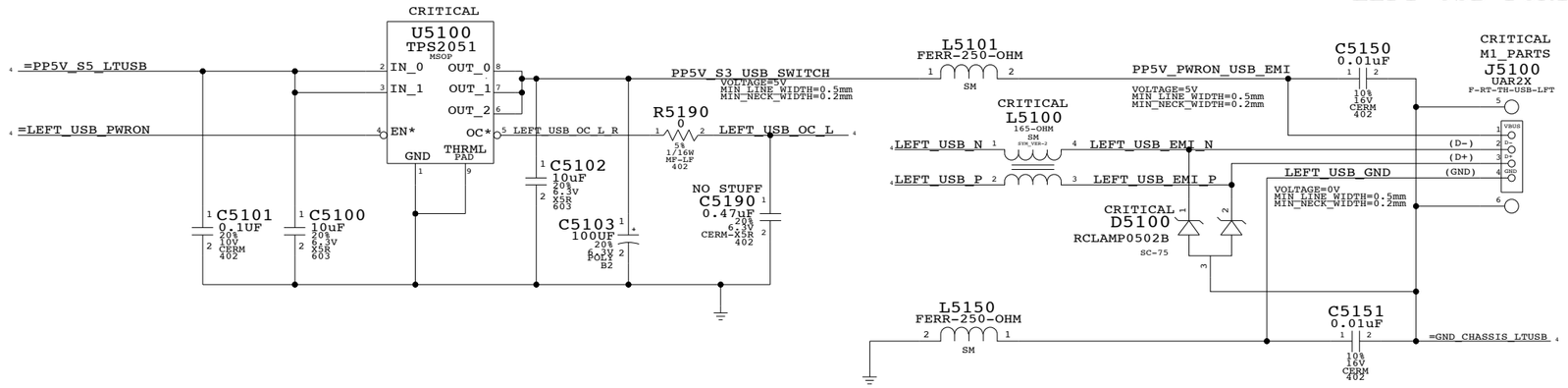
Aliases

SYNC_MASTER=(Master) SYNC_DATE=(Master)

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6951	A
SCALE: NONE	SHT 6 OF 86	

LEFT USB PORT

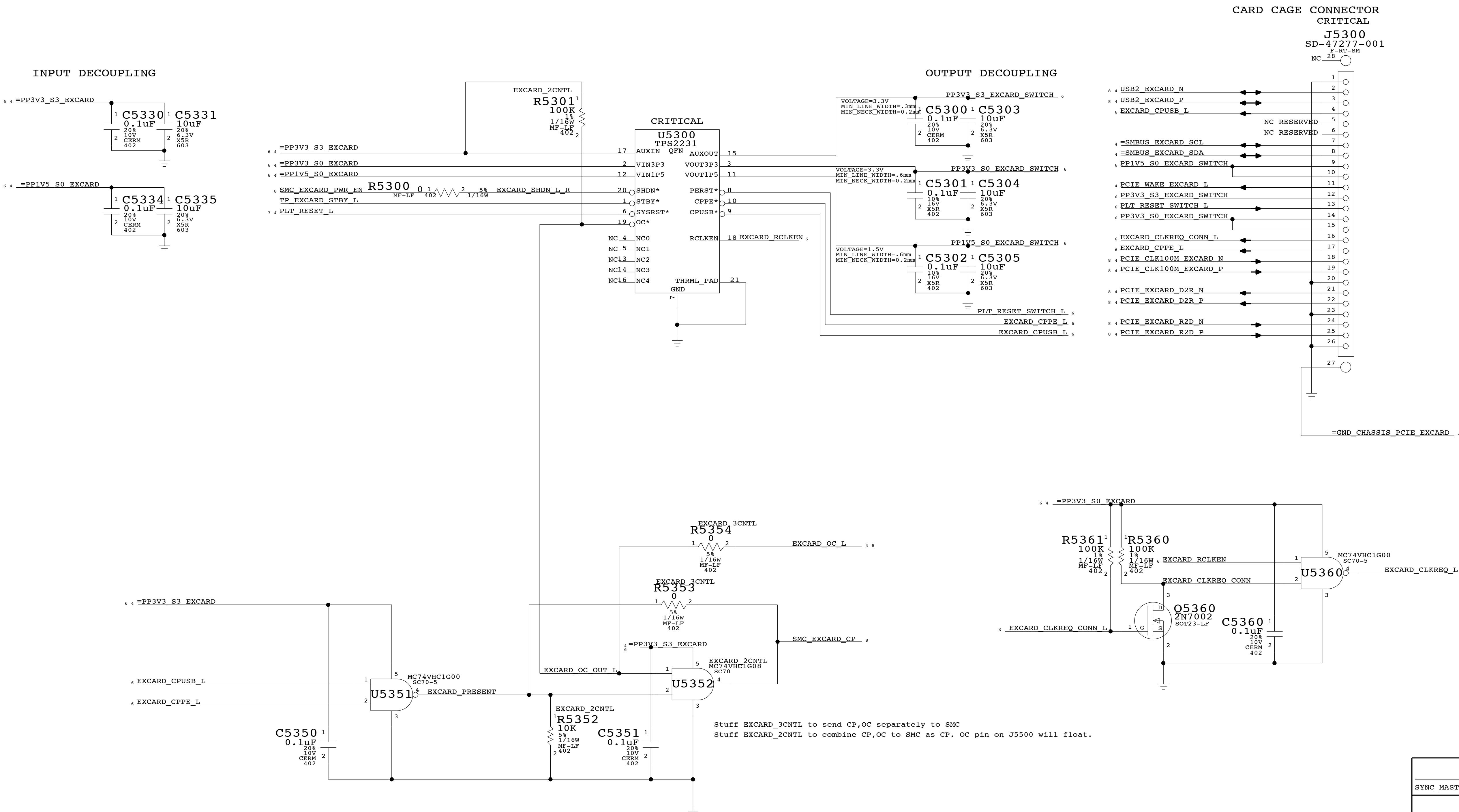


PUT L5100, L5101, AND L5150 ACROSS THE MOAT

Left USB Port		
SYNC_MASTER=(Master)	SYNC_DATE=(Master)	

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6951	A
	SCALE: NONE	SHT 51 OF 86	

EXPRESSCARD/34 TOP MOUNT CONNECTOR



Stuff EXCARD_3CNTL to send CP,OC separately to SMC
Stuff EXCARD_2CNTL to combine CP,OC to SMC as CP. OC pin on J5500 will float.

ExpressCard Connector

SYNC_MASTER=(Master) SYNC_DATE=(Master)

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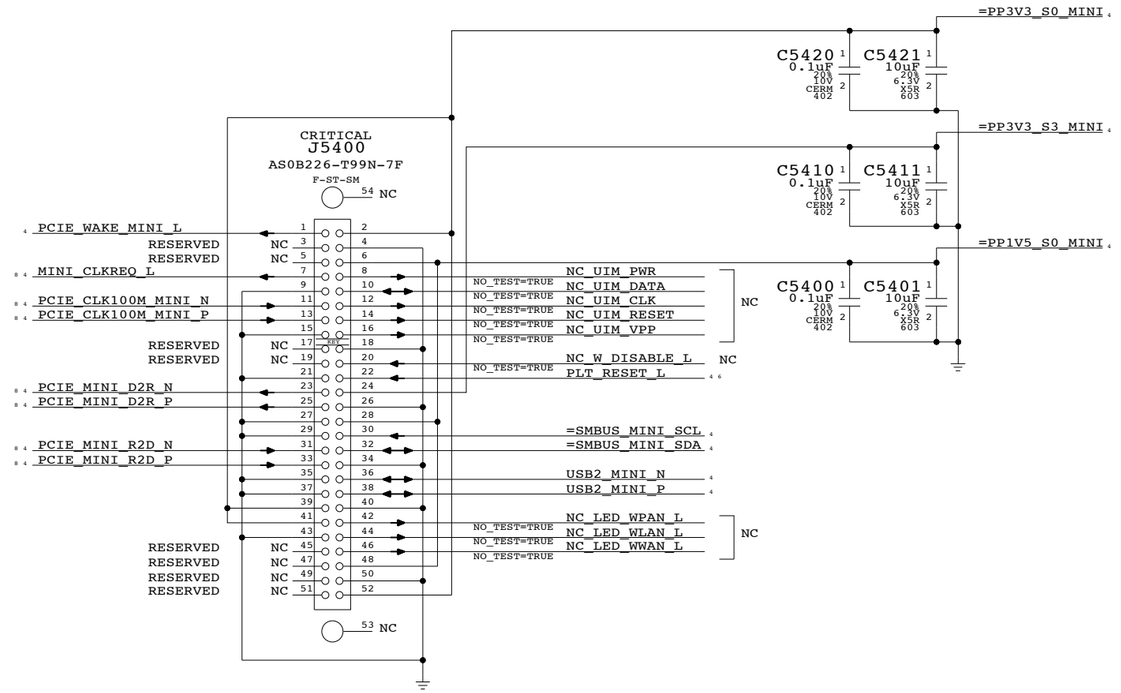
APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6951	A
SCALE NONE	SHT 53	OF 86

PCI-EXPRESS MINI CARD CONNECTOR



PCI-E MiniCard Connector

SYNC_MASTER=(Master) SYNC_DATE=(Master)

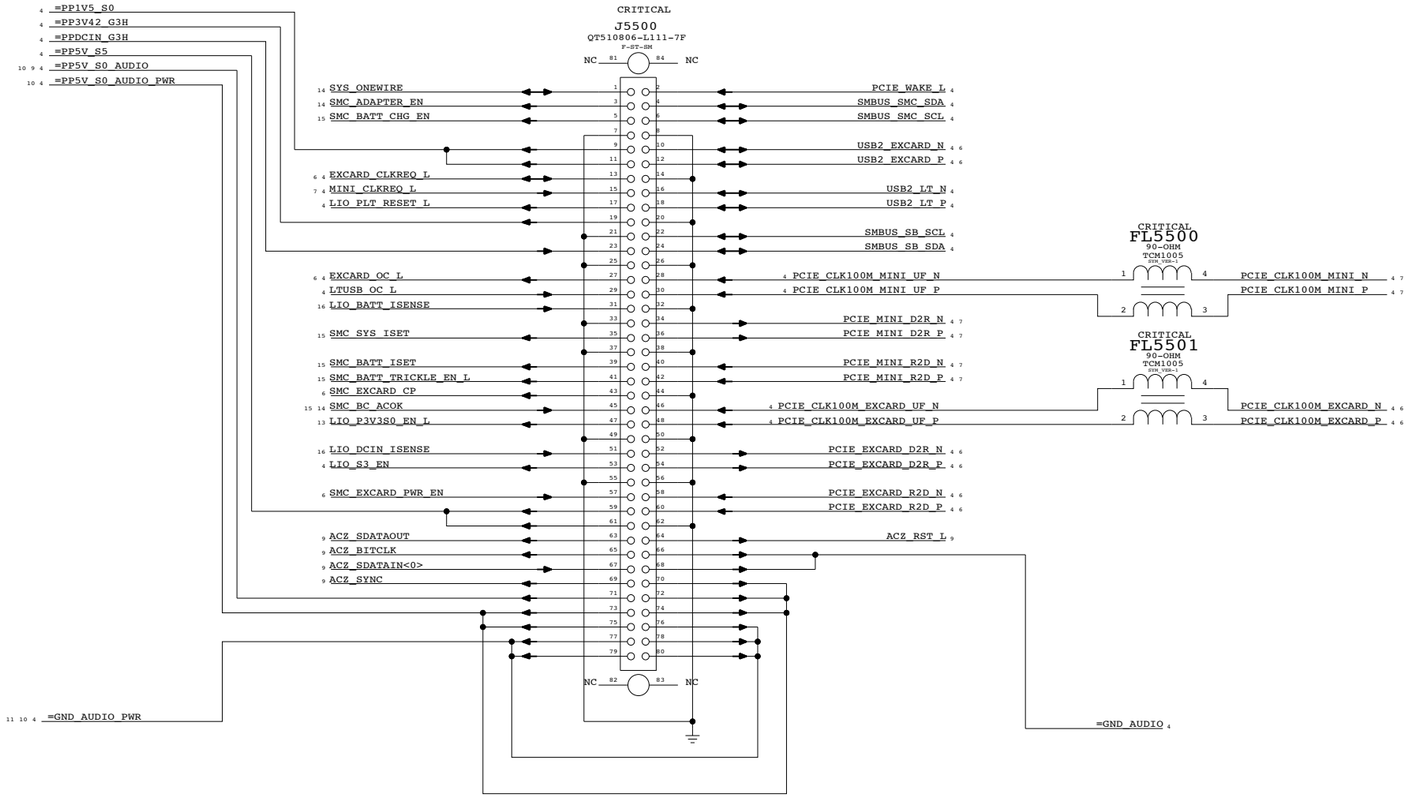
NOTICE OF PROPRIETARY PROPERTY

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6951	A

SCALE	SHT	OF
NONE	54	86

Left I/O Board Connector



MLB I/O Board Connector

SYNC_MASTER=(Master) SYNC_DATE=(Master)

NOTICE OF PROPRIETARY PROPERTY

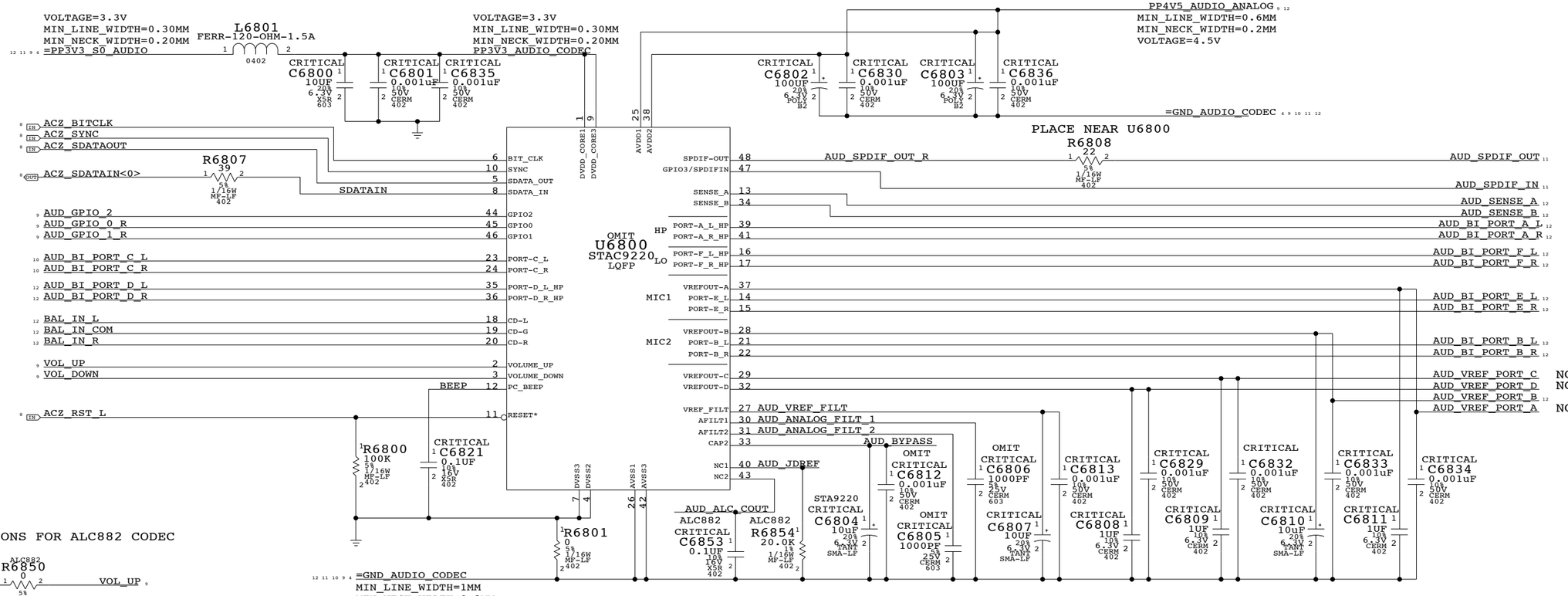
APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6951	A

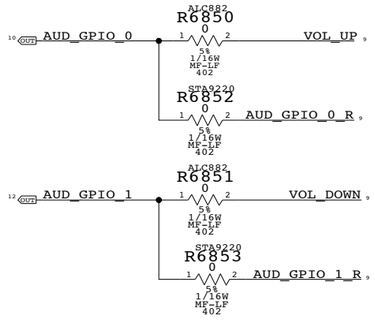
SCALE	SHT	OF
NONE	55	86

AUDIO CODEC

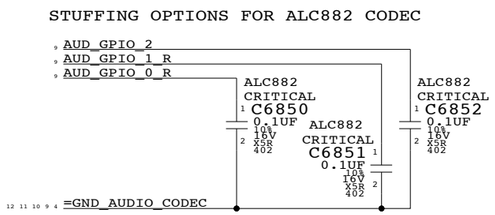
APPLE P/N 353S1345

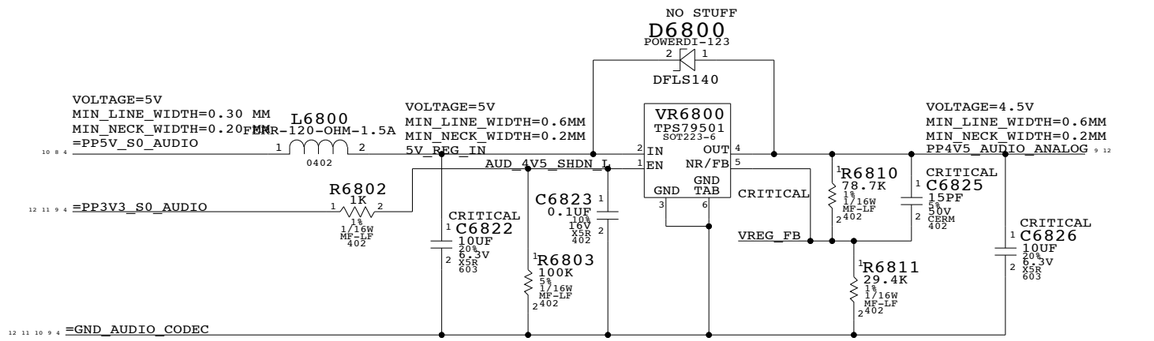


STUFFING OPTIONS FOR ALC882 CODEC



STUFFING OPTIONS FOR ALC882 CODEC





APN: 353S1233
4.5V POWER SUPPLY FOR CODEC

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
353S1345	1	SIGMATEL STA9220 CODEC	U6800	CRITICAL	STA9220
131S1034	1	1000PF NPO CAPACITOR	C6805	CRITICAL	STA9220
131S1034	1	1000PF NPO CAPACITOR	C6806	CRITICAL	STA9220
132S0045	1	0.001uF 50V 10% X7R Capacitor	C6812	CRITICAL	STA9220
353S1268	1	Realtek ALC882 CODEC	U6800	CRITICAL	ALC882
138S0541	1	1uF 10V X7R Capacitor	C6805	CRITICAL	ALC882
138S0541	1	1uF 10V X7R Capacitor	C6806	CRITICAL	ALC882
116S0004	1	0 ohms 0402 Resistor	C6812	CRITICAL	ALC882

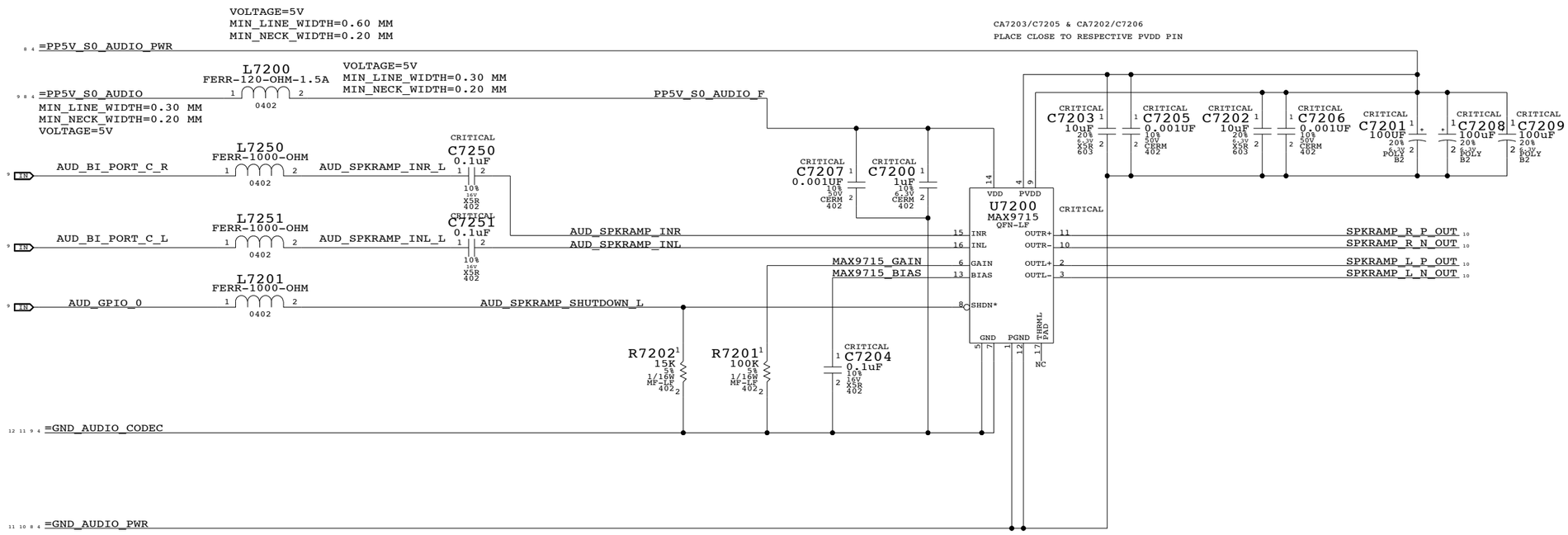
AUDIO: CODEC

SYNC_MASTER=LENGO_M1_LIOSYNC_DATE=01/06/2006

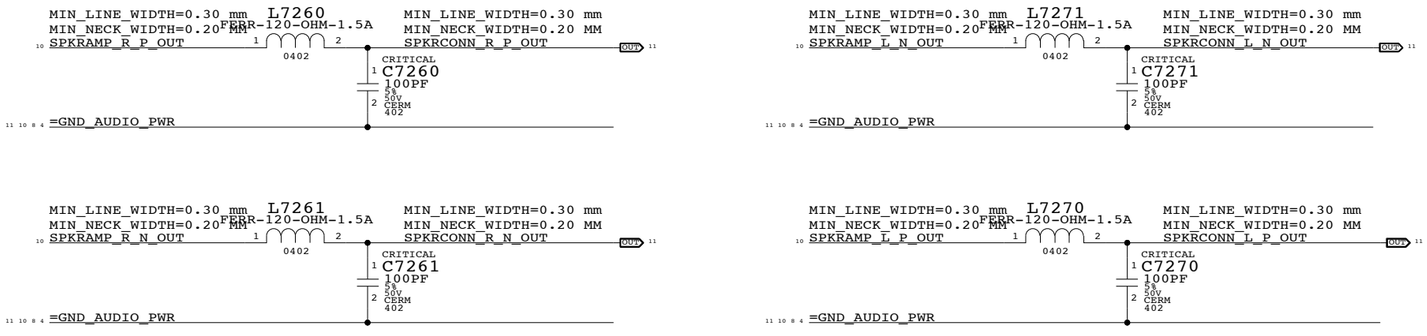
APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6951	A
SCALE: NONE	SHT: 68 OF 86	

STEREO SPEAKER AMPLIFIERS (MAX9715)
APN: 353S1283
Gain = 10.5dB
118 < FC < 245Hz



EMI FILTERS FOR AMPLIFIER OUTPUTS



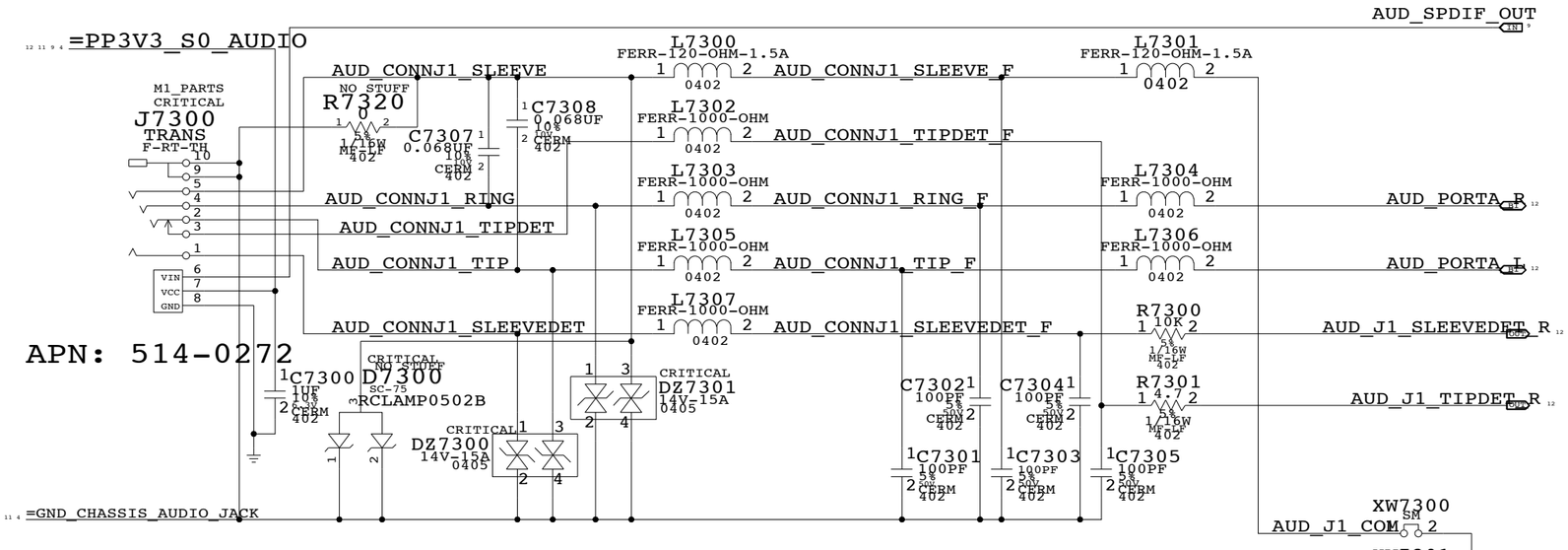
AUDIO:SPEAKER AMP

SYNC_MASTER=LENGO_M1_LR SYNC_DATE=01/06/2006

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6951	A
SCALE: NONE	SHT 72 OF 86	

AUDIO JACK 1/DEFAULT LO/HP CONNECTOR, SPDIF TX

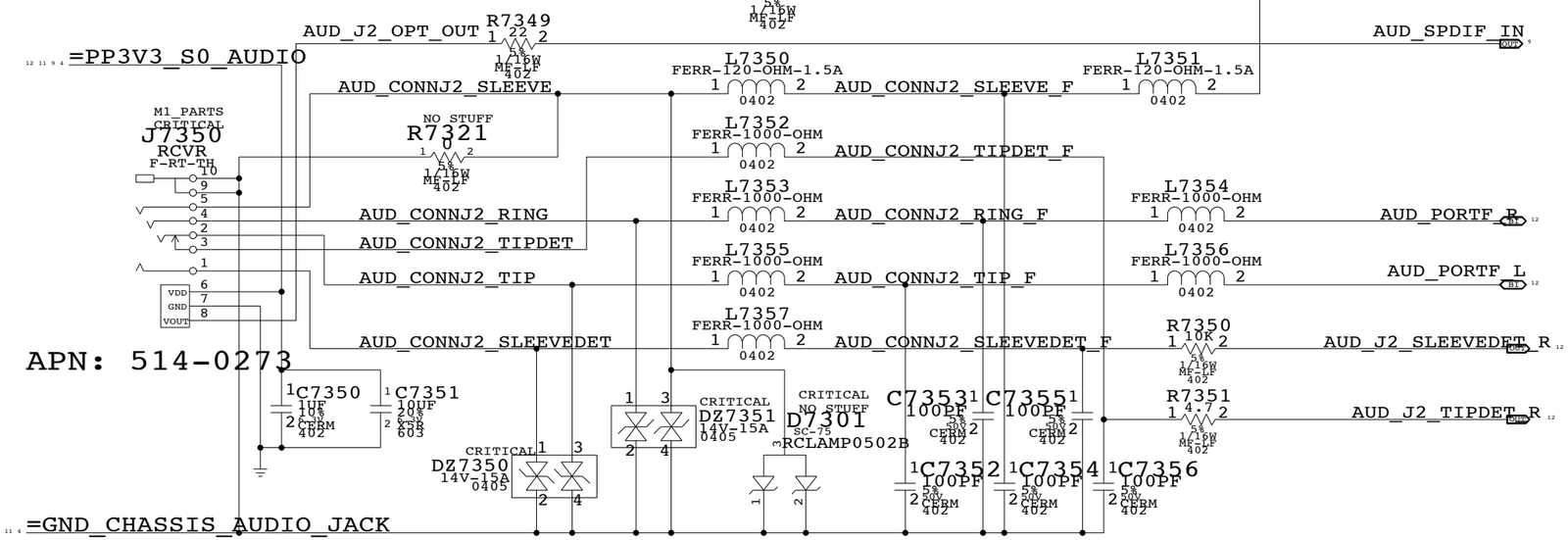


PLACE AT GROUND MOAT

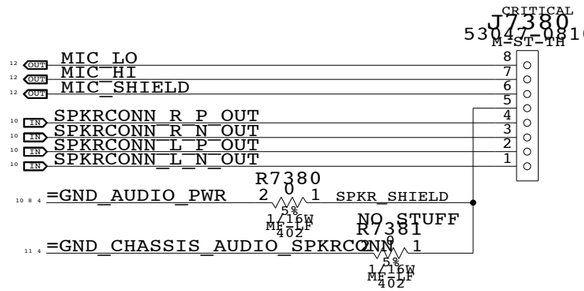
NO STUFF R7391 0

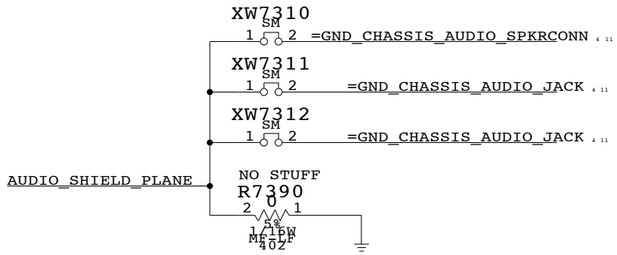
=GND_CHASSIS_AUDIO_JACK =GND_AUDIO_CODEC

AUDIO JACK 2/DEFAULT LINE IN CONNECTOR, SPDIF RX



SPEAKER/MIC CONNECTOR
APN: 518-0216





AUDIO SHIELD
(FILLED SHAPE)

AUDIO: JACKS

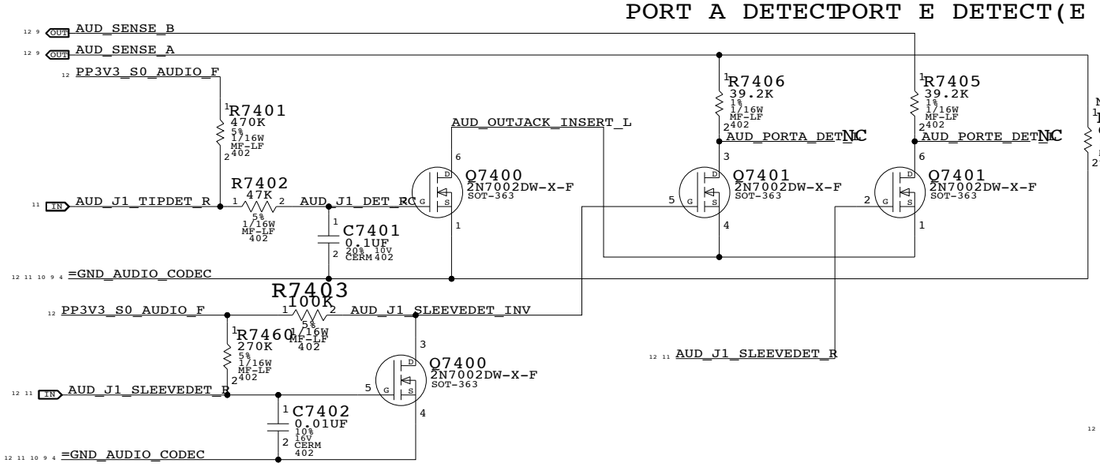
SYNC_MASTER=LENGO_M1_I SYNC_DATE=01/06/2006

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6951	A

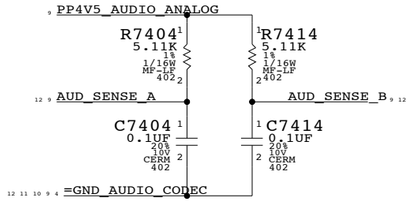
SCALE	SHT	OF
NONE	73	86

PORT A DETECT



PORT E DETECT(E TELLS H TO TURN ON)

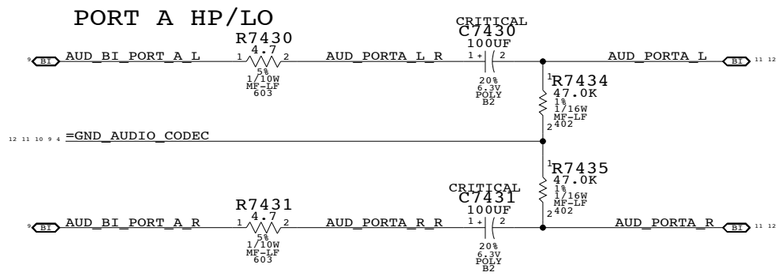
PORT B & C DETECT



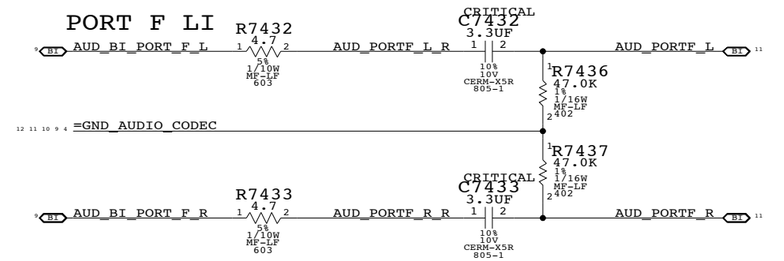
CODEC PORT ASSIGNMENTS

PORT A :HEADPHONE/LINE OUT
PORT B :MICROPHONE ON BOTH CH
PORT C :SPEAKER AMP
PORT D :UNUSED
PORT E :SW USES TO TRIGGER DIGITAL OUT
PORT F :LINE IN
CD INPUT :UNUSED

PORT A HP/LO

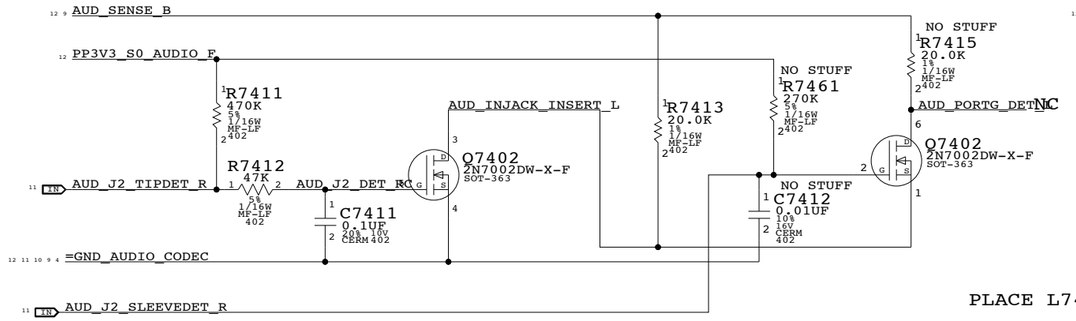


PORT F LI

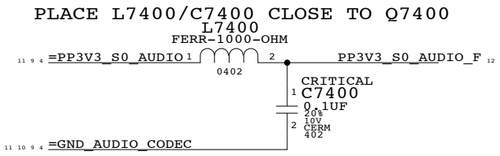


PORT F DETECT

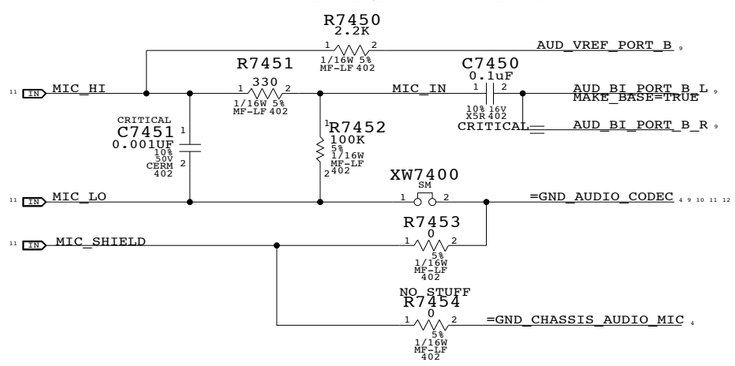
PORT G DETECT

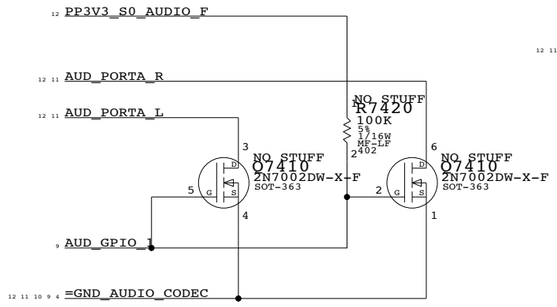


PLACE L7400/C7400 CLOSE TO Q7400

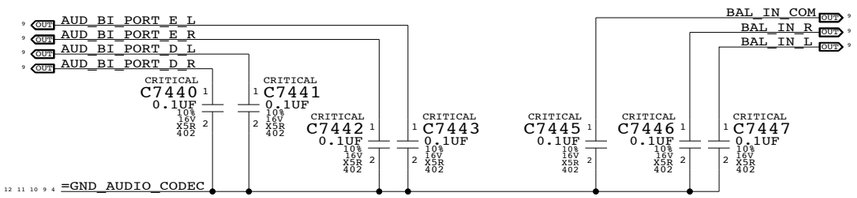


MIC INPUT CIRCUITRY





UNUSED CODEC ANALOG PORT TERMINATIONS



AUDIO: JACK TRANSLATORS

SYNC_MASTER=LENGO_M1_LIO SYNC_DATE=01/06/2006

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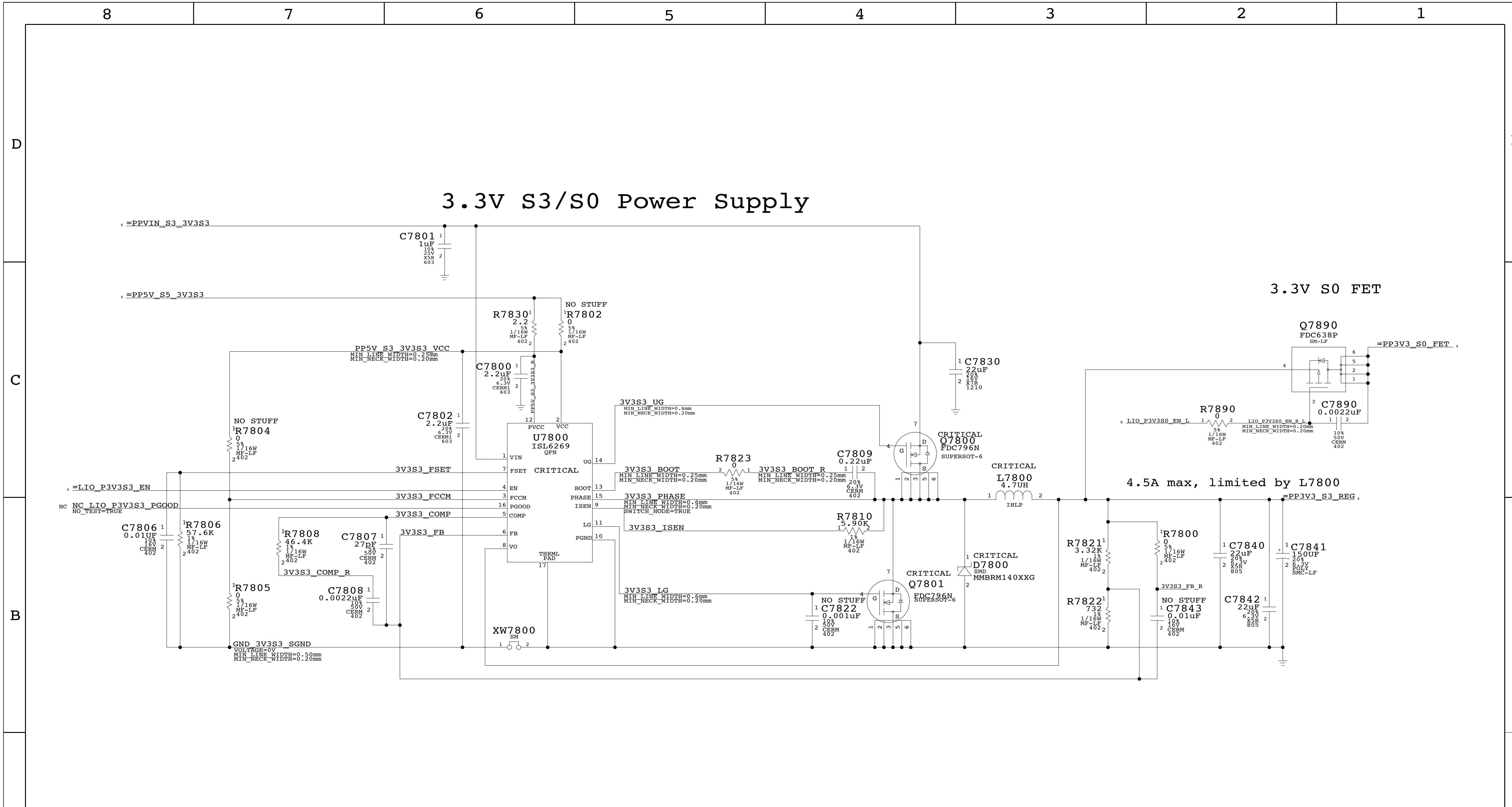
D	051-6951	A
SCALE: NONE	SHT 74 OF 86	



3.3V S3/S0 Power Supply
=PPVIN_S3_3V3S3
=PP5V_S5_3V3S3
C7801 1uF 10% 25V X5R 603
R7830 2.2 5% 1/16W MF-LF 402
NO STUFF R7802 0 5% 1/16W MF-LF 402
PP5V_S3_3V3S3_VCC
C7800 2.2uF 20% 6.3V CERM1 603
C7802 2.2uF 20% 6.3V CERM1 603
NO STUFF R7804 0 5% 1/16W MF-LF 402
U7800 ISL6269 QFN CRITICAL
VIN FSET EN FCCM PGOOD COMP FB VO
PVCC VCC UG BOOT PHASE ISEN LG PGND THRML PAD
3V3S3_FSET
=LIO_P3V3S3_EN
3V3S3_FCCM
NC NC_LIO_P3V3S3_PGOOD NO_TEST=TRUE
3V3S3_COMP
3V3S3_FB
C7806 0.01UF 10% 16V CERM 402
R7806 57.6K 1% 1/16W MF-LF 402
R7808 46.4K 1% 1/16W MF-LF 402
C7807 27pF 5% 50V CERM 402
3V3S3_COMP_R
R7805 0 5% 1/16W MF-LF 402
C7808 0.0022uF 10% 50V CERM 402
GND_3V3S3_SGND VOLTAGE=0V MIN_LINE_WIDTH=0.50mm MIN_NECK_WIDTH=0.20mm
XW7800 SM
3V3S3_UG MIN_LINE_WIDTH=0.6mm MIN_NECK_WIDTH=0.20mm
3V3S3_BOOT MIN_LINE_WIDTH=0.25mm MIN_NECK_WIDTH=0.20mm
R7823 0 5% 1/16W MF-LF 402
3V3S3_BOOT_R MIN_LINE_WIDTH=0.25mm MIN_NECK_WIDTH=0.20mm
C7809 0.22uF 20% 6.3V CERM 402
3V3S3_PHASE MIN_LINE_WIDTH=0.6mm MIN_NECK_WIDTH=0.20mm SWITCH_NODE=TRUE
3V3S3_ISEN
R7810 5.90K 1% 1/16W MF-LF 402
3V3S3_LG MIN_LINE_WIDTH=0.6mm MIN_NECK_WIDTH=0.20mm
NO STUFF C7822 0.001uF 10% 50V CERM 402
CRITICAL Q7800 FDC796N SUPERSOT-6
CRITICAL Q7801 FDC796N SUPERSOT-6
C7830 22uF 20% 16V X7R 1210
CRITICAL L7800 4.7UH IHLP
CRITICAL D7800 SMD MMBRM140XXG
4.5A max, limited by L7800
=PP3V3_S3_REG
R7821 3.32K 1% 1/16W MF-LF 402
R7822 732 1% 1/16W MF-LF 402
R7800 0 5% 1/16W MF-LF 402
3V3S3_FB_R
NO STUFF C7843 0.01uF 10% 16V CERM 402
C7840 22uF 20% 6.3V X5R 805
C7842 22uF 20% 6.3V X5R 805
C7841 150UF 20% 6.3V POLY SMC-LF
3.3V S0 FET
Q7890 FDC638P SM-LF
=PP3V3_S0_FET
LIO_P3V3S0_EN_L
R7890 0 5% 1/16W MF-LF 402
LIO_P3V3S0_EN_R_L MIN_LINE_WIDTH=0.20mm MIN_NECK_WIDTH=0.20mm
C7890 0.0022uF 10% 50V CERM 402

3.3V Supply

SYNC_MASTER=(Master) SYNC_DATE=(Master)

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D	051-6951	A
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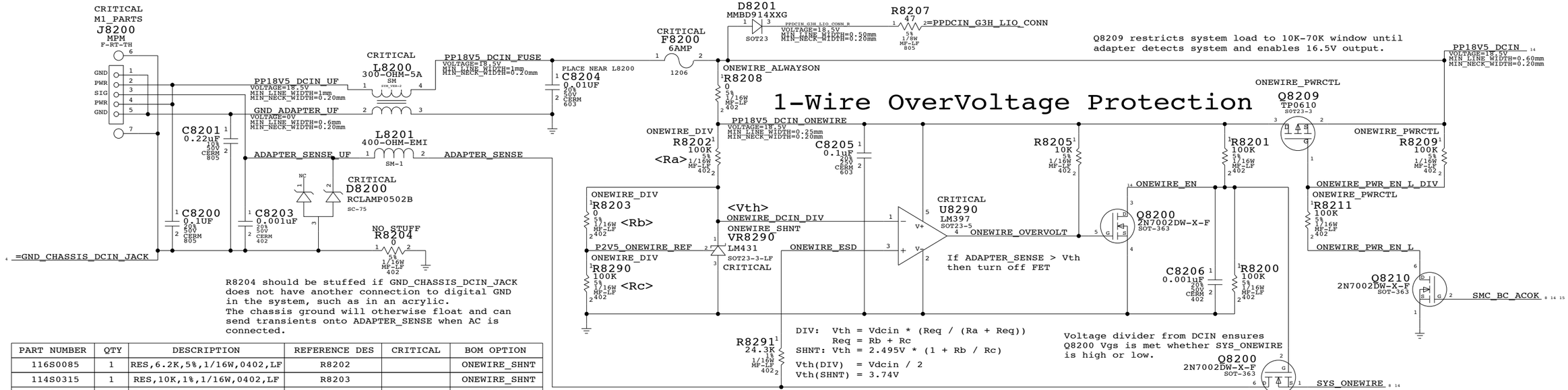
DC Power Jack



1-Wire OverVoltage Protection

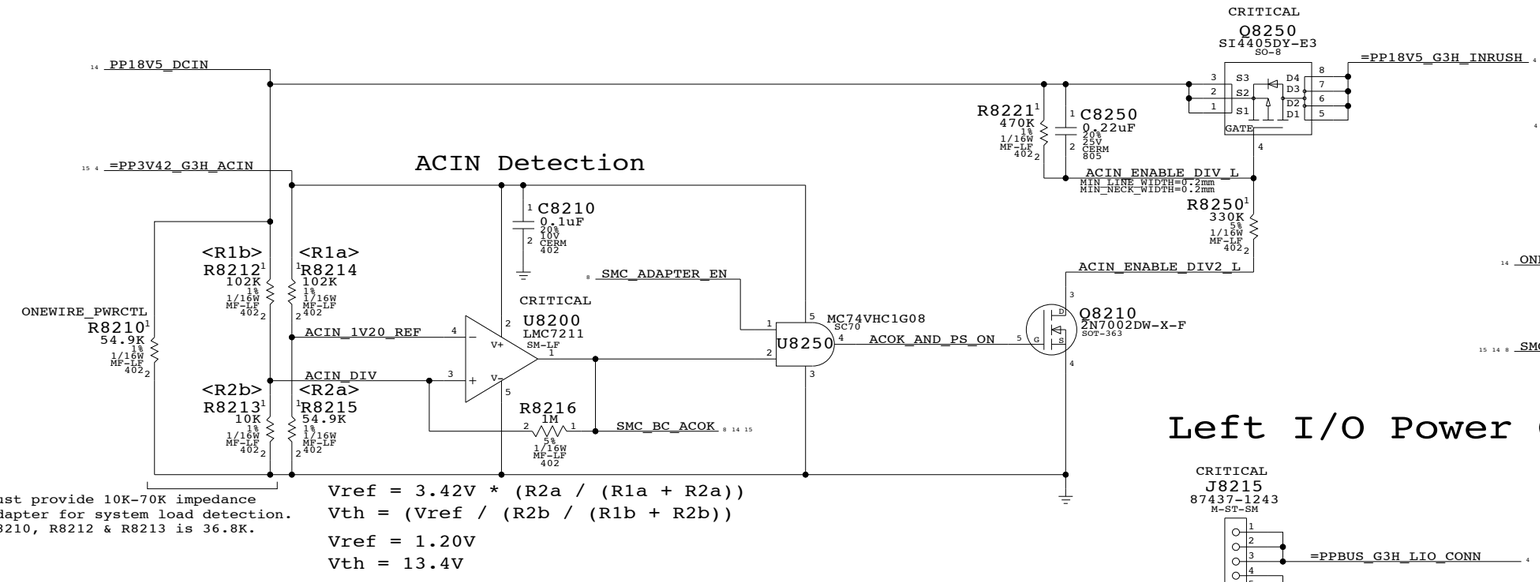
R8204 should be stuffed if GND_CHASSIS_DCIN_JACK does not have another connection to digital GND in the system, such as in an acrylic.
The chassis ground will otherwise float and can send transients onto ADAPTER_SENSE when AC is connected.

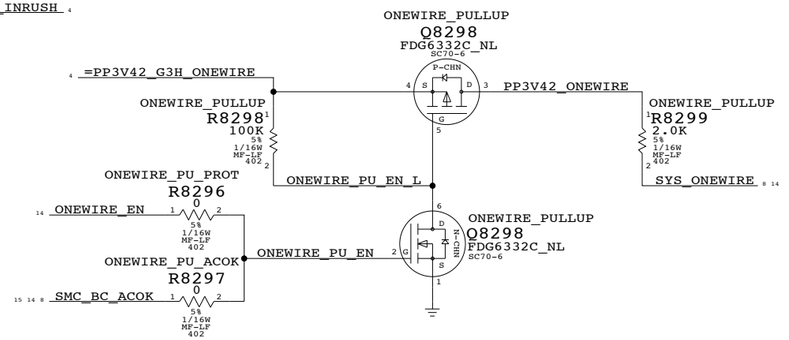
PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
116S0085	1	RES,6.2K,5%,1/16W,0402,LF	R8202		ONEWIRE_SHNT
114S0315	1	RES,10K,1%,1/16W,0402,LF	R8203		ONEWIRE_SHNT
114S0343	1	RES,20K,1%,1/16W,0402,LF	R8290		ONEWIRE_SHNT

ONEWIRE_SHNT BOM option allows the use of an adjustable shunt voltage regulator to provide the reference to the LM397 comparator. This allows the protection circuit to enforce a ~3.7V max signal on ADAPTER_SENSE instead of the voltage divider DCIN/2 approach.

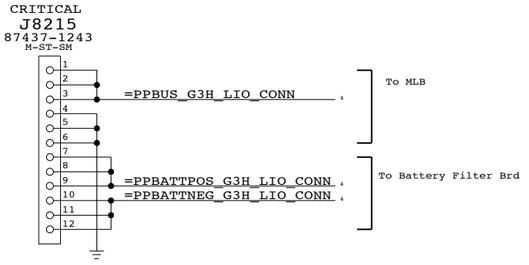
R8202 value ensures 1mA current for DCIN >= 13.4V per LM431 spec.

Inrush Limiter





Left I/O Power Connector



DC-In & Battery Connectors

SYNC_MASTER=(Master) SYNC_DATE=(Master)

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6951	A
SCALE: NONE	SHT 82 OF 86	

PBUS SUPPLY

BATTERY CHARGER

=PP18V5_G3H_CHGR
CHGR_VDD
Q8340 IRLML5203-2.6A SM
R8342 93.1K 1% 1/16W MF-LF 402
R8340 118K 1% 1/16W MF-LF 402
R8341 10K 1% 1/16W MF-LF 402
D8340 MMBD914XXG SOT23
CHGR_ACSET
CHGR_CSON_R
R8343 10K 1% 1/16W MF-LF 402
CHGR_ACSET_D
R8344 100K 1% 1/16W MF-LF 402
C8341 0.1UF 10% 16V X5R 402
CHGR_SGND
CHGR_DCSET
CHGR_DCIN
C8340 0.1UF 20% 25V CERM 603
NO STUFF C8307 0.1UF 20% 25V CERM 603
C8300 0.033uF 10% 16V X5R 402
R8311 0 5% 1/16W MF-LF 402
CHGR_ICOMP_R
R8301 34.8K 1% 1/16W MF-LF 402
CHGR_VCOMP_C1
C8301 0.022uF 10% 16V CERM-X5R 402
NO STUFF C8314 0.068UF 10% 10V CERM 402
NO STUFF C8315 680pF 10% 50V CERM 402
C8302 0.0082uF 10% 25V X7R 402
CRITICAL Q8300 SI4405DY-E3 SO-8
C8311 1uF 10% 6.3V CERM 402
C8319 0.0033uF 10% 50V CERM 402
R8300 4.7 5% 1/16W MF-LF 402
C8312 1uF 10% 6.3V CERM 402
R8302 100 1% 1/16W MF-LF 402
CHGR_ICM_R
PPVDCIN_G3H_PRE
MIN_LINE_WIDTH=0.6mm MIN_NECK_WIDTH=0.25mm
NO STUFF R8310 100K 1% 1/16W MF-LF 402
MIN_LINE_WIDTH=0.2mm MIN_NECK_WIDTH=0.2mm
XW8301 SM
C8304 0.1UF 20% 10V CERM 402
R8305 10 1% 1/16W MF-LF 402
CHGR_CSIN_R
XW8302 SM
R8307 0.02 0.5% 1W MF 0612
PPVDCIN_G3H_R
MIN_LINE_WIDTH=0.6mm MIN_NECK_WIDTH=0.25mm
C8316 10UF 10% 25V X5R 1206
C8305 10UF 10% 25V X5R 1206
C8306 10UF 10% 25V X5R 1206
U8300 ISL6255AHRZ QFN
VDD VDDP ICM ACLIM ICOMP VCOMP VADJ CELLS CSOP CSON CHLIM VREF ACPRN ACSET THRML_PAD GND PGND
SGATE CSIP CSIN BGATE DCIN BOOT UGATE PHASE LGATE EN DCPRN DCSET
CHGR_SGATE CHGR_CSI_P CHGR_CSI_N NC_CHGR_BGATE CHGR_DCIN CHGR_BOOT CHGR_UGATE CHGR_LGATE CHGR_EN CHGR_DCPRN CHGR_DCSET
NO_TEST=TRUE
R8303 2.2 5% 1/16W MF-LF 402
CHGR_BOOT_R
D8300 SOD-123 B0530WXF
CHGR_VDDP
C8303 0.1UF 20% 25V CERM 603
MIN_LINE_WIDTH=0.6mm MIN_NECK_WIDTH=0.2mm
CRITICAL Q8301 HAT2168H LFPAK
L8300 4.7UH SM NC
10A max, limited by L8300, Q8301
=PPVBAT_G3H_CHGR_REG
C8308 22UF 20% 16V X7R 1210
C8309 100uF 20% 16V ELEC 6.3X5.5SM1
R8308 0.01 0.5% 1W MF 0612
PPVBAT_G3H_CHGR_OUT
MIN_LINE_WIDTH=0.6mm MIN_NECK_WIDTH=0.25mm
C8310 22UF 20% 16V X7R 1210
XW8304 SM
XW8303 SM
R8304 270 5% 1/16W MF-LF 402
CHGR_CSON_R
CHGR_CSO_N
R8306 2.2 5% 1/16W MF-LF 402
CHGR_CSOP_R
CHGR_CSO_P
CHGR_CSO_P
CHGR_CSO_N
CHGR_CSIN_R
CHGR_CSI_P
CHGR_CSI_N
CHGR_ACLIM
CHGR_ICOMP
CHGR_VCOMP
CHGR_VADJ
CHGR_VREF
CHGR_ACPRN
CHGR_ACSET
CHGR_CHLIM
CHGR_PHASE
SWITCH_NODE=TRUE
R8370 49.9 1% 1/16W MF-LF 603
C8370 0.0022uF 10% 50V CERM 603
CRITICAL Q8302 HAT2165H LFPAK
XW8300 SM
CHGR_SGND
CHGR_VDD
R8350 100K 1% 1/16W MF-LF 402
R8352 100K 1% 1/16W MF-LF 402
CHGR_EN
Q8350 2N7002 SOT23-LF
CHGR_ACPRN
CHGR_DCPRN
CHGR_VREF
NO STUFF R8364 0 5% 1/16W MF-LF 402
NO STUFF R8365 0 5% 1/16W MF-LF 402
CHGR_VADJ
If stuffed, R8364+R8365 > 70K so Iref < 100uA
PPVBATT_G3H_PRE
R8320 27 5% 3W MF 2525
CRITICAL Q8320 SI4405DY-E3 SO-8
CRITICAL Q8321 SI4405DY-E3 SO-8
D8321 MMBD914XXG SOT23
=PPVBATT_G3H_LIO_CONN
CRITICAL F8302 8AMP-24V 1206
PPVBATT_G3H_FET
MIN_LINE_WIDTH=0.6mm MIN_NECK_WIDTH=0.25mm
PPVBATT_G3H_FUSE
MIN_LINE_WIDTH=0.6mm MIN_NECK_WIDTH=0.25mm
L8320 FERR-50-OHM SM-LF
=BATT_POS
MIN_LINE_WIDTH=0.6mm MIN_NECK_WIDTH=0.25mm
C8327 0.001UF 10% 50V CERM 402
GND_BATT_CHGND
C8330 0.001UF 10% 50V CERM 402
L8321 FERR-50-OHM SM-LF
=BATT_NEG
MIN_LINE_WIDTH=0.6mm MIN_NECK_WIDTH=0.25mm
PPVBAT_G3H_CHGR_OUT
C8320 0.01uF 10% 16V X5R 402
C8321 0.1uF 10% 16V X5R 402
NO STUFF C8322 0.01UF 10% 16V CERM 402
C8325 0.1uF 10% 16V X5R 402
R8330 470K 1% 1/16W MF-LF 402
CHG_EN_DIV_L
MIN_LINE_WIDTH=0.2mm MIN_NECK_WIDTH=0.2mm
R8322 39.2K 1% 1/16W MF-LF 402
TCHG_EN_DIV_L
MIN_LINE_WIDTH=0.2mm MIN_NECK_WIDTH=0.2mm
NO STUFF C8324 0.01UF 10% 16V CERM 402
R8331 330K 1% 1/16W MF-LF 402
CHG_EN_DIV2_L
R8323 35.7K 1% 1/16W MF-LF 402
TCHG_EN_DIV2_L
=PP3V42_G3H_ACIN
R8324 100K 1% 1/16W MF-LF 402
Q8322 2N7002DW-X-F SOT-363
Q8324 2N7002DW-X-F SOT-363
Q8322 2N7002DW-X-F SOT-363
Q8324 2N7002DW-X-F SOT-363
SMC_BATT_TRICKLE_EN_L
R8325 10K 1% 1/16W MF-LF 402
SMC_BC_ACOK
SMC_BC_ACOK_R
C8323 0.1UF 20% 10V CERM 402
SMC_BATT_CHG_EN
R8360 2.37K 1% 1/16W MF-LF 402
R8363 3.32K 1% 1/16W MF-LF 402
C8361 0.1UF 20% 10V CERM 402
SMC_SYS_ISET
CHGR_ACLIM
Using PWM drive
R8367 24.3K 1% 1/16W MF-LF 402
R8362 24.3K 1% 1/16W MF-LF 402
R8366 6.04K 1% 1/16W MF-LF 402
C8362 0.1UF 20% 10V CERM 402
SMC_BATT_ISET
CHGR_CHLIM
As shown, Ichg = 3.9A max
Using PWM drive

PBus Supply & Batt. Charger

SYNC_MASTER=(Master) SYNC_DATE=(Master)

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APPLE COMPUTER INC.

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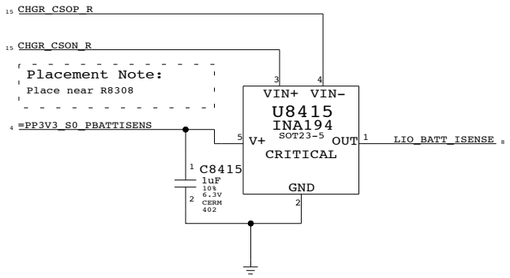
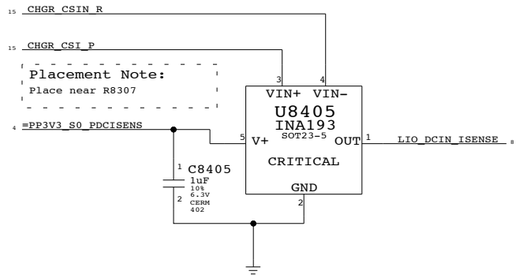
SCALE: NONE	SHT 83 OF 86	

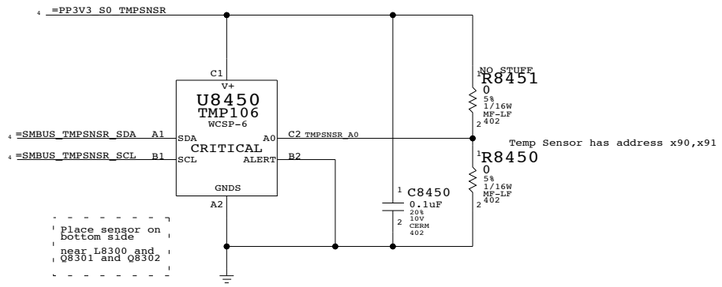
Battery Current Sense



DCIn Current Sense



TMP106 Thermal Sensor



Current & Thermal Sensors

SYNC_MASTER=(Master) SYNC_DATE=(Master)

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6951	A

SCALE	SHT	OF
NONE	84	86

Title: Basenet Report
Design: buzz
Date: Jan 6 17:35:06 2006

Base nets and synonyms for buzz_lib.BUZZ(@buzz_lib.buzz(sch_1))

Base Signal	Synonyms	Location([Zone][dir])
3V3S3_BOOT	3V3S3_BOOT - @buzz_lib.BUZZ	13C5
3V3S3_BOOT_R	3V3S3_BOOT_R - @buzz_lib.BUZZ	13C5
3V3S3_COMP	3V3S3_COMP - @buzz_lib.BUZZ	13B6
3V3S3_COMP_R	3V3S3_COMP_R - @buzz_lib.BUZZ	13B7
3V3S3_FB	3V3S3_FB - @buzz_lib.BUZZ	13B6
3V3S3_FB_R	3V3S3_FB_R - @buzz_lib.BUZZ	13B2
3V3S3_FCCM	3V3S3_FCCM - @buzz_lib.BUZZ	13B6
3V3S3_FSET	3V3S3_FSET - @buzz_lib.BUZZ	13C6
3V3S3_ISEN	3V3S3_ISEN - @buzz_lib.BUZZ	13B5
3V3S3_LG	3V3S3_LG - @buzz_lib.BUZZ	13B5
3V3S3_PHASE	3V3S3_PHASE - @buzz_lib.BUZZ	13B5
3V3S3_UG	3V3S3_UG - @buzz_lib.BUZZ	13C5
5V_REG_IN	5V_REG_IN - @buzz_lib.BUZZ	9B3
=BATT_NEG	=BATT_NEG - @buzz_lib.BUZZ	4A6 15A1
	=PPBATTNEG_G3H_LIO_CONN - @buzz_lib.BUZZ	4A8 14A3
	BATT_NEG - @buzz_lib.BUZZ	4A6
	=PPBATTNEG_G3H_LIO_CONN - @buzz_lib.BUZZ	4A8 14A3
=BATT_POS	=BATT_POS - @buzz_lib.BUZZ	4A6 15B1
	=PPBATTPOS_G3H_LIO_CONN - @buzz_lib.BUZZ	4A8 14A3
	BATT_POS - @buzz_lib.BUZZ	4A6
	=PPBATTPOS_G3H_LIO_CONN - @buzz_lib.BUZZ	4A8 14A3
=GND_AUDIO	=GND_AUDIO - @buzz_lib.BUZZ	4B8 8B3
	=GND_AUDIO_CODEC - @buzz_lib.BUZZ	4B6 9A5 9A6 9B6 9D2 10C7 11B2 11B5 12A3 12A5 12A6 12B5 12B8 12C3 12C3 12C5 12C8 12D8
	GND_AUDIO - @buzz_lib.BUZZ	4B6
	=GND_AUDIO_CODEC - @buzz_lib.BUZZ	4B6 9A5 9A6 9B6 9D2 10C7 11B2 11B5 12A3 12A5 12A6 12B5 12B8 12C3 12C3 12C5 12C8 12D8
=GND_AUDIO_PWR	=GND_AUDIO_PWR - @buzz_lib.BUZZ	4B8 8B6 10A4 10A4 10A7 10A7 10B7 11C3
	GND_AUDIO_PWR - @buzz_lib.BUZZ	4B6
=GND_CHASSIS_AUDIO_JACK	=GND_CHASSIS_AUDIO_JACK - @buzz_lib.BUZZ	4B6 11A8 11B1 11B1 11B7 11C8
	CHASSIS_GND1 - @buzz_lib.BUZZ	4B8
=GND_CHASSIS_AUDIO_MIC	=GND_CHASSIS_AUDIO_MIC - @buzz_lib.BUZZ	4A6 12A5
	=GND_CHASSIS_AUDIO_SPKRCONN - @buzz_lib.BUZZ	4A6 11B1 11C3
	=GND_CHASSIS_PCIE_EXCARD - @buzz_lib.BUZZ	4A6 6B2
	CHASSIS_GND2 - @buzz_lib.BUZZ	4A8
	=GND_CHASSIS_PCIE_EXCARD - @buzz_lib.BUZZ	4A6 6B2
	=GND_CHASSIS_AUDIO_SPKRCONN - @buzz_lib.BUZZ	4A6 11B1 11C3
=GND_CHASSIS_DCIN_JACK	=GND_CHASSIS_DCIN_JACK - @buzz_lib.BUZZ	4A6 14C8
	=GND_CHASSIS_LTUSB - @buzz_lib.BUZZ	4A6 5B2
	CHASSIS_GND3 - @buzz_lib.BUZZ	4A8
	=GND_CHASSIS_LTUSB - @buzz_lib.BUZZ	4A6 5B2
=LEFT_USB_PWRON	=LEFT_USB_PWRON - @buzz_lib.BUZZ	4D1 5C7
	=LIO_P3V3S3_EN - @buzz_lib.BUZZ	4D1 13B8
	LIO_S3_EN - @buzz_lib.BUZZ	4D3 8B6
	=LIO_P3V3S3_EN - @buzz_lib.BUZZ	4D1 13B8
=PP1V5_S0	=PP1V5_S0 - @buzz_lib.BUZZ	4C8 8C6
	=PP1V5_S0_EXCARD - @buzz_lib.BUZZ	4C6 6C7 6C8
	=PP1V5_S0_MINI - @buzz_lib.BUZZ	4C6 7C2
	PP1V5_S0 - @buzz_lib.BUZZ	4C6
	=PP1V5_S0_MINI - @buzz_lib.BUZZ	4C6 7C2
	=PP1V5_S0_EXCARD - @buzz_lib.BUZZ	4C6 6C7 6C8
=PP3V3_S0_AUDIO	=PP3V3_S0_AUDIO - @buzz_lib.BUZZ	4C6 9B5 9D7 11B8 11D8 12B5
	=PP3V3_S0_EXCARD - @buzz_lib.BUZZ	4C6 6B4 6C7
	=PP3V3_S0_FET - @buzz_lib.BUZZ	4C8 13C1
	=PP3V3_S0_PDCISENS - @buzz_lib.BUZZ	4C6 16C4
	=PP3V3_S0_TMPSNSR - @buzz_lib.BUZZ	4C6 16B5
	=PP3V3_S0_PBATTISENS - @buzz_lib.BUZZ	4C6 16C7
	=PP3V3_S0_MINI - @buzz_lib.BUZZ	4C6 7D2
	PP3V3_S0_LIO - @buzz_lib.BUZZ	4C6
	=PP3V3_S0_TMPSNSR - @buzz_lib.BUZZ	4C6 16B5
	=PP3V3_S0_PDCISENS - @buzz_lib.BUZZ	4C6 16C4
	=PP3V3_S0_PBATTISENS - @buzz_lib.BUZZ	4C6 16C7
	=PP3V3_S0_MINI - @buzz_lib.BUZZ	4C6 7D2
	=PP3V3_S0_FET - @buzz_lib.BUZZ	4C8 13C1
	=PP3V3_S0_EXCARD - @buzz_lib.BUZZ	4C6 6B4 6C7
=PP3V3_S3_EXCARD	=PP3V3_S3_EXCARD - @buzz_lib.BUZZ	4C6 6A5 6B8 6C7 6C8
	=PP3V3_S3_REG - @buzz_lib.BUZZ	4C8 13B1
	=PP3V3_S3_MINI - @buzz_lib.BUZZ	4C6 7C2
	PP3V3_S3_LIO - @buzz_lib.BUZZ	4C6
	=PP3V3_S3_REG - @buzz_lib.BUZZ	4C8 13B1
	=PP3V3_S3_MINI - @buzz_lib.BUZZ	4C6 7C2
=PP3V42_G3H	=PP3V42_G3H - @buzz_lib.BUZZ	4D8 8C6
	=PP3V42_G3H_ONEWIRE - @buzz_lib.BUZZ	4C6 14B3
	=PP3V42_G3H_ACIN - @buzz_lib.BUZZ	4C6 14B8 15B6
	PP3V42_G3H - @buzz_lib.BUZZ	4D6
	=PP3V42_G3H_ONEWIRE - @buzz_lib.BUZZ	4C6 14B3
	=PP3V42_G3H_ACIN - @buzz_lib.BUZZ	4C6 14B8 15B6
=PP5V_S0_AUDIO	=PP5V_S0_AUDIO - @buzz_lib.BUZZ	4B8 8C6 9B5 10D7
	PP5V_S0_AUDIO - @buzz_lib.BUZZ	4B6
=PP5V_S0_AUDIO_PWR	=PP5V_S0_AUDIO_PWR - @buzz_lib.BUZZ	4B8 8C6 10D7
	PP5V_S0_AUDIO_PWR - @buzz_lib.BUZZ	4B6
=PP5V_S5	=PP5V_S5 - @buzz_lib.BUZZ	4B8 8C6
	=PP5V_S5_3V3S3 - @buzz_lib.BUZZ	4B6 13C8
	=PP5V_S5_LTUSB - @buzz_lib.BUZZ	4B6 5C7
	PP5V_S5 - @buzz_lib.BUZZ	4B6
	=PP5V_S5_LTUSB - @buzz_lib.BUZZ	4B6 5C7
	=PP5V_S5_3V3S3 - @buzz_lib.BUZZ	4B6 13C8
=PP18V5_G3H_CHGR	=PP18V5_G3H_CHGR - @buzz_lib.BUZZ	4D6 15D8
	=PP18V5_G3H_INRUSH - @buzz_lib.BUZZ	4D8 14B3
	PP18V5_G3H_CHGR - @buzz_lib.BUZZ	4D6
	=PP18V5_G3H_INRUSH - @buzz_lib.BUZZ	4D8 14B3
=PPBUS_G3H_LIO_CONN	=PPBUS_G3H_LIO_CONN - @buzz_lib.BUZZ	4D6 14A3
	=PPVBAT_G3H_CHGR_REG - @buzz_lib.BUZZ	4D8 15C1
	=PPVIN_S3_3V3S3 - @buzz_lib.BUZZ	4D6 13D8
	PPVBAT_G3H_CHGR_REG - @buzz_lib.BUZZ	4D6
	=PPVIN_S3_3V3S3 - @buzz_lib.BUZZ	4D6 13D8
	=PPVBAT_G3H_CHGR_REG - @buzz_lib.BUZZ	4D8 15C1
=PPDCIN_G3H	=PPDCIN_G3H - @buzz_lib.BUZZ	4D6 8C6
	=PPVBATT_G3H_LIO_CONN - @buzz_lib.BUZZ	4D8 15B3
	=PPDCIN_G3H_LIO_CONN - @buzz_lib.BUZZ	4D8 14D3
	PPDCIN_G3H - @buzz_lib.BUZZ	4D6
	=PPVBATT_G3H_LIO_CONN - @buzz_lib.BUZZ	4D8 15B3
	=PPDCIN_G3H_LIO_CONN - @buzz_lib.BUZZ	4D8 14D3
=SMBUS_EXCARD_SCL	=SMBUS_EXCARD_SCL - @buzz_lib.BUZZ	4A3 6C3
	SMBUS_SB_SCL - @buzz_lib.BUZZ	4A6 8C3
=SMBUS_EXCARD_SDA	=SMBUS_EXCARD_SDA - @buzz_lib.BUZZ	4A3 6C3
	SMBUS_SB_SDA - @buzz_lib.BUZZ	4A6 8C3
=SMBUS_MINI_SCL	=SMBUS_MINI_SCL - @buzz_lib.BUZZ	4A3 7B3
	SMBUS_MINI_SCL - @buzz_lib.BUZZ	4A5
=SMBUS_MINI_SDA	=SMBUS_MINI_SDA - @buzz_lib.BUZZ	4A3 7B3
	SMBUS_MINI_SDA - @buzz_lib.BUZZ	4A5
=SMBUS_TMPSNSR_SCL	=SMBUS_TMPSNSR_SCL - @buzz_lib.BUZZ	4A3 16B5
	SMBUS_SMC_SCL - @buzz_lib.BUZZ	4A6 8C3
=SMBUS_TMPSNSR_SDA	=SMBUS_TMPSNSR_SDA - @buzz_lib.BUZZ	4A3 16B5
	SMBUS_SMC_SDA - @buzz_lib.BUZZ	4A6 8C3
ACIN_1V20_REF	ACIN_1V20_REF - @buzz_lib.BUZZ	14A7
ACIN_DIV	ACIN_DIV - @buzz_lib.BUZZ	14A7
ACIN_ENABLE_DIV2_L	ACIN_ENABLE_DIV2_L - @buzz_lib.BUZZ	14B5
ACIN_ENABLE_DIV_L	ACIN_ENABLE_DIV_L - @buzz_lib.BUZZ	14B5
ACOK_AND_PS_ON	ACOK_AND_PS_ON - @buzz_lib.BUZZ	14A5
ACZ_BITCLK	ACZ_BITCLK - @buzz_lib.BUZZ	8B6 9D7
ACZ_RST_L	ACZ_RST_L - @buzz_lib.BUZZ	8B3 9C7
ACZ_SDATAIN<0>	ACZ_SDATAIN<0> - @buzz_lib.BUZZ	8B6 9D7
ACZ_SDATAOUT	ACZ_SDATAOUT - @buzz_lib.BUZZ	8B6 9D7
ACZ_SYNC	ACZ_SYNC - @buzz_lib.BUZZ	8B6 9D7
ADAPTER_SENSE	ADAPTER_SENSE - @buzz_lib.BUZZ	14D6
ADAPTER_SENSE_UF	ADAPTER_SENSE_UF - @buzz_lib.BUZZ	14D7
AUDIO_SHIELD_PLANE	AUDIO_SHIELD_PLANE - @buzz_lib.BUZZ	11B3
AUD_4V5_SHDN_L	AUD_4V5_SHDN_L - @buzz_lib.BUZZ	9B3
AUD_ALC_COUT	AUD_ALC_COUT - @buzz_lib.BUZZ	9B5
AUD_ANALOG_FILT_1	AUD_ANALOG_FILT_1 - @buzz_lib.BUZZ	9C4
AUD_ANALOG_FILT_2	AUD_ANALOG_FILT_2 - @buzz_lib.BUZZ	9C4
AUD_BI_PORT_A_L	AUD_BI_PORT_A_L - @buzz_lib.BUZZ	9C1 12D3
AUD_BI_PORT_A_R	AUD_BI_PORT_A_R - @buzz_lib.BUZZ	9C1 12C3
AUD_BI_PORT_B_L	AUD_BI_PORT_B_L - @buzz_lib.BUZZ	9C1 12A6
	AUD_BI_PORT_B_R - @buzz_lib.BUZZ	9C1 12A5
AUD_BI_PORT_C_L	AUD_BI_PORT_C_L - @buzz_lib.BUZZ	9C7 10C7
AUD_BI_PORT_C_R	AUD_BI_PORT_C_R - @buzz_lib.BUZZ	9C7 10C7
AUD_BI_PORT_D_L	AUD_BI_PORT_D_L - @buzz_lib.BUZZ	9C7 12B3
AUD_BI_PORT_D_R	AUD_BI_PORT_D_R - @buzz_lib.BUZZ	9C7 12B3
AUD_BI_PORT_E_L	AUD_BI_PORT_E_L - @buzz_lib.BUZZ	9C1 12B3
AUD_BI_PORT_E_R	AUD_BI_PORT_E_R - @buzz_lib.BUZZ	9C1 12B3
AUD_BI_PORT_F_L	AUD_BI_PORT_F_L - @buzz_lib.BUZZ	9C1 12C3
AUD_BI_PORT_F_R	AUD_BI_PORT_F_R - @buzz_lib.BUZZ	9C1 12B3
AUD_BYPASS	AUD_BYPASS - @buzz_lib.BUZZ	9C4
AUD_CONNJ1_RING	AUD_CONNJ1_RING - @buzz_lib.BUZZ	11C7
AUD_CONNJ1_RING_F	AUD_CONNJ1_RING_F - @buzz_lib.BUZZ	11C6
AUD_CONNJ1_SLEEVE	AUD_CONNJ1_SLEEVE - @buzz_lib.BUZZ	11D7
AUD_CONNJ1_SLEEVEDET	AUD_CONNJ1_SLEEVEDET - @buzz_lib.BUZZ	11C7
AUD_CONNJ1_SLEEVEDET_F	AUD_CONNJ1_SLEEVEDET_F - @buzz_lib.BUZZ	11C6
AUD_CONNJ1_SLEEVE_F	AUD_CONNJ1_SLEEVE_F - @buzz_lib.BUZZ	11D6
AUD_CONNJ1_TIP	AUD_CONNJ1_TIP - @buzz_lib.BUZZ	11C7
AUD_CONNJ1_TIPDET	AUD_CONNJ1_TIPDET - @buzz_lib.BUZZ	11C7
AUD_CONNJ1_TIPDET_F	AUD_CONNJ1_TIPDET_F - @buzz_lib.BUZZ	11D6
AUD_CONNJ1_TIP_F	AUD_CONNJ1_TIP_F - @buzz_lib.BUZZ	11C6
AUD_CONNJ2_RING	AUD_CONNJ2_RING - @buzz_lib.BUZZ	11B7
AUD_CONNJ2_RING_F	AUD_CONNJ2_RING_F - @buzz_lib.BUZZ	11B5
AUD_CONNJ2_SLEEVE	AUD_CONNJ2_SLEEVE - @buzz_lib.BUZZ	11B7
AUD_CONNJ2_SLEEVEDET	AUD_CONNJ2_SLEEVEDET - @buzz_lib.BUZZ	11A7
AUD_CONNJ2_SLEEVEDET_F	AUD_CONNJ2_SLEEVEDET_F - @buzz_lib.BUZZ	11A5
AUD_CONNJ2_SLEEVE_F	AUD_CONNJ2_SLEEVE_F - @buzz_lib.BUZZ	11B5
AUD_CONNJ2_TIP	AUD_CONNJ2_TIP - @buzz_lib.BUZZ	11B7
AUD_CONNJ2_TIPDET	AUD_CONNJ2_TIPDET - @buzz_lib.BUZZ	11B7
AUD_CONNJ2_TIPDET_F	AUD_CONNJ2_TIPDET_F - @buzz_lib.BUZZ	11B5
AUD_CONNJ2_TIP_F	AUD_CONNJ2_TIP_F - @buzz_lib.BUZZ	11B5
AUD_GPIO_0	AUD_GPIO_0 - @buzz_lib.BUZZ	9B8 10C7
AUD_GPIO_0_R	AUD_GPIO_0_R - @buzz_lib.BUZZ	9B6 9B7 9C7
AUD_GPIO_1	AUD_GPIO_1 - @buzz_lib.BUZZ	9B8 12A5
AUD_GPIO_1_R	AUD_GPIO_1_R - @buzz_lib.BUZZ	9A7 9B6 9C7
AUD_GPIO_2	AUD_GPIO_2 - @buzz_lib.BUZZ	9B6 9C7
AUD_INJACK_INSERT_L	AUD_INJACK_INSERT_L - @buzz_lib.BUZZ	12C7
AUD_J1_COM	AUD_J1_COM - @buzz_lib.BUZZ	11C4
AUD_J1_DET_RC	AUD_J1_DET_RC - @buzz_lib.BUZZ	12D7
AUD_J1_SLEEVEDET_INV	AUD_J1_SLEEVEDET_INV - @buzz_lib.BUZZ	12C7
AUD_J1_SLEEVEDET_R	AUD_J1_SLEEVEDET_R - @buzz_lib.BUZZ	11C3 12C6 12C8
AUD_J1_TIPDET_R	AUD_J1_TIPDET_R - @buzz_lib.BUZZ	11C3 12D8
AUD_J2_COM	AUD_J2_COM - @buzz_lib.BUZZ	11B4
AUD_J2_DET_RC	AUD_J2_DET_RC - @buzz_lib.BUZZ	12B7
AUD_J2_OPT_OUT	AUD_J2_OPT_OUT - @buzz_lib.BUZZ	11B7
AUD_J2_SLEEVEDET_R	AUD_J2_SLEEVEDET_R - @buzz_lib.BUZZ	11A3 12B8
AUD_J2_TIPDET_R	AUD_J2_TIPDET_R - @buzz_lib.BUZZ	11A3 12B8
AUD_JDREF	AUD_JDREF - @buzz_lib.BUZZ	9C4
AUD_OUTJACK_INSERT_L	AUD_OUTJACK_INSERT_L - @buzz_lib.BUZZ	12D7
AUD_PORTA_DET_L	AUD_PORTA_DET_L - @buzz_lib.BUZZ	12D5
AUD_PORTA_L	AUD_PORTA_L - @buzz_lib.BUZZ	11C3 12A5 12D1
AUD_PORTA_L_R	AUD_PORTA_L_R - @buzz_lib.BUZZ	12D2
AUD_PORTA_R	AUD_PORTA_R - @buzz_lib.BUZZ	11C3 12A5 12C1
AUD_PORTA_R_R	AUD_PORTA_R_R - @buzz_lib.BUZZ	12C2
AUD_PORTE_DET_L	AUD_PORTE_DET_L - @buzz_lib.BUZZ	12D5
AUD_PORTF_L	AUD_PORTF_L - @buzz_lib.BUZZ	11B3 12C1
AUD_PORTF_L_R	AUD_PORTF_L_R - @buzz_lib.BUZZ	12C2
AUD_PORTF_R	AUD_PORTF_R - @buzz_lib.BUZZ	11B3 12B1
AUD_PORTF_R_R	AUD_PORTF_R_R - @buzz_lib.BUZZ	12B2
AUD_PORTG_DET_L	AUD_PORTG_DET_L - @buzz_lib.BUZZ	12C5
AUD_SENSE_A	AUD_SENSE_A - @buzz_lib.BUZZ	9C1 12C5 12D8
AUD_SENSE_B	AUD_SENSE_B - @buzz_lib.BUZZ	9C1 12C4 12C8 12D8
AUD_SPDIF_IN	AUD_SPDIF_IN - @buzz_lib.BUZZ	9C1 11B3
AUD_SPDIF_OUT	AUD_SPDIF_OUT - @buzz_lib.BUZZ	9D1 11D3
AUD_SPDIF_OUT_R	AUD_SPDIF_OUT_R - @buzz_lib.BUZZ	9D4
AUD_SPKRAMP_INL	AUD_SPKRAMP_INL - @buzz_lib.BUZZ	10C5
AUD_SPKRAMP_INL_L	AUD_SPKRAMP_INL_L - @buzz_lib.BUZZ	10C6
AUD_SPKRAMP_INR	AUD_SPKRAMP_INR - @buzz_lib.BUZZ	10C5
AUD_SPKRAMP_INR_L	AUD_SPKRAMP_INR_L - @buzz_lib.BUZZ	10C6
AUD_SPKRAMP_SHUTDOWN_L	AUD_SPKRAMP_SHUTDOWN_L - @buzz_lib.BUZZ	10C5
AUD_VREF_FILT	AUD_VREF_FILT - @buzz_lib.BUZZ	9C4
AUD_VREF_PORT_A	AUD_VREF_PORT_A - @buzz_lib.BUZZ	9C1
AUD_VREF_PORT_B	AUD_VREF_PORT_B - @buzz_lib.BUZZ	9C1 12B6
AUD_VREF_PORT_C	AUD_VREF_PORT_C - @buzz_lib.BUZZ	9C1
AUD_VREF_PORT_D	AUD_VREF_PORT_D - @buzz_lib.BUZZ	9C1
BAL_IN_COM	BAL_IN_COM - @buzz_lib.BUZZ	9C7 12B1
BAL_IN_L	BAL_IN_L - @buzz_lib.BUZZ	9C7 12B1
BAL_IN_R	BAL_IN_R - @buzz_lib.BUZZ	9C7 12B1
BEEP	BEEP - @buzz_lib.BUZZ	9C6
CHGR_ACLIM	CHGR_ACLIM - @buzz_lib.BUZZ	15B7 15D6
CHGR_ACPRN	CHGR_ACPRN - @buzz_lib.BUZZ	15A7 15B7 15C6
CHGR_ACSET	CHGR_ACSET - @buzz_lib.BUZZ	15C6 15D7
CHGR_ACSET_D	CHGR_ACSET_D - @buzz_lib.BUZZ	15C7
CHGR_BOOT	CHGR_BOOT - @buzz_lib.BUZZ	15C5
CHGR_BOOT_R	CHGR_BOOT_R - @buzz_lib.BUZZ	15C4
CHGR_CHLIM	CHGR_CHLIM - @buzz_lib.BUZZ	15A7 15C6
CHGR_CSIN_R	CHGR_CSIN_R - @buzz_lib.BUZZ	15C3 16D4
CHGR_CSI_N	CHGR_CSI_N - @buzz_lib.BUZZ	15C5
CHGR_CSI_P	CHGR_CSI_P - @buzz_lib.BUZZ	15D5 16D4
CHGR_CSON_R	CHGR_CSON_R - @buzz_lib.BUZZ	15B2 15C7 16D7
CHGR_CSOP_R	CHGR_CSOP_R - @buzz_lib.BUZZ	15B2 16D7
CHGR_CSO_N	CHGR_CSO_N - @buzz_lib.BUZZ	15B1 15C6
CHGR_CSO_P	CHGR_CSO_P - @buzz_lib.BUZZ	15B1 15C6
CHGR_DCIN	CHGR_DCIN - @buzz_lib.BUZZ	15C4 15D7
CHGR_DCPRN	CHGR_DCPRN - @buzz_lib.BUZZ	15B7 15C4
CHGR_DCSET	CHGR_DCSET - @buzz_lib.BUZZ	15C4 15C8
CHGR_EN	CHGR_EN - @buzz_lib.BUZZ	15B7 15C4
CHGR_ICM	CHGR_ICM - @buzz_lib.BUZZ	15D6
CHGR_ICM_R	CHGR_ICM_R - @buzz_lib.BUZZ	15D6
CHGR_ICOMP	CHGR_ICOMP - @buzz_lib.BUZZ	15C6
CHGR_ICOMP_R	CHGR_ICOMP_R - @buzz_lib.BUZZ	15D6
CHGR_LGATE	CHGR_LGATE - @buzz_lib.BUZZ	15C5
CHGR_PHASE	CHGR_PHASE - @buzz_lib.BUZZ	15C4
CHGR_PHASE_R	CHGR_PHASE_R - @buzz_lib.BUZZ	15C5
CHGR_SGATE	CHGR_SGATE - @buzz_lib.BUZZ	15D5
CHGR_SGND	CHGR_SGND - @buzz_lib.BUZZ	15A8 15A8 15B6 15B8 15C5 15C7 15D6
CHGR_UGATE	CHGR_UGATE - @buzz_lib.BUZZ	15C5
CHGR_VADJ	CHGR_VADJ - @buzz_lib.BUZZ	15B6 15C6
CHGR_VCOMP	CHGR_VCOMP - @buzz_lib.BUZZ	15C6
CHGR_VCOMP_C1	CHGR_VCOMP_C1 - @buzz_lib.BUZZ	15C6
CHGR_VDD	CHGR_VDD - @buzz_lib.BUZZ	15C8 15D6 15D8
CHGR_VDDP	CHGR_VDDP - @buzz_lib.BUZZ	15C3 15D5
CHGR_VREF	CHGR_VREF - @buzz_lib.BUZZ	15A8 15C6 15C6
CHG_EN_DIV2_L	CHG_EN_DIV2_L - @buzz_lib.BUZZ	15A5
CHG_EN_DIV_L	CHG_EN_DIV_L - @buzz_lib.BUZZ	15B5
EXCARD_CLKREQ_CONN	EXCARD_CLKREQ_CONN - @buzz_lib.BUZZ	6B3
EXCARD_CLKREQ_CONN_L	EXCARD_CLKREQ_CONN_L - @buzz_lib.BUZZ	6A4 6C3
EXCARD_CLKREQ_L	EXCARD_CLKREQ_L - @buzz_lib.BUZZ	4C6 6B2 8C6
EXCARD_CPPE_L	EXCARD_CPPE_L - @buzz_lib.BUZZ	6A8 6C3 6C3
EXCARD_CPUSB_L	EXCARD_CPUSB_L - @buzz_lib.BUZZ	6A8 6C3 6C3
EXCARD_OC_L	EXCARD_OC_L - @buzz_lib.BUZZ	4C6 6B4 8C6
EXCARD_OC_OUT_L	EXCARD_OC_OUT_L - @buzz_lib.BUZZ	6A6
EXCARD_PRESENT	EXCARD_PRESENT - @buzz_lib.BUZZ	6A6
EXCARD_RCLKEN	EXCARD_RCLKEN - @buzz_lib.BUZZ	6B3 6C5
EXCARD_SHDN_L_R	EXCARD_SHDN_L_R - @buzz_lib.BUZZ	6C6
GND_3V3S3_SGND	GND_3V3S3_SGND - @buzz_lib.BUZZ	13B7
GND_ADAPTER_UF	GND_ADAPTER_UF - @buzz_lib.BUZZ	14D7
GND_BATT_CHGND	GND_BATT_CHGND - @buzz_lib.BUZZ	15A1
LEFT_USB_EMI_N	LEFT_USB_EMI_N - @buzz_lib.BUZZ	5C4
LEFT_USB_EMI_P	LEFT_USB_EMI_P - @buzz_lib.BUZZ	5C4
LEFT_USB_GND	LEFT_USB_GND - @buzz_lib.BUZZ	5C3
LEFT_USB_N	LEFT_USB_N - @buzz_lib.BUZZ	4A3 5C4
	USB2_LT_N - @buzz_lib.BUZZ	4A6 8C3
LEFT_USB_OC_L	LEFT_USB_OC_L - @buzz_lib.BUZZ	4A3 5C4
	LTUSB_OC_L - @buzz_lib.BUZZ	4A6 8C6
LEFT_USB_OC_L_R	LEFT_USB_OC_L_R - @buzz_lib.BUZZ	5C6
LEFT_USB_P	LEFT_USB_P - @buzz_lib.BUZZ	4B3 5C4
	USB2_LT_P - @buzz_lib.BUZZ	4B6 8C3
LIO_BATT_ISENSE	LIO_BATT_ISENSE - @buzz_lib.BUZZ	8C6 16C6
LIO_DCIN_ISENSE	LIO_DCIN_ISENSE - @buzz_lib.BUZZ	8B6 16C2
LIO_P3V3S0_EN_L	LIO_P3V3S0_EN_L - @buzz_lib.BUZZ	8B6 13C3
LIO_P3V3S0_EN_R_L	LIO_P3V3S0_EN_R_L - @buzz_lib.BUZZ	13C2
LIO_PLT_RESET_L	LIO_PLT_RESET_L - @buzz_lib.BUZZ	4C6 8C6
	PLT_RESET_L - @buzz_lib.BUZZ	4C3 6C7 7C3
MAX9715_BIAS	MAX9715_BIAS - @buzz_lib.BUZZ	10C4
MAX9715_GAIN	MAX9715_GAIN - @buzz_lib.BUZZ	10C4
MIC_HI	MIC_HI - @buzz_lib.BUZZ	11C3 12A8
MIC_IN	MIC_IN - @buzz_lib.BUZZ	12A6
MIC_LO	MIC_LO - @buzz_lib.BUZZ	11C3 12A8
MIC_SHIELD	MIC_SHIELD - @buzz_lib.BUZZ	11C3 12A8
MINI_CLKREQ_L	MINI_CLKREQ_L - @buzz_lib.BUZZ	4B6 7C6 8C6
NC_CHGR_BGATE	NC_CHGR_BGATE - @buzz_lib.BUZZ	15C4
NC_LED_WLAN_L	NC_LED_WLAN_L - @buzz_lib.BUZZ	7B3
NC_LED_WPAN_L	NC_LED_WPAN_L - @buzz_lib.BUZZ	7B3
NC_LED_WWAN_L	NC_LED_WWAN_L - @buzz_lib.BUZZ	7B3
NC_LIO_P3V3S3_PGOOD	NC_LIO_P3V3S3_PGOOD - @buzz_lib.BUZZ	13B8
NC_UIM_CLK	NC_UIM_CLK - @buzz_lib.BUZZ	7C3
NC_UIM_DATA	NC_UIM_DATA - @buzz_lib.BUZZ	7C3
NC_UIM_PWR	NC_UIM_PWR - @buzz_lib.BUZZ	7C3
NC_UIM_RESET	NC_UIM_RESET - @buzz_lib.BUZZ	7C3
NC_UIM_VPP	NC_UIM_VPP - @buzz_lib.BUZZ	7C3
NC_W_DISABLE_L	NC_W_DISABLE_L - @buzz_lib.BUZZ	7C3
ONEWIRE_DCIN_DIV	ONEWIRE_DCIN_DIV - @buzz_lib.BUZZ	14C5
ONEWIRE_EN	ONEWIRE_EN - @buzz_lib.BUZZ	14B3 14C3
ONEWIRE_ESD	ONEWIRE_ESD - @buzz_lib.BUZZ	14C4
ONEWIRE_OVERVOLT	ONEWIRE_OVERVOLT - @buzz_lib.BUZZ	14C4
ONEWIRE_PU_EN	ONEWIRE_PU_EN - @buzz_lib.BUZZ	14B2
ONEWIRE_PU_EN_L	ONEWIRE_PU_EN_L - @buzz_lib.BUZZ	14B2
ONEWIRE_PWR_EN_L	ONEWIRE_PWR_EN_L - @buzz_lib.BUZZ	14C2
ONEWIRE_PWR_EN_L_DIV	ONEWIRE_PWR_EN_L_DIV - @buzz_lib.BUZZ	14C2
P2V5_ONEWIRE_REF	P2V5_ONEWIRE_REF - @buzz_lib.BUZZ	14C5
PCIE_CLK100M_EXCARD_N	PCIE_CLK100M_EXCARD_N - @buzz_lib.BUZZ	4D6 6C3 8B2
PCIE_CLK100M_EXCARD_P	PCIE_CLK100M_EXCARD_P - @buzz_lib.BUZZ	4D6 6C3 8B2
PCIE_CLK100M_EXCARD_UF_N	PCIE_CLK100M_EXCARD_UF_N - @buzz_lib.BUZZ	4D6 8B4
PCIE_CLK100M_EXCARD_UF_P	PCIE_CLK100M_EXCARD_UF_P - @buzz_lib.BUZZ	4D6 8B4
PCIE_CLK100M_MINI_N	PCIE_CLK100M_MINI_N - @buzz_lib.BUZZ	4B6 7C6 8C2
PCIE_CLK100M_MINI_P	PCIE_CLK100M_MINI_P - @buzz_lib.BUZZ	4B6 7C6 8C2
PCIE_CLK100M_MINI_UF_N	PCIE_CLK100M_MINI_UF_N - @buzz_lib.BUZZ	4B6 8C4
PCIE_CLK100M_MINI_UF_P	PCIE_CLK100M_MINI_UF_P - @buzz_lib.BUZZ	4B6 8C4
PCIE_EXCARD_D2R_N	PCIE_EXCARD_D2R_N - @buzz_lib.BUZZ	4D6 6C3 8B3
PCIE_EXCARD_D2R_P	PCIE_EXCARD_D2R_P - @buzz_lib.BUZZ	4D6 6C3 8B3
PCIE_EXCARD_R2D_N	PCIE_EXCARD_R2D_N - @buzz_lib.BUZZ	4C6 6C3 8B3
PCIE_EXCARD_R2D_P	PCIE_EXCARD_R2D_P - @buzz_lib.BUZZ	4D6 6C3 8B3
PCIE_MINI_D2R_N	PCIE_MINI_D2R_N - @buzz_lib.BUZZ	4B6 7C6 8C3
PCIE_MINI_D2R_P	PCIE_MINI_D2R_P - @buzz_lib.BUZZ	4B6 7C6 8C3
PCIE_MINI_R2D_N	PCIE_MINI_R2D_N - @buzz_lib.BUZZ	4C6 7B6 8C3
PCIE_MINI_R2D_P	PCIE_MINI_R2D_P - @buzz_lib.BUZZ	4C6 7B6 8B3
PCIE_WAKE_EXCARD_L	PCIE_WAKE_EXCARD_L - @buzz_lib.BUZZ	4C3 6C3
	PCIE_WAKE_L - @buzz_lib.BUZZ	4C6 8C3
	PCIE_WAKE_MINI_L - @buzz_lib.BUZZ	4C3 7C6
	PCIE_WAKE_L - @buzz_lib.BUZZ	4C6 8C3
PLT_RESET_SWITCH_L	PLT_RESET_SWITCH_L - @buzz_lib.BUZZ	6C3 6C3
PP1V5_S0_EXCARD_SWITCH	PP1V5_S0_EXCARD_SWITCH - @buzz_lib.BUZZ	6C3 6C3
PP3V3_AUDIO_CODEC	PP3V3_AUDIO_CODEC - @buzz_lib.BUZZ	9D6
PP3V3_S0_AUDIO_F	PP3V3_S0_AUDIO_F - @buzz_lib.BUZZ	12A5 12B4 12C8 12C8 12D8
PP3V3_S0_EXCARD_SWITCH	PP3V3_S0_EXCARD_SWITCH - @buzz_lib.BUZZ	6C3 6C3
PP3V3_S3_EXCARD_SWITCH	PP3V3_S3_EXCARD_SWITCH - @buzz_lib.BUZZ	6C3 6C3
PP3V42_ONEWIRE	PP3V42_ONEWIRE - @buzz_lib.BUZZ	14B2
PP4V5_AUDIO_ANALOG	PP4V5_AUDIO_ANALOG - @buzz_lib.BUZZ	9B1 9D2 12C5
PP5V_PWRON_USB_EMI	PP5V_PWRON_USB_EMI - @buzz_lib.BUZZ	5C3
PP5V_S0_AUDIO_F	PP5V_S0_AUDIO_F - @buzz_lib.BUZZ	10D5
PP5V_S3_3V3S3_R	PP5V_S3_3V3S3_R - @buzz_lib.BUZZ	13C6
PP5V_S3_3V3S3_VCC	PP5V_S3_3V3S3_VCC - @buzz_lib.BUZZ	13C7
PP5V_S3_USB_SWITCH	PP5V_S3_USB_SWITCH - @buzz_lib.BUZZ	5C5
PP18V5_DCIN	PP18V5_DCIN - @buzz_lib.BUZZ	14B8 14D1
PP18V5_DCIN_FUSE	PP18V5_DCIN_FUSE - @buzz_lib.BUZZ	14D6
PP18V5_DCIN_ONEWIRE	PP18V5_DCIN_ONEWIRE - @buzz_lib.BUZZ	14D5
PP18V5_DCIN_UF	PP18V5_DCIN_UF - @buzz_lib.BUZZ	14D7
PPDCIN_G3H_LIO_CONN_R	PPDCIN_G3H_LIO_CONN_R - @buzz_lib.BUZZ	14D4
PPVBATT_G3H_FET	PPVBATT_G3H_FET - @buzz_lib.BUZZ	15B4
PPVBATT_G3H_FUSE	PPVBATT_G3H_FUSE - @buzz_lib.BUZZ	15B3
PPVBATT_G3H_PRE	PPVBATT_G3H_PRE - @buzz_lib.BUZZ	15B5
PPVBAT_G3H_CHGR_OUT	PPVBAT_G3H_CHGR_OUT - @buzz_lib.BUZZ	15B7 15C1
PPVDCIN_G3H_PRE	PPVDCIN_G3H_PRE - @buzz_lib.BUZZ	15D4
PPVDCIN_G3H_R	PPVDCIN_G3H_R - @buzz_lib.BUZZ	15C3
SDATAIN	SDATAIN - @buzz_lib.BUZZ	9C6
SMC_ADAPTER_EN	SMC_ADAPTER_EN - @buzz_lib.BUZZ	8C6 14B6
SMC_BATT_CHG_EN	SMC_BATT_CHG_EN - @buzz_lib.BUZZ	8C6 15A5
SMC_BATT_ISET	SMC_BATT_ISET - @buzz_lib.BUZZ	8C6 15A8
SMC_BATT_TRICKLE_EN_L	SMC_BATT_TRICKLE_EN_L - @buzz_lib.BUZZ	8B6 15A4
SMC_BC_ACOK	SMC_BC_ACOK - @buzz_lib.BUZZ	8B6 14A3 14A6 14C1 15A7
SMC_BC_ACOK_R	SMC_BC_ACOK_R - @buzz_lib.BUZZ	15A6
SMC_EXCARD_CP	SMC_EXCARD_CP - @buzz_lib.BUZZ	6A4 8B6
SMC_EXCARD_PWR_EN	SMC_EXCARD_PWR_EN - @buzz_lib.BUZZ	6C7 8B6
SMC_SYS_ISET	SMC_SYS_ISET - @buzz_lib.BUZZ	8C6 15B8
SPKRAMP_L_N_OUT	SPKRAMP_L_N_OUT - @buzz_lib.BUZZ	10B4 10C2
SPKRAMP_L_P_OUT	SPKRAMP_L_P_OUT - @buzz_lib.BUZZ	10A4 10C2
SPKRAMP_R_N_OUT	SPKRAMP_R_N_OUT - @buzz_lib.BUZZ	10A7 10C2
SPKRAMP_R_P_OUT	SPKRAMP_R_P_OUT - @buzz_lib.BUZZ	10B7 10C2
SPKRCONN_L_N_OUT	SPKRCONN_L_N_OUT - @buzz_lib.BUZZ	10B3 11C3
SPKRCONN_L_P_OUT	SPKRCONN_L_P_OUT - @buzz_lib.BUZZ	10A3 11C3
SPKRCONN_R_N_OUT	SPKRCONN_R_N_OUT - @buzz_lib.BUZZ	10A5 11C3
SPKRCONN_R_P_OUT	SPKRCONN_R_P_OUT - @buzz_lib.BUZZ	10B5 11C3
SPKR_SHIELD	SPKR_SHIELD - @buzz_lib.BUZZ	11C2
SYS_ONEWIRE	SYS_ONEWIRE - @buzz_lib.BUZZ	8C6 14B1 14C2
TCHG_EN_DIV2_L	TCHG_EN_DIV2_L - @buzz_lib.BUZZ	15A5
TCHG_EN_DIV_L	TCHG_EN_DIV_L - @buzz_lib.BUZZ	15B5
TMPSNSR_A0	TMPSNSR_A0 - @buzz_lib.BUZZ	16B4
TP_EXCARD_STBY_L	TP_EXCARD_STBY_L - @buzz_lib.BUZZ	6C7
TP_USB2_MININ	TP_USB2_MININ - @buzz_lib.BUZZ	4B3
	USB2_MINI_N - @buzz_lib.BUZZ	4B6 7B3
TP_USB2_MINIP	TP_USB2_MINIP - @buzz_lib.BUZZ	4B3
	USB2_MINI_P - @buzz_lib.BUZZ	4B6 7B3
USB2_EXCARD_N	USB2_EXCARD_N - @buzz_lib.BUZZ	4D6 6C3 8C3
USB2_EXCARD_P	USB2_EXCARD_P - @buzz_lib.BUZZ	4D6 6C3 8C3
VOL_DOWN	VOL_DOWN - @buzz_lib.BUZZ	9B7 9C7
VOL_UP	VOL_UP - @buzz_lib.BUZZ	9B7 9C7
VREG_FB	VREG_FB - @buzz_lib.BUZZ	9A2

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C5100	CAP_603	buzz[5B6]
C5101	CAP_402	buzz[5B7]
C5102	CAP_603	buzz[5C5]
C5103	CAP_P_B2	buzz[5B5]
C5150	CAP_402	buzz[5C3]
C5151	CAP_402	buzz[5B3]
C5190	CAP_402	buzz[5B5]
C5300	CAP_402	buzz[6C4]
C5301	CAP_402	buzz[6C4]
C5302	CAP_402	buzz[6C4]
C5303	CAP_603	buzz[6C4]
C5304	CAP_603	buzz[6C4]
C5305	CAP_603	buzz[6C4]
C5330	CAP_402	buzz[6C8]
C5331	CAP_603	buzz[6C7]
C5334	CAP_402	buzz[6C8]
C5335	CAP_603	buzz[6C7]
C5350	CAP_402	buzz[6A7]
C5351	CAP_402	buzz[6A5]
C5360	CAP_402	buzz[6A3]
C5400	CAP_402	buzz[7C3]
C5401	CAP_603	buzz[7C3]
C5410	CAP_402	buzz[7C3]
C5411	CAP_603	buzz[7C3]
C5420	CAP_402	buzz[7C3]
C5421	CAP_603	buzz[7C3]
C6800	CAP_603	buzz[9D6]
C6801	CAP_402	buzz[9D6]
C6802	CAP_P_B2	buzz[9D4]
C6803	CAP_P_B2	buzz[9D3]
C6804	CAP_P_SMA-LF	buzz[9B4]
C6805	CAP_603	buzz[9B4]
C6806	CAP_603	buzz[9B3]
C6807	CAP_P_SMA-LF	buzz[9B3]
C6808	CAP_402	buzz[9B3]
C6809	CAP_402	buzz[9B2]
C6810	CAP_P_SMA-LF	buzz[9B2]
C6811	CAP_402	buzz[9B2]
C6812	CAP_402	buzz[9B4]
C6813	CAP_402	buzz[9B3]
C6821	CAP_402	buzz[9C6]
C6822	CAP_603	buzz[9A3]
C6823	CAP_402	buzz[9B3]
C6825	CAP_402	buzz[9B2]
C6826	CAP_603	buzz[9A1]
C6829	CAP_402	buzz[9B3]
C6830	CAP_402	buzz[9D4]
C6832	CAP_402	buzz[9B2]
C6833	CAP_402	buzz[9B2]
C6834	CAP_402	buzz[9B2]
C6835	CAP_402	buzz[9D6]
C6836	CAP_402	buzz[9D3]
C6850	CAP_402	buzz[9B6]
C6851	CAP_402	buzz[9A5]
C6852	CAP_402	buzz[9B5]
C6853	CAP_402	buzz[9B4]
C7200	CAP_402	buzz[10C4]
C7201	CAP_P_B2	buzz[10D2]
C7202	CAP_603	buzz[10D2]
C7203	CAP_603	buzz[10D3]
C7204	CAP_402	buzz[10C4]
C7205	CAP_402	buzz[10D3]
C7206	CAP_402	buzz[10D2]
C7207	CAP_402	buzz[10C4]
C7208	CAP_P_B2	buzz[10D2]
C7209	CAP_P_B2	buzz[10D1]
C7250	CAP_402	buzz[10C6]
C7251	CAP_402	buzz[10C6]
C7260	CAP_402	buzz[10B6]
C7261	CAP_402	buzz[10A6]
C7270	CAP_402	buzz[10A3]
C7271	CAP_402	buzz[10B3]
C7300	CAP_402	buzz[11C7]
C7301	CAP_402	buzz[11C5]
C7302	CAP_402	buzz[11C5]
C7303	CAP_402	buzz[11C5]
C7304	CAP_402	buzz[11C5]
C7305	CAP_402	buzz[11C5]
C7307	CAP_402	buzz[11D7]
C7308	CAP_402	buzz[11D6]
C7350	CAP_402	buzz[11A7]
C7351	CAP_603	buzz[11A7]
C7352	CAP_402	buzz[11A5]
C7353	CAP_402	buzz[11A5]
C7354	CAP_402	buzz[11A5]
C7355	CAP_402	buzz[11A5]
C7356	CAP_402	buzz[11A5]
C7400	CAP_402	buzz[12B4]
C7401	CAP_402	buzz[12D7]
C7402	CAP_402	buzz[12C7]
C7404	CAP_402	buzz[12C4]
C7411	CAP_402	buzz[12B7]
C7412	CAP_402	buzz[12B6]
C7414	CAP_402	buzz[12C4]
C7430	CAP_P_B2	buzz[12D2]
C7431	CAP_P_B2	buzz[12C2]
C7432	CAP_805-1	buzz[12C2]
C7433	CAP_805-1	buzz[12B2]
C7440	CAP_402	buzz[12B3]
C7441	CAP_402	buzz[12B3]
C7442	CAP_402	buzz[12A2]
C7443	CAP_402	buzz[12A2]
C7445	CAP_402	buzz[12A2]
C7446	CAP_402	buzz[12A1]
C7447	CAP_402	buzz[12A1]
C7450	CAP_402	buzz[12A6]
C7451	CAP_402	buzz[12A7]
C7800	CAP_603	buzz[13C6]
C7801	CAP_603	buzz[13C6]
C7802	CAP_603	buzz[13C6]
C7806	CAP_402	buzz[13B8]
C7807	CAP_402	buzz[13B6]
C7808	CAP_402	buzz[13B7]
C7809	CAP_402	buzz[13C4]
C7822	CAP_402	buzz[13B4]
C7830	CAP_1210	buzz[13C3]
C7840	CAP_805	buzz[13B2]
C7841	CAP_P_SMC-LF	buzz[13B2]
C7842	CAP_805	buzz[13B2]
C7843	CAP_402	buzz[13B2]
C7890	CAP_402	buzz[13C1]
C8200	CAP_805	buzz[14C7]
C8201	CAP_805	buzz[14D7]
C8203	CAP_402	buzz[14C7]
C8204	CAP_603	buzz[14D6]
C8205	CAP_603	buzz[14D4]
C8206	CAP_402	buzz[14C2]
C8210	CAP_402	buzz[14B6]
C8250	CAP_805	buzz[14B5]
C8300	CAP_402	buzz[15D6]
C8301	CAP_402	buzz[15C6]
C8302	CAP_402	buzz[15C6]
C8303	CAP_603	buzz[15C4]
C8304	CAP_402	buzz[15D4]
C8305	CAP_1206	buzz[15C2]
C8306	CAP_1206	buzz[15C2]
C8307	CAP_603	buzz[15D7]
C8308	CAP_1210	buzz[15C3]
C8309	CAP_P_6.3X5.5SM1	buzz[15C2]
C8310	CAP_1210	buzz[15B2]
C8311	CAP_402	buzz[15D6]
C8312	CAP_402	buzz[15D5]
C8314	CAP_402	buzz[15C6]
C8315	CAP_402	buzz[15C6]
C8316	CAP_1206	buzz[15C2]
C8319	CAP_402	buzz[15D6]
C8320	CAP_402	buzz[15B7]
C8321	CAP_402	buzz[15B6]
C8322	CAP_402	buzz[15B6]
C8323	CAP_402	buzz[15A6]
C8324	CAP_402	buzz[15B5]
C8325	CAP_402	buzz[15B5]
C8327	CAP_402	buzz[15B2]
C8330	CAP_402	buzz[15A2]
C8340	CAP_603	buzz[15D7]
C8341	CAP_402	buzz[15C7]
C8361	CAP_402	buzz[15A7]
C8362	CAP_402	buzz[15A7]
C8370	CAP_603	buzz[15C5]
C8405	CAP_402	buzz[16C3]
C8415	CAP_402	buzz[16C7]
C8450	CAP_402	buzz[16B4]
D5100	DIODE_SCHOT_3P_A_SC-75	buzz[5B3]
D6800	DIODE_SCHOT_POWERDI-123	buzz[9B3]
D7300	DIODE_SCHOT_3P_A_SC-75	buzz[11C7]
D7301	DIODE_SCHOT_3P_A_SC-75	buzz[11A6]
D7800	DIO_MBRM140T3_SM_SMD	buzz[13B3]
D8200	DIODE_SCHOT_3P_A_SC-75	buzz[14C7]
D8201	DIODE_SOT23	buzz[14D5]
D8300	DIODE_SCHOT_SOD-123	buzz[15C3]
D8321	DIODE_SOT23	buzz[15B4]
D8340	DIODE_SOT23	buzz[15C7]
DZ7300	SUPPR_TRANSIENT_4P1-0405	buzz[11C6]
DZ7301	SUPPR_TRANSIENT_4P1-0405	buzz[11C6]
DZ7350	SUPPR_TRANSIENT_4P1-0405	buzz[11A6]
DZ7351	SUPPR_TRANSIENT_4P1-0405	buzz[11A6]
F8200	FUSE_1206	buzz[14D5]
F8302	FUSE_1206	buzz[15B3]
FL5500	FILTER_4P_TCM1005	buzz[8C3]
FL5501	FILTER_4P_TCM1005	buzz[8B3]
J5100	CON_F4RT_USB_S2MT_TH_F-RT-TH-USB-LFT	buzz[5C2]
J5300	CON_F26RT_S2MT_SM_F-RT-SM	buzz[6D2]
J5400	CON_F52RT_D2MT_SM_F-ST-SM	buzz[7C5]
J5500	CON_F80ST_D4MT_SM_F-ST-SM	buzz[8C4]
J7300	CON_F8RT_SPDIFTRAN_TH2_F-RT-TH	buzz[11D8]
J7350	CON_F8RT_SPDIFRCVR_TH2_F-RT-TH	buzz[11B8]
J7380	CON_M8ST_S_TH_M-ST-TH	buzz[11C1]
J8200	CON_F5RT_S2MT_TH3_F-RT-TH	buzz[14D8]
J8215	CON_M12ST_S_SM_M-ST-SM	buzz[14A4]
L5100	FILTER_4P_SM	buzz[5C4]
L5101	IND_SM	buzz[5C4]
L5150	IND_SM	buzz[5B4]
L6800	IND_0402	buzz[9B4]
L6801	IND_0402	buzz[9D6]
L7200	IND_0402	buzz[10D6]
L7201	IND_0402	buzz[10C6]
L7250	IND_0402	buzz[10C6]
L7251	IND_0402	buzz[10C6]
L7260	IND_0402	buzz[10B6]
L7261	IND_0402	buzz[10A6]
L7270	IND_0402	buzz[10A4]
L7271	IND_0402	buzz[10B4]
L7300	IND_0402	buzz[11D6]
L7301	IND_0402	buzz[11D4]
L7302	IND_0402	buzz[11D6]
L7303	IND_0402	buzz[11C6]
L7304	IND_0402	buzz[11C4]
L7305	IND_0402	buzz[11C6]
L7306	IND_0402	buzz[11C4]
L7307	IND_0402	buzz[11C6]
L7350	IND_0402	buzz[11B6]
L7351	IND_0402	buzz[11B4]
L7352	IND_0402	buzz[11B6]
L7353	IND_0402	buzz[11B6]
L7354	IND_0402	buzz[11B4]
L7355	IND_0402	buzz[11B6]
L7356	IND_0402	buzz[11B4]
L7357	IND_0402	buzz[11A6]
L7400	IND_0402	buzz[12B4]
L7800	IND_IHLP	buzz[13B3]
L8200	FILTER_4P_SM	buzz[14D6]
L8201	IND_SM-1	buzz[14D6]
L8300	IND_3P_SM	buzz[15C3]
L8320	IND_SM-LF	buzz[15B2]
L8321	IND_SM-LF	buzz[15A2]
Q5360	TRA_2N7002_SOT23-LF	buzz[6B3]
Q7400	TRA_2N7002DW_SOT-363	buzz[12D7 12C7]
Q7401	TRA_2N7002DW_SOT-363	buzz[12D6 12D5]
Q7402	TRA_2N7002DW_SOT-363	buzz[12C5 12B7]
Q7410	TRA_2N7002DW_SOT-363	buzz[12A4 12A5]
Q7800	TRA_FDC796N_SUPERSOT-6	buzz[13C4]
Q7801	TRA_FDC796N_SUPERSOT-6	buzz[13B4]
Q7890	TRA_FDC638P_SM-LF	buzz[13C2]
Q8200	TRA_2N7002DW_SOT-363	buzz[14C2 14C3]
Q8209	TRA_TP0610_SOT23-3	buzz[14D2]
Q8210	TRA_2N7002DW_SOT-363	buzz[14B5 14C1]
Q8250	TRA_SI4405DY_SO-8	buzz[14B4]
Q8298	TRA_DUAL_MOSFET_NPCHN2_SC70-6	buzz[14B2 14B2]
Q8300	TRA_SI4405DY_SO-8	buzz[15D4]
Q8301	TRA_HAT2168H_LFPAK	buzz[15C3]
Q8302	TRA_HAT2165H_LFPAK	buzz[15C3]
Q8320	TRA_SI4405DY_SO-8	buzz[15B5]
Q8321	TRA_SI4405DY_SO-8	buzz[15B4]
Q8322	TRA_2N7002DW_SOT-363	buzz[15A6 15A5]
Q8324	TRA_2N7002DW_SOT-363	buzz[15A5 15A4]
Q8340	TRA_IRLML5203_SM	buzz[15D7]
Q8350	TRA_2N7002_SOT23-LF	buzz[15B7]
R0600	RES_402	buzz[4A5]
R0601	RES_402	buzz[4A5]
R5190	RES_402	buzz[5C5]
R5300	RES_402	buzz[6C6]
R5301	RES_402	buzz[6C6]
R5352	RES_402	buzz[6A6]
R5353	RES_402	buzz[6B5]
R5354	RES_402	buzz[6B5]
R5360	RES_402	buzz[6B3]
R5361	RES_402	buzz[6B3]
R6800	RES_402	buzz[9C6]
R6801	RES_402	buzz[9B5]
R6802	RES_402	buzz[9B4]
R6803	RES_402	buzz[9A3]
R6807	RES_402	buzz[9D7]
R6808	RES_402	buzz[9D3]
R6810	RES_402	buzz[9B2]
R6811	RES_402	buzz[9A2]
R6850	RES_402	buzz[9B7]
R6851	RES_402	buzz[9B7]
R6852	RES_402	buzz[9B7]
R6853	RES_402	buzz[9A7]
R6854	RES_402	buzz[9B4]
R7201	RES_402	buzz[10C4]
R7202	RES_402	buzz[10C5]
R7300	RES_402	buzz[11C4]
R7301	RES_402	buzz[11C4]
R7320	RES_402	buzz[11D7]
R7321	RES_402	buzz[11B7]
R7349	RES_402	buzz[11B7]
R7350	RES_402	buzz[11A4]
R7351	RES_402	buzz[11A4]
R7380	RES_402	buzz[11C2]
R7381	RES_402	buzz[11C1]
R7390	RES_402	buzz[11B2]
R7391	RES_402	buzz[11B6]
R7401	RES_402	buzz[12D8]
R7402	RES_402	buzz[12D7]
R7403	RES_402	buzz[12C7]
R7404	RES_402	buzz[12C4]
R7405	RES_402	buzz[12D5]
R7406	RES_402	buzz[12D6]
R7407	RES_402	buzz[12D5]
R7411	RES_402	buzz[12C8]
R7412	RES_402	buzz[12B7]
R7413	RES_402	buzz[12C6]
R7414	RES_402	buzz[12C4]
R7415	RES_402	buzz[12C5]
R7420	RES_402	buzz[12A4]
R7430	RES_603	buzz[12D3]
R7431	RES_603	buzz[12C3]
R7432	RES_603	buzz[12C3]
R7433	RES_603	buzz[12B3]
R7434	RES_402	buzz[12C2]
R7435	RES_402	buzz[12C2]
R7436	RES_402	buzz[12C2]
R7437	RES_402	buzz[12B2]
R7450	RES_402	buzz[12B7]
R7451	RES_402	buzz[12A7]
R7452	RES_402	buzz[12A7]
R7453	RES_402	buzz[12A6]
R7454	RES_402	buzz[12A6]
R7460	RES_402	buzz[12C7]
R7461	RES_402	buzz[12C6]
R7800	RES_402	buzz[13B2]
R7802	RES_402	buzz[13C6]
R7804	RES_402	buzz[13C7]
R7805	RES_402	buzz[13B7]
R7806	RES_402	buzz[13B8]
R7808	RES_402	buzz[13B7]
R7810	RES_402	buzz[13B4]
R7821	RES_402	buzz[13B3]
R7822	RES_402	buzz[13B3]
R7823	RES_402	buzz[13C5]
R7830	RES_402	buzz[13C6]
R7890	RES_402	buzz[13C2]
R8200	RES_402	buzz[14C2]
R8201	RES_402	buzz[14D2]
R8202	RES_402	buzz[14D5]
R8203	RES_402	buzz[14C5]
R8204	RES_402	buzz[14C6]
R8205	RES_402	buzz[14D3]
R8207	RES_805	buzz[14D4]
R8208	RES_402	buzz[14D5]
R8209	RES_402	buzz[14D1]
R8210	RES_402	buzz[14A8]
R8211	RES_402	buzz[14C2]
R8212	RES_402	buzz[14B7]
R8213	RES_402	buzz[14A7]
R8214	RES_402	buzz[14B7]
R8215	RES_402	buzz[14A7]
R8216	RES_402	buzz[14A6]
R8221	RES_402	buzz[14B5]
R8250	RES_402	buzz[14B4]
R8290	RES_402	buzz[14C5]
R8291	RES_402	buzz[14C4]
R8296	RES_402	buzz[14B3]
R8297	RES_402	buzz[14A3]
R8298	RES_402	buzz[14B2]
R8299	RES_402	buzz[14B1]
R8300	RES_402	buzz[15D5]
R8301	RES_402	buzz[15C6]
R8302	RES_402	buzz[15D6]
R8303	RES_402	buzz[15C4]
R8304	RES_402	buzz[15B1]
R8305	RES_402	buzz[15C4]
R8306	RES_402	buzz[15B1]
R8307	RES_0612	buzz[15D3]
R8308	RES_0612	buzz[15C2]
R8310	RES_402	buzz[15D4]
R8311	RES_402	buzz[15D6]
R8320	RES_2525	buzz[15B4]
R8322	RES_402	buzz[15B5]
R8323	RES_402	buzz[15A5]
R8324	RES_402	buzz[15A6]
R8325	RES_402	buzz[15A6]
R8330	RES_402	buzz[15B5]
R8331	RES_402	buzz[15B5]
R8340	RES_402	buzz[15D8]
R8341	RES_402	buzz[15D8]
R8342	RES_402	buzz[15D8]
R8343	RES_402	buzz[15C8]
R8344	RES_402	buzz[15C7]
R8350	RES_402	buzz[15C7]
R8352	RES_402	buzz[15B8]
R8360	RES_402	buzz[15B8]
R8362	RES_402	buzz[15A8]
R8363	RES_402	buzz[15A7]
R8364	RES_402	buzz[15C6]
R8365	RES_402	buzz[15B6]
R8366	RES_402	buzz[15A7]
R8367	RES_402	buzz[15A7]
R8370	RES_603	buzz[15C4]
R8450	RES_402	buzz[16B4]
R8451	RES_402	buzz[16B4]
U5100	SWI_TPS2051BDGN_MSOP	buzz[5C6]
U5300	PWR_CNTRL_TPS2231_QFN	buzz[6C5]
U5351	MC74VHC1G00_SC70-5	buzz[6A6]
U5352	MC74VHC1G08_SC70	buzz[6A5]
U5360	MC74VHC1G00_SC70-5	buzz[6B2]
U6800	AUDIO_STAC9220_LQFP	buzz[9D5]
U7200	MAX9715_QFN-LF	buzz[10C4]
U7800	ISL6269_QFN	buzz[13C6]
U8200	COMPARATOR_LMC7211_SM-LF	buzz[14A6]
U8250	MC74VHC1G08_SC70	buzz[14A5]
U8290	COMPARATOR_LM397_SOT23-5	buzz[14C4]
U8300	ISL6255_QFN	buzz[15D5]
U8405	INA193_SOT23-5	buzz[16C3]
U8415	INA194_SOT23-5	buzz[16C6]
U8450	TMP106_WCSP-6	buzz[16B5]
VR6800	LREG_TPS79501_SOT223-6	buzz[9B3]
VR8290	SHNTREG_LM431_SOT23-3-LF	buzz[14C5]
XW7300	SHORT_SM	buzz[11C4]
XW7301	SHORT_SM	buzz[11B4]
XW7310	SHORT_SM	buzz[11B2]
XW7311	SHORT_SM	buzz[11B2]
XW7312	SHORT_SM	buzz[11B2]
XW7400	SHORT_SM	buzz[12A6]
XW7800	SHORT_SM	buzz[13B6]
XW8300	SHORT_SM	buzz[15C5]
XW8301	SHORT_SM	buzz[15D3]
XW8302	SHORT_SM	buzz[15C3]
XW8303	SHORT_SM	buzz[15B2]
XW8304	SHORT_SM	buzz[15B2]