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- 1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
- 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
- 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ZONE	ECN	DESCRIPTION OF CHANGE	CK APPD DATE	ENG APPD DATE
02		421247	ENGINEERING RELEASED	01/19/06?	



SCHEM, SYMPHONY, M9

EVT

1/19/06

PDF PAGE	CSA PAGE	CONTENTS	SYNC MASTER	DATE
1	1	Table of Contents	N/A	N/A
2	2	Block Diagram	SIREN	01/06/2006
3	4	BOM CONFIGURATION	(MASTER)	(MASTER)
4	6	Aliases	SIREN	01/06/2006
5	51	Left USB Port	SIREN	01/06/2006
6	53	ExpressCard Connector	SIREN	01/06/2006
7	54	PCI-E MiniCard Connector	SIREN	01/06/2006
8	55	MLB I/O Board Connector	SIREN	01/06/2006
9	64	Left ALS	SIREN	01/06/2006
10	68	AUDIO: CODEC	AUDIO_M9_PRO_LIO	01/06/2006
11	70	AUDIO: LINE IN	AUDIO_M9_PRO_LIO	01/06/2006
12	71	AUDIO: HEADPHONE AMP	AUDIO_M9_PRO_LIO	01/06/2006
13	72	AUDIO: SPEAKER AMP	AUDIO_M9_PRO_LIO	01/06/2006
14	73	AUDIO: JACKS	AUDIO_M9_PRO_LIO	01/19/2006
15	74	AUDIO: JACK TRANSLATORS	AUDIO_M9_PRO_LIO	01/06/2006
16	78	3.3V Supply	SIREN	01/06/2006
17	82	DC-In & Battery Connectors	SIREN	01/06/2006
18	84	LEFT I/O POWER CONNECTOR	SIREN	01/06/2006
19	85	Cross Reference Page		
20	86	Cross Reference Page		
21	87	Cross Reference Page		

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
051-7066	1	SCHEM, SYMPHONY, NEW, M9	SCH1	
820-1970	1	PCBF, SYMPHONY, M9	PCB1	

DIMENSIONS ARE IN MILLIMETERS		METRIC		 Apple Computer Inc.	
XX : _____	_____	DRAPTER	DESIGN CK	NOTICE OF PROPRIETARY PROPERTY THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART	
X.XX : _____	_____	ENG APPD	MFG APPD		
X.XXX : _____	_____	QA APPD	DESIGNER		
ANGLES : _____	_____	RELEASE	SCALE		
DO NOT SCALE DRAWING		SCALE NONE		TITLE	
 THIRD ANGLE PROJECTION		MATERIAL/FINISH NOTED AS APPLICABLE		SIZE D	SCHEM, SYMPHONY, NEW, M9 DRAWING NUMBER 051-7066 REV. 02
				SHT 1 OF 87	

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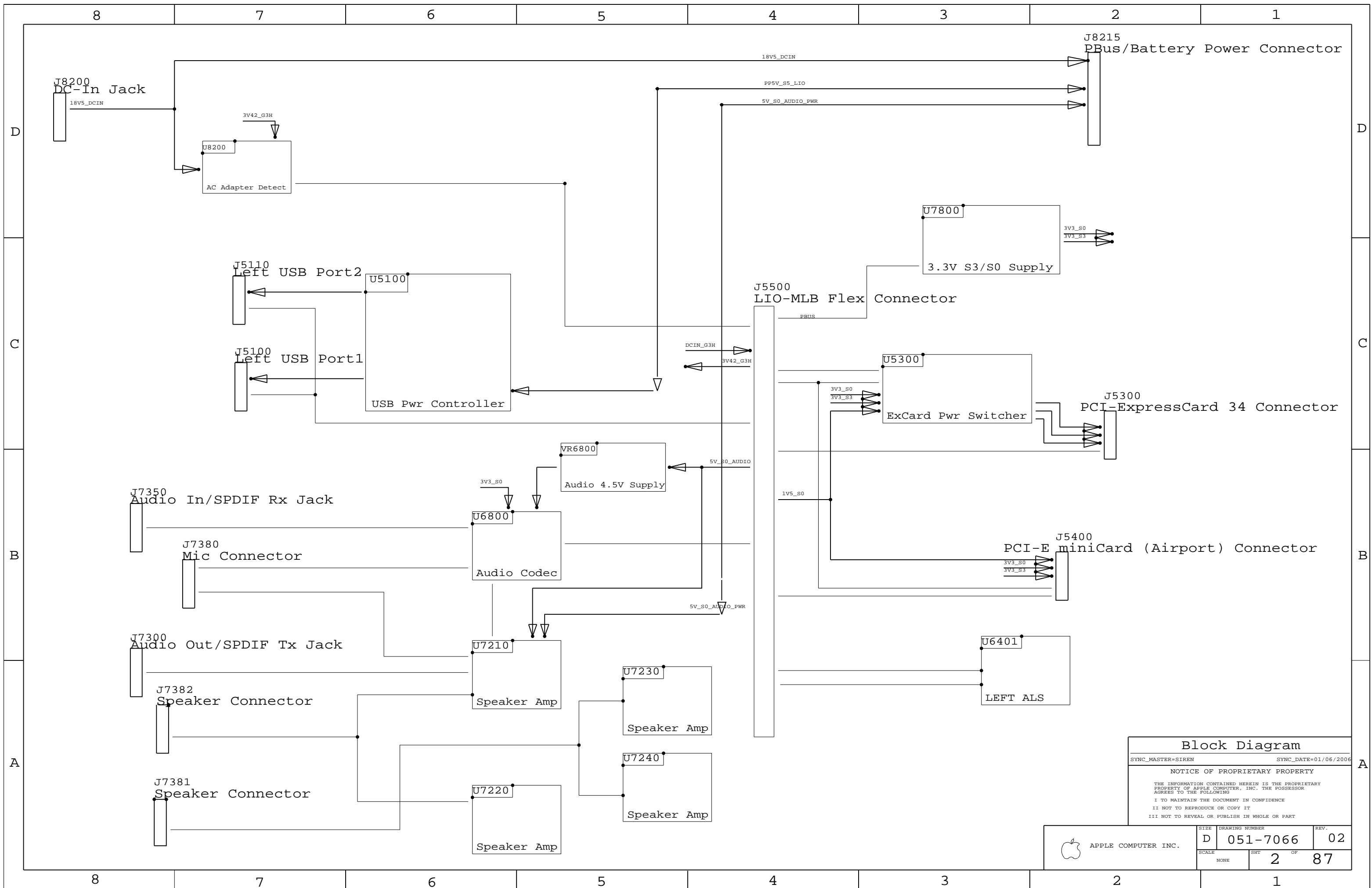
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1



Block Diagram

SYNC_MASTER=SIREN SYNC_DATE=01/06/2006

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	SCALE NONE	SH1 2	OF 87

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BOM NUMBER	BOM NAME	BOM OPTIONS
630-7510	PCBA, SYMPHONY, NEW, M9	075-0199, 075-0200
075-0200	PROJ_PTS, SYMPHONY, NEW, M9	COMMON, EXCARD_3CNTL, ONEWIRE_DIV, ONEWIRE_PWRCTL
075-0199	LEMENU_PTS, SYMPHONY, NEW, M9	LEMENU

PHANTOM BOM #'S

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
075-0200	1	PROJ_PTS, SYMPHONY, M9	BOM1		075-0200
075-0199	1	LEMENU_PTS, SYMPHONY, M9	BOM2		075-0199

BAR CODE LABEL / EEE#'S

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
000-0041	1	PLACEHOLDER FOR EEE/CCC INFO	[EEE:V3P]	CRITICAL	

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BOM CONFIGURATION		
SYNC_MASTER=(MASTER)	SYNC_DATE=(MASTER)	
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SCALE	SHT	OF	
NONE	4	87	

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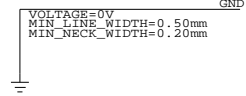
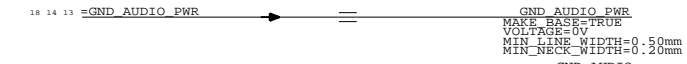
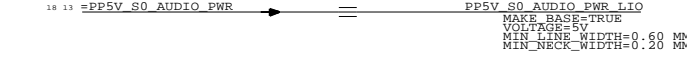
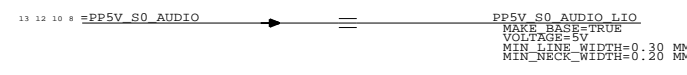
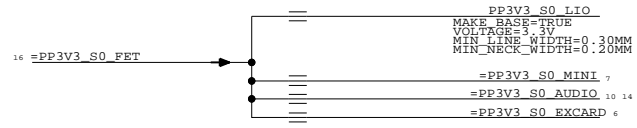
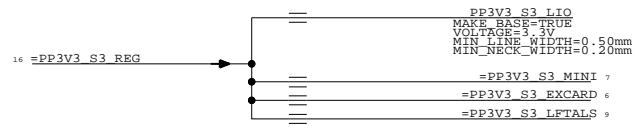
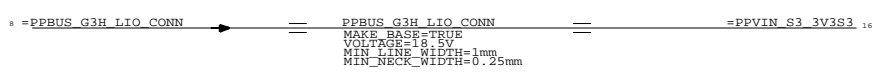
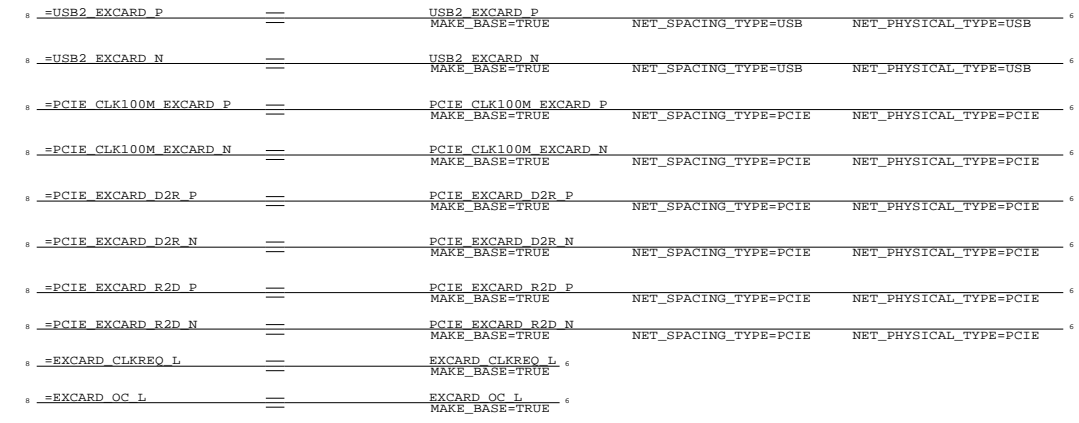
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2

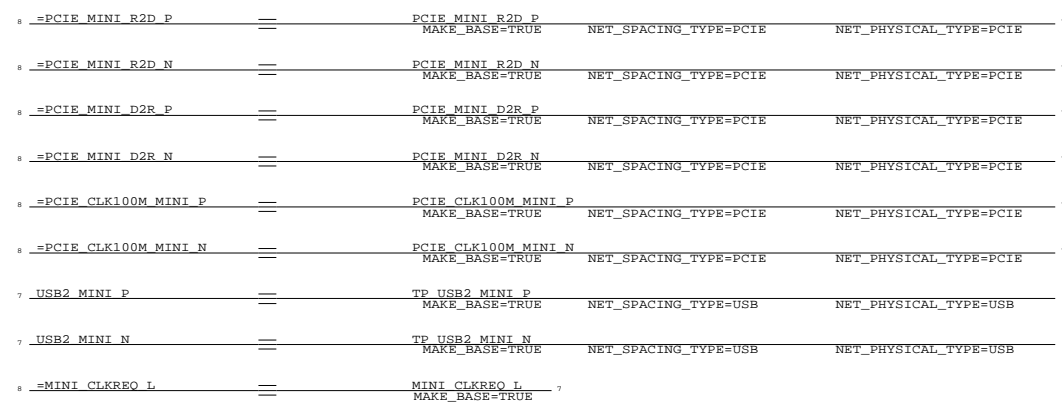
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POWER & GROUNDS

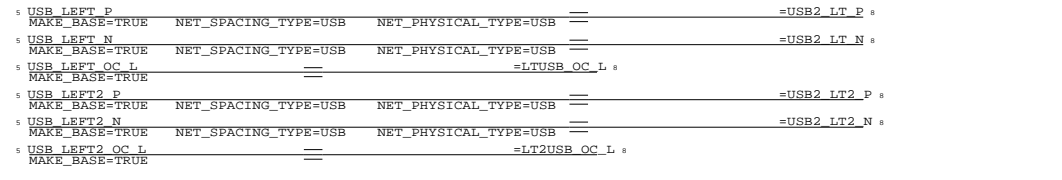
PCI-E EXPRESS CARD 34



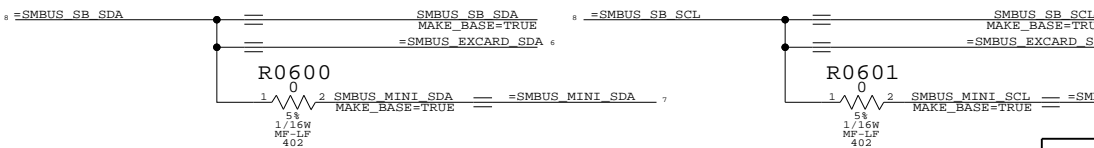
PCI-E MINICARD



USB



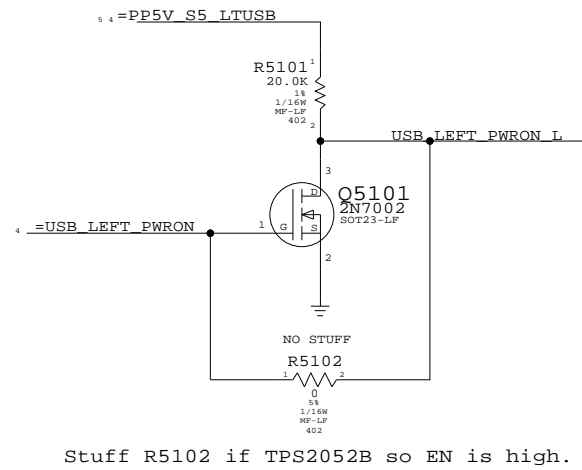
SMBUS



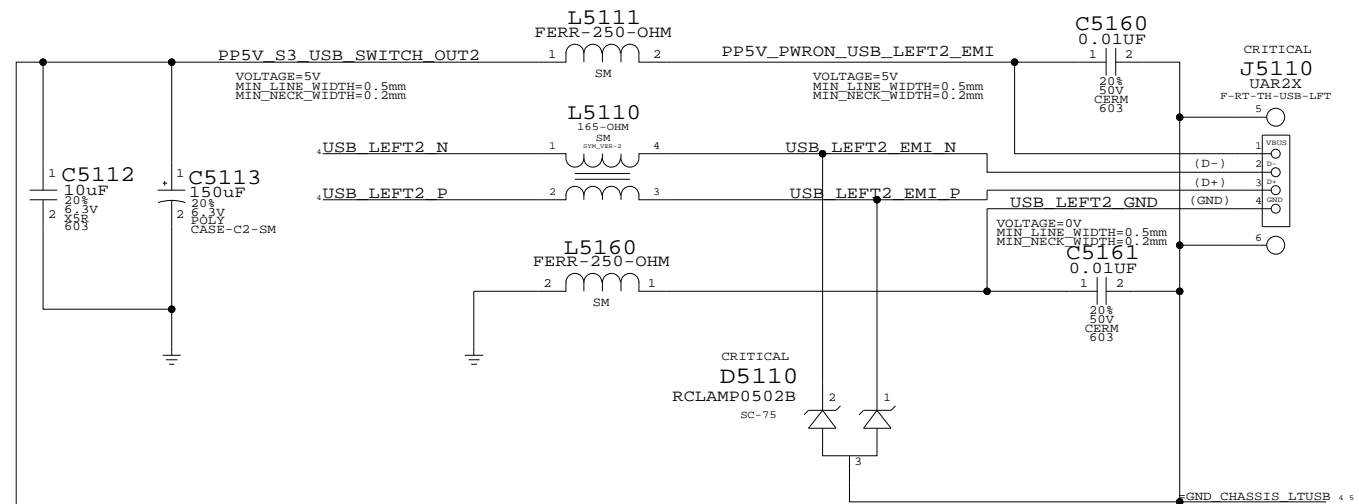
Aliases	
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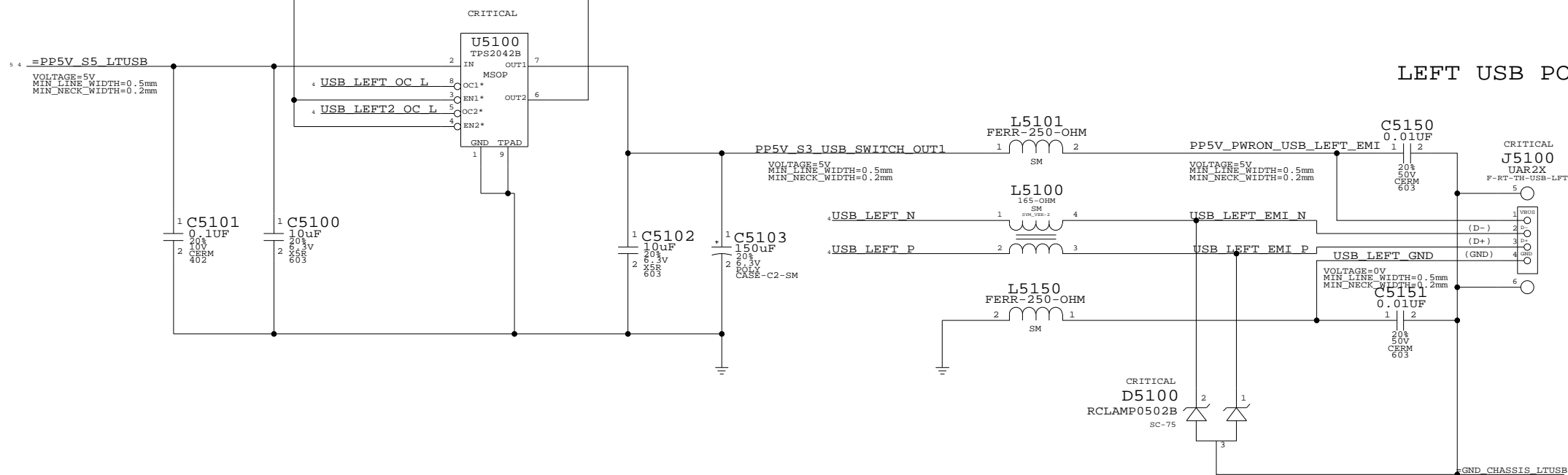
APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	02
SCALE	SHT	OF	
NONE	6	87	



Stuff R5102 if TPS2052B so EN is high.



PUT L5110, L5111, AND L5160 ACROSS THE MOAT

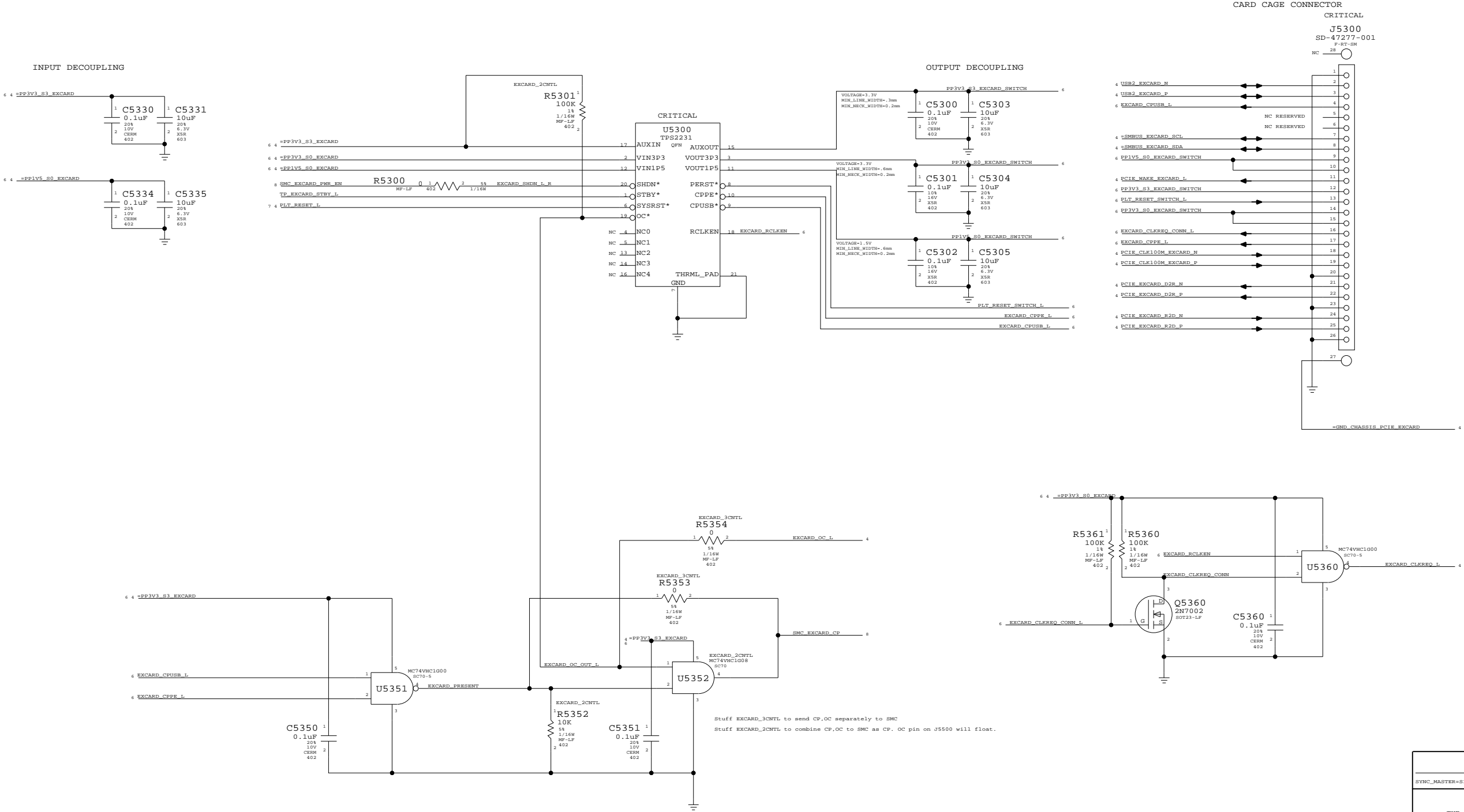


PUT L5100, L5101, AND L5150 ACROSS THE MOAT

Left USB Port
 SYNC_MASTER=SIREN SYNC_DATE=01/06/2006
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	D	051-7066	02
SCALE	SHT		OF
NONE	51		87

PCI-EXPRESS CARD 34 TOP MOUNT CONNECTOR



ExpressCard Connector

SYNC_MASTER=SIREN SYNC_DATE=01/06/2006

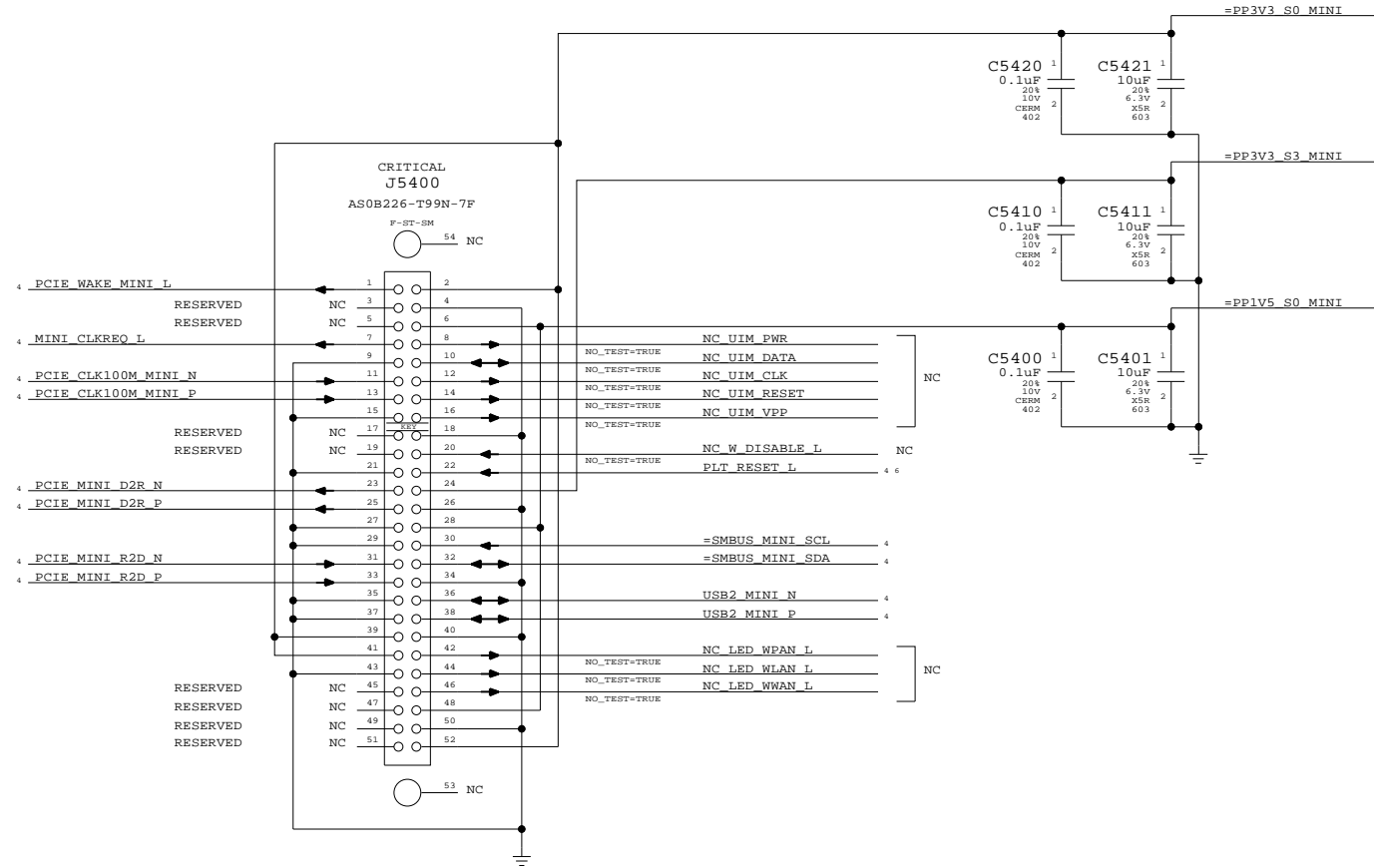
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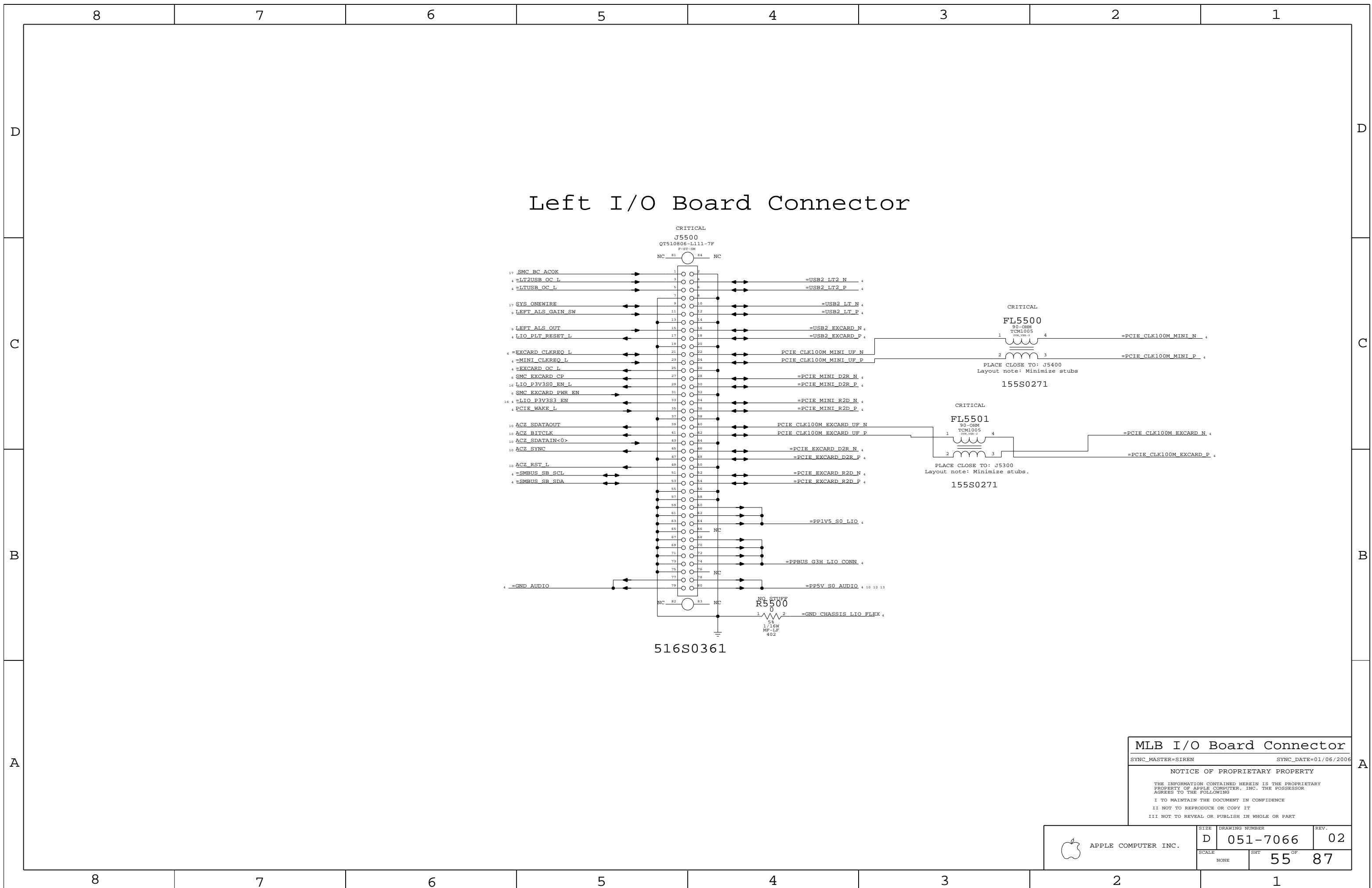
APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7066	REV. 02
	SCALE NONE	SHT 53	OF 87

PCI-EXPRESS MINI CARD CONNECTOR



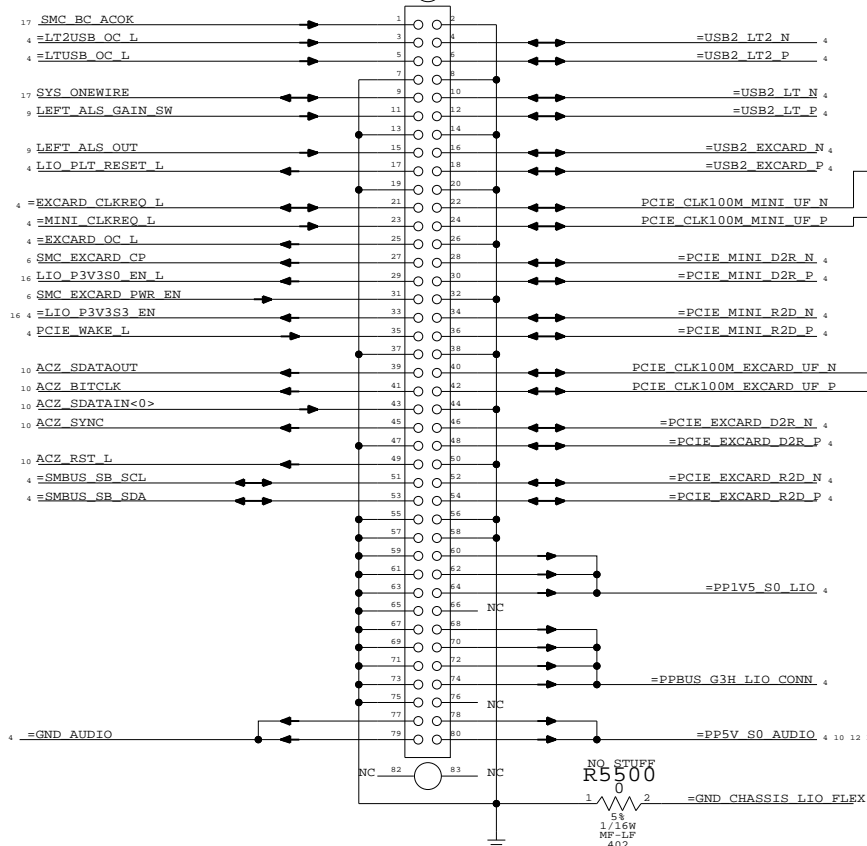
PCI-E MiniCard Connector
 SYNC_MASTER=SIREN SYNC_DATE=01/06/2006
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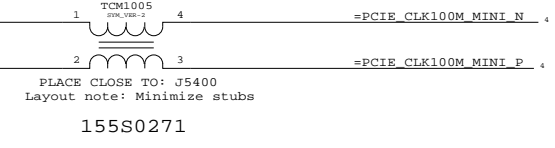


Left I/O Board Connector

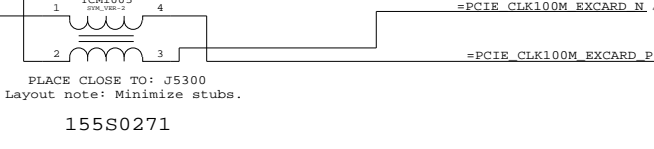
CRITICAL
J5500
 QT510806-L111-7F
 P-ST-SM
 NC 81 84 NC



CRITICAL
FL5500
 90-OHM
 TCM1005
 2PK_100-2



CRITICAL
FL5501
 90-OHM
 TCM1005
 2PK_100-2



NO STUFF
R5500
 0
 5.6
 1/16W
 MF-LF
 402

516S0361

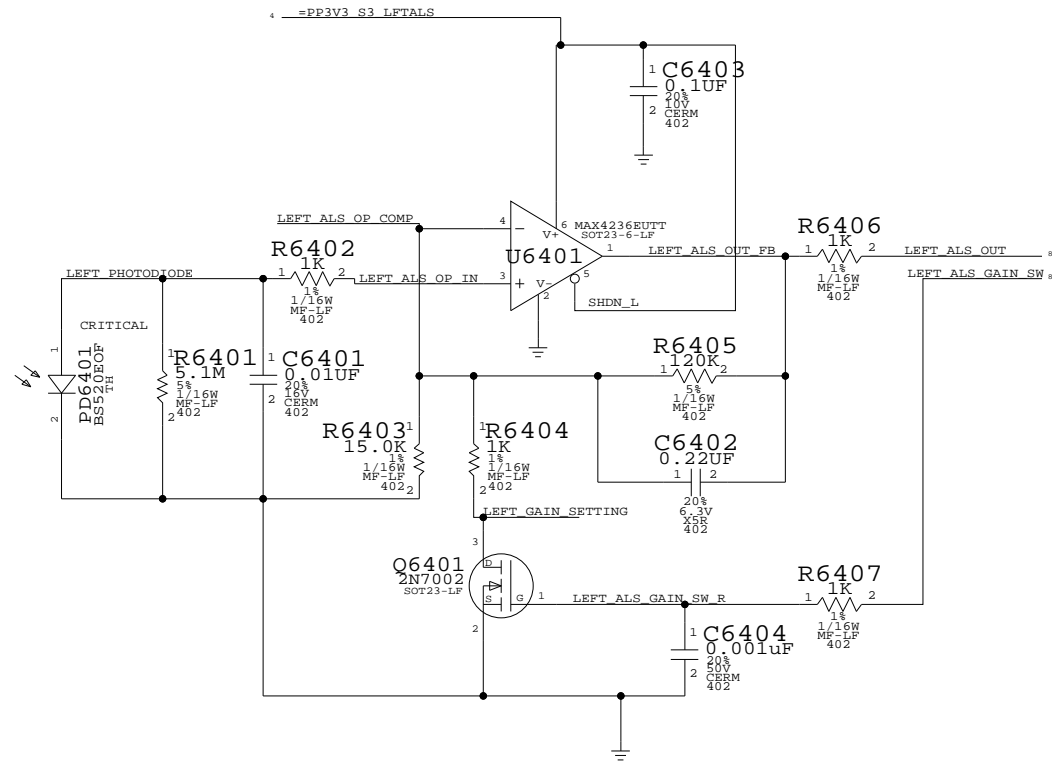
MLB I/O Board Connector

SYNC_MASTER=SIREN SYNC_DATE=01/06/2006

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SCALE	NONE	SHT	55 OF 87



Left ALS

SYNC_MASTER=SIREN SYNC_DATE=01/06/2006

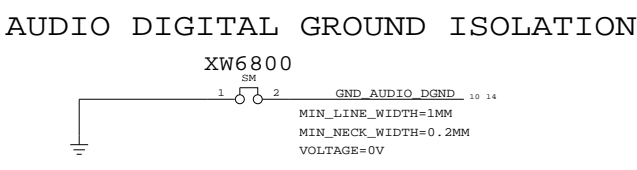
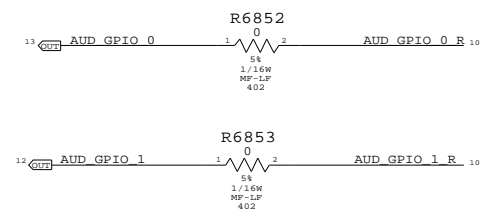
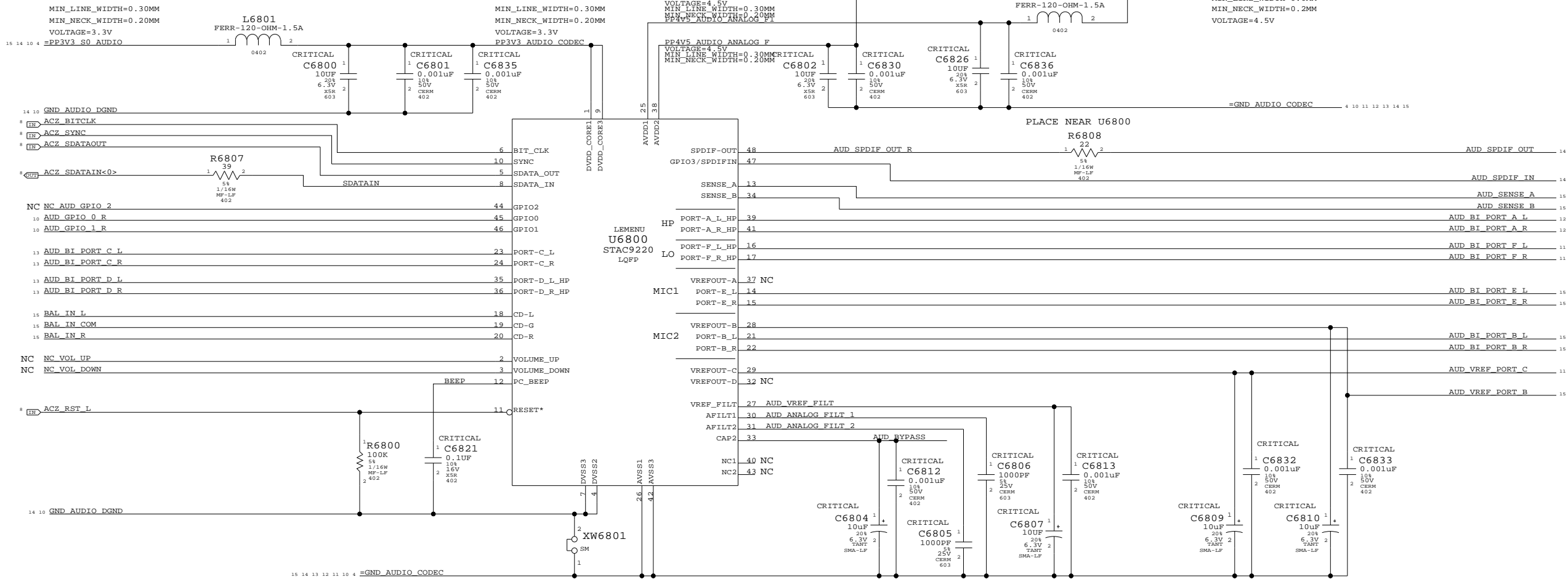
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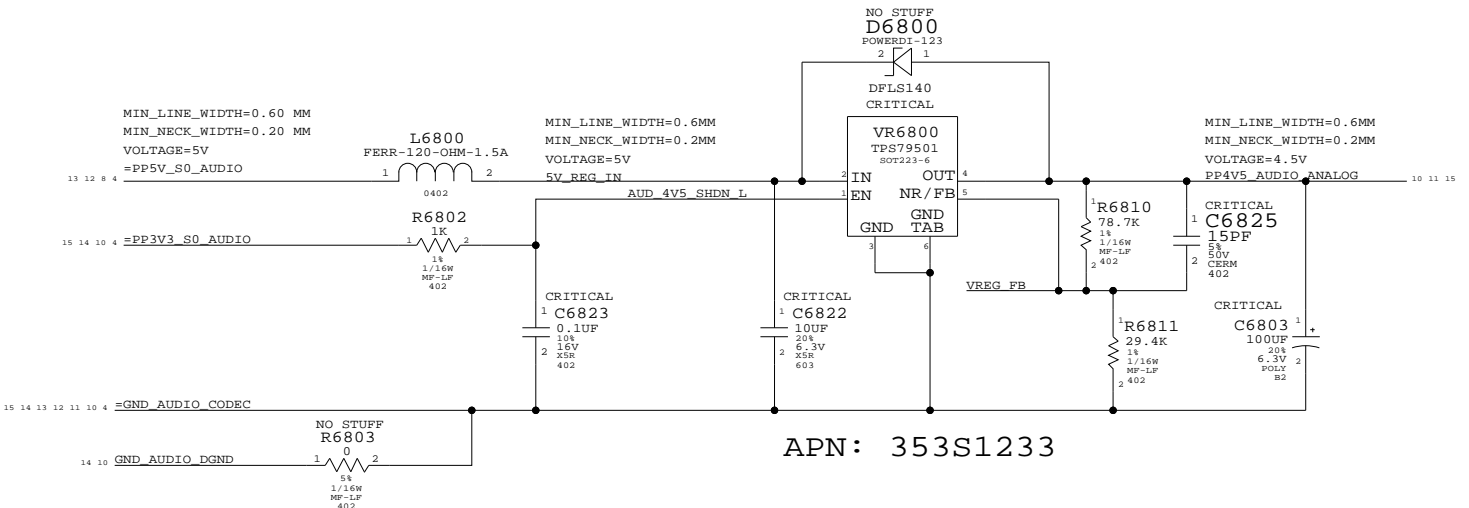
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	D	051-7066	02
SCALE	NONE	SHT	64 OF 87

AUDIO CODEC
APPLE P/N 353S1345



4.5V POWER SUPPLY FOR CODEC



AUDIO: CODEC
 SYNC_MASTER=AUDIO_M9_PRO_LIO SYNC_DATE=01/06/2006
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
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SCALE	NONE	SHT	68 OF 87

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C

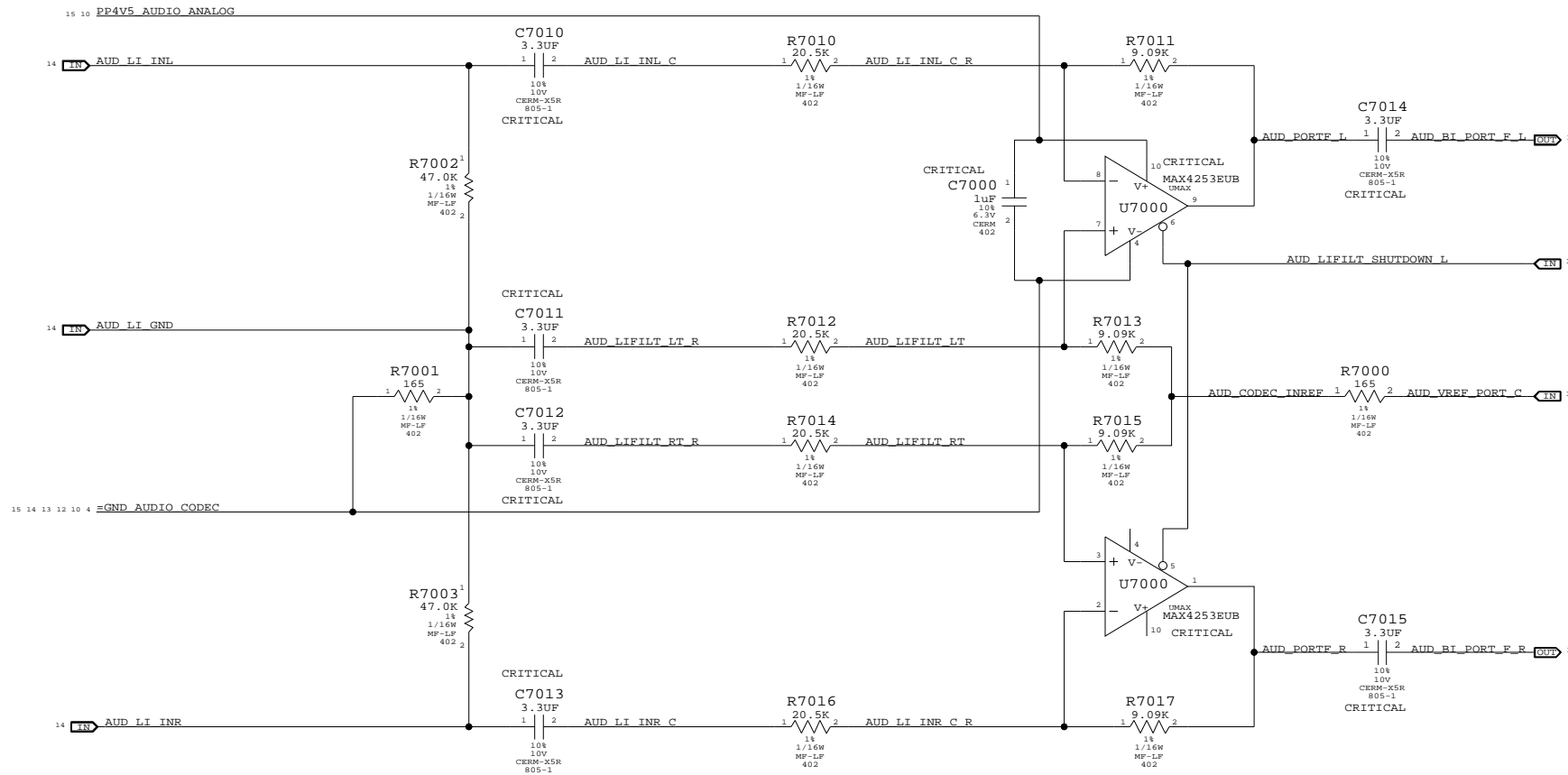
B

B

A

A

Pseudo-Diff Line-In Filter
 GAIN = -7.1DB AV = 0.44
 FC = 2.4 HZ



AUDIO: LINE IN
 SYNC_MASTER=AUDIO_M9_PRO_LIO SYNC_DATE=01/06/2006
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NONE		70	87

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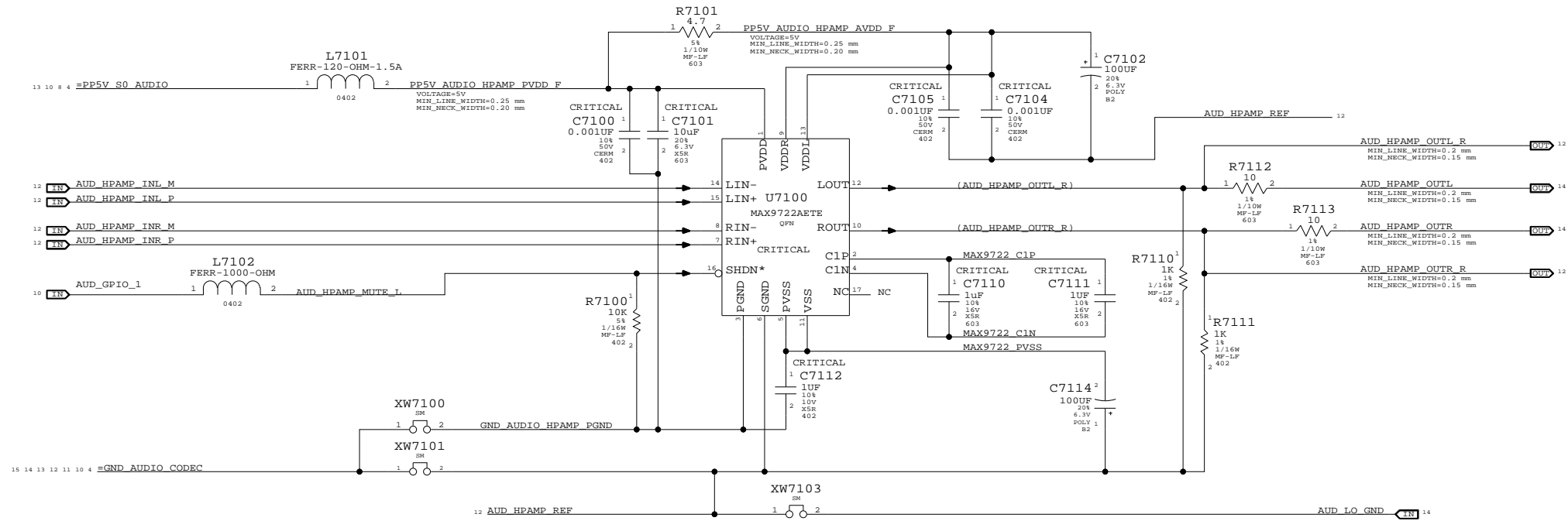
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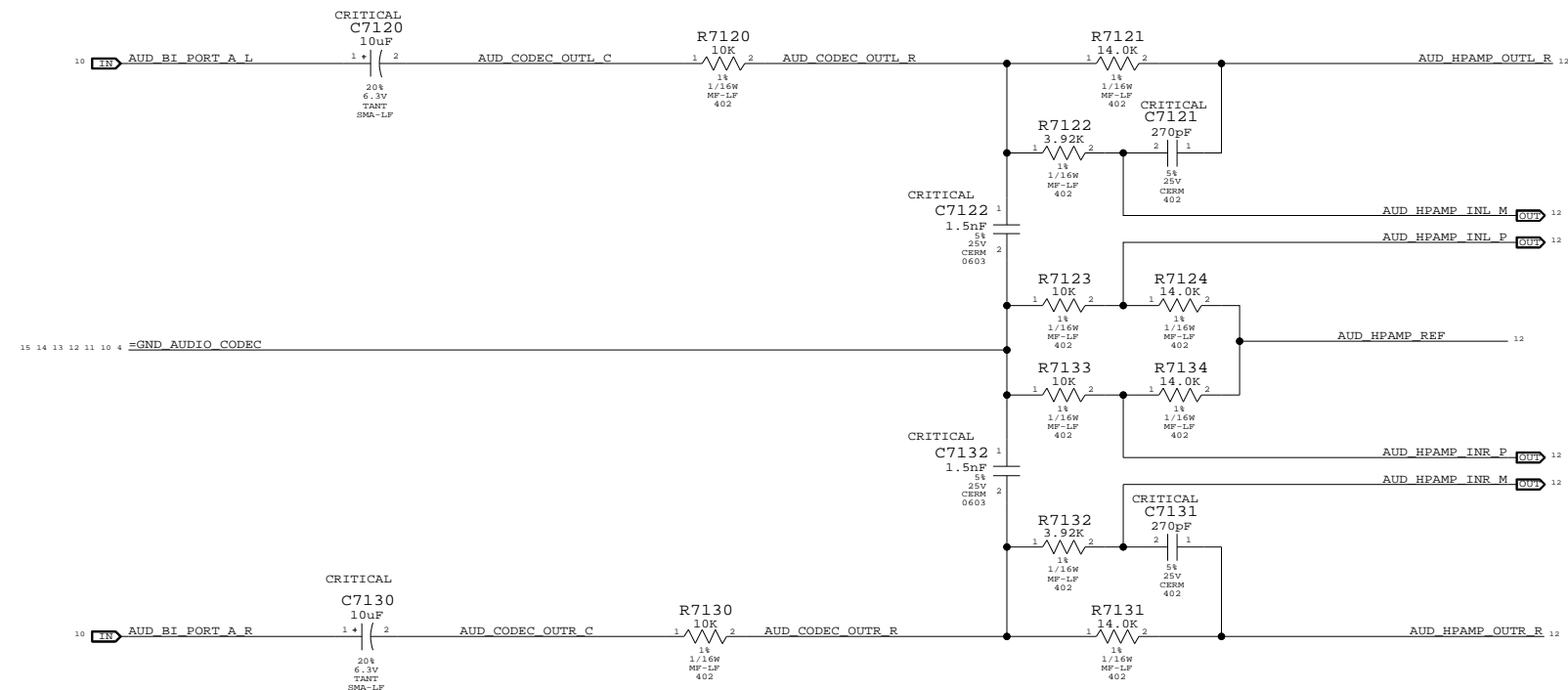
2

1

Headphone Amplifier (MAX9722)
 APN:353S0959
 VOLTAGE GAIN:1.4



2nd Order DAC Filter
 HP:1.6 HZ



AUDIO: HEADPHONE AMP

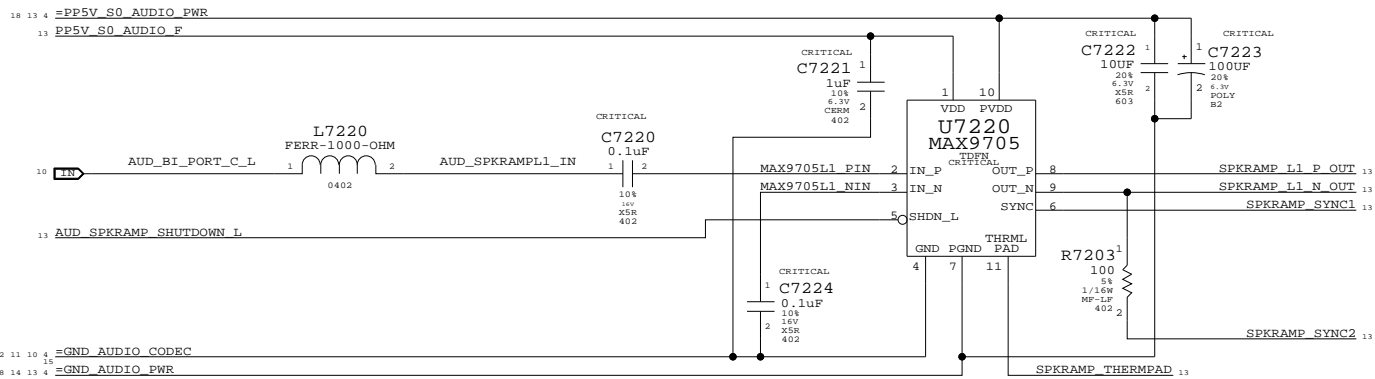
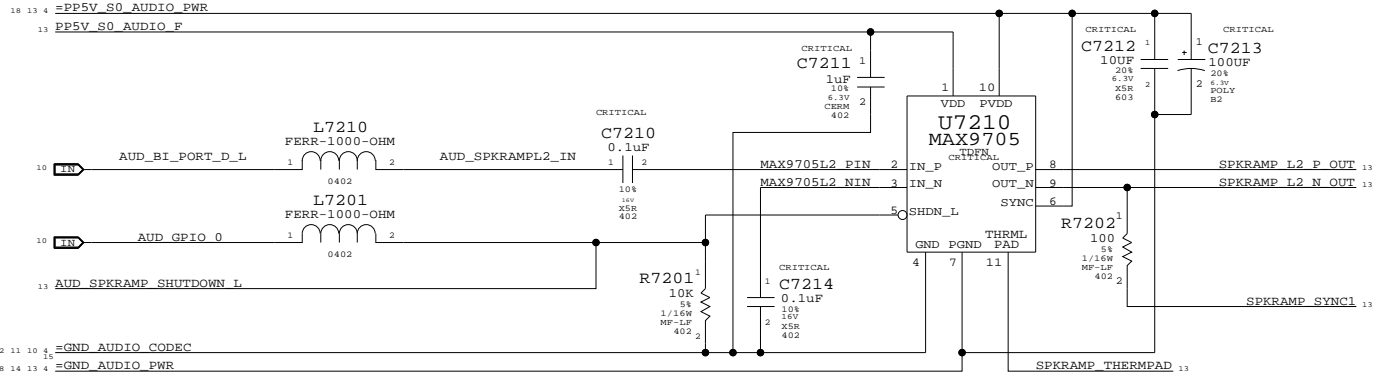
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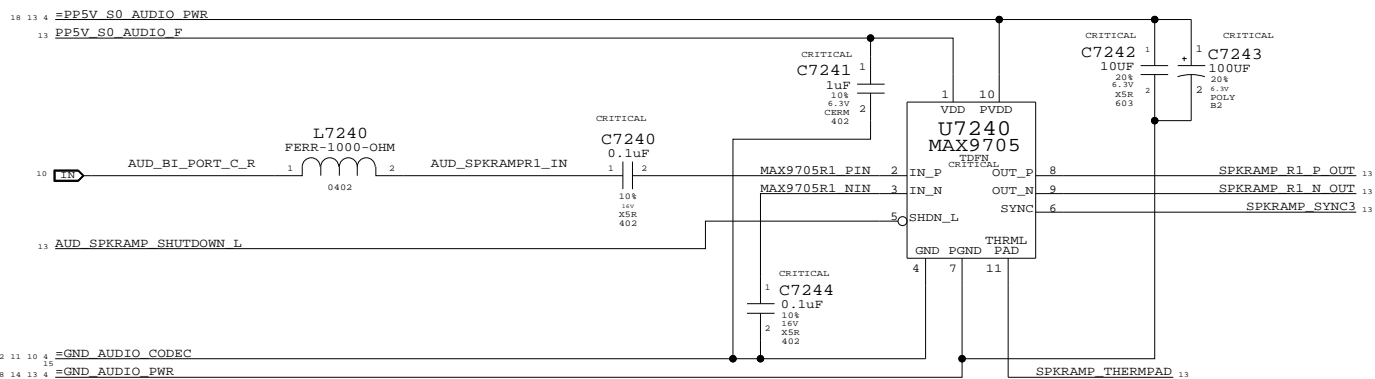
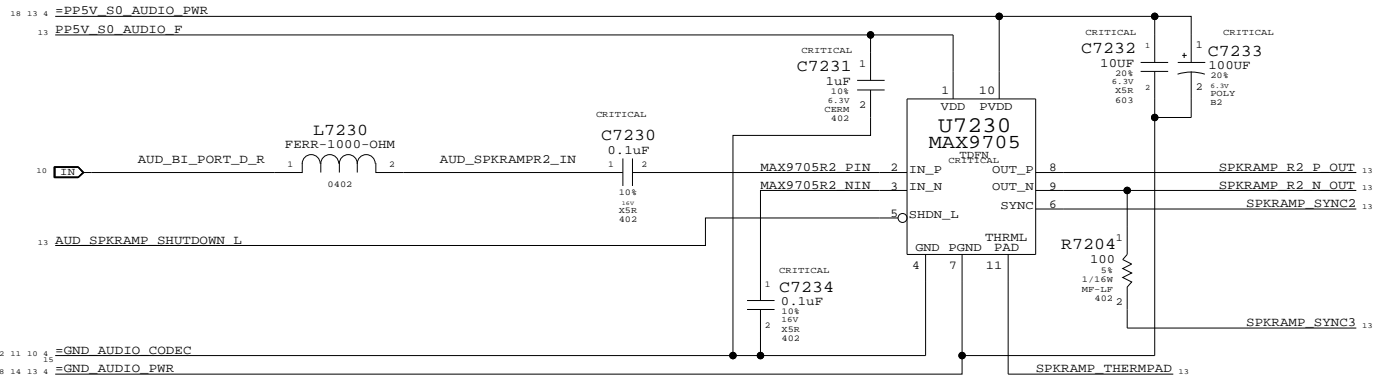
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	D	051-7066	02
SCALE	NONE	SHT	71 OF 87

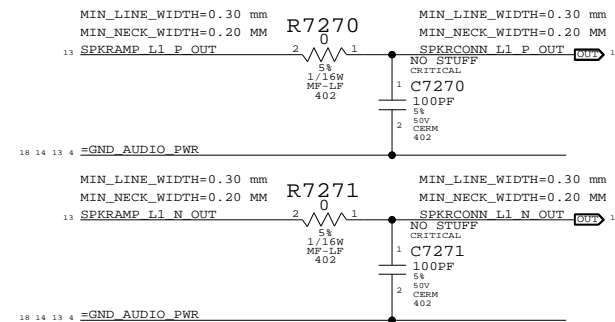
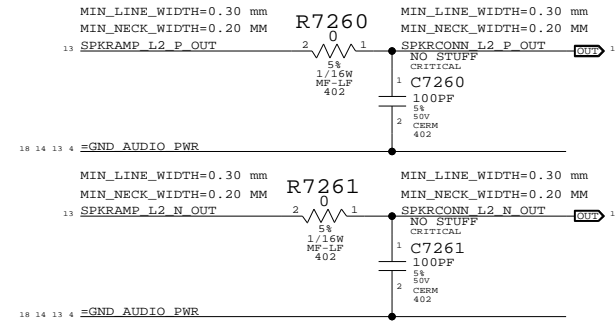
SPEAKER AMPLIFIERS (MAX9705) APN: 353S1355 TURN ON TIME: 30MS
Gain = 6dB 80 < FC < 132Hz TURN ON DELAY: 60MS



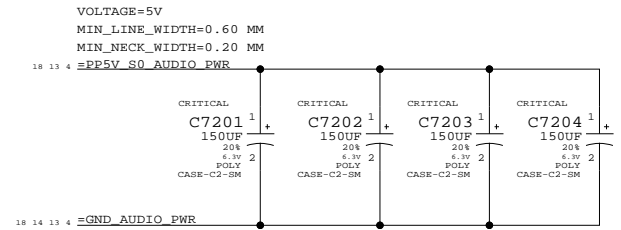
XW7200



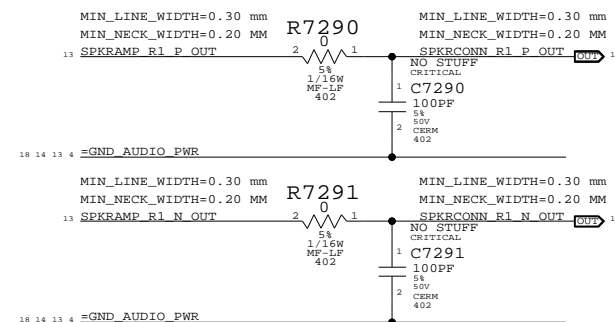
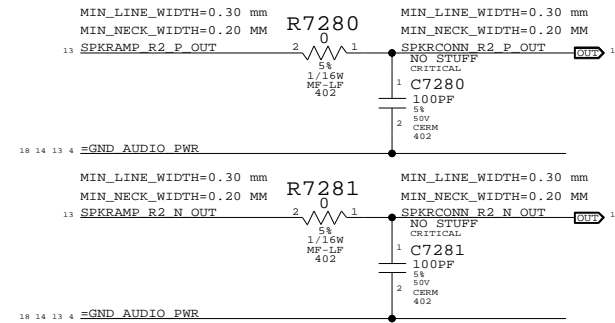
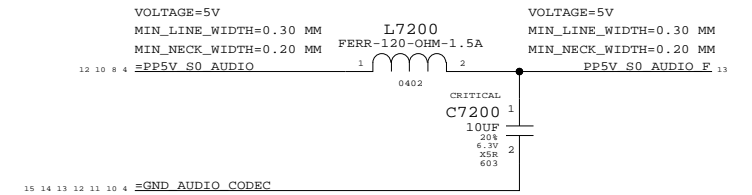
EMI FILTERS FOR AMPLIFIER OUTPUTS



POWER AMPLIFIER SUPPLY BULK CAPS



ANALOG POWER RAIL



AUDIO: SPEAKER AMP

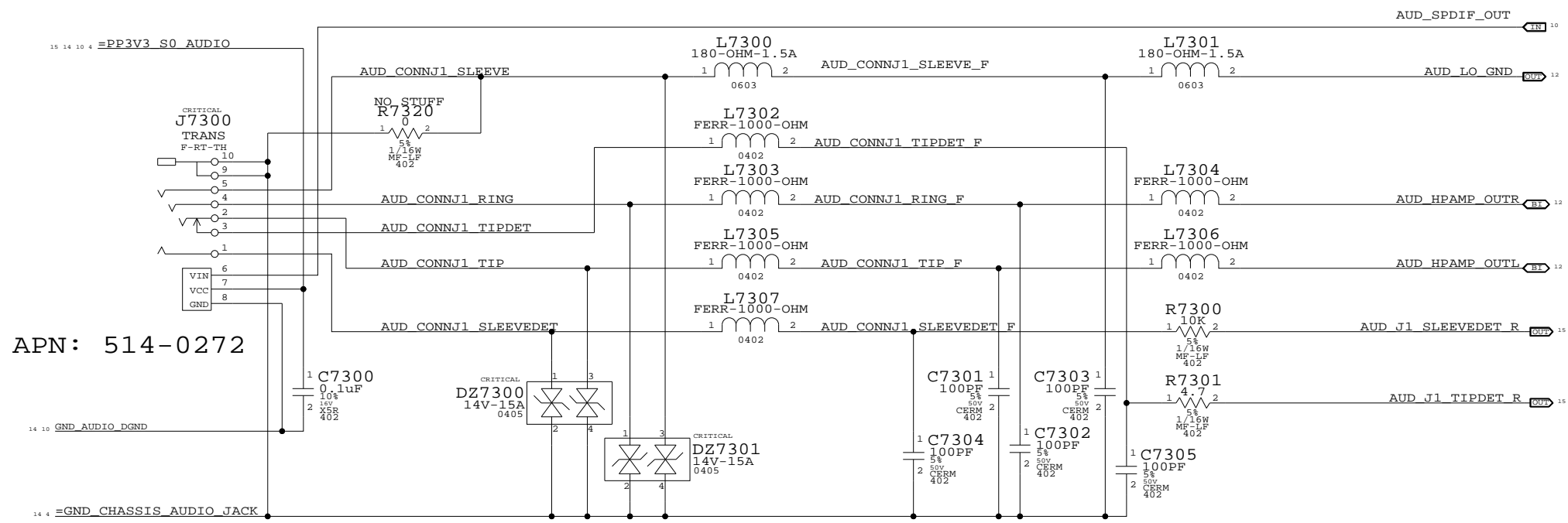
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NOTICE OF PROPRIETARY PROPERTY

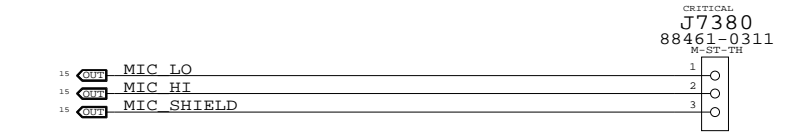
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	D	051-7066	02
SCALE	SHT	72 OF	87
NONE			

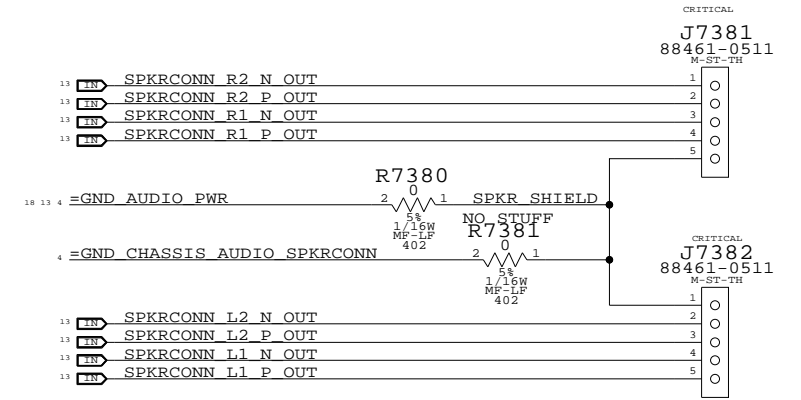
AUDIO JACK 1/DEFAULT LO/HP CONNECTOR, SPDIF TX



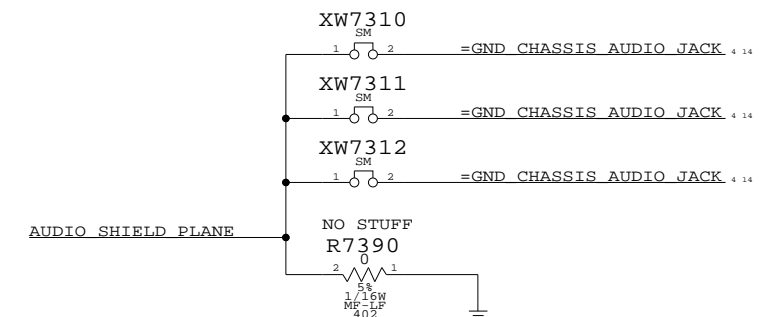
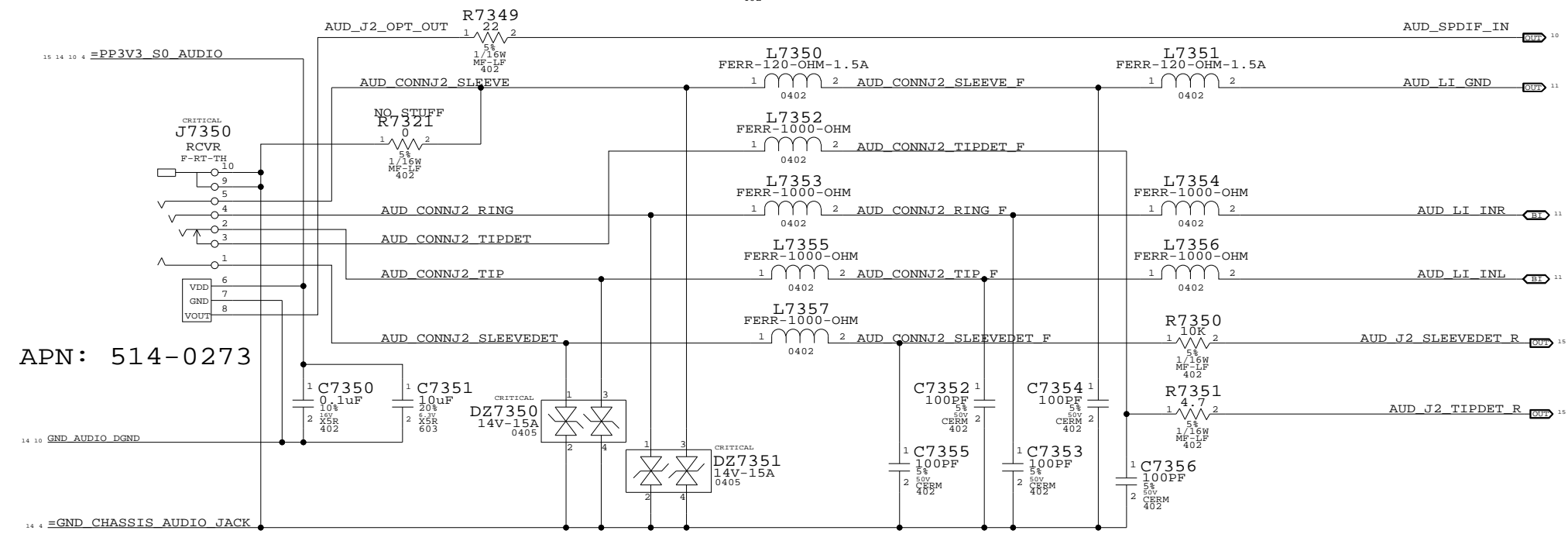
MIC CONNECTOR
APN: 518-0230



SPEAKER CONNECTORS
APN: 518-0229



AUDIO JACK 2/DEFAULT LINE IN CONNECTOR, SPDIF RX



AUDIO SHIELD
(FILLED SHAPE)

AUDIO: JACKS

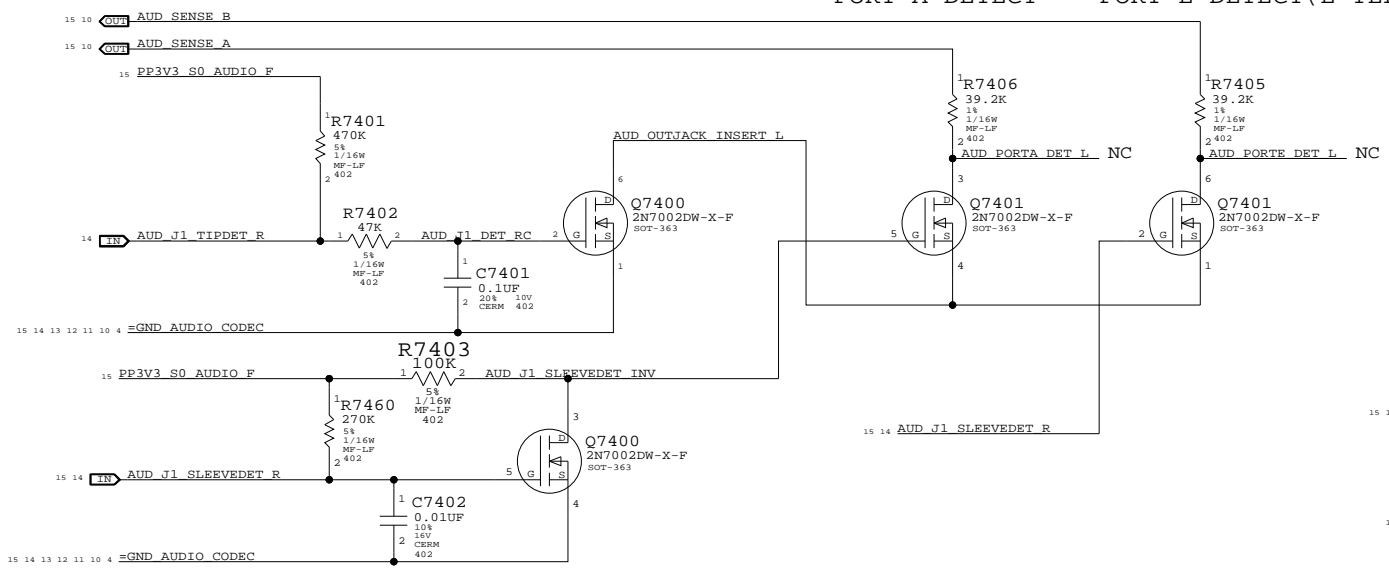
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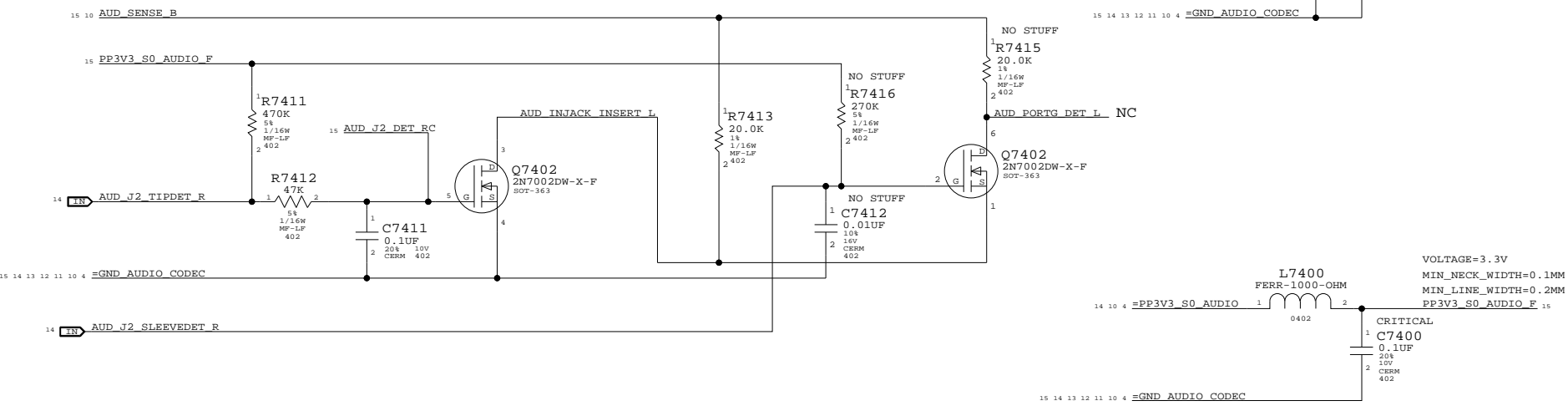
APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	02
SCALE	SHT	REV.	
NONE	73 OF 87		

PORT A DETECT PORT E DETECT(E TELLS H TO TURN ON)



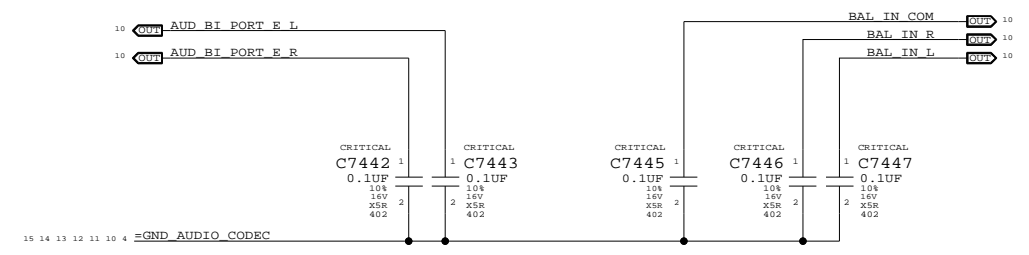
CODEC PORT ASSIGNMENTS
 PORT A : HEADPHONE/LINE OUT
 PORT B : MICROPHONE ON BOTH CH (ADC 0)
 PORT C : TRANSDUCER 1 ON LEFT/RIGHT SPEAKER
 PORT D : TRANSDUCER 2 ON LEFT/RIGHT SPEAKER
 PORT E : SW USES TO TRIGGER DIGITAL OUT
 PORT F : LINE IN (ADC 1)
 CD INPUT : UNUSED

PORT F DETECT PORT G DETECT

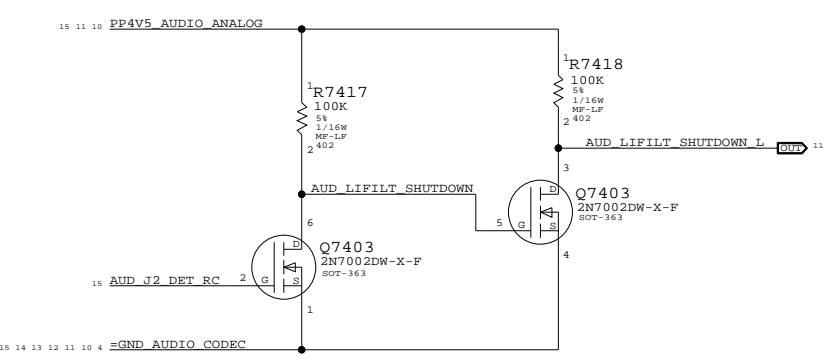


PLACE L7400/C7400 CLOSE TO Q7400

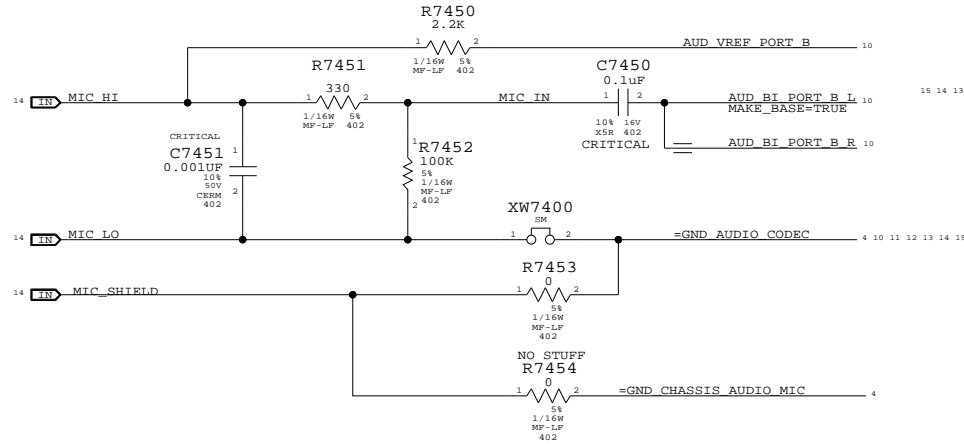
UNUSED CODEC ANALOG PORT TERMINATIONS



LINE IN FILTER SHUTDOWN CONTROL



MIC INPUT CIRCUITRY

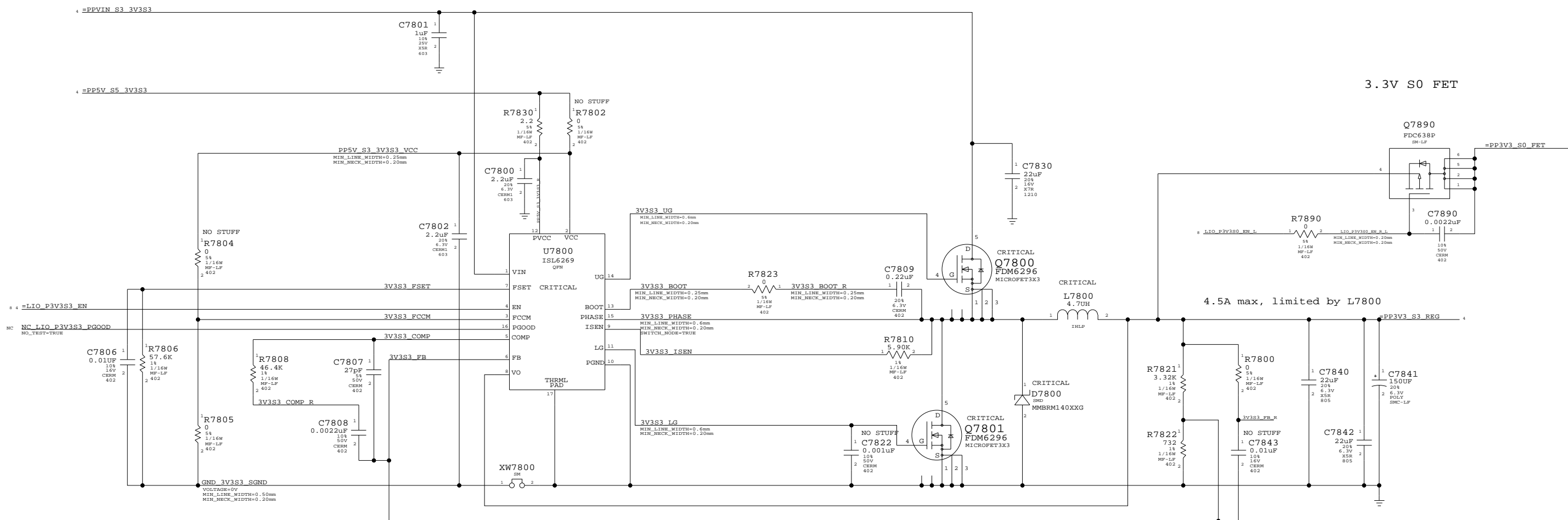


AUDIO: JACK TRANSLATORS

SYNC_MASTER=AUDIO_M9_PROG_L10 SYNC_DATE=01/06/2006
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	D	051-7066	02
SCALE	NONE	SHT	74 OF 87

3.3V S3/S0 Power Supply



3.3V Supply

SYNC_MASTER=SIREN SYNC_DATE=01/06/2006

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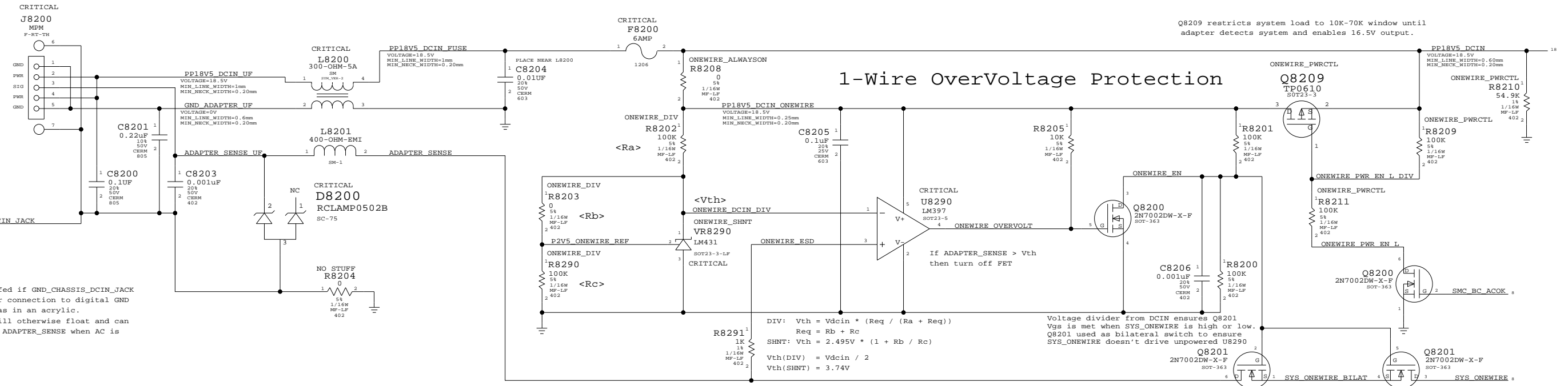
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	02
SCALE	SHT	78 OF	87
NONE			

DC Power Jack



R8204 should be stuffed if GND_CHASSIS_DCIN_JACK does not have another connection to digital GND in the system, such as in an acrylic. The chassis ground will otherwise float and can send transients onto ADAPTER_SENSE when AC is connected.

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
116S0085	1	RES, 6.2K, 5%, 1/16W, 0402, LF	R8202		ONEWIRE_SHNT
114S0315	1	RES, 10K, 1%, 1/16W, 0402, LF	R8203		ONEWIRE_SHNT
114S0343	1	RES, 20K, 1%, 1/16W, 0402, LF	R8290		ONEWIRE_SHNT

ONEWIRE_SHNT BOM option allows the use of an adjustable shunt voltage regulator to provide the reference to the LM397 comparator. This allows the protection circuit to enforce a -3.7V max signal on ADAPTER_SENSE instead of the voltage divider DCIN/2 approach.
R8202 value ensures mA current for DCIN >= 13.4V per LM431 spec.

DC-In & Battery Connectors
 SYNC_MASTER=SIREN SYNC_DATE=01/06/2006
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	02
SCALE	SHT		OF
NONE	82		87

8

7

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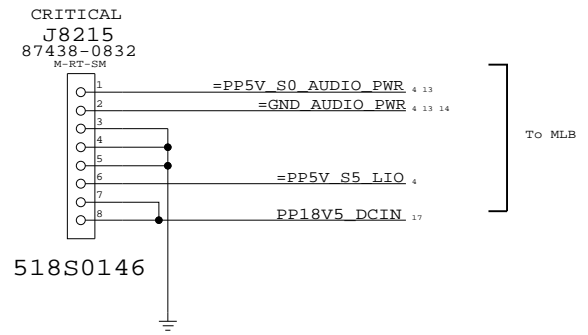
2

1

D

D

Left I/O Power Connector



C

C

B

B

A

A

8

7

6

5

4

3

2

1

LEFT I/O POWER CONNECTOR

SYNC_MASTER=SIREN SYNC_DATE=01/06/2006

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	02
SCALE	SHT		
NONE	84 OF		87

	8	7	6	5	4	3	2	1	
D	MIC_SHIELD	MIC_SHIELD - @alt_l1o_l1b.ALT_L1O	14D3 15A6						
	NC_AUD_GPIO_2	NC_AUD_GPIO_2 - @alt_l1o_l1b.ALT_L1O	10C7	SPKRCNN_L2_P_OUT	@alt_l1o_l1b.ALT_L1O	13D3 14C3			
	NC_LED_WLAN_L	NC_LED_WLAN_L - @alt_l1o_l1b.ALT_L1O	7B3	SPKRCNN_R1_N_OUT	@alt_l1o_l1b.ALT_L1O	13A3 14C3			
	NC_LED_WFAN_L	NC_LED_WFAN_L - @alt_l1o_l1b.ALT_L1O	7B3	SPKRCNN_R1_P_OUT	@alt_l1o_l1b.ALT_L1O	13A3 14C3			
	NC_LED_WWAN_L	NC_LED_WWAN_L - @alt_l1o_l1b.ALT_L1O	7B3	SPKRCNN_R2_N_OUT	@alt_l1o_l1b.ALT_L1O	13B3 14C3			
	NC_L1O_P3V3S3_PGOOD	NC_L1O_P3V3S3_PGOOD - @alt_l1o_l1b.ALT_L1O	16B8	SPKRCNN_R2_P_OUT	@alt_l1o_l1b.ALT_L1O	13B3 14C3			
	NC_UIM_CLK	NC_UIM_CLK - @alt_l1o_l1b.ALT_L1O	7C3	SPKR_SHIELD	@alt_l1o_l1b.ALT_L1O	14C2			
	NC_UIM_DATA	NC_UIM_DATA - @alt_l1o_l1b.ALT_L1O	7C3	SYS_ONEWIRE	@alt_l1o_l1b.ALT_L1O	8C6 17C1			
	NC_UIM_PWR	NC_UIM_PWR - @alt_l1o_l1b.ALT_L1O	7C3	SYS_ONEWIRE_BILAT	@alt_l1o_l1b.ALT_L1O	17C2			
	NC_UIM_RESET	NC_UIM_RESET - @alt_l1o_l1b.ALT_L1O	7C3	TP_EXCARD_STBY_L	@alt_l1o_l1b.ALT_L1O	6C7			
	NC_UIM_VFP	NC_UIM_VFP - @alt_l1o_l1b.ALT_L1O	7C3	TP_EXCARD_STBY_N	@alt_l1o_l1b.ALT_L1O	4B2			
	NC_VOL_DOWN	NC_VOL_DOWN - @alt_l1o_l1b.ALT_L1O	10C7	TP_USB2_MINI_N	@alt_l1o_l1b.ALT_L1O	4B2			
	NC_VOL_UP	NC_VOL_UP - @alt_l1o_l1b.ALT_L1O	10C7	TP_USB2_MINI_P	@alt_l1o_l1b.ALT_L1O	4B2			
	NC_M_DISABLE_L	NC_M_DISABLE_L - @alt_l1o_l1b.ALT_L1O	7C3	USB_LEFT2_EMI_N	@alt_l1o_l1b.ALT_L1O	4B5 7B3			
	ONEWIRE_DCIN_DIV	ONEWIRE_DCIN_DIV - @alt_l1o_l1b.ALT_L1O	17C5	USB_LEFT2_EMI_P	@alt_l1o_l1b.ALT_L1O	5C4			
	ONEWIRE_EN	ONEWIRE_EN - @alt_l1o_l1b.ALT_L1O	17D3	USB_LEFT2_GND	@alt_l1o_l1b.ALT_L1O	5C3			
	ONEWIRE_ESD	ONEWIRE_ESD - @alt_l1o_l1b.ALT_L1O	17C4	USB_LEFT2_GND	@alt_l1o_l1b.ALT_L1O	5C3			
	ONEWIRE_OVERVOLT	ONEWIRE_OVERVOLT - @alt_l1o_l1b.ALT_L1O	17C4	USB_LEFT_EMI_N	@alt_l1o_l1b.ALT_L1O	5B4			
	ONEWIRE_PWR_EN_L	ONEWIRE_PWR_EN_L - @alt_l1o_l1b.ALT_L1O	17C2	USB_LEFT_EMI_P	@alt_l1o_l1b.ALT_L1O	5B4			
	C	ONEWIRE_PWR_EN_L_DIV	ONEWIRE_PWR_EN_L_DIV - @alt_l1o_l1b.ALT_L1O	17D2	USB_LEFT_GND	@alt_l1o_l1b.ALT_L1O	5B3		
P2V5_ONEWIRE_REF		P2V5_ONEWIRE_REF - @alt_l1o_l1b.ALT_L1O	17C5	USB_LEFT_PWRON_L	@alt_l1o_l1b.ALT_L1O	5D7			
PCIE_CLK100M_EXCARD_UP_N		PCIE_CLK100M_EXCARD_UP_N - @alt_l1o_l1b.ALT_L1O	8C4	VREG_FB	@alt_l1o_l1b.ALT_L1O	10A3			
PCIE_CLK100M_EXCARD_UP_P		PCIE_CLK100M_EXCARD_UP_P - @alt_l1o_l1b.ALT_L1O	8B4						
PCIE_CLK100M_MINI_UP_N		PCIE_CLK100M_MINI_UP_N - @alt_l1o_l1b.ALT_L1O	8C4						
PCIE_CLK100M_MINI_UP_P		PCIE_CLK100M_MINI_UP_P - @alt_l1o_l1b.ALT_L1O	8C4						
PCIE_WAKE_EXCARD_L		PCIE_WAKE_EXCARD_L - @alt_l1o_l1b.ALT_L1O	4C3 6C3						
PCIE_WAKE_L		PCIE_WAKE_L - @alt_l1o_l1b.ALT_L1O	4C4 8C6						
PCIE_WAKE_MINI_L		PCIE_WAKE_MINI_L - @alt_l1o_l1b.ALT_L1O	4C3 7C6						
PLT_RESET_SWITCH_L		PLT_RESET_SWITCH_L - @alt_l1o_l1b.ALT_L1O	4C4 8C6						
PP1V5_S0_EXCARD_SWIT		PP1V5_S0_EXCARD_SWIT - @alt_l1o_l1b.ALT_L1O	6C3 6C3						
PP3V3_AUDIO_CODEC		PP3V3_AUDIO_CODEC - @alt_l1o_l1b.ALT_L1O	10D6						
PP3V3_S0_AUDIO_F		PP3V3_S0_AUDIO_F - @alt_l1o_l1b.ALT_L1O	15B4 15C8 15C8 15D8						
PP3V3_S0_EXCARD_SWIT		PP3V3_S0_EXCARD_SWIT - @alt_l1o_l1b.ALT_L1O	6C3 6C3						
PP3V3_S3_EXCARD_SWIT		PP3V3_S3_EXCARD_SWIT - @alt_l1o_l1b.ALT_L1O	6C3 6C3						
PP4V5_AUDIO_ANALOG		PP4V5_AUDIO_ANALOG - @alt_l1o_l1b.ALT_L1O	10A2 10D2 11C7 15A8 15C5						
PP4V5_AUDIO_ANALOG_F		PP4V5_AUDIO_ANALOG_F - @alt_l1o_l1b.ALT_L1O	10D5						
PP4V5_AUDIO_ANALOG_F1		PP4V5_AUDIO_ANALOG_F1 - @alt_l1o_l1b.ALT_L1O	10D5						
PP5V_AUDIO_HPAMP_AVD		PP5V_AUDIO_HPAMP_AVD - @alt_l1o_l1b.ALT_L1O	12D4						
PP5V_AUDIO_HPAMP_FVD		PP5V_AUDIO_HPAMP_FVD - @alt_l1o_l1b.ALT_L1O	12D5						
PP5V_PWRON_USB_LEFT2	PP5V_PWRON_USB_LEFT2 - @alt_l1o_l1b.ALT_L1O	5D4							
PP5V_PWRON_USB_LEFT	PP5V_PWRON_USB_LEFT - @alt_l1o_l1b.ALT_L1O	5B4							
PP5V_S0_AUDIO_F	PP5V_S0_AUDIO_F - @alt_l1o_l1b.ALT_L1O	13A8 13B1 13B8 13C8 13D8							
PP5V_S3_3V3S3_R	PP5V_S3_3V3S3_R - @alt_l1o_l1b.ALT_L1O	16C6							
PP5V_S3_3V3S3_VCC	PP5V_S3_3V3S3_VCC - @alt_l1o_l1b.ALT_L1O	16C7							
PP5V_S3_USB_SWITCH_O	PP5V_S3_USB_SWITCH_OUT1 - @alt_l1o_l1b.ALT_L1O	5B5							
PP5V_S3_USB_SWITCH_O	PP5V_S3_USB_SWITCH_OUT2 - @alt_l1o_l1b.ALT_L1O	5D5							
PP18V5_DCIN	PP18V5_DCIN - @alt_l1o_l1b.ALT_L1O	17D1 18C4							
PP18V5_DCIN_FUSE	PP18V5_DCIN_FUSE - @alt_l1o_l1b.ALT_L1O	17D6							
PP18V5_DCIN_ONEWIRE	PP18V5_DCIN_ONEWIRE - @alt_l1o_l1b.ALT_L1O	17D5							
PP18V5_DCIN_UP	PP18V5_DCIN_UP - @alt_l1o_l1b.ALT_L1O	17D7							
SDATAIN	SDATAIN - @alt_l1o_l1b.ALT_L1O	10C6							
SMC_BC_ACOK	SMC_BC_ACOK - @alt_l1o_l1b.ALT_L1O	8C6 17C1							
SMC_EXCARD_CP	SMC_EXCARD_CP - @alt_l1o_l1b.ALT_L1O	6A4 8C6							
SMC_EXCARD_PWR_EN	SMC_EXCARD_PWR_EN - @alt_l1o_l1b.ALT_L1O	6C7 8C6							
SPKRAMP_L1_N_OUT	SPKRAMP_L1_N_OUT - @alt_l1o_l1b.ALT_L1O	13C4 13C5							
SPKRAMP_L1_P_OUT	SPKRAMP_L1_P_OUT - @alt_l1o_l1b.ALT_L1O	13C4 13C5							
SPKRAMP_L2_N_OUT	SPKRAMP_L2_N_OUT - @alt_l1o_l1b.ALT_L1O	13D4 13D5							
SPKRAMP_L2_P_OUT	SPKRAMP_L2_P_OUT - @alt_l1o_l1b.ALT_L1O	13D4 13D5							
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SPKRAMP_R2_N_OUT	SPKRAMP_R2_N_OUT - @alt_l1o_l1b.ALT_L1O	13B4 13B5							
SPKRAMP_R2_P_OUT	SPKRAMP_R2_P_OUT - @alt_l1o_l1b.ALT_L1O	13B4 13B5							
SPKRAMP_SYNC1	SPKRAMP_SYNC1 - @alt_l1o_l1b.ALT_L1O	13C5 13D5							
SPKRAMP_SYNC2	SPKRAMP_SYNC2 - @alt_l1o_l1b.ALT_L1O	13B5 13C5							
SPKRAMP_SYNC3	SPKRAMP_SYNC3 - @alt_l1o_l1b.ALT_L1O	13A5 13B5							
SPKRAMP_THERMPAD	SPKRAMP_THERMPAD - @alt_l1o_l1b.ALT_L1O	13A5 13B5 13B5 13C5 13C5							
SPKRCONN_L1_N_OUT	SPKRCONN_L1_N_OUT - @alt_l1o_l1b.ALT_L1O	13C3 14C3							
SPKRCONN_L1_P_OUT	SPKRCONN_L1_P_OUT - @alt_l1o_l1b.ALT_L1O	13C3 14C3							
SPKRCONN_L2_N_OUT	SPKRCONN_L2_N_OUT - @alt_l1o_l1b.ALT_L1O	13D3 14C3							

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	Title: Cref Part Report Design: alt_lio Date: Jan 19 16:34:21 2006							
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alt_lio[8B3] J5100 CON_F4RT_USB_S2MT_TH alt_lio[5B2] _F-RT-TH-USB-LFT J5110 CON_F4RT_USB_S2MT_TH alt_lio[5C2] _F-RT-TH-USB-LFT J5300 CON_F26RT_S2MT_SM_F- alt_lio[6D2] RT-SM J5400 CON_F52RT_D2MT_SM_F- alt_lio[7C5] ST-SM J5500 CON_F80ST_D4MT_SM_F- alt_lio[8C4] ST-SM J7300 CON_F8RT_SPDIFFRAN_T alt_lio[14D8] H2_F-RT-TH J7350 CON_F8RT_SPDIFRCVR_T alt_lio[14B8] H2_F-RT-TH J7380 CON_M3ST_S_TH_M-ST-T alt_lio[14D1] H J7381 CON_M5ST_S_TH_M-ST-T alt_lio[14C1] H J7382 CON_M5ST_S_TH_M-ST-T alt_lio[14C1] H J8200 CON_F8RT_S2MT_TH3_F- alt_lio[17D8] RT-TH J8215 CON_M8RT_S_SM_M-RT-S alt_lio[18C5] M L5100 FILTER_4P_SM alt_lio[5B4] L5101 IND_SM alt_lio[5B4] L5110 FILTER_4P_SM alt_lio[5C4] L5111 IND_SM alt_lio[5D4] L5150 IND_SM alt_lio[5A4] L5160 IND_SM alt_lio[5C4] L6800 IND_0402 alt_lio[10A5] L6801 IND_0402 alt_lio[10D6] L6802 IND_0402 alt_lio[10D4] L6803 IND_0402 alt_lio[10D3] L7101 IND_0402 alt_lio[12D6] L7102 IND_0402 alt_lio[12C6] L7200 IND_0402 alt_lio[13B2] L7201 IND_0402 alt_lio[13D7] L7210 IND_0402 alt_lio[13D7] L7220 IND_0402 alt_lio[13C7] L7230 IND_0402 alt_lio[13B7] L7240 IND_0402 alt_lio[13A7] L7300 IND_0603 alt_lio[14D6] L7301 IND_0603 alt_lio[14D4] L7302 IND_0402 alt_lio[14D6] L7303 IND_0402 alt_lio[14C6] L7304 IND_0402 alt_lio[14C4] L7305 IND_0402 alt_lio[14C6] L7306 IND_0402 alt_lio[14C4] L7307 IND_0402 alt_lio[14C6] L7350 IND_0402 alt_lio[14B6] L7351 IND_0402 alt_lio[14B4] L7352 IND_0402 alt_lio[14B6] L7353 IND_0402 alt_lio[14B6] L7354 IND_0402 alt_lio[14B4] L7355 IND_0402 alt_lio[14B6] L7356 IND_0402 alt_lio[14B4] L7357 IND_0402 alt_lio[14A6] L7400 IND_0402 alt_lio[15B4] L7800 IND_IHLP alt_lio[16B3] L8200 FILTER_4P_SM alt_lio[17D6] L8201 IND_SM-1 alt_lio[17D6] PD6401 PHOTODIODE_2P_TH alt_lio[9C6] Q5101 TRA_2N7002_SOT23-LF alt_lio[5C7] Q5360 TRA_2N7002_SOT23-LF alt_lio[6B3] Q6401 TRA_2N7002_SOT23-LF alt_lio[9C5]	Q7400 TRA_2N7002DW_SOT-363 alt_lio[15D7] 15C7] Q7401 TRA_2N7002DW_SOT-363 alt_lio[15D6] 15D5] Q7402 TRA_2N7002DW_SOT-363 alt_lio[15B7] 15C5] Q7403 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AUDIO_STAC9220_LQFP alt_lio[10D5] U7000 OPAMP_MAX4253_UMAX alt_lio[11B4] 11C4] U7100 MAX9722_QFN alt_lio[12D4] U7210 MAX9705_TDFN alt_lio[13D6] U7220 MAX9705_TDFN alt_lio[13C6] U7230 MAX9705_TDFN alt_lio[13B6] U7240 MAX9705_TDFN alt_lio[13A6] U7800 ISL6269_QFN alt_lio[16C6] U8290 COMPARATOR_LM397_SOT alt_lio[17C4] 23-5 VR6800 LREG_TPS79501_SOT223 alt_lio[10A4] -6 VR8290 SHNTRREG_LM431_SOT23- alt_lio[17C5] 3-LF XW6800 SHORT_SM alt_lio[10A8] XW6801 SHORT_SM alt_lio[10B5] XW7100 SHORT_SM alt_lio[12C5] XW7101 SHORT_SM alt_lio[12C5] XW7103 SHORT_SM alt_lio[12C4] XW7200 SHORT_SM alt_lio[13B6] XW7310 SHORT_SM alt_lio[14B2] XW7311 SHORT_SM alt_lio[14B2] XW7312 SHORT_SM alt_lio[14B2] XW7400 SHORT_SM alt_lio[15A4] XW7800 SHORT_SM alt_lio[16B6]				
C								
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