1. All capacitance values are in microfarads.
2. All resistance values are in ohms, 0.1 watt +/- 5%.
3. All crystals & oscillator values are in hertz.

Non-inverting input
Both inputs are compared to REF - Hysteresis voltage (1.182V +/- 0.5 * VHB)
Hysteresis band voltage (VHB) = 2 * I(REF) * R7 = 77.9mV
I(REF) = 1.182V / (R7 + R5) = 0.799uA

From Li Cell -> [QTY]
PCBF, ENZO, Q16A
SCHEM, ENZO, Q16A

<table>
<thead>
<tr>
<th>DESCRIPTION</th>
<th>Q1</th>
<th>Q2</th>
<th>Q5</th>
<th>Q6</th>
</tr>
</thead>
<tbody>
<tr>
<td>BBATT_INPUT_ENABLE</td>
<td>7</td>
<td>402</td>
<td>1/16W</td>
<td>1%</td>
</tr>
<tr>
<td>BBATT_DISCHRG_ENABLE</td>
<td>402</td>
<td>MF</td>
<td>5%</td>
<td></td>
</tr>
<tr>
<td>24V_PBUS</td>
<td>2</td>
<td>402</td>
<td>1/16W</td>
<td>1%</td>
</tr>
<tr>
<td>14V_PBUS</td>
<td>7</td>
<td>402</td>
<td>1/16W</td>
<td>1%</td>
</tr>
</tbody>
</table>

To Boost/Output ->
1. BBATT_VDD
2. BBATT_BOOST
3. BBATT_OUT
4. +V_BBATT_RAW

Revision History
Rev C
(10/27/03) - 1/2 - SCHEM ORIGINATED FROM Q16 LIBRA

Line Constraints
Default line width should be 8 mil
MIN_LINE_WIDTH = 20 mil

Apple Computer Inc.

www.vinafix.vn