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- 1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
- 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
- 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ZONE	ECN	DESCRIPTION OF CHANGE	CK APPD	ENG APPD
				DATE	DATE
A		432604	PRODUCTION RELEASED	03/31/06	?

# SCHEM, IR, M9



## PVT

### 3/24/06

PDF PAGE	CSA PAGE	CONTENTS	SYNC MASTER	DATE
1	1	Table of Contents	N/A	N/A
2	2		MASTER	N/A
3	3	Cross Reference Page		
4	4	Cross Reference Page		

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
000-0041	1	PLACEHOLDER FOR EEE/CCC INFO	[EEE:UR2]	CRITICAL	

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
051-7031	1	SCHEM, PHASER, M9	SCH1	
820-1959	1	PCBF, PHASER, M9	PCB1	

<p>DIMENSIONS ARE IN MILLIMETERS</p> <p>XX : _____</p> <p>X.XX : _____</p> <p>X.XXX : _____</p> <p>ANGLES : _____</p> <p>DO NOT SCALE DRAWING</p> <p style="text-align: center;"></p> <p>THIRD ANGLE PROJECTION</p>	<p><b>METRIC</b></p>	<p style="text-align: right;"> Apple Computer Inc.</p> <p style="text-align: center;"><b>NOTICE OF PROPRIETARY PROPERTY</b></p> <p style="font-size: small; text-align: center;">THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING</p> <p style="font-size: x-small; text-align: center;">I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART</p> <p style="text-align: center;"><b>SCHEM, PHASER, M9</b></p> <p style="text-align: right;">DRAWING NUMBER <b>051-7031</b> REV. <b>A</b></p> <p style="text-align: right; font-size: x-small;">SHT 1 OF 4</p>										
<table border="1" style="width: 100%;"> <tr> <td>DRAPFER</td> <td>DESIGN CK</td> </tr> <tr> <td>ENG APPD</td> <td>MFG APPD</td> </tr> <tr> <td>QA APPD</td> <td>DESIGNER</td> </tr> <tr> <td>RELEASE</td> <td>SCALE</td> </tr> </table> <p style="font-size: x-small;">MATERIAL/FINISH NOTED AS APPLICABLE</p>	DRAPFER	DESIGN CK	ENG APPD	MFG APPD	QA APPD	DESIGNER	RELEASE	SCALE	<table border="1" style="width: 100%;"> <tr> <td>SIZE</td> <td>D</td> </tr> </table>	SIZE	D	
DRAPFER	DESIGN CK											
ENG APPD	MFG APPD											
QA APPD	DESIGNER											
RELEASE	SCALE											
SIZE	D											



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Title:      Basenet Report
Design:    irb
Date:      Jan 30 16:58:56 2006

Base nets and synonyms for irb_lib.IRB(@irb_lib.irb(sch_1))
Base Signal      Synonyms      Location([Zone](dir))
IR_RX_OUT        IR_RX_OUT - @irb_lib.IRB 2B5
IR_RX_PWR        IR_RX_PWR - @irb_lib.IRB 2B5
IR_USB_D+        IR_USB_D+ - @irb_lib.IRB 2C5
IR_USB_D-        IR_USB_D- - @irb_lib.IRB 2C5
NC_CHINOOK_SCL  NC_CHINOOK_SCL - @irb_lib.IRB 2C3
NC_CHINOOK_SDA  NC_CHINOOK_SDA - @irb_lib.IRB 2C3
NC_P2_2          NC_P2_2 - @irb_lib.IRB 2B5
NC_P2_3          NC_P2_3 - @irb_lib.IRB 2B5
NC_P2_4          NC_P2_4 - @irb_lib.IRB 2B5
NC_P2_5          NC_P2_5 - @irb_lib.IRB 2B5

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D

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C

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Title: Cref Part Report  
Design: irb  
Date: Jan 30 16:58:56 2006

BR1	BRACKET_2P_TH	irb[2C2]
C2	CAP_402	irb[2C5]
C3	CAP_402	irb[2C5]
C4	CAP_402	irb[2C5]
C5	CAP_402	irb[2C5]
C7	CAP_402	irb[2C4]
C9	CAP_603	irb[2B5]
J1	CON_F4RT_S2MT_SM_SM1	irb[2C8]
R4	RES_402	irb[2B6]
U1	CY8C24794_MLF	irb[2C4]
U2	TSOP6238_SM	irb[2B5]
ZH1	SLOT_TH	irb[2C4]
ZH2	SLOT_TH	irb[2C4]

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