

8

7

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2

1

- 1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
- 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
- 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ZONE	ECN	DESCRIPTION OF CHANGE	CK APPD	ENG APPD
				DATE	DATE
A		432619	PRODUCTION RELEASED	03/31/06?	

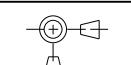

SCHEM, LEFT I/O AUDIO, M9

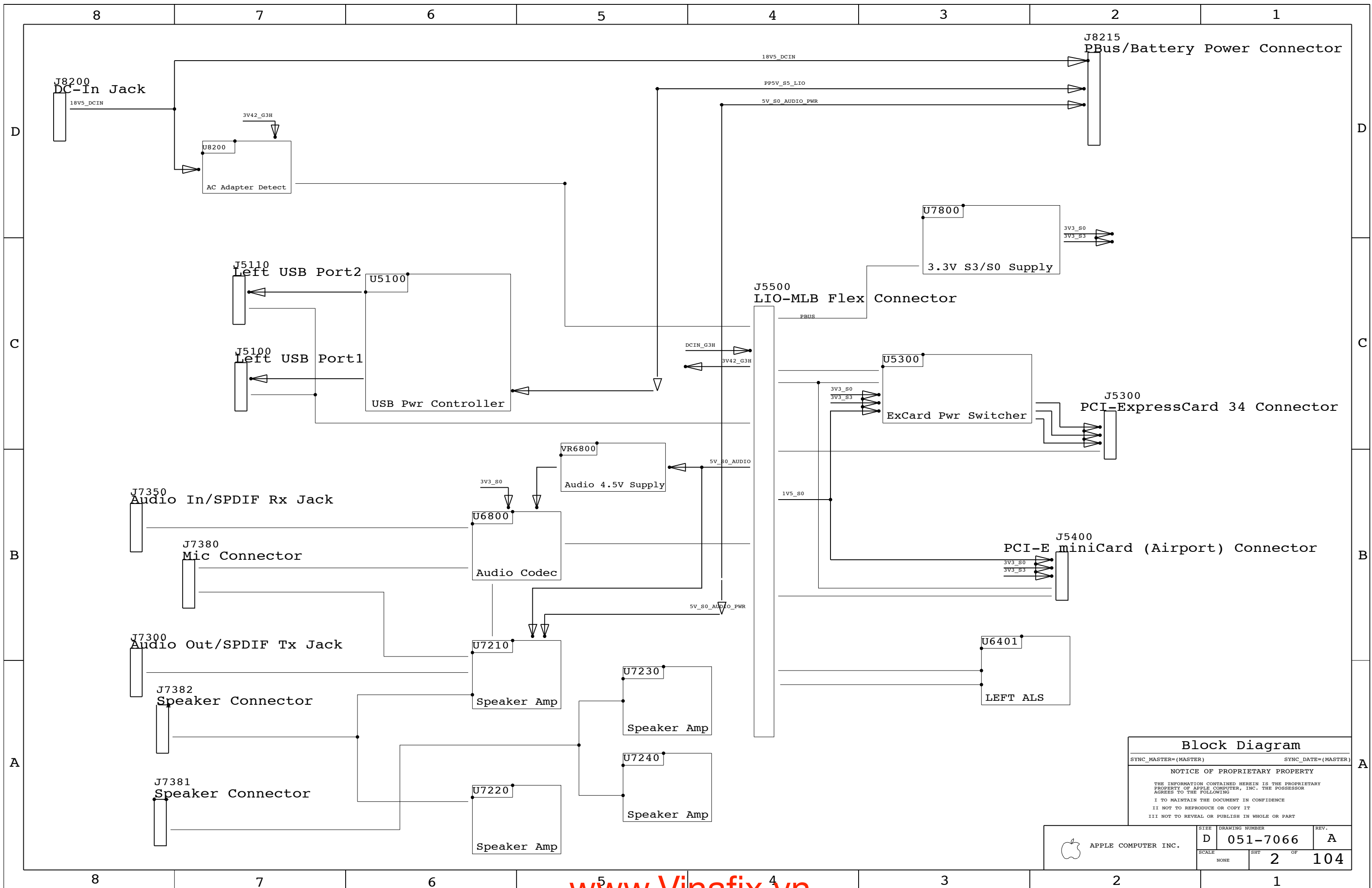
PVT

03/29/06

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2	2	Block Diagram
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20	101	HISTORY- AUDIO
21	102	Cross Reference Page
22	103	Cross Reference Page
23	104	Cross Reference Page

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
051-7066	1	SCHEM, SYMPHONY, NEW, M9	SCH1	
820-1970	1	PCBF, SYMPHONY, M9	PCB1	

<p style="text-align: center;">DIMENSIONS ARE IN MILLIMETERS</p> <p>XX : _____</p> <p>X.XX : _____</p> <p>X.XXX : _____</p> <p>ANGLES : _____</p> <p style="text-align: center;">DO NOT SCALE DRAWING</p> <p style="text-align: center;">  THIRD ANGLE PROJECTION </p>	METRIC	<p style="text-align: right;">  Apple Computer Inc. </p> <p style="text-align: center;">NOTICE OF PROPRIETARY PROPERTY</p> <p style="font-size: small;">THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING</p> <p style="font-size: x-small;">I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART</p> <p style="text-align: center;">TITLE</p> <p style="text-align: center; font-size: large;">SCHEM, LIO AUDIO, M9</p> <p style="text-align: right;">DRAWING NUMBER 051-7066 REV. A</p> <p style="text-align: right; font-size: x-small;">SHT 1 OF 104</p>
DRAFTER _____ ENG APPD _____ QA APPD _____ RELEASE _____	DESIGN CK _____ MFG APPD _____ DESIGNER _____ SCALE NONE	MATERIAL/FINISH NOTED AS APPLICABLE SIZE D



Block Diagram

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	A
SCALE	SHT		OF
NONE	2		104

8

7

6

5

4

3

2

1

BOM NUMBER	BOM NAME	BOM OPTIONS
630-7510	PCBA, SYMPHONY, NEW, M9	COMMON, EXCARD_3CNTL, ONEWIRE_DIV, ONEWIRE_PWRCTL, ALTERNATE

D

D

BAR CODE LABEL / EEE#'S

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
000-0041	1	PLACEHOLDER FOR EEE/CCC INFO	[EEE:V3P]	CRITICAL	

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
815-8851	1	ALS SPACER, M9	SP6401	CRITICAL	

C

C


B

B

A

A

BOM CONFIGURATION	
SYNC_MASTER=(MASTER)	SYNC_DATE=(MASTER)
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	D	051-7066	A
SCALE	SHT	OF	
NONE	4	104	

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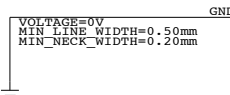
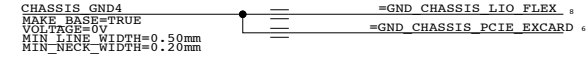
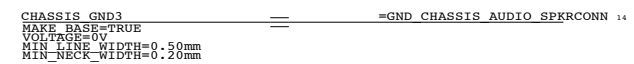
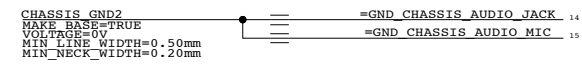
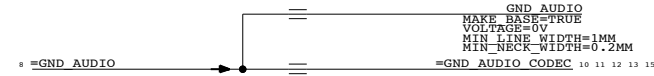
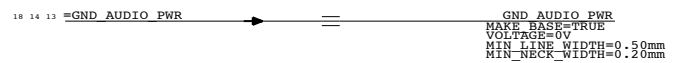
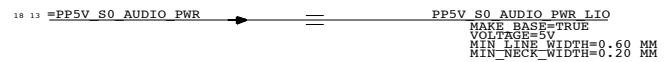
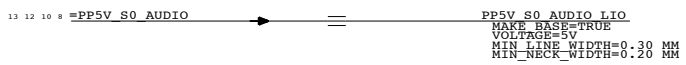
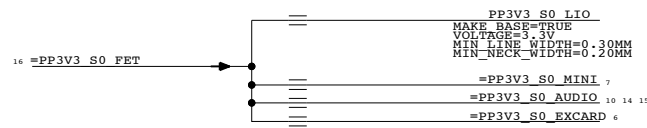
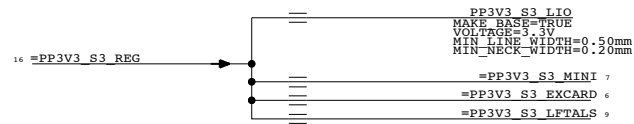
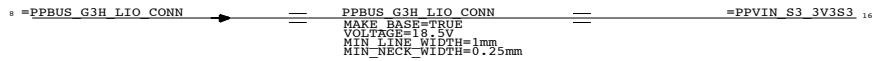
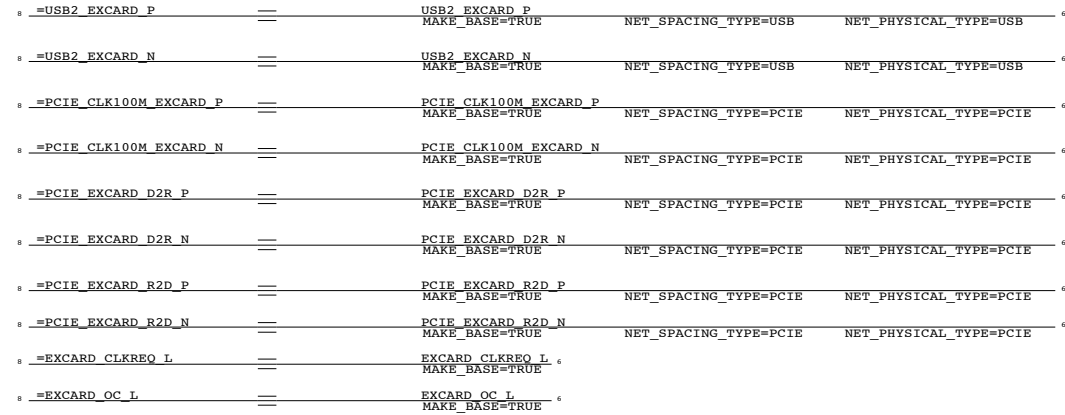
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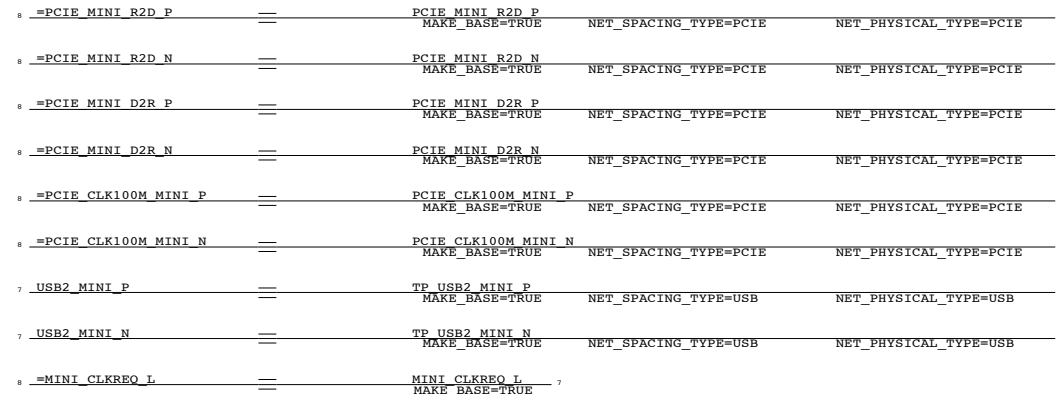
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POWER & GROUNDS

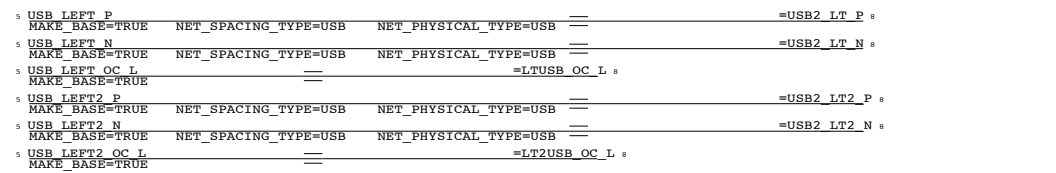
PCI-E EXPRESS CARD 34



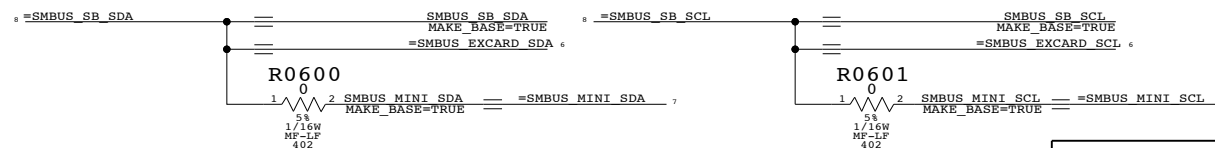
PCI-E MINICARD



USB



SMBUS



Aliases

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NOTICE OF PROPRIETARY PROPERTY

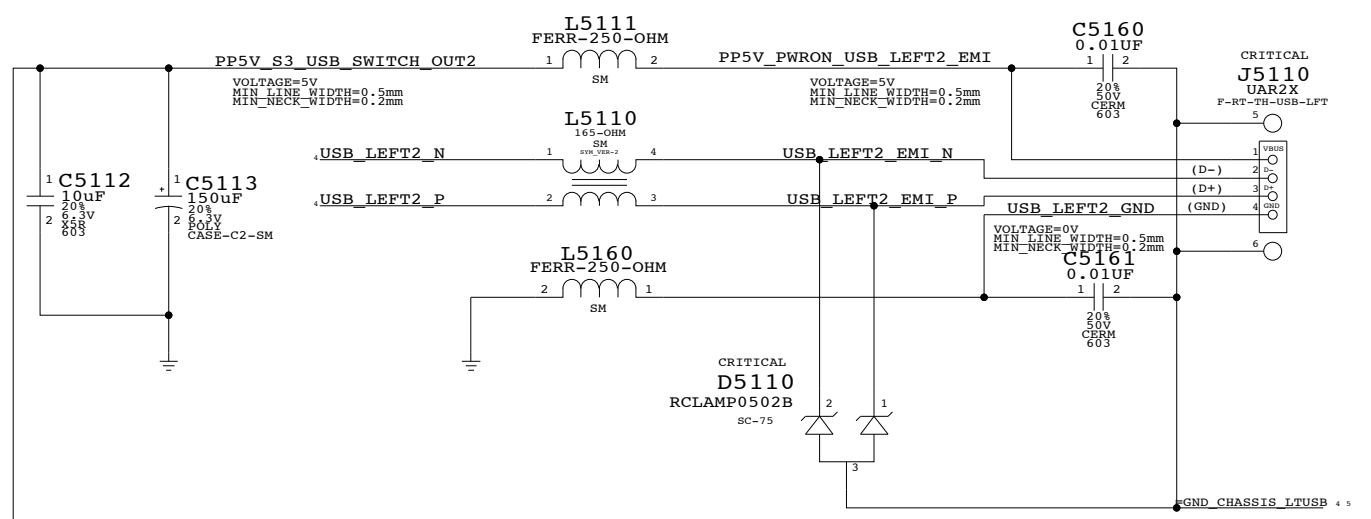
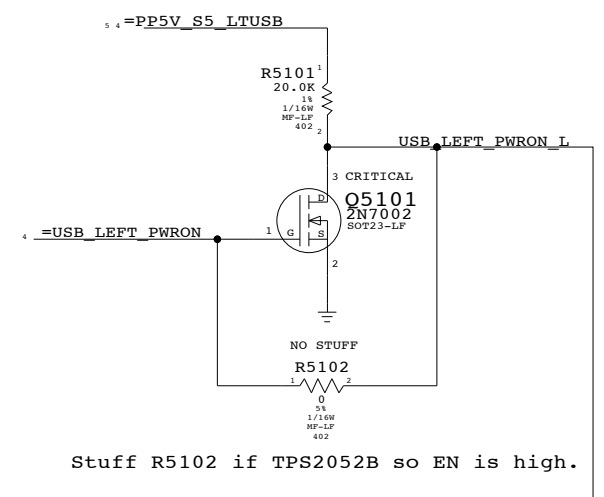
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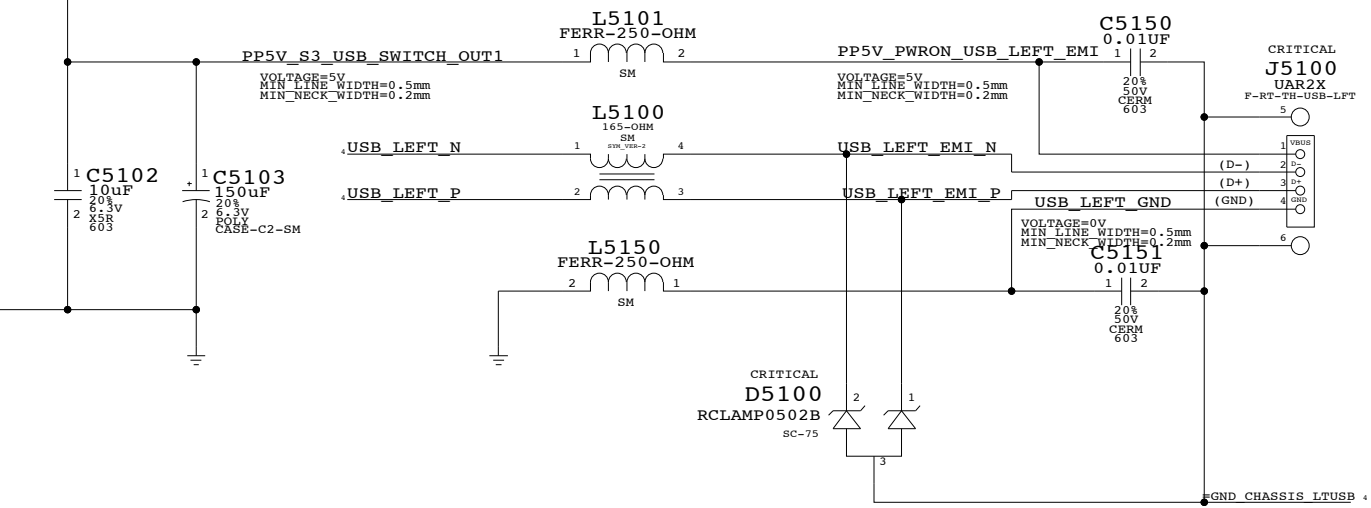
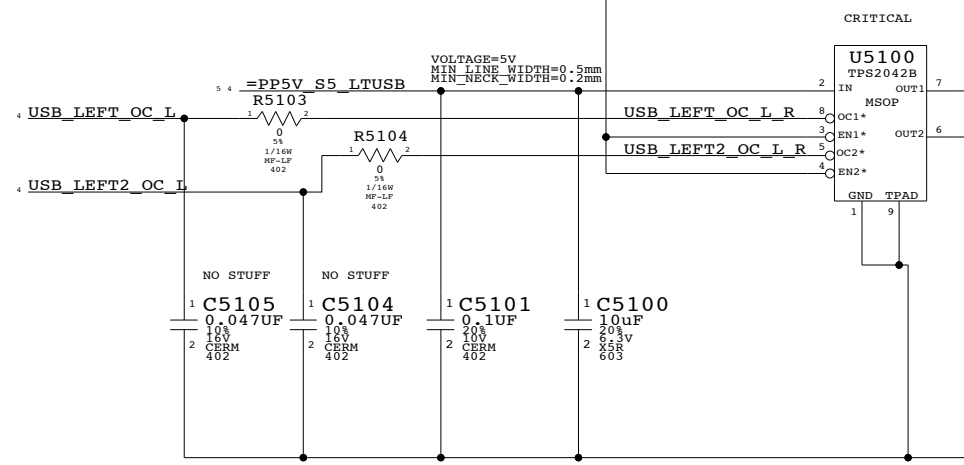
II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	A
SCALE	SHT	OF	
NONE	6	104	



PUT L5110, L5111, AND L5160 ACROSS THE MOAT

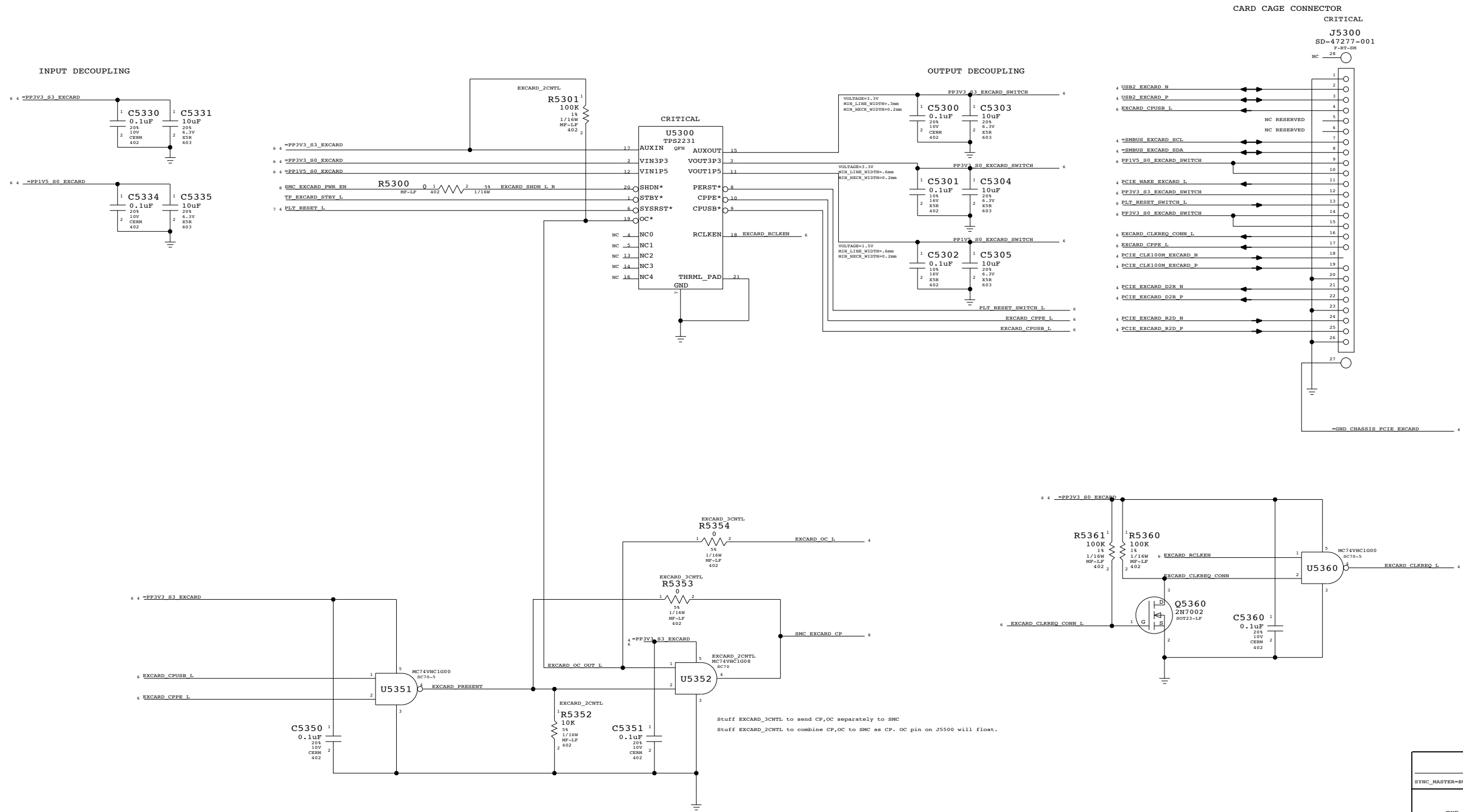


PUT L5100, L5101, AND L5150 ACROSS THE MOAT

Left USB Port
 SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)
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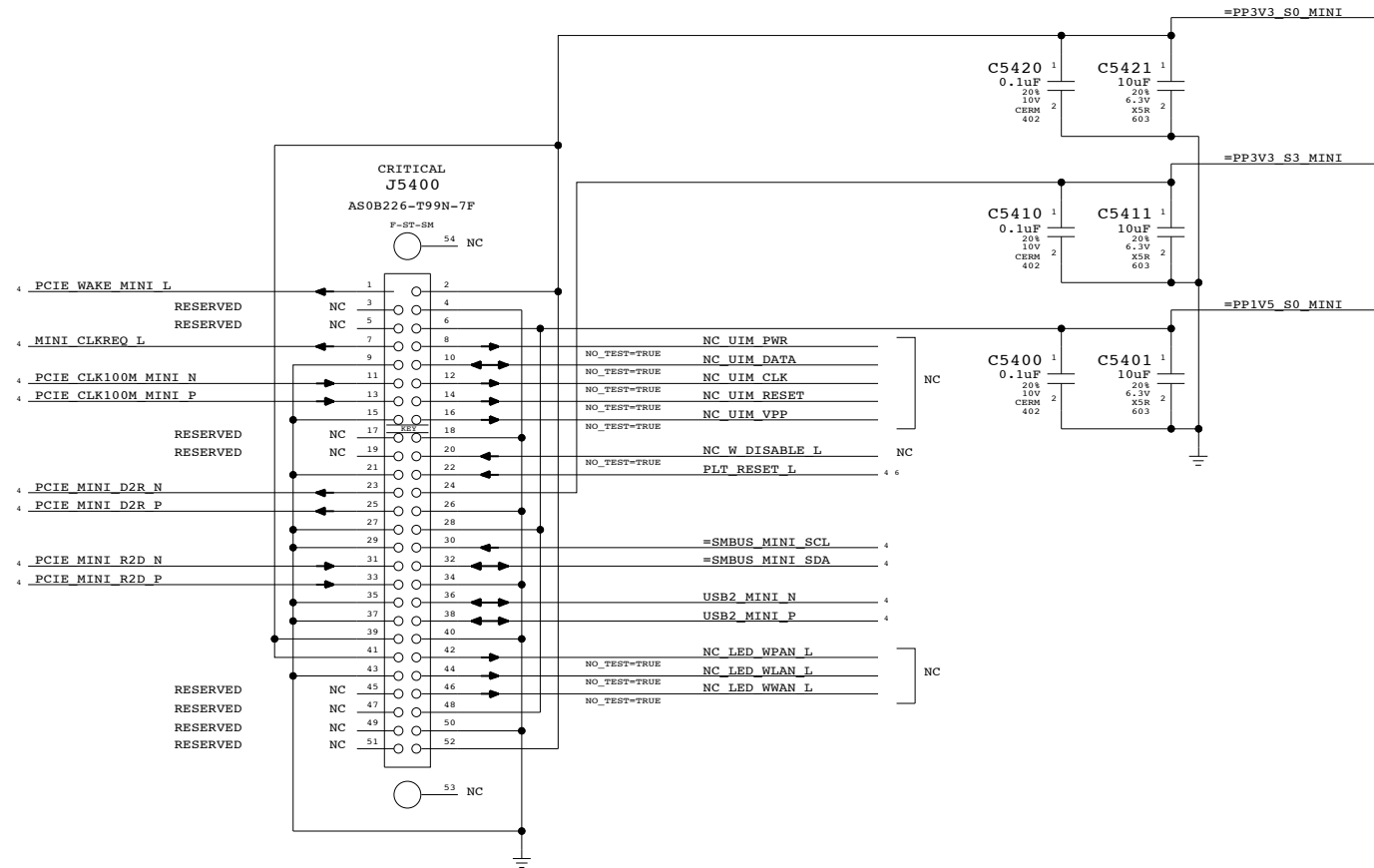
APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	A
SCALE	SHT	OF	
NONE	51	104	

EXPRESSCARD/34 TOP MOUNT CONNECTOR



APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	A
SCALE	SHT 53 OF 104		
NONE			

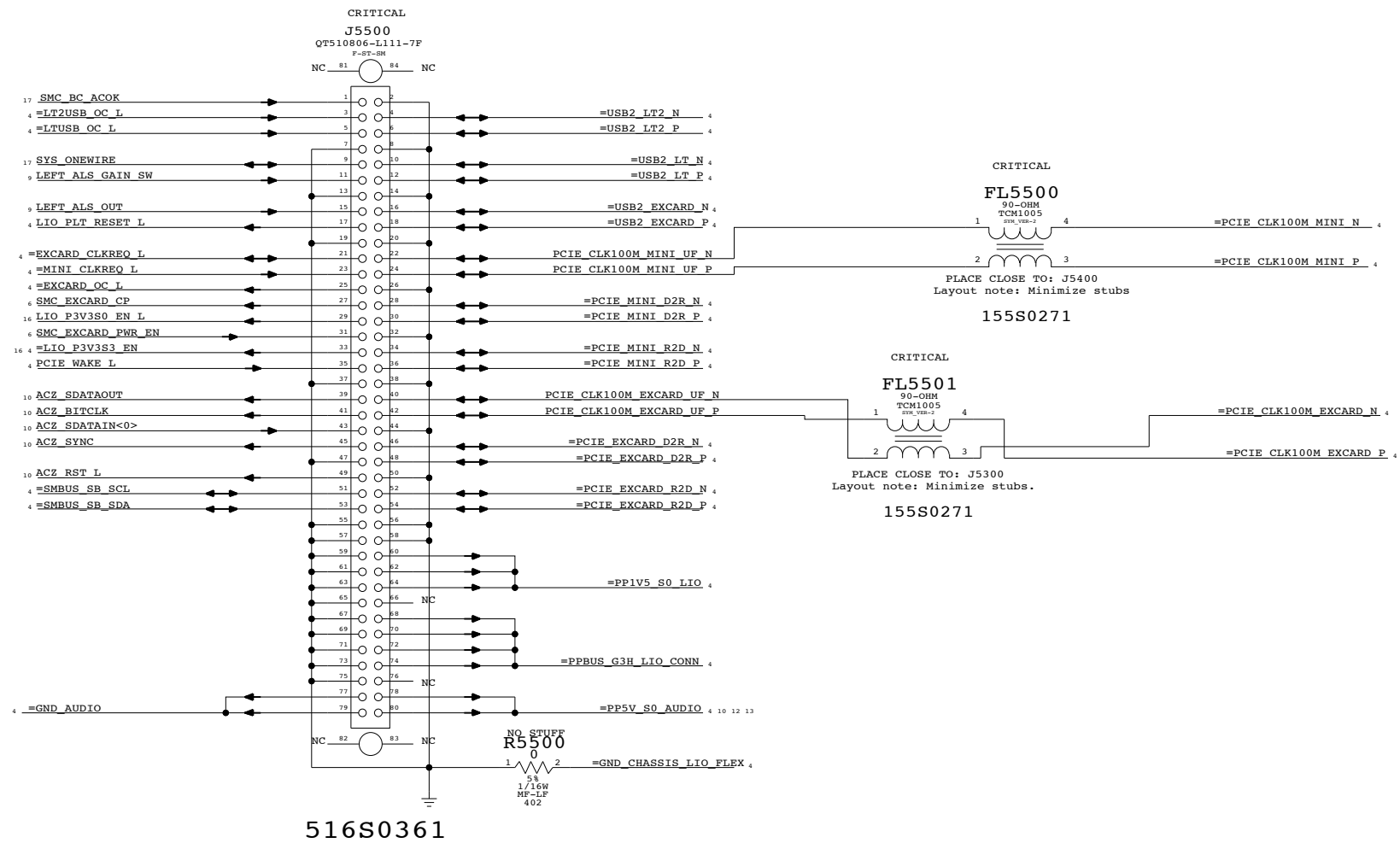
PCI-EXPRESS MINI CARD CONNECTOR



PCI-E MiniCard Connector
 SYNC_MASTER=BUZZ SYNC_DATE=03/29/2006
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	D	051-7066	A
SCALE	SHT		OF
NONE	54		104

Left I/O Board Connector



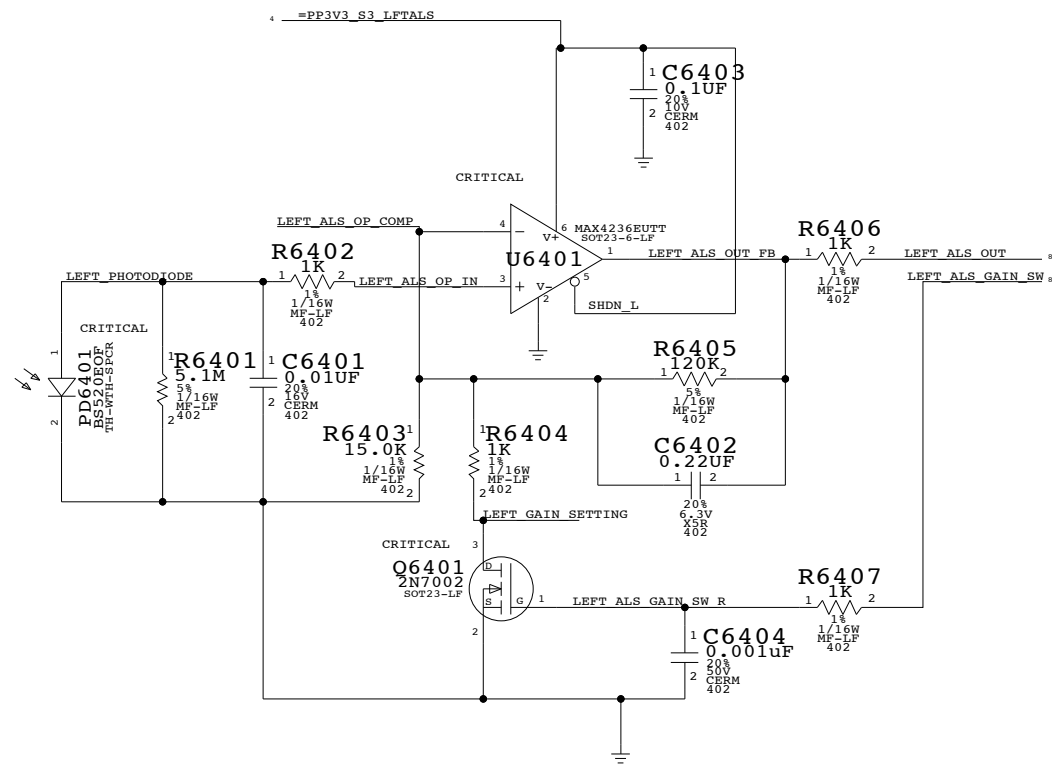
MLB I/O Board Connector

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	A
SCALE	NONE	SHT	55 OF 104



Left ALS

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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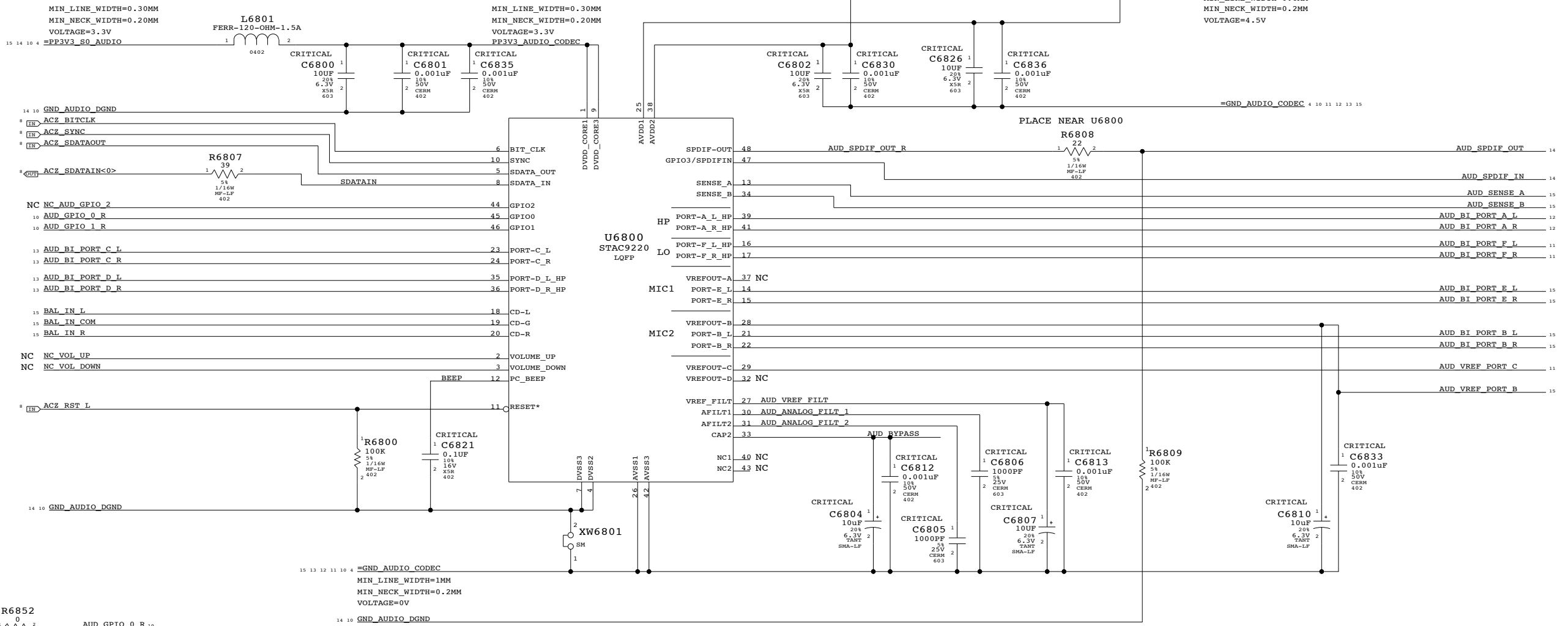
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- II NOT TO REPRODUCE OR COPY IT
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APPLE COMPUTER INC.

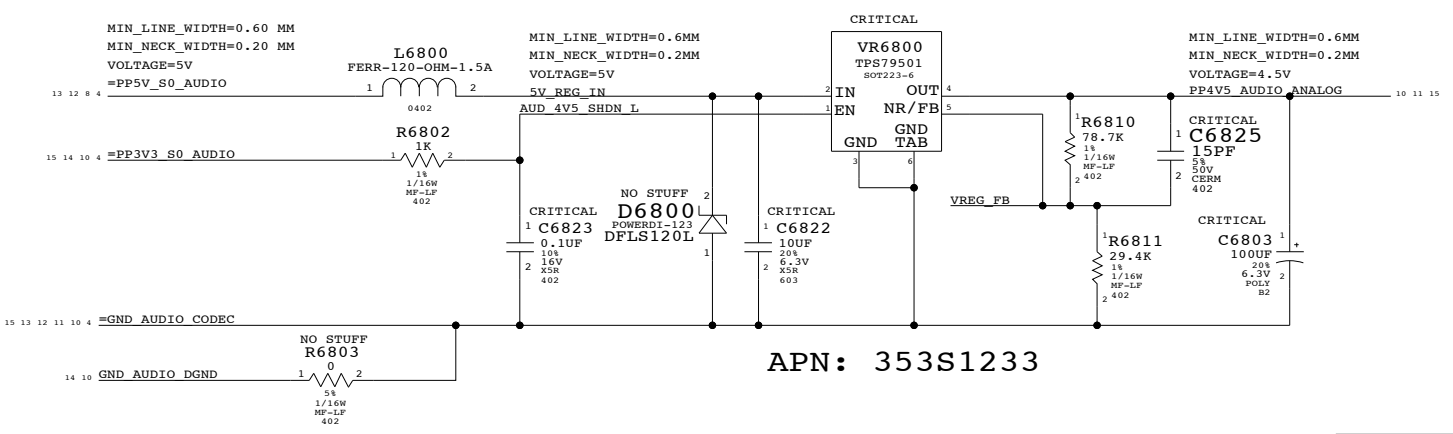
SIZE	DRAWING NUMBER	REV.
D	051-7066	A
SCALE	SHT	
NONE	64 OF 104	

AUDIO CODEC
APPLE P/N 353S1345

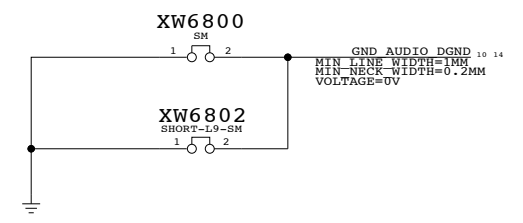


PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
353S1458	353S1345	?	U6800	DC OFFSET SCREENED

4.5V POWER SUPPLY FOR CODEC



AUDIO DIGITAL GROUND ISOLATION



AUDIO: CODEC

SYNC_MASTER=AUDIO_M9_PRO_LIO SYNC_DATE=03/29/2006

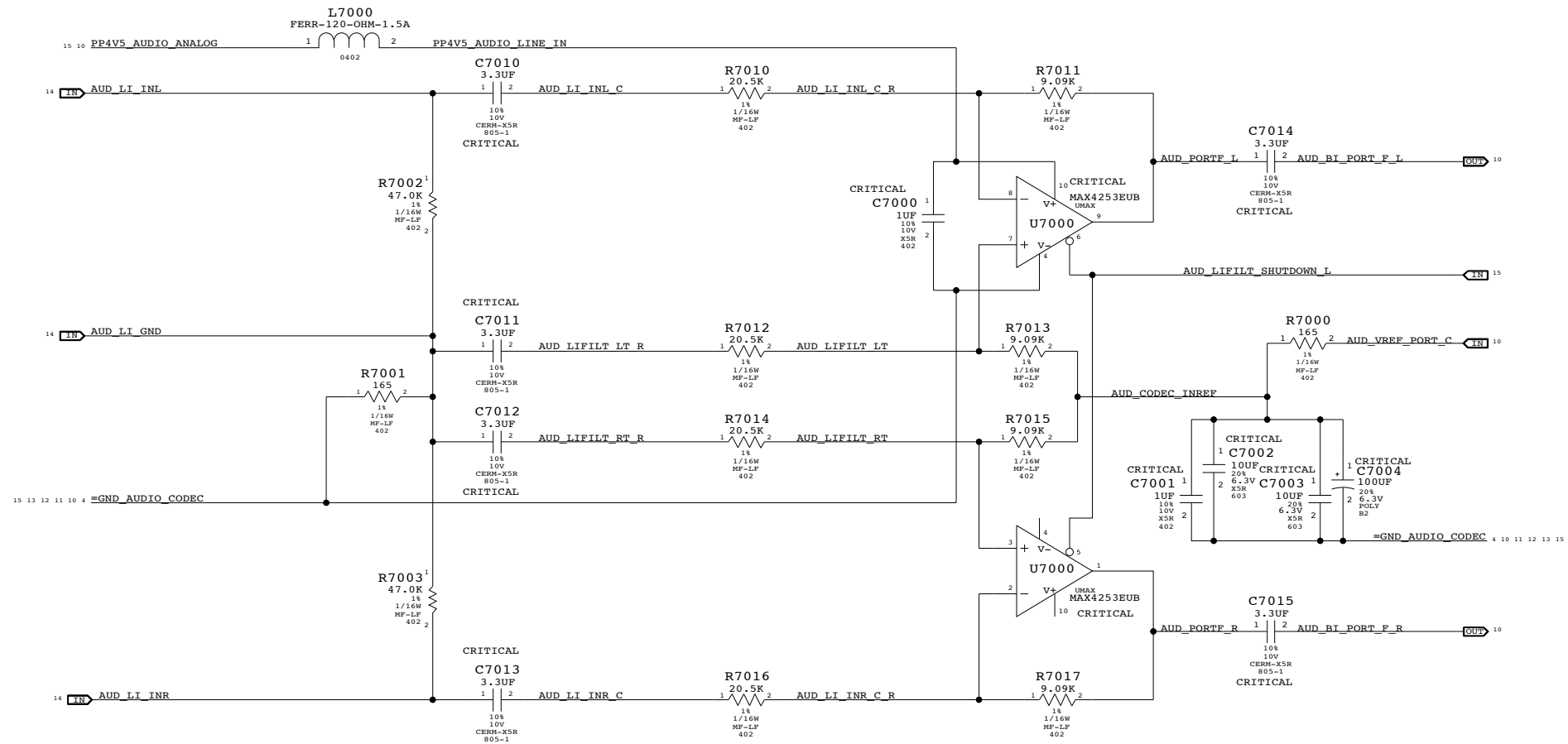
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	A
SCALE	NONE	SHT	68 OF 104

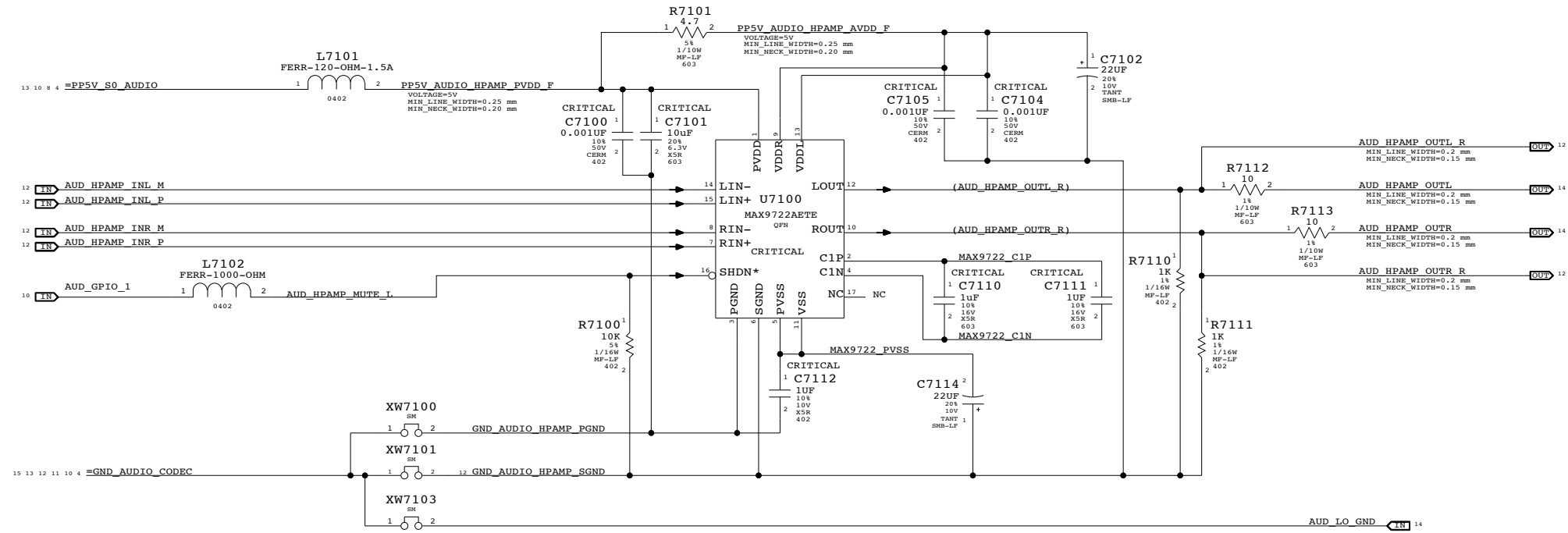
Pseudo-Diff Line-In Filter
 GAIN = -7.1DB AV = 0.44
 FC = 2.4 HZ



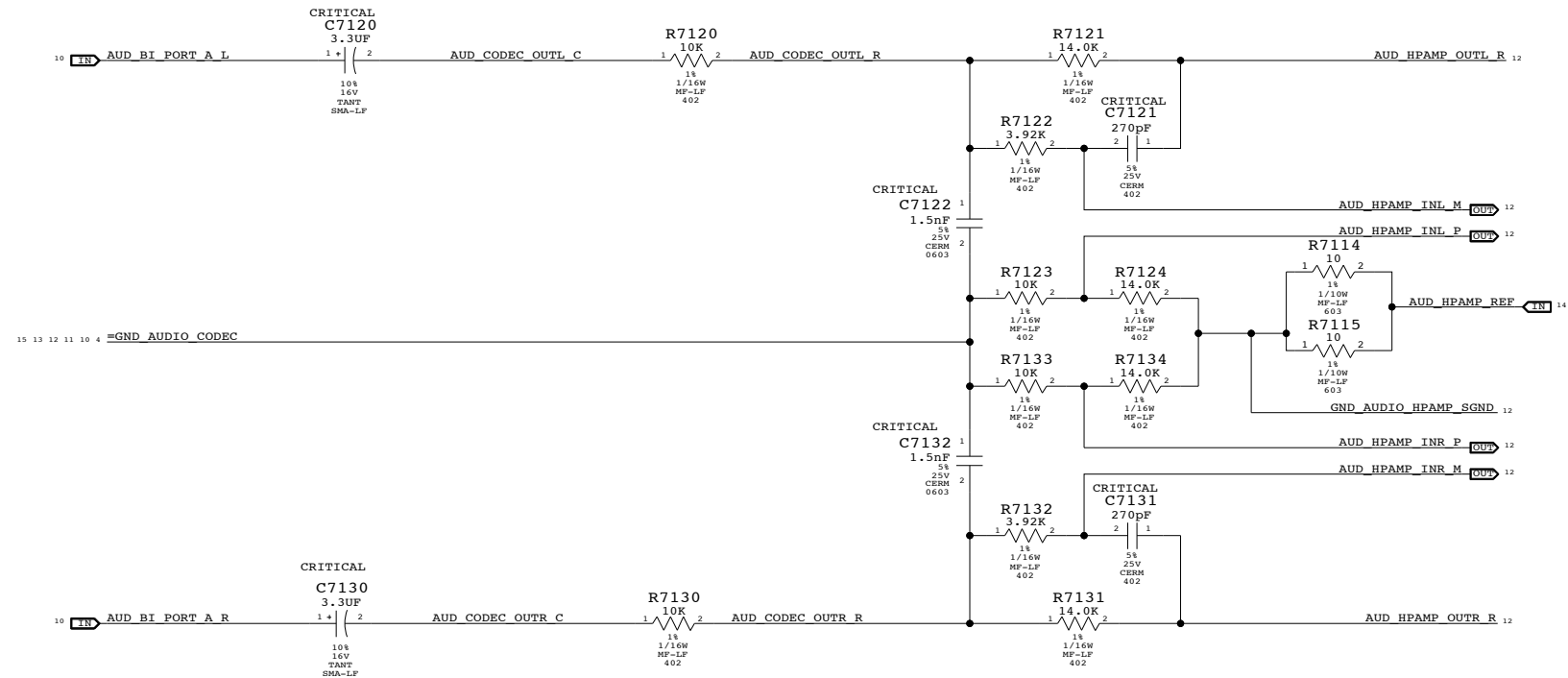
AUDIO: LINE IN
 SYNC_MASTER=AUDIO_M9_PRO_LIO SYNC_DATE=03/29/2006
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	A
SCALE	NONE	SHT	70 OF 104

Headphone Amplifier (MAX9722)
 APN:353S0959
 VOLTAGE GAIN:1.4



2nd Order DAC Filter
 HP:4.8 HZ



AUDIO: HEADPHONE AMP

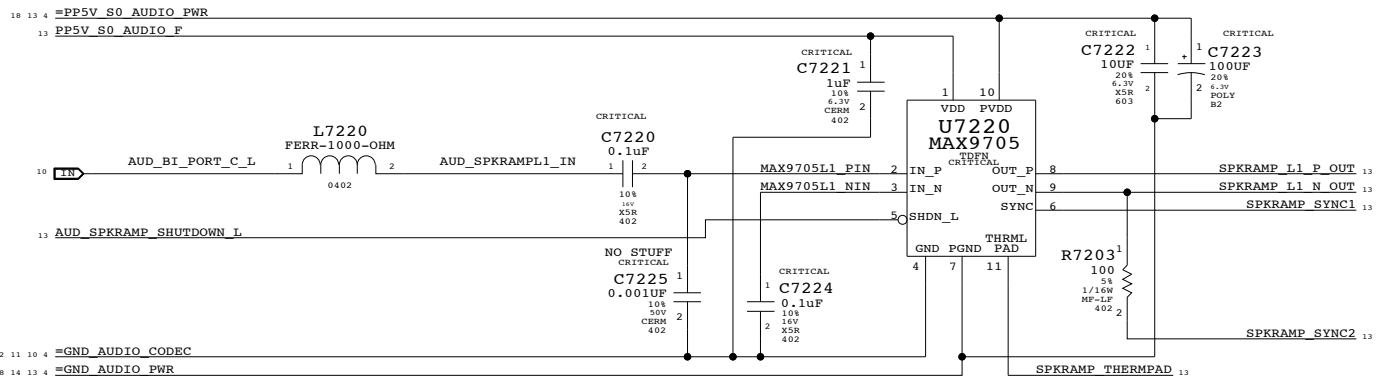
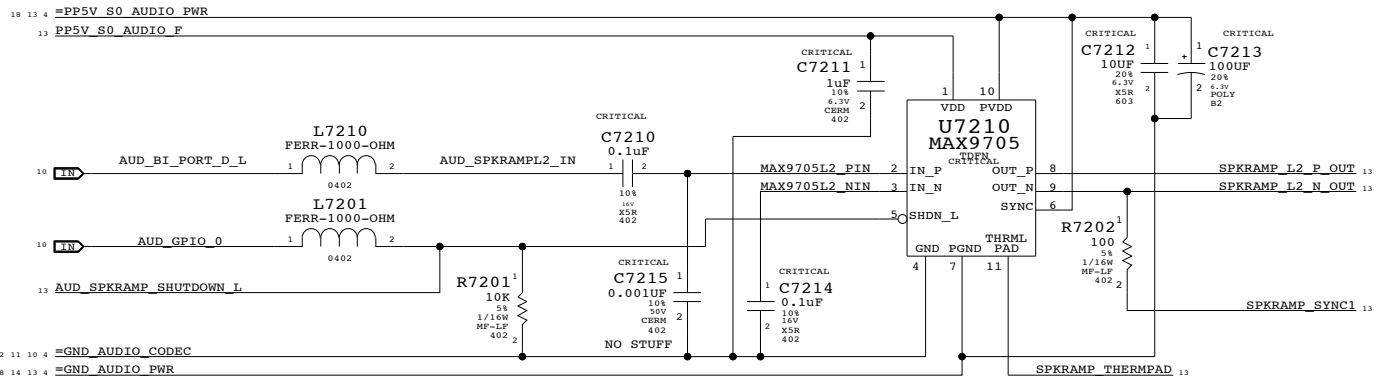
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NOTICE OF PROPRIETARY PROPERTY

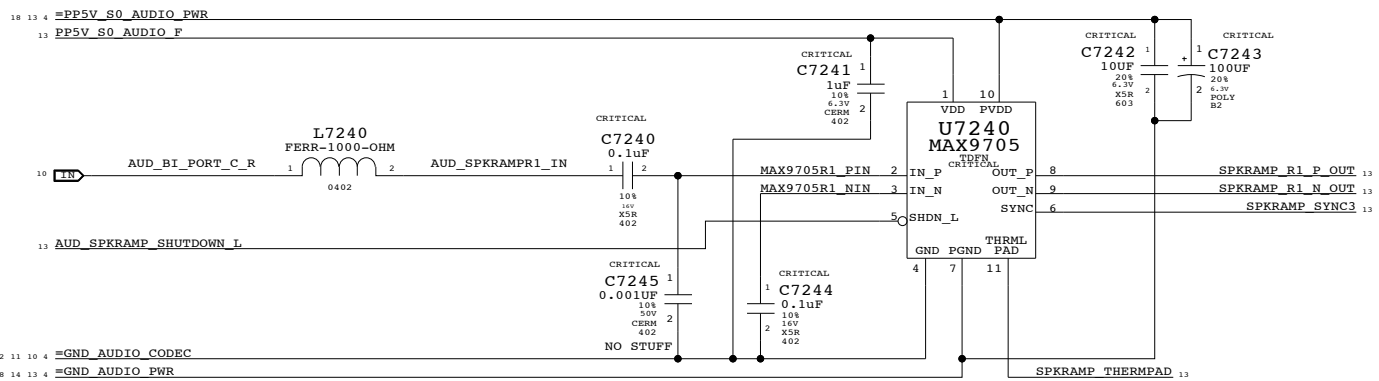
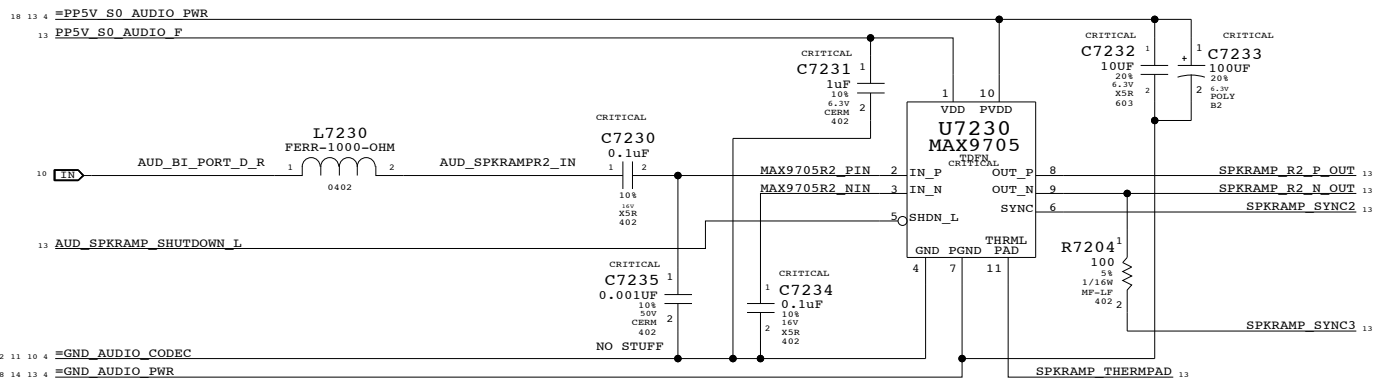
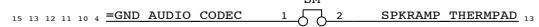
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	A
SCALE	NONE	SHT	71 OF 104

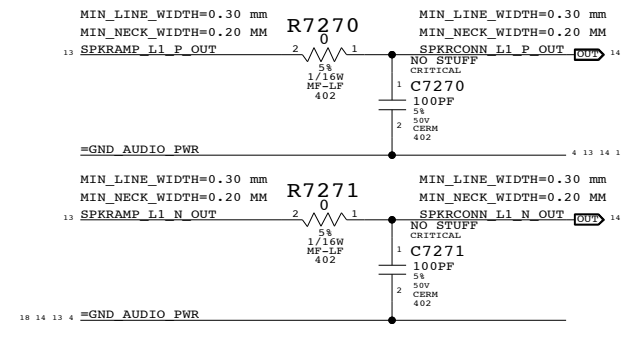
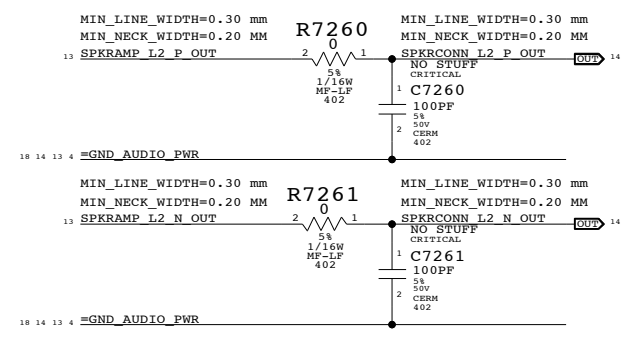
SPEAKER AMPLIFIERS (MAX9705) APN: 353S1355 TURN ON TIME: 30MS
 GAIN = 12DB 80 < FC < 132Hz TURN ON DELAY: 60MS



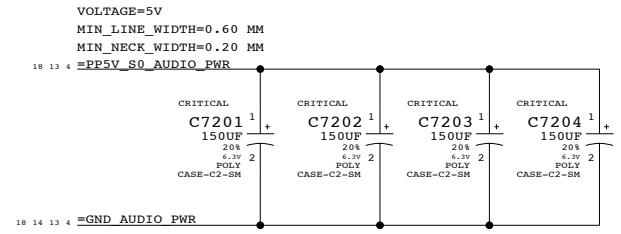
XW7200



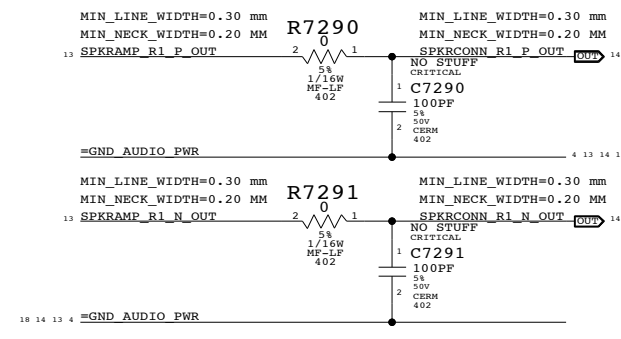
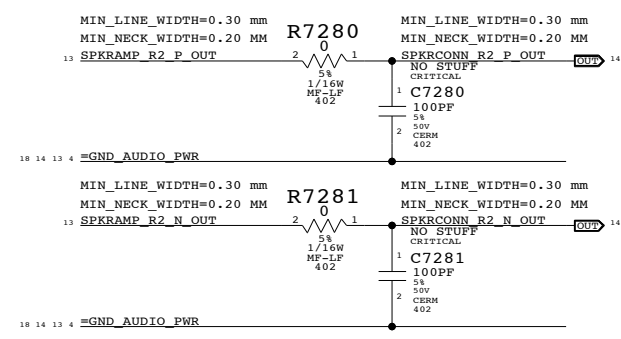
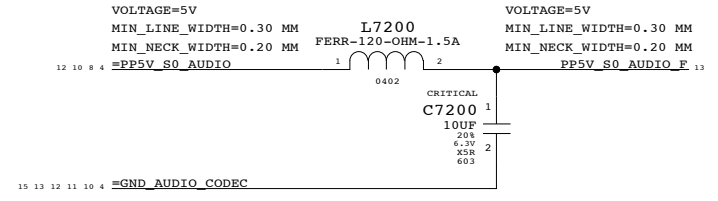
EMI FILTERS FOR AMPLIFIER OUTPUTS



POWER AMPLIFIER SUPPLY BULK CAPS



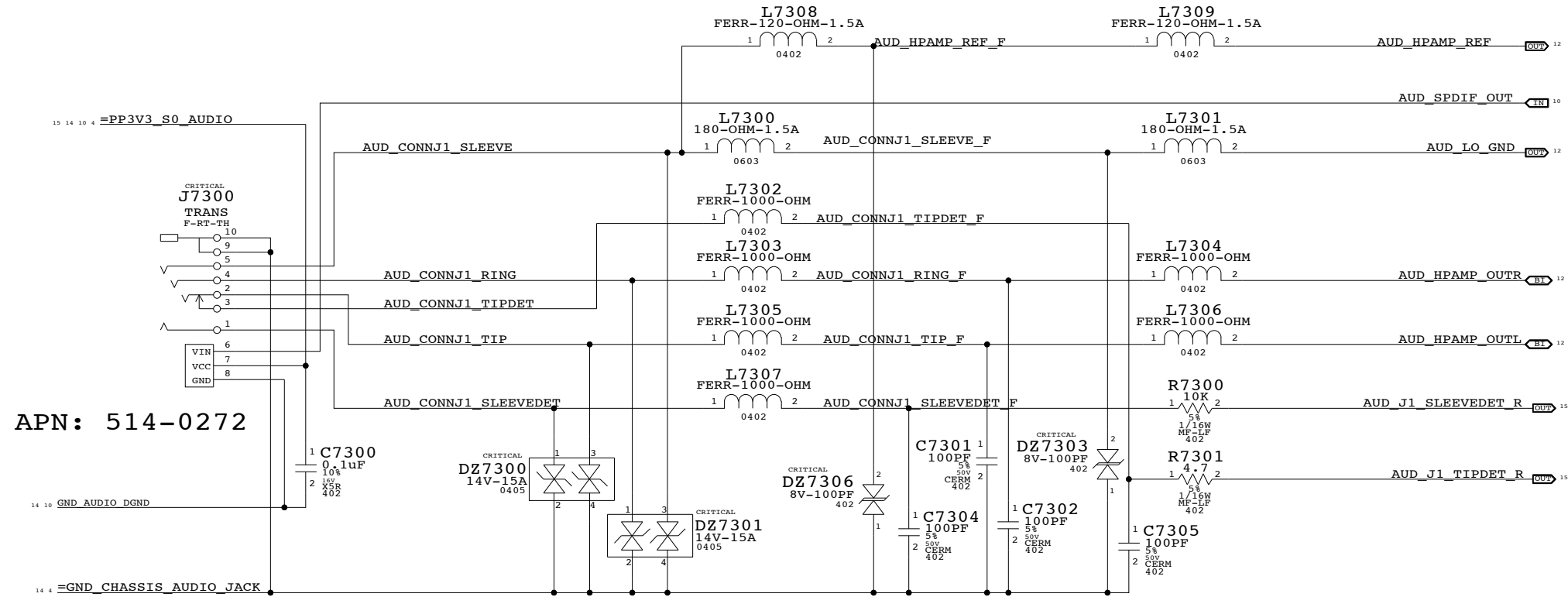
ANALOG POWER RAIL



AUDIO: SPEAKER AMP
 SYNC_MASTER=AUDIO_M9_PRO_LIO SYNC_DATE=03/29/2006
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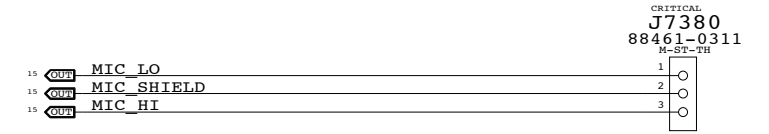
APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	A
SCALE	NONE	SHT	72 OF 104

AUDIO JACK 1 LO/HP CONNECTOR, SPDIF TX

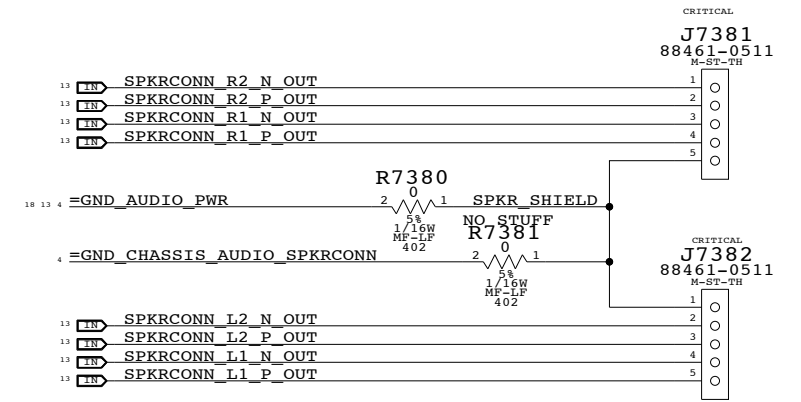


APN: 514-0272

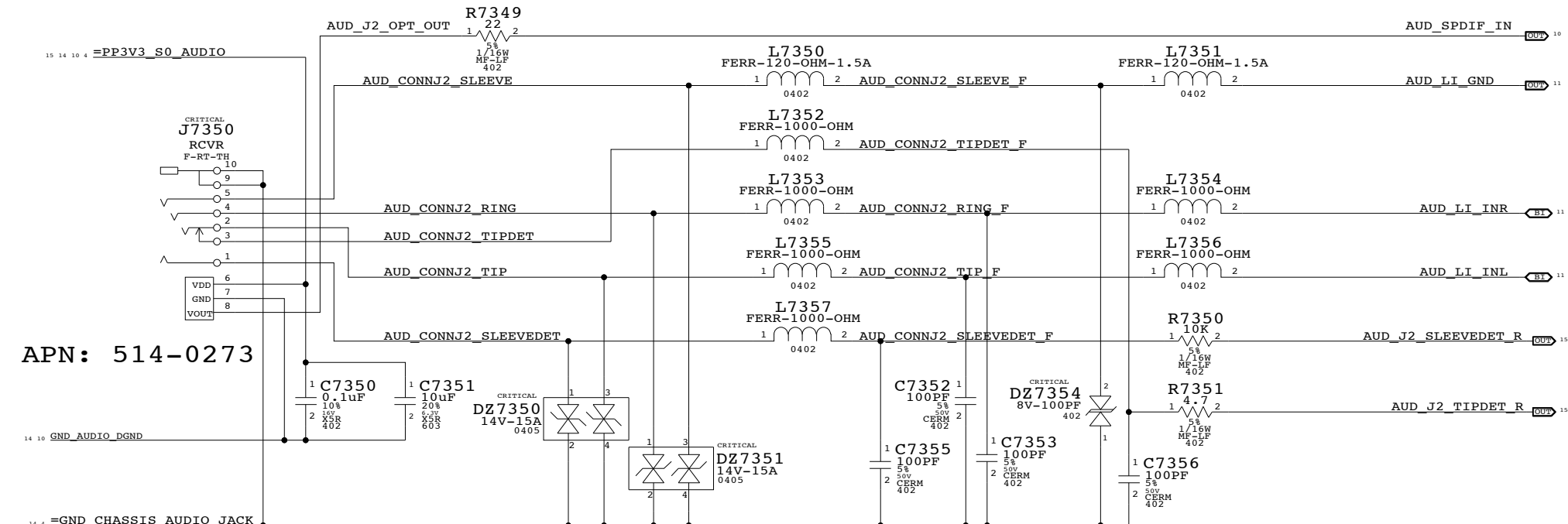
**MIC CONNECTOR
APN: 518-0230**



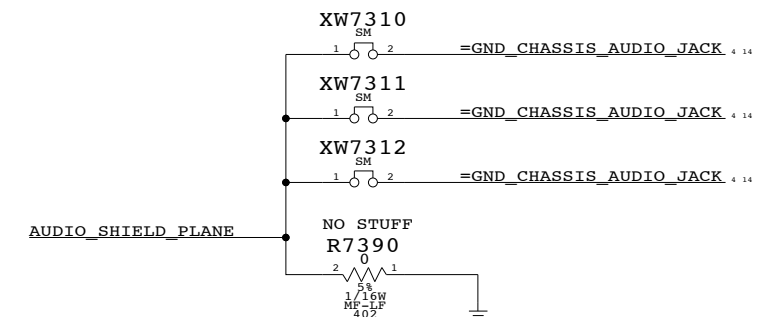
**SPEAKER CONNECTORS
APN: 518-0229**



AUDIO JACK 2 LINE IN CONNECTOR, SPDIF RX



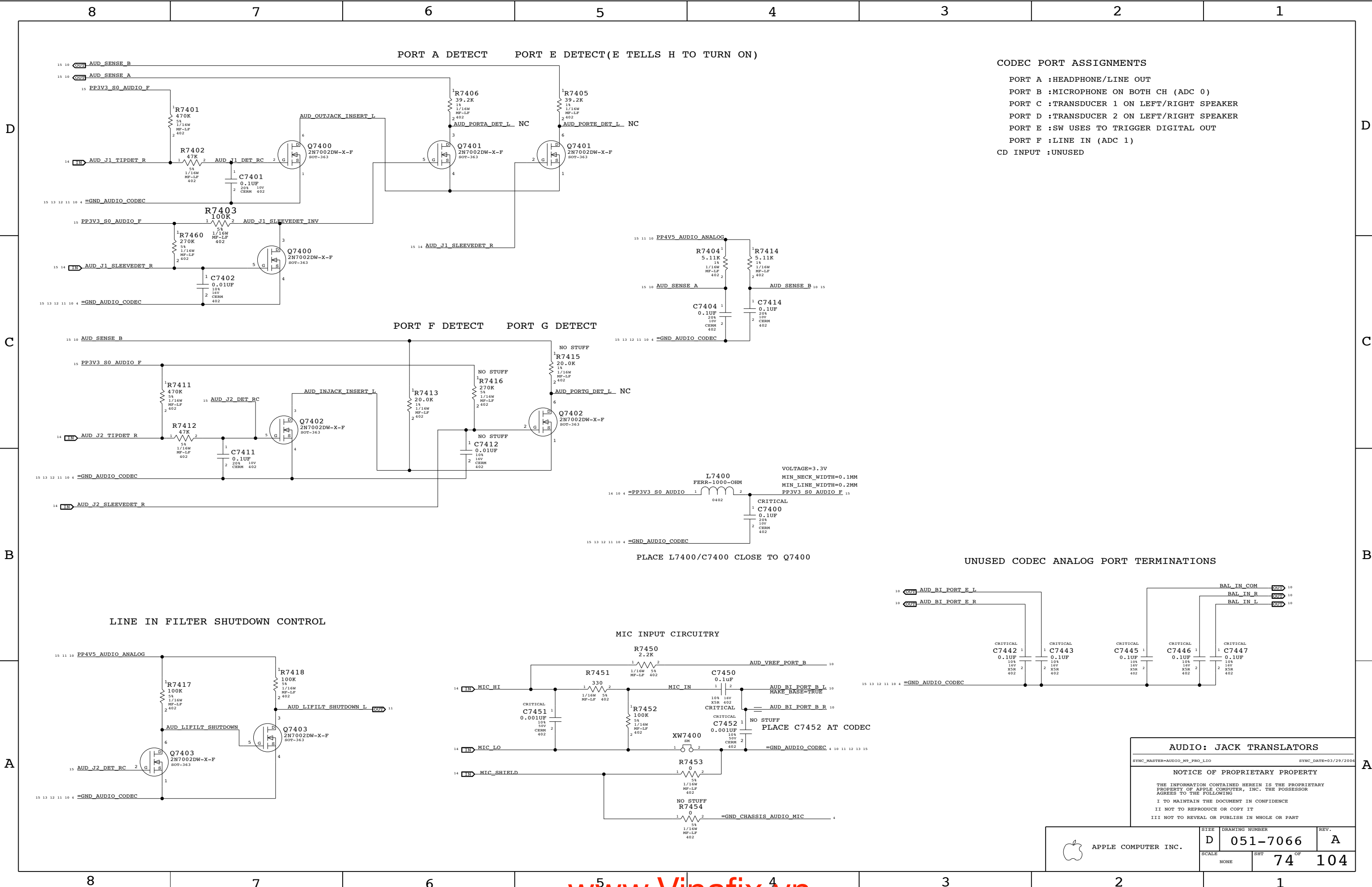
APN: 514-0273



**AUDIO SHIELD
(FILLED SHAPE)**

AUDIO: JACKS
 SYNC_MASTER=AUDIO_M9_PRO_LIO SYNC_DATE=03/29/2006
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	A
SCALE	NONE	SHT	73 OF 104



CODEC PORT ASSIGNMENTS

- PORT A : HEADPHONE/LINE OUT
- PORT B : MICROPHONE ON BOTH CH (ADC 0)
- PORT C : TRANSDUCER 1 ON LEFT/RIGHT SPEAKER
- PORT D : TRANSDUCER 2 ON LEFT/RIGHT SPEAKER
- PORT E : SW USES TO TRIGGER DIGITAL OUT
- PORT F : LINE IN (ADC 1)
- CD INPUT : UNUSED

UNUSED CODEC ANALOG PORT TERMINATIONS

AUDIO: JACK TRANSLATORS

SYNC_MASTER=AUDIO_M9_PRO_L10 SYNC_DATE=03/29/2006

NOTICE OF PROPRIETARY PROPERTY

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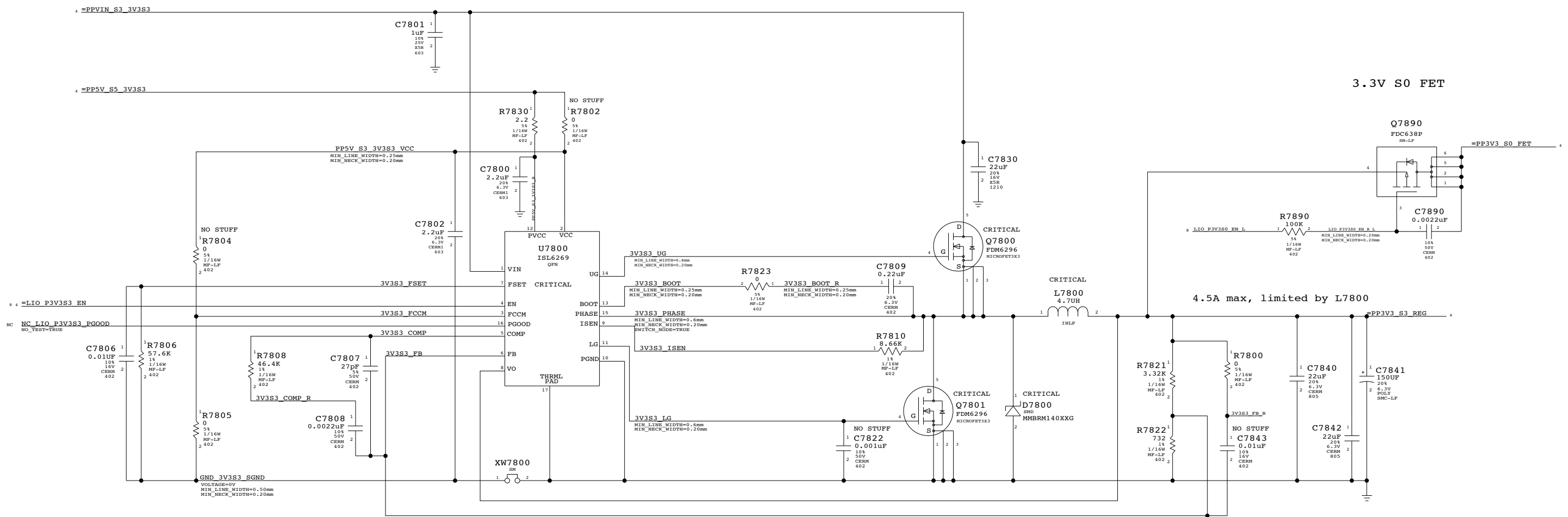
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	A
SCALE	NONE	SHT	74 OF 104

3.3V S3/S0 Power Supply



PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
37680448	37680445		ALL	

3.3V Supply

SYNC_MASTER=BUZZ SYNC_DATE=03/29/2006

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	A
SCALE	SHT	REV.	
NONE	78	104	

8

7

6

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4

3

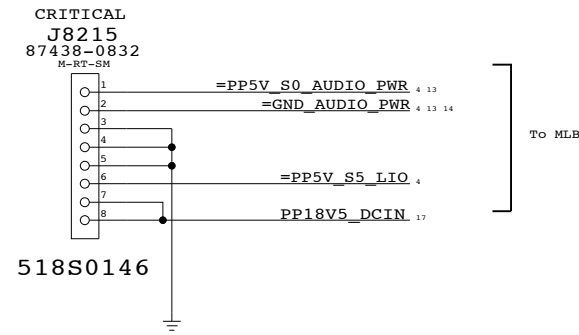
2

1

D

D

Left I/O Power Connector



C

C

B

B

A

A

LEFT I/O POWER CONNECTOR

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7066	A
SCALE	SHT		
NONE	84 OF		104

8

7

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2

1

CHANGE LIST

1/27/06

BEGINNING OF EVT TO EVT2 CHANGES ON ALT_LIO_EVT DIRECTORY
CHANGED PD6401 SYMBOL TO SPACER JEDEC PACK TYPE.
CHANGED REV TO 03.

2/1/06:

PG 82.
CHANGED R8291 TO 24.3K ON 1-WIRE CKT TO MATCH M1 CKT.
ADDED C8207 AND R8292 ON Q8200 PIN 2 TO ADD ESD PROTECTION ON GATE OF Q8200.

2/2/06:

PG. 64.
ADDED ALS SPACER 815-8851 IN BOM TABLE. GOES WITH PD6401.

PG. 78
ADDED 337S0448 AS ALTERNATE FOR 337S0445 ON Q7800 AND Q7801.

2/6/06:

PG. 78
CHANGED R7890 TO 100K FOR <RDAR://PROBLEM/4435222> MOSFET CR: QUAL LIO BOARD 3.3V @ 1.8V
PG 82.
CORRECTED R8292 VALUE TO 1K PER <RDAR://PROBLEM/4426307> M9 EVT SYMPHONY: FLOATING FET GATE ON LIO CONNECTOR SHOULD HAVE ESD PROTECTION.

2/7/06:

PG 3.
MOVE ALL BOM TABLES TO PG 3. CORRECTED APN OF FETS IN ALTERNATE BOM TABLE.

2/9/06:

PG. 78
CHANGED R7810 TO 8.66K TO MATCH M1.

2/10/06:

PG. 51
ADDED R5103, C5105 AND R5104, C5104 TO USB_LEFT_OC_L AND USB_LEFT2_OC_L TO MATCH M1.

2/23/06:

REMOVE LEMENU BOMOPTION FROM CODEC. REMOVE BOM NUMBERS TABLE ALONG WITH LE_MENU & PROJ_PARTS BOMS. FLAT BOM NOW.
ADDED CRITICAL ATTRIBUTES TO Q5101, Q6401, U6401, DZ7303, DZ7306, DZ7354, Q8200, Q8201, Q8209
SYNCED FROM M1_LIO_MOSFET REV A.0.0
===CHANGED===

C7830 [ON PAGE(S) 78] CHANGED FROM CAP_1210-22UF,20%,16V,X7R TO CAP_1210-22UF,20%,16V,X5R
C7840 [ON PAGE(S) 78] CHANGED FROM CAP_805-22UF,20%,6.3V,X5R TO CAP_805-22UF,20%,6.3V,CERM
C7842 [ON PAGE(S) 78] CHANGED FROM CAP_805-22UF,20%,6.3V,X5R TO CAP_805-22UF,20%,6.3V,CERM

3/29/06:

PG. 82
CHANGED J8200 FROM 514-0282 TO 514-0348.

HISTORY- NON-AUDIO		
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SCALE	SHT		OF
NONE	100		104

CHANGE LIST


- OCT 19 2005 : INITIAL RELEASE
- OCT 26 2005 : CHANGE ALL SPEAKER OUTPUT INDUCTORS TO 0 OHM SHORTING RESISTORS
ADDED OPTIONAL SHORTING RESISTORS FROM AUDIO JACKS TO CHASSIS GROUND
- OCT 27 2005 : MOVE ESD DIODE NEARER TO JACK, DAISY CHAIN SYNC PIN ON SPEAKER AMPLIFIER
FLIP SPEAKER CONNECTOR PIN ASSIGNMENT TO ACCOMODATE CABLE ROUTING
- NOV 03 2005 : CHANGE SPEAKER CONNECTOR J7380/J7381 TO 518S0053
- NOV 03 2005 : CHANGE SPEAKER CONNECTOR J7380/J7381 TO 518S0215(WHITE) AND 518S0316(BLACK)
- NOV 04 2005 : CHANGE SPEAKER CONNECTOR J7380/J7381 TO 518S0053
- NOV 07 2005 : ADDED PAGE 70, INPUT FILTER & 71, HEADPHONE AMPLIFIER
- DEC 01 2005 : CHANGE PIN OUT OF MIC CONNECTOR
- DEC 05 2005 : CHANGE MIC CONNECTOR TO APN 518-0152
- DEC 06 2005 : CHANGE Q7403 CIRCUITRY, ADDED R7418, REMOVED R7407
CHANGE BOTH AUDIO 5V S0 RAIL TO S5
- DEC 07 2005 : CHANGE APN OF C7301, C7302, C7303 & C7304 TO 128S0081 TO REDUCE HEIGHT
CHANGE PIN OUT OF MIC CONNECTOR TO MATCH SIREN PROTO
- DEC 08 2005 : UPDATE SYMBOL FOR 128S0081 TO MATCH LATEST LIBRARY SYMBOL
CHANGE BOTH AUDIO 5V S5 RAIL TO S0
- DEC 20 2005 : ADDED CRITICAL ATTRIBUTE TO CONNECTORS
CHANGE R7100 TO 10K
- DEC 23 2005 : REPLACE R7114/R7115 WITH XW7103, REMOVE XW7102, CHANGE C7112 TO 0402 10V
CHANGE L7300/L7301 TO LOWER DCR 0603 FERRITE, CHANGE R7112/13 FROM 14 TO 10 OHMS
REMOVE STUFFING OPTION FOR ALC882 CODEC, C6850/51/52, R6850/51/53/54
REPLACE R6801 WITH XW6801, ISOLATED AUDIO DIGITAL GND THROUGH XW6800
- JAN 02 2006 : ADD "NC " PREFIX TO AUD GPIO 2, VOL UP, VOL DOWN NETS, CHANGE C7112 TO 138S0578
ADD ALTERNATE BOM TABLE FOR CONNECTORS J7380, J7381 AND J7382
- JAN 05 2006 : ADD D6800 TO PROVIDE DISCHARGE PATH FOR BULK CAPS ON 4.5V POWER
CHANGE D6825 FROM 1UF TO 15PF TO PREVENT PREMATURE FAILURE OF VR6800
- JAN 06 2006 : ADD NO STUFF BOMOPTION TO D6800
- JAN 20 2006 : ADD R6809 AS A PULL DOWN ON SPDIF OUTPUT TO HOLD NET IN INACTIVE STATE BY DEFAULT
CHANGE CONNECTION FOR D6800
- JAN 25 2006 : ADD L7000, REMOVE L6802/L6803
- JAN 26 2006 : REMOVE R7320, R7321, R7323, ADD L7308, C7452, C7306, R7114, R7115
- JAN 27 2006 : ADDED C7215, C7225, C7235, C7245 FOR HF IMMUNITY
- JAN 30 2006 : CHANGED PINOUT OF MIC CONNECTOR TO MATCH MIC ASSEMBLY
- JAN 31 2006 : ADDED XW6802 TO PROVIDE RETURN PATH FOR AZALIA BUS SIGNALS (EMI)
ADDED L7309 FOR HEADPHONE AMP REFERENCE TAP POINT
- FEB 03 2006 : CHANGED C7306, C7303 & C7354 TO DZ7306, DZ7303 & DZ7354 TO SOLVE ESD ISSUE
ADDED C7001-C7004 TO REDUCE NOISE LEVEL ON LINE-IN BUFFER VREF
REMOVE R6809, C6832. REPLACED BY C7001-C7004
- FEB 06 2006 : CHANGED XW6802 FROM LAYER 8 TO LAYER 9 SHORT DUE TO TECHNICAL LIMITATION ALLEGRO
- FEB 07 2006 : "NO STUFF" C7452, C7215, C7225, C7235 & C7245
- FEB 15 2006 : CHANGE VALUE OF C7120 & C7130 FROM 10UF TO 3.3UF TO REDUCE INTENSITY OF CLICK DURING UNMUTE.
THIS MOVES THE CORNER FREQUENCY FROM 1.6HZ TO 4.8HZ.
- MAR 29 2006 : ADDED ALTERNATE BOM TABLE FOR CODEC. 353S1458 IS SCREENED VERSION OF 353S1345.

HISTORY- AUDIO

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SCALE	SHT	OF	
NONE	101	104	

8	7	6	5	4	3	2	1
<p>Title: Basenet Report Design: alt_lio Date: Mar 29 13:32:22 2006</p> <p>Base nets and synonyms for alt_lio.lib.ALT_LIO (@alt_lio.lib.alt_lio(sch_1)) Base Signal Synonyms Location((Zone)[dir])</p> <p>3V3S3_BOOT 3V3S3_BOOT - @alt_lio.lib.ALT_LIO 16C5 3V3S3_BOOT_R 3V3S3_BOOT_R - @alt_lio.lib.ALT_LIO 16C5 3V3S3_COMP 3V3S3_COMP - @alt_lio.lib.ALT_LIO 16B6 3V3S3_COMP_R 3V3S3_COMP_R - @alt_lio.lib.ALT_LIO 16B7 3V3S3_FB 3V3S3_FB - @alt_lio.lib.ALT_LIO 16B6 3V3S3_FB_R 3V3S3_FB_R - @alt_lio.lib.ALT_LIO 16B2 3V3S3_FCCM 3V3S3_FCCM - @alt_lio.lib.ALT_LIO 16B6 3V3S3_FSET 3V3S3_FSET - @alt_lio.lib.ALT_LIO 16C6 3V3S3_ISEN 3V3S3_ISEN - @alt_lio.lib.ALT_LIO 16B5 3V3S3_LG 3V3S3_LG - @alt_lio.lib.ALT_LIO 16B5 3V3S3_PHASE 3V3S3_PHASE - @alt_lio.lib.ALT_LIO 16B5 3V3S3_UG 3V3S3_UG - @alt_lio.lib.ALT_LIO 16C5 5V_REG_IN 5V_REG_IN - @alt_lio.lib.ALT_LIO 10A4 =EXCARD_CLKREQ_L - @alt_lio.lib.ALT_LIO 4C5 8C6 =EXCARD_CLKREQ_L - @alt_lio.lib.ALT_LIO 4C4 8B2 =EXCARD_OC_L - @alt_lio.lib.ALT_LIO 4C5 8C6 =EXCARD_OC_L - @alt_lio.lib.ALT_LIO 4C4 8B4 =GND_AUDIO =GND_AUDIO - @alt_lio.lib.ALT_LIO 4A8 8B6 =GND_AUDIO_CODEC - @alt_lio.lib.ALT_LIO 4A7 10A6 10B6 10D2 11B2 11B7 12B6 12C6 13A8 13B2 13B6 13B8 13C8 13C8 15A3 15A4 15A8 15B5 15B8 15C5 15C8 15D8 =GND_AUDIO_PWR =GND_AUDIO_PWR - @alt_lio.lib.ALT_LIO 4A7 10A6 10B6 10D2 11B2 11B7 12B6 12C6 13A8 13B2 13B6 13B8 13C8 13C8 15A3 15A4 15A8 15B5 15B8 15C5 15C8 15D8 =GND_CHASSIS_AUDIO_JACK =GND_CHASSIS_AUDIO_JACK - @alt_lio.lib.ALT_LIO 4A7 14A8 14B1 14B1 14B1 14B1 14B8 =GND_CHASSIS_AUDIO_MIC =GND_CHASSIS_AUDIO_MIC - @alt_lio.lib.ALT_LIO 4A7 15A4 =GND_CHASSIS_GND2 =GND_CHASSIS_GND2 - @alt_lio.lib.ALT_LIO 4A8 =GND_CHASSIS_AUDIO_MIC =GND_CHASSIS_AUDIO_MIC - @alt_lio.lib.ALT_LIO 4A7 15A4 =GND_CHASSIS_AUDIO_SPKRCONN =GND_CHASSIS_AUDIO_SPKRCONN - @alt_lio.lib.ALT_LIO 4A7 14C3 =GND_CHASSIS_DCIN_JACK =GND_CHASSIS_DCIN_JACK - @alt_lio.lib.ALT_LIO 4A7 17C8 =GND_CHASSIS_LTUSB =GND_CHASSIS_LTUSB - @alt_lio.lib.ALT_LIO 4A7 5A2 5C2 =GND_CHASSIS_GND1 =GND_CHASSIS_GND1 - @alt_lio.lib.ALT_LIO 4A8 =GND_CHASSIS_LTUSB =GND_CHASSIS_LTUSB - @alt_lio.lib.ALT_LIO 4A7 5A2 5C2 =GND_CHASSIS_LIO_FLEX =GND_CHASSIS_LIO_FLEX - @alt_lio.lib.ALT_LIO 4A7 8B3 =GND_CHASSIS_PCIE_EXCARD =GND_CHASSIS_PCIE_EXCARD - @alt_lio.lib.ALT_LIO 4A7 6B2 =GND_CHASSIS_GND4 =GND_CHASSIS_GND4 - @alt_lio.lib.ALT_LIO 4A8 =GND_CHASSIS_PCIE_EXCARD =GND_CHASSIS_PCIE_EXCARD - @alt_lio.lib.ALT_LIO 4A7 6B2 =LIO_P3V3S3_EN =LIO_P3V3S3_EN - @alt_lio.lib.ALT_LIO 4C4 8C6 16B8 =USB_LEFT_PWRON =USB_LEFT_PWRON - @alt_lio.lib.ALT_LIO 4C2 5C8 =LIO_P3V3S3_EN =LIO_P3V3S3_EN - @alt_lio.lib.ALT_LIO 4C3 =USB_LEFT_PWRON =USB_LEFT_PWRON - @alt_lio.lib.ALT_LIO 4C2 5C8 =L72USB_OC_L =L72USB_OC_L - @alt_lio.lib.ALT_LIO 4A3 8C6 =L72USB_OC_L =L72USB_OC_L - @alt_lio.lib.ALT_LIO 4A5 5B8 =LTUSB_OC_L =LTUSB_OC_L - @alt_lio.lib.ALT_LIO 4A3 8C6 =USB_LEFT_OC_L =USB_LEFT_OC_L - @alt_lio.lib.ALT_LIO 4A5 5B8 =MINI_CLKREQ_L =MINI_CLKREQ_L - @alt_lio.lib.ALT_LIO 4B5 8C6 =MINI_CLKREQ_L =MINI_CLKREQ_L - @alt_lio.lib.ALT_LIO 4B4 7C6 =PCIE_CLK100M_EXCARD =PCIE_CLK100M_EXCARD_N - @alt_lio.lib.ALT_LIO 4D5 8B1 =PCIE_CLK100M_EXCARD =PCIE_CLK100M_EXCARD_N - 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@alt_lio.lib.ALT_LIO 4D7 =PPVIN_S3_3V3S3 =PPVIN_S3_3V3S3 - @alt_lio.lib.ALT_LIO 4D6 16D8 =SMBUS_EXCARD_SCL =SMBUS_EXCARD_SCL - @alt_lio.lib.ALT_LIO 4A2 6C3 =SMBUS_EXCARD_SCL =SMBUS_EXCARD_SCL - @alt_lio.lib.ALT_LIO 4A4 8B6 =SMBUS_EXCARD_SDA =SMBUS_EXCARD_SDA - @alt_lio.lib.ALT_LIO 4A2 =SMBUS_EXCARD_SDA =SMBUS_EXCARD_SDA - @alt_lio.lib.ALT_LIO 4A4 6C3 =SMBUS_EXCARD_SDA =SMBUS_EXCARD_SDA - @alt_lio.lib.ALT_LIO 4A5 8B6 =SMBUS_EXCARD_SDA =SMBUS_EXCARD_SDA - @alt_lio.lib.ALT_LIO 4A4 =SMBUS_MINI_SCL =SMBUS_MINI_SCL - @alt_lio.lib.ALT_LIO 4A2 7B3 =SMBUS_MINI_SCL =SMBUS_MINI_SCL - @alt_lio.lib.ALT_LIO 4A3 =SMBUS_MINI_SDA =SMBUS_MINI_SDA - @alt_lio.lib.ALT_LIO 4A3 7B3 =SMBUS_MINI_SDA =SMBUS_MINI_SDA - @alt_lio.lib.ALT_LIO 4A4 =SMBUS_MINI_SDA =SMBUS_MINI_SDA - @alt_lio.lib.ALT_LIO 4A4 =USB2_EXCARD_N =USB2_EXCARD_N - @alt_lio.lib.ALT_LIO 4D5 8C3 =USB2_EXCARD_N =USB2_EXCARD_N - @alt_lio.lib.ALT_LIO 4D2 6C3 =USB2_EXCARD_P =USB2_EXCARD_P - @alt_lio.lib.ALT_LIO 4D5 8C3 =USB2_EXCARD_P =USB2_EXCARD_P - @alt_lio.lib.ALT_LIO 4D2 6C3 =USB2_LT2_N =USB2_LT2_N - @alt_lio.lib.ALT_LIO 4A3 8C3 =USB2_LT2_P =USB2_LT2_P - @alt_lio.lib.ALT_LIO 4A5 5C5 =USB2_LT2_P =USB2_LT2_P - @alt_lio.lib.ALT_LIO 4A3 8C3 =USB2_LT_N =USB2_LT_N - @alt_lio.lib.ALT_LIO 4A3 8C3 =USB2_LT_N =USB2_LT_N - @alt_lio.lib.ALT_LIO 4A5 5B5 =USB2_LT_P =USB2_LT_P - @alt_lio.lib.ALT_LIO 4B3 8C3 =USB2_LT_P =USB2_LT_P - @alt_lio.lib.ALT_LIO 4B5 5B5 =AC2_BITCLK =AC2_BITCLK - @alt_lio.lib.ALT_LIO 8B6 10D7 =AC2_RST_L =AC2_RST_L - @alt_lio.lib.ALT_LIO 8B6 10C7 =AC2_RST_L =AC2_RST_L - @alt_lio.lib.ALT_LIO 8B6 10C7 =AC2_SDATAIN<0> =AC2_SDATAIN<0> - @alt_lio.lib.ALT_LIO 8B6 10C7 =AC2_SDATAOUT =AC2_SDATAOUT - @alt_lio.lib.ALT_LIO 8C6 10D7 =AC2_SYNC =AC2_SYNC - @alt_lio.lib.ALT_LIO 8B6 10D7 =ADAPTER_SENSE =ADAPTER_SENSE - @alt_lio.lib.ALT_LIO 17B6 =ADAPTER_SENSE_UF =ADAPTER_SENSE_UF - @alt_lio.lib.ALT_LIO 17D7 =AUDIO_SHIELD_PLANE =AUDIO_SHIELD_PLANE - @alt_lio.lib.ALT_LIO 14B3 =AUD_4V5_SHDN_L =AUD_4V5_SHDN_L - @alt_lio.lib.ALT_LIO 10A5</p>	<p>AUD_ANALOG_FILT_1 AUD_ANALOG_FILT_1 - @alt_lio.lib.ALT_LIO 10C4 AUD_ANALOG_FILT_2 AUD_ANALOG_FILT_2 - @alt_lio.lib.ALT_LIO 10C4 AUD_BI_PORT_A_L AUD_BI_PORT_A_L - @alt_lio.lib.ALT_LIO 10C1 12B7 AUD_BI_PORT_A_R AUD_BI_PORT_A_R - @alt_lio.lib.ALT_LIO 10C1 12A7 AUD_BI_PORT_B_L AUD_BI_PORT_B_L - @alt_lio.lib.ALT_LIO 10C1 15A4 AUD_BI_PORT_B_R AUD_BI_PORT_B_R - @alt_lio.lib.ALT_LIO 10C1 15A4 AUD_BI_PORT_C_L AUD_BI_PORT_C_L - @alt_lio.lib.ALT_LIO 10C7 13C8 AUD_BI_PORT_C_R AUD_BI_PORT_C_R - @alt_lio.lib.ALT_LIO 10C7 13A8 AUD_BI_PORT_D_L AUD_BI_PORT_D_L - @alt_lio.lib.ALT_LIO 10C7 13D8 AUD_BI_PORT_D_R AUD_BI_PORT_D_R - @alt_lio.lib.ALT_LIO 10C7 13B8 AUD_BI_PORT_E_L AUD_BI_PORT_E_L - @alt_lio.lib.ALT_LIO 10C1 15B3 AUD_BI_PORT_E_R AUD_BI_PORT_E_R - @alt_lio.lib.ALT_LIO 10C1 15B3 AUD_BI_PORT_F_L AUD_BI_PORT_F_L - @alt_lio.lib.ALT_LIO 10C1 11C2 AUD_BI_PORT_F_R AUD_BI_PORT_F_R - @alt_lio.lib.ALT_LIO 10C1 11B2 AUD_BYPASS AUD_BYPASS - @alt_lio.lib.ALT_LIO 10C4 AUD_CODEC_INREF AUD_CODEC_INREF - @alt_lio.lib.ALT_LIO 11B3 AUD_CODEC_OUTL_C AUD_CODEC_OUTL_C - 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@alt_lio.lib.ALT_LIO 11C4</p>	<p>AUD_LIFILT_LT_R AUD_LIFILT_LT_R - @alt_lio.lib.ALT_LIO 11C5 AUD_LIFILT_RT AUD_LIFILT_RT - @alt_lio.lib.ALT_LIO 11B4 AUD_LIFILT_RT_R AUD_LIFILT_RT_R - @alt_lio.lib.ALT_LIO 11B5 AUD_LIFILT_SHUTDOWN AUD_LIFILT_SHUTDOWN - @alt_lio.lib.ALT_LIO 15A8 AUD_LIFILT_SHUTDOWN_L AUD_LIFILT_SHUTDOWN_L - @alt_lio.lib.ALT_LIO 11C2 15A6 AUD_LI_GND AUD_LI_GND - @alt_lio.lib.ALT_LIO 11C7 14B3 AUD_LI_INL AUD_LI_INL - @alt_lio.lib.ALT_LIO 11C7 14A3 AUD_LI_INL_C AUD_LI_INL_C - @alt_lio.lib.ALT_LIO 11C5 AUD_LI_INL_C_R AUD_LI_INL_C_R - @alt_lio.lib.ALT_LIO 11C4 AUD_LI_INR AUD_LI_INR - @alt_lio.lib.ALT_LIO 11B7 14B3 AUD_LI_INR_C AUD_LI_INR_C - @alt_lio.lib.ALT_LIO 11B5 AUD_LI_INR_C_R AUD_LI_INR_C_R - @alt_lio.lib.ALT_LIO 11B4 AUD_LO_GND AUD_LO_GND - @alt_lio.lib.ALT_LIO 12C2 14D3 AUD_OUTJACK_INSERT_L AUD_OUTJACK_INSERT_L - @alt_lio.lib.ALT_LIO 15D7 AUD_PORTA_DET_L AUD_PORTA_DET_L - @alt_lio.lib.ALT_LIO 15D5 AUD_PORTA_DET_R AUD_PORTA_DET_R - @alt_lio.lib.ALT_LIO 15D5 AUD_PORTF_DET_L AUD_PORTF_DET_L - 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@alt_lio.lib.ALT_LIO 10C7 15B1 BEEP BEEP - @alt_lio.lib.ALT_LIO 10C6 EXCARD_CLKREQ_CONN EXCARD_CLKREQ_CONN - @alt_lio.lib.ALT_LIO 6B3 EXCARD_CLKREQ_CONN_L EXCARD_CLKREQ_CONN_L - @alt_lio.lib.ALT_LIO 6A4 6C3 EXCARD_CPPE_L EXCARD_CPPE_L - @alt_lio.lib.ALT_LIO 6A8 6C3 6C3 EXCARD_CPPE_L EXCARD_CPPE_L - @alt_lio.lib.ALT_LIO 6A8 6C3 6C3 EXCARD_CPUSB_L EXCARD_CPUSB_L - @alt_lio.lib.ALT_LIO 6A8 6C3 6C3 EXCARD_OC_OUT_L EXCARD_OC_OUT_L - @alt_lio.lib.ALT_LIO 6A6 EXCARD_PRESENT EXCARD_PRESENT - @alt_lio.lib.ALT_LIO 6A6 EXCARD_RCLKEN EXCARD_RCLKEN - @alt_lio.lib.ALT_LIO 6B3 6C5 EXCARD_SHDN_L_R EXCARD_SHDN_L_R - @alt_lio.lib.ALT_LIO 6C6 GND_3V3S3_SGND GND_3V3S3_SGND - @alt_lio.lib.ALT_LIO 16B7 GND_ADAPTER_UP GND_ADAPTER_UP - @alt_lio.lib.ALT_LIO 17D7 GND_AUDIO_DGND GND_AUDIO_DGND - @alt_lio.lib.ALT_LIO 10A6 10A7 10B6 10B7 10D7 GND_AUDIO_HPAMP_PGND GND_AUDIO_HPAMP_PGND - @alt_lio.lib.ALT_LIO 12C5 GND_AUDIO_HPAMP_SGND GND_AUDIO_HPAMP_SGND - @alt_lio.lib.ALT_LIO 12A3 12C5 LEFT_ALS_GAIN_SW LEFT_ALS_GAIN_SW - @alt_lio.lib.ALT_LIO 8C6 9C4 LEFT_ALS_GAIN_SW_R LEFT_ALS_GAIN_SW_R - @alt_lio.lib.ALT_LIO 9C5 LEFT_ALS_OP_COMP LEFT_ALS_OP_COMP - @alt_lio.lib.ALT_LIO 9C6 LEFT_ALS_OP_IN LEFT_ALS_OP_IN - @alt_lio.lib.ALT_LIO 9C5 LEFT_ALS_OUT LEFT_ALS_OUT - @alt_lio.lib.ALT_LIO 8C6 9C4 LEFT_ALS_OUT_FB LEFT_ALS_OUT_FB - @alt_lio.lib.ALT_LIO 9C5 LEFT_GAIN_SETTING LEFT_GAIN_SETTING - @alt_lio.lib.ALT_LIO 9C5 LEFT_PHOTODIODE LEFT_PHOTODIODE - @alt_lio.lib.ALT_LIO 9C6 LIO_P3V3S0_EN_L LIO_P3V3S0_EN_L - @alt_lio.lib.ALT_LIO 8C6 16C3 LIO_P3V3S0_EN_R_L LIO_P3V3S0_EN_R_L - @alt_lio.lib.ALT_LIO 16C2 LIO_PLT_RESET_L LIO_PLT_RESET_L - @alt_lio.lib.ALT_LIO 4C3 8C6 LIO_PLT_RESET_L LIO_PLT_RESET_L - @alt_lio.lib.ALT_LIO 4C4 6C7 7C3 MAX9705L1_NIN MAX9705L1_NIN - @alt_lio.lib.ALT_LIO 13C6 MAX9705L1_PIN MAX9705L1_PIN - @alt_lio.lib.ALT_LIO 13C6 MAX9705L2_NIN MAX9705L2_NIN - @alt_lio.lib.ALT_LIO 13D6 MAX9705L2_PIN MAX9705L2_PIN - @alt_lio.lib.ALT_LIO 13D6 MAX9705R1_NIN MAX9705R1_NIN - @alt_lio.lib.ALT_LIO 13A6 MAX9705R1_PIN MAX9705R1_PIN - @alt_lio.lib.ALT_LIO 13A6 MAX9705R2_NIN MAX9705R2_NIN - @alt_lio.lib.ALT_LIO 13B6 MAX9705R2_PIN MAX9705R2_PIN - @alt_lio.lib.ALT_LIO 13B6 MAX9722_C1N MAX9722_C1N - @alt_lio.lib.ALT_LIO 12C4 MAX9722_C1P MAX9722_C1P - @alt_lio.lib.ALT_LIO 12C4</p>				

	8	7	6	5	4	3	2	1
D	MAX9722_PVSS	MAX9722_PVSS - #alt_l1o.lib.ALT_L1O	12C4					
	MIC_HI	MIC_HI - #alt_l1o.lib.ALT_L1O	14D3 15A6	SPKRCONN_L1_P_OUT	#alt_l1o.lib.ALT_L1O			13C3 14C3
	MIC_IN	MIC_IN - #alt_l1o.lib.ALT_L1O	15A5	SPKRCONN_L2_N_OUT	#alt_l1o.lib.ALT_L1O			13D3 14C3
	MIC_LO	MIC_LO - #alt_l1o.lib.ALT_L1O	14D3 15A6	SPKRCONN_L2_P_OUT	#alt_l1o.lib.ALT_L1O			13D3 14C3
	MIC_SHIELD	MIC_SHIELD - #alt_l1o.lib.ALT_L1O	14D3 15A6	SPKRCONN_R1_N_OUT	#alt_l1o.lib.ALT_L1O			13A3 14C3
	NC_AUD_GPIO_2	NC_AUD_GPIO_2 -	10C7	SPKRCONN_R1_P_OUT	#alt_l1o.lib.ALT_L1O			13A3 14C3
	NC_LED_WLAN_L	NC_LED_WLAN_L -	7B3	SPKRCONN_R2_N_OUT	#alt_l1o.lib.ALT_L1O			13B3 14C3
	NC_LED_WPAN_L	NC_LED_WPAN_L -	7B3	SPKRCONN_R2_P_OUT	#alt_l1o.lib.ALT_L1O			13B3 14C3
	NC_LED_WWAN_L	NC_LED_WWAN_L -	7B3	SPKR_SHIELD	#alt_l1o.lib.ALT_L1O			14C2
	NC_L1O_P3V3S3_PGOOD	NC_L1O_P3V3S3_PGOOD -	16B8	SYS_ONEWIRE	#alt_l1o.lib.ALT_L1O			8C6 17C1
C	NC_UIM_CLK	NC_UIM_CLK - #alt_l1o.lib.ALT_L1O	7C3	SYS_ONEWIRE_BILAT	#alt_l1o.lib.ALT_L1O			17C2
	NC_UIM_DATA	NC_UIM_DATA - #alt_l1o.lib.ALT_L1O	7C3	TP_EXCARD_STBY_L	#alt_l1o.lib.ALT_L1O			6C7
	NC_UIM_PWR	NC_UIM_PWR - #alt_l1o.lib.ALT_L1O	7C3	TP_USB2_MINI_N	#alt_l1o.lib.ALT_L1O			4B2
	NC_UIM_RESET	NC_UIM_RESET - #alt_l1o.lib.ALT_L1O	7C3	TP_USB2_MINI_P	#alt_l1o.lib.ALT_L1O			4B2
	NC_UIM_VPP	NC_UIM_VPP - #alt_l1o.lib.ALT_L1O	7C3	USB_LEFT2_EMI_N	#alt_l1o.lib.ALT_L1O			4B5 7B3
	NC_VOL_DOWN	NC_VOL_DOWN - #alt_l1o.lib.ALT_L1O	10C7	USB_LEFT2_EMI_P	#alt_l1o.lib.ALT_L1O			5C4
	NC_VOL_UP	NC_VOL_UP - #alt_l1o.lib.ALT_L1O	10C7	USB_LEFT2_GND	#alt_l1o.lib.ALT_L1O			5C3
	NC_W_DISABLE_L	NC_W_DISABLE_L -	7C3	USB_LEFT2_OC_L_R	#alt_l1o.lib.ALT_L1O			5B7
	ONEWIRE_DCIIN_DIV	ONEWIRE_DCIIN_DIV -	17C5	USB_LEFT2_OC_L_R	#alt_l1o.lib.ALT_L1O			5B7
	ONEWIRE_EN	ONEWIRE_EN - #alt_l1o.lib.ALT_L1O	17D3	USB_LEFT2_OC_L_R	#alt_l1o.lib.ALT_L1O			5B7
B	ONEWIRE_ESD	ONEWIRE_ESD - #alt_l1o.lib.ALT_L1O	17C4	USB_LEFT2_OC_L_R	#alt_l1o.lib.ALT_L1O			5B7
	ONEWIRE_OVERRVOLT	ONEWIRE_OVERRVOLT -	17C4	USB_LEFT2_OC_L_R	#alt_l1o.lib.ALT_L1O			5B7
	ONEWIRE_PWR_EN_L	ONEWIRE_PWR_EN_L -	17C2	USB_LEFT2_OC_L_R	#alt_l1o.lib.ALT_L1O			5B7
	ONEWIRE_PWR_EN_L_DIV	ONEWIRE_PWR_EN_L_DIV -	17D2	USB_LEFT2_OC_L_R	#alt_l1o.lib.ALT_L1O			5B7
	P2V5_ONEWIRE_REF	P2V5_ONEWIRE_REF -	17C5	USB_LEFT2_OC_L_R	#alt_l1o.lib.ALT_L1O			5B7
	PCIE_CLK100M_EXCARD	PCIE_CLK100M_EXCARD_UP_N -	8C4	USB_LEFT2_OC_L_R	#alt_l1o.lib.ALT_L1O			5B7
	PCIE_CLK100M_EXCARD	PCIE_CLK100M_EXCARD_UP_P -	8B4	USB_LEFT2_OC_L_R	#alt_l1o.lib.ALT_L1O			5B7
	PCIE_CLK100M_MINI_UF	PCIE_CLK100M_MINI_UF_N -	8C4	USB_LEFT2_OC_L_R	#alt_l1o.lib.ALT_L1O			5B7
	PCIE_CLK100M_MINI_UF	PCIE_CLK100M_MINI_UF_P -	8C4	USB_LEFT2_OC_L_R	#alt_l1o.lib.ALT_L1O			5B7
	PCIE_WAKE_EXCARD_L	PCIE_WAKE_EXCARD_L -	4C3 6C3	USB_LEFT2_OC_L_R	#alt_l1o.lib.ALT_L1O			5B7
A	PCIE_WAKE_L	PCIE_WAKE_L - #alt_l1o.lib.ALT_L1O	4C4 8C6	VREG_FB	#alt_l1o.lib.ALT_L1O			10A3
	PCIE_WAKE_MINI_L	PCIE_WAKE_MINI_L -	4C3 7C6					
	PLT_RESET_SWITCH_L	PLT_RESET_SWITCH_L -	6C3 6C3					
	PP1V5_S0_EXCARD_SWIT	PP1V5_S0_EXCARD_SWITCH -	6C3 6C3					
	PP3V3_AUDIO_CODEC	PP3V3_AUDIO_CODEC -	10D6					
	PP3V3_S0_AUDIO_F	PP3V3_S0_AUDIO_F -	15B4 15C8 15C8 15D8					
	PP3V3_S0_EXCARD_SWIT	PP3V3_S0_EXCARD_SWITCH -	6C3 6C3					
	PP3V3_S3_EXCARD_SWIT	PP3V3_S3_EXCARD_SWITCH -	6C3 6C3					
	PP4V5_AUDIO_ANALOG	PP4V5_AUDIO_ANALOG -	10A2 10D2 11C7 15A8 15C5					
	PP4V5_AUDIO_LINE_IN	PP4V5_AUDIO_LINE_IN -	11C5					
PP5V_AUDIO_HPAMP_AVDD	PP5V_AUDIO_HPAMP_AVDD_F -	12D4						
PP5V_AUDIO_HPAMP_PVD	PP5V_AUDIO_HPAMP_PVDD_F -	12D5						
PP5V_PWRON_USB_LEFT2	PP5V_PWRON_USB_LEFT2_EMI -	5D4						
PP5V_PWRON_USB_LEFT2	PP5V_PWRON_USB_LEFT2_EMI -	5B4						
PP5V_S0_AUDIO_F	PP5V_S0_AUDIO_F -	13A8 13B1 13B8 13C8 13D8						
PP5V_S3_3V3S3_R	PP5V_S3_3V3S3_R -	16C6						
PP5V_S3_3V3S3_VCC	PP5V_S3_3V3S3_VCC -	16C7						
PP5V_S3_USB_SWITCH_O	PP5V_S3_USB_SWITCH_OUT1 -	5B5						
PP5V_S3_USB_SWITCH_O	PP5V_S3_USB_SWITCH_OUT2 -	5D5						
PP18V5_DCIIN	PP18V5_DCIIN - #alt_l1o.lib.ALT_L1O	17D1 18C4						
PP18V5_DCIIN_FUSE	PP18V5_DCIIN_FUSE -	17D6						
PP18V5_DCIIN_ONEWIRE	PP18V5_DCIIN_ONEWIRE -	17D5						
PP18V5_DCIIN_UF	PP18V5_DCIIN_UF -	17D7						
SDATAIN	SDATAIN - #alt_l1o.lib.ALT_L1O	10C6						
SMC_BC_ACOK	SMC_BC_ACOK - #alt_l1o.lib.ALT_L1O	8C6 17B2						
SMC_BC_ACOK_R	SMC_BC_ACOK_R -	17C1						
SMC_EXCARD_CP	SMC_EXCARD_CP -	6A4 8C6						
SMC_EXCARD_PWR_EN	SMC_EXCARD_PWR_EN -	6C7 8C6						
SPKRAMP_L1_N_OUT	SPKRAMP_L1_N_OUT -	13C4 13C5						
SPKRAMP_L1_P_OUT	SPKRAMP_L1_P_OUT -	13C4 13C5						
SPKRAMP_L2_N_OUT	SPKRAMP_L2_N_OUT -	13D4 13D5						
SPKRAMP_L2_P_OUT	SPKRAMP_L2_P_OUT -	13D4 13D5						
SPKRAMP_R1_N_OUT	SPKRAMP_R1_N_OUT -	13A4 13A5						
SPKRAMP_R1_P_OUT	SPKRAMP_R1_P_OUT -	13A4 13A5						
SPKRAMP_R2_N_OUT	SPKRAMP_R2_N_OUT -	13B4 13B5						
SPKRAMP_R2_P_OUT	SPKRAMP_R2_P_OUT -	13B4 13B5						
SPKRAMP_SYNC1	SPKRAMP_SYNC1 -	13C5 13D5						
SPKRAMP_SYNC2	SPKRAMP_SYNC2 -	13B5 13C5						
SPKRAMP_SYNC3	SPKRAMP_SYNC3 -	13A5 13B5						
SPKRAMP_THERMPAD	SPKRAMP_THERMPAD -	13A5 13B5 13B5 13C5 13C5						
SPKRCONN_L1_N_OUT	SPKRCONN_L1_N_OUT -	13C3 14C3						

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0405 D87303 SUPPR_TRANSIENT1_402 alt_lio[14C5] D87306 SUPPR_TRANSIENT1_402 alt_lio[14C5] D87350 SUPPR_TRANSIENT_4P1_ alt_lio[14A6] 0405 D87351 SUPPR_TRANSIENT_4P1_ alt_lio[14A6] 0405 D87354 SUPPR_TRANSIENT1_402 alt_lio[14A5] F8200 FUSE_1206 alt_lio[17D5] FL5500 FILTER_4P_TCM1005 alt_lio[8C3] FL5501 FILTER_4P_TCM1005 alt_lio[8B3] J5100 CON_F4RT_USB_S2MT_TH alt_lio[5B2] _F-RT-TH-USB-LFT J5110 CON_F4RT_USB_S2MT_TH alt_lio[5C2] _F-RT-TH-USB-LFT J5300 CON_F26RT_S2MT_SM_F- alt_lio[6D2] RT-SM J5400 CON_F52RT_D2MT_SM_F- alt_lio[7C5] ST-SM J5500 CON_F80ST_D4MT_SM_F- alt_lio[8C4] ST-SM J7300 CON_F8RT_SPDIFFRAN_T alt_lio[14C8] H2_F-RT-TH J7350 CON_F8RT_SPDIFRCVR_T alt_lio[14B8] H2_F-RT-TH J7380 CON_M3ST_S_TH_M-ST-T alt_lio[14D1] H J7381 CON_M5ST_S_TH_M-ST-T alt_lio[14C1] H J7382 CON_M5ST_S_TH_M-ST-T alt_lio[14C1] H J8200 CON_F5RT_S2MT_TH3_F- alt_lio[17D8] RT-TH-MG3 J8215 CON_M8RT_S_SM_M-ST-S alt_lio[18C5] M L5100 FILTER_4P_SM alt_lio[5B4] L5101 IND_SM alt_lio[5B4] L5110 FILTER_4P_SM alt_lio[5C4] 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C								
B								
A								