

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

|     |      |     |                       |                 |                  |
|-----|------|-----|-----------------------|-----------------|------------------|
| REV | ZONE | ECN | DESCRIPTION OF CHANGE | CK APPD<br>DATE | ENG APPD<br>DATE |
| ?   |      | ?   | ?                     | ?               | ?                |

# Q42

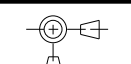
## DUAL PCI RISER

- CHANGES FROM Q28;
- \* ADD SERIES RESISTOR ON EACH LEG OF THE PCI BUS
  - \* ADD TEMPERATURE SENSOR
  - \* ADD PCI-X SUPPORT

| LAYER             | THICKNESS (MILS) | TRACE WIDTH (MILS) |
|-------------------|------------------|--------------------|
| CONFORMAL COATING | ???              | ---                |
| SIGNAL-TOP        | 1.9              | 4.0                |
| PREPREG           | 4.0              | ---                |
| POWER PLANE       | 1.3              | ---                |
| CORE              | 4.0              | ---                |
| SIGNAL1           | 1.2              | 4.0                |
| CORE FILLER       | VENDOR           | ---                |
| SIGNAL2           | 1.2              | 4.0                |
| CORE              | 4.0              | ---                |
| GROUND2           | 1.3              | ---                |
| PREPREG           | 4.0              | ---                |
| SIGNAL-BOTTOM     | 1.9              | 4.0                |
| CONFORMAL COATING | ???              | ---                |
| <b>TOTAL</b>      | <b>62</b>        | <b>---</b>         |

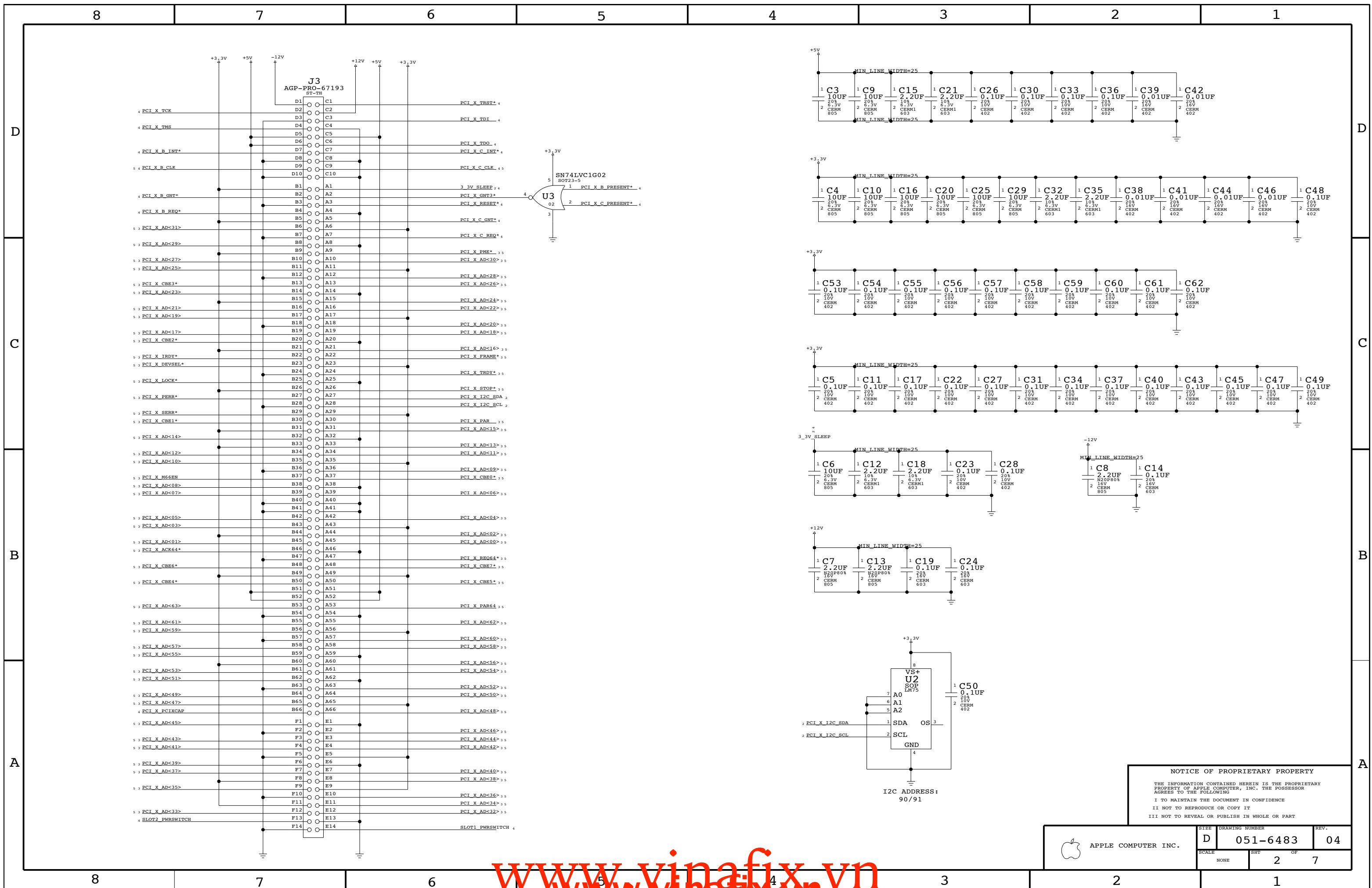
TARGET IMPEDANCE = 60 OHMS

| PART#    | QTY | DESCRIPTION            | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|----------|-----|------------------------|-------------------------|----------|------------|
| 820-1544 | 1   | PCBF,DUAL,PCI,RISER    | X1                      | CRITICAL | ?          |
| 825-2029 | 1   | LBL,SERR#,INP DEV      | X2                      |          | ?          |
| 815-7323 | 1   | PROTECTOR/RET DUAL PCI | X3                      |          | ?          |
| 815-6876 | 2   | SNAP RIVET             | ZH1,ZH2                 |          | ?          |

|   |       |                                     |           |  |  |
|---|-------|-------------------------------------|-----------|--|--|
| DIMENSIONS ARE IN MILLIMETERS   |       | METRIC                              |           | Apple Computer Inc.  |  |
| XX : _____  | _____ | DRAPPER                             | DESIGN CK | <b>NOTICE OF PROPRIETARY PROPERTY</b><br>THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING:<br>I. TO MAINTAIN THE DOCUMENT IN CONFIDENCE<br>II. NOT TO REPRODUCE OR COPY IT<br>III. NOT TO REVEAL OR PUBLISH IN WHOLE OR PART |  |
| X.XX : _____  | _____ | ENG APPD                            | MFG APPD  |  |  |
| X.XXX : _____   | _____ | QA APPD                             | DESIGNER  |  |  |
| ANGLES : _____  | _____ | RELEASE                             | SCALE     |  |  |
| DO NOT SCALE DRAWING  |       | NONE                                |           | <b>TITLE</b><br>SCHEM, DUAL, PCI, RISER  |  |
| <br>THIRD ANGLE PROJECTION |       | MATERIAL/FINISH NOTED AS APPLICABLE |           |  |  |
|   |       | SIZE D                              |           | SHT 1 OF 7   |  |

DRAWING LAST MODIFIED= Tue Sep 30 10:50:21 2003

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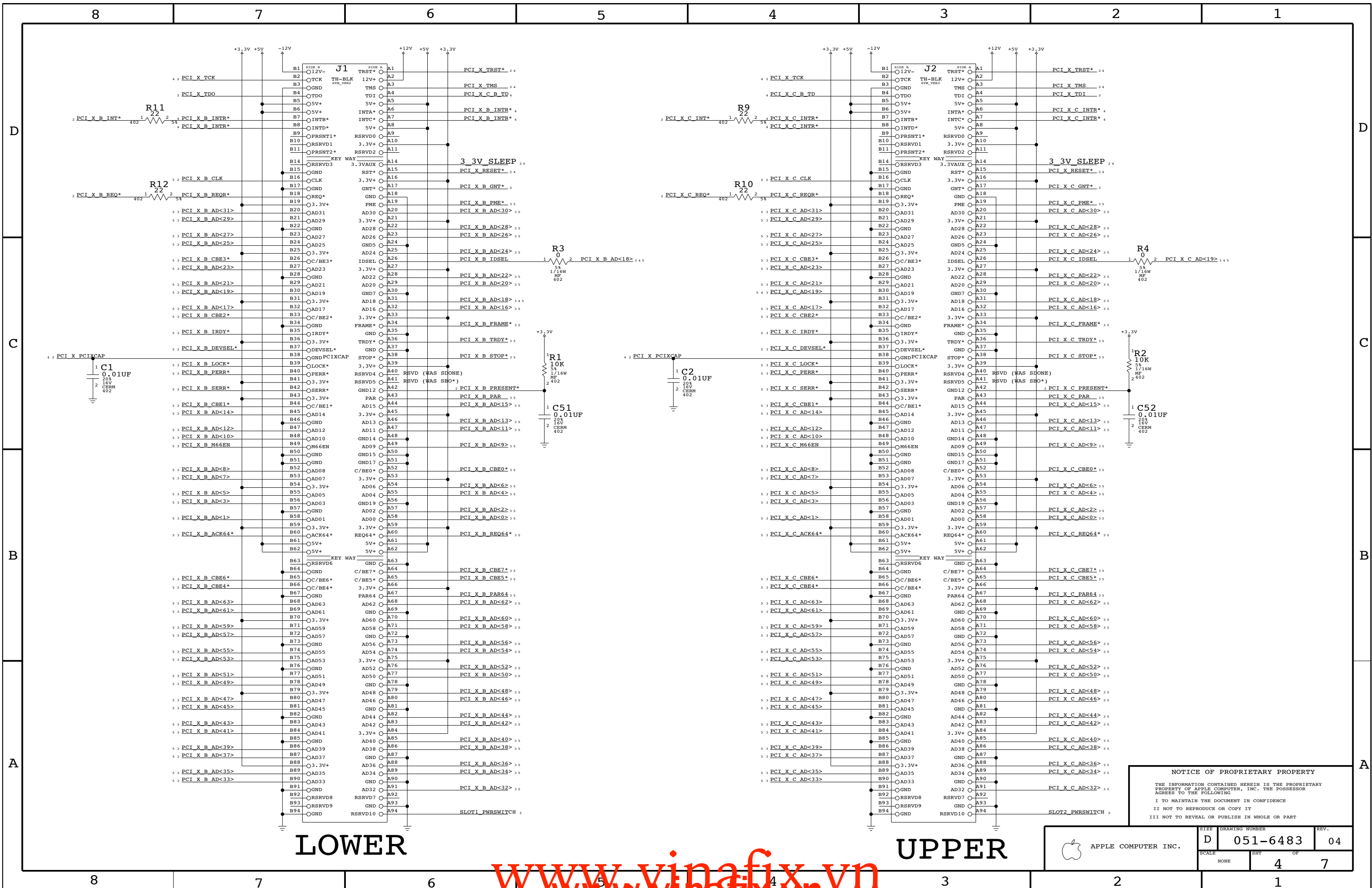
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| APPLE COMPUTER INC. | SIZE: D     | DRAWING NUMBER: 051-6483 | REV.: 04 |
|                     | SCALE: NONE | SHEET: 2                 | OF: 7    |

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| SCALE               | NONE | SHT            | OF   |
|                     |      | 3              | 7    |



LOWER

UPPER

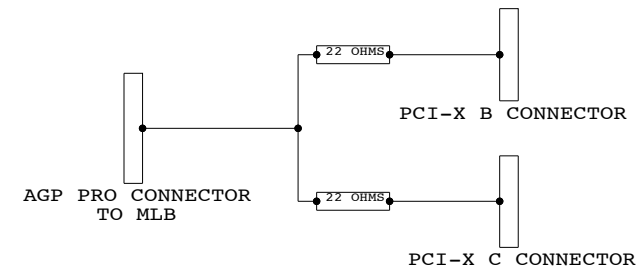
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| SCALE               | NONE | SHT            | OF   |
|                     |      | 4              | 7    |

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### DIGITAL SIGNALS

| GROUP | SIG_NAME          | PROPAGATION DELAY | MAX_VIAS | NET_PHYSICAL_TYPE | STUB_LENGTH | NET_SPACING_TYPE | PULSE_PARAM |      |
|-------|-------------------|-------------------|----------|-------------------|-------------|------------------|-------------|------|
| 2,2   | PCI_X_AD<63..0>   | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4872 |
| 2,2   | PCI_X_TRDY*       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4873 |
| 2,2   | PCI_X_IRDY*       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4874 |
| 2,2   | PCI_X_FRAME*      | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4875 |
| 2,2   | PCI_X_DEVSEL*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4876 |
| 2,2   | PCI_X_STOP*       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4877 |
| 2,2   | PCI_X_PERR*       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4878 |
| 2,2   | PCI_X_SERR*       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4879 |
| 2,2   | PCI_X_PAR         | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4880 |
| 2,2   | PCI_X_PAR64       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4881 |
| 2,2   | PCI_X_REQ64*      | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4882 |
| 2,2   | PCI_X_ACK64*      | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4883 |
| 2,2   | PCI_X_LOCK*       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4884 |
| 2,2   | PCI_X_PME*        | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4885 |
| 2,2   | PCI_X_M66EN       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4886 |
| 2,2   | PCI_X_CBEO*       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4887 |
| 2,2   | PCI_X_CBE1*       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4888 |
| 2,2   | PCI_X_CBE2*       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4889 |
| 2,2   | PCI_X_CBE3*       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4890 |
| 2,2   | PCI_X_CBE4*       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4891 |
| 2,2   | PCI_X_CBE5*       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4892 |
| 2,2   | PCI_X_CBE6*       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4893 |
| 2,2   | PCI_X_CBE7*       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4894 |
| 4,3   | PCI_X_B_AD<63..0> | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4895 |
| 4,3   | PCI_X_B_TRDY*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4896 |
| 4,3   | PCI_X_B_IRDY*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4897 |
| 4,3   | PCI_X_B_FRAME*    | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4898 |
| 4,3   | PCI_X_B_DEVSEL*   | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4899 |
| 4,3   | PCI_X_B_STOP*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4900 |
| 4,3   | PCI_X_B_PERR*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4901 |
| 4,3   | PCI_X_B_SERR*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4902 |
| 4,3   | PCI_X_B_PAR       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4903 |
| 4,3   | PCI_X_B_PAR64     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4904 |
| 4,3   | PCI_X_B_REQ64*    | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4905 |
| 4,3   | PCI_X_B_ACK64*    | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4906 |
| 4,3   | PCI_X_B_LOCK*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4907 |
| 4,3   | PCI_X_B_PME*      | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4908 |
| 4,3   | PCI_X_B_M66EN     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4909 |
| 4,3   | PCI_X_B_CBEO*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4910 |
| 4,3   | PCI_X_B_CBE1*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4911 |
| 4,3   | PCI_X_B_CBE2*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4912 |
| 4,3   | PCI_X_B_CBE3*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4913 |
| 4,3   | PCI_X_B_CBE4*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4914 |
| 4,3   | PCI_X_B_CBE5*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4915 |
| 4,3   | PCI_X_B_CBE6*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4916 |
| 4,3   | PCI_X_B_CBE7*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4917 |
| 4,2   | PCI_X_B_CLK       | 1:1450:1500       | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4918 |
| 4,3   | PCI_X_C_AD<63..0> | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4919 |
| 4,3   | PCI_X_C_TRDY*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4920 |
| 4,3   | PCI_X_C_IRDY*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4921 |
| 4,3   | PCI_X_C_FRAME*    | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4922 |
| 4,3   | PCI_X_C_DEVSEL*   | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4923 |
| 4,3   | PCI_X_C_STOP*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4924 |
| 4,3   | PCI_X_C_PERR*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4925 |
| 4,3   | PCI_X_C_SERR*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4926 |
| 4,3   | PCI_X_C_PAR       | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4927 |
| 4,3   | PCI_X_C_PAR64     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4928 |
| 4,3   | PCI_X_C_REQ64*    | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4929 |
| 4,3   | PCI_X_C_ACK64*    | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4930 |
| 4,3   | PCI_X_C_LOCK*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4931 |
| 4,3   | PCI_X_C_PME*      | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4932 |
| 4,3   | PCI_X_C_M66EN     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4933 |
| 4,3   | PCI_X_C_CBEO*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4934 |
| 4,3   | PCI_X_C_CBE1*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4935 |
| 4,3   | PCI_X_C_CBE2*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4936 |
| 4,3   | PCI_X_C_CBE3*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4937 |
| 4,3   | PCI_X_C_CBE4*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4938 |
| 4,3   | PCI_X_C_CBE5*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4939 |
| 4,3   | PCI_X_C_CBE6*     | L:S:1600          | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4940 |
| 4,2   | PCI_X_C_CLK       | 1:1450:1500       | 5        | PCI_X_NP          |             | PCI_X_NSP        | 133MHZ      | 4941 |



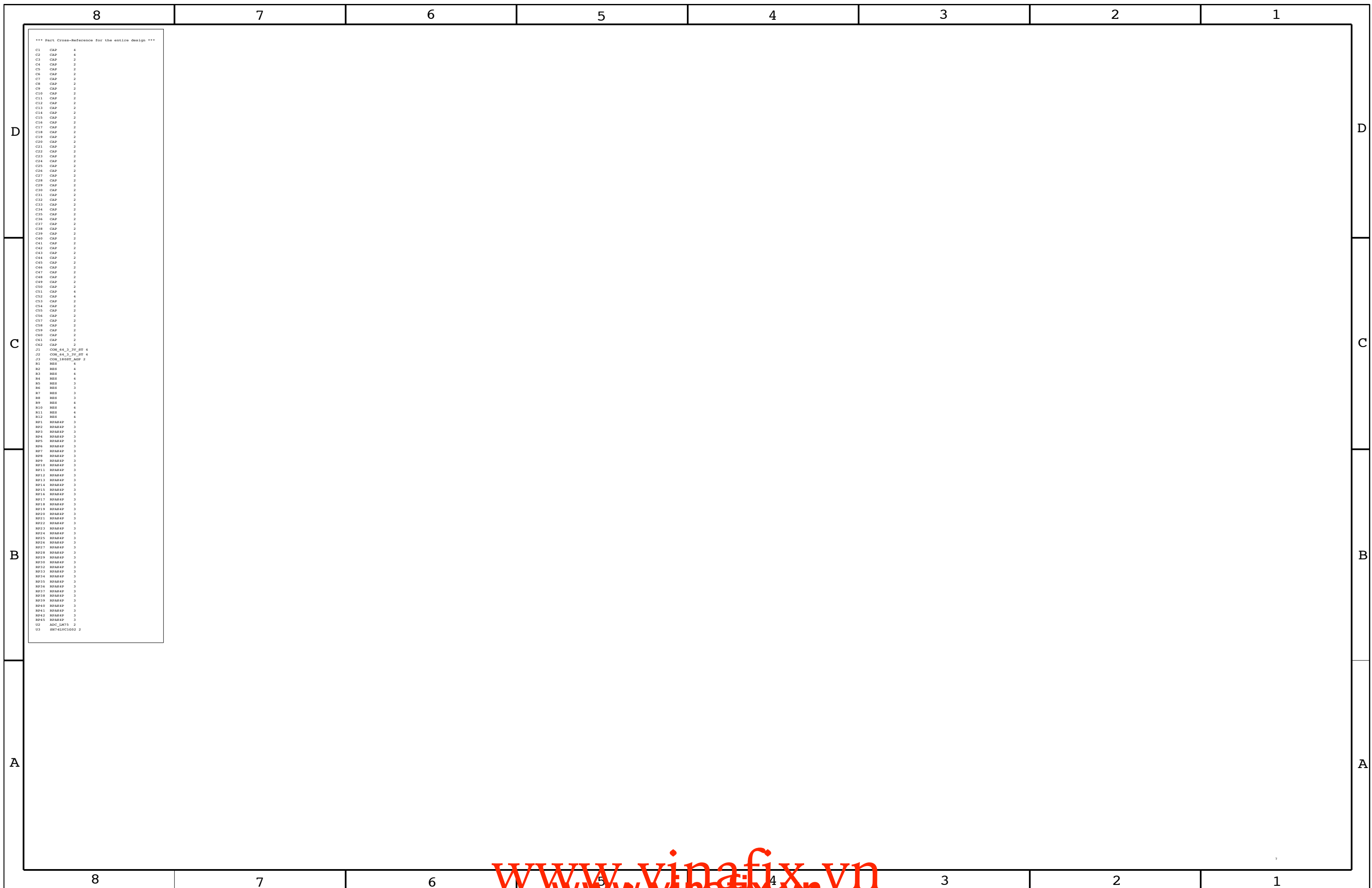
#### SIGNAL CONSTRAINTS

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|                     |      | SHT            | OF   |
|                     |      | 5              | 7    |

|   |   |   |   |   |   |   |   |   |   |
|---|---|---|---|---|---|---|---|---|---|
|   | 8   | 7 | 6 | 5 | 4 | 3 | 2 | 1 |   |
| D | <pre> *** Signal Cross-Reference for the entire design *** 3_V3_SLEEP 284&lt; 2D6&lt;&gt; 4D2&lt;&gt; 4D6&lt;&gt; PCI_X_ACR64* 288&lt;&gt; 3A4&lt; 3B6&lt; 5C8&lt; PCI_X_AD0&lt; 286&lt;&gt; 3D4&lt; 3D8&lt; PCI_X_AD01..0 5D8&lt; PCI_X_AD1&lt; 286&lt;&gt; 3D4&lt; 3D8&lt; PCI_X_AD2&lt; 286&lt;&gt; 3D4&lt; 3D8&lt; PCI_X_AD3&lt; 286&lt;&gt; 3D4&lt; 3D8&lt; PCI_X_AD4&lt; 286&lt;&gt; 3D4&lt; 3D8&lt; PCI_X_AD5&lt; 288&lt;&gt; 3D4&lt; 3D8&lt; PCI_X_AD6&lt; 286&lt;&gt; 3D4&lt; 3D8&lt; PCI_X_AD7&lt; 286&lt;&gt; 3D4&lt; 3D8&lt; PCI_X_AD8&lt; 288&lt;&gt; 3D4&lt; 3D8&lt; PCI_X_AD9&lt; 286&lt;&gt; 3C8&lt; 3D4&lt; PCI_X_AD10&lt; 286&lt;&gt; 3C4&lt; 3C8&lt; PCI_X_AD11&lt; 286&lt;&gt; 3C4&lt; 3C8&lt; PCI_X_AD12&lt; 286&lt;&gt; 3C4&lt; 3C8&lt; PCI_X_AD13&lt; 286&lt;&gt; 3C4&lt; 3C8&lt; PCI_X_AD14&lt; 288&lt;&gt; 3C4&lt; 3C8&lt; PCI_X_AD15&lt; 286&lt;&gt; 3C4&lt; 3C8&lt; PCI_X_AD16&lt; 2C6&lt;&gt; 3C4&lt; 3C8&lt; PCI_X_AD17&lt; 2C8&lt;&gt; 3C4&lt; 3C8&lt; PCI_X_AD18&lt; 2C6&lt;&gt; 3C4&lt; 3C8&lt; PCI_X_AD19&lt; 2C8&lt;&gt; 3C4&lt; 3C8&lt; PCI_X_AD20&lt; 2C6&lt;&gt; 3C4&lt; 3C8&lt; PCI_X_AD21&lt; 2C6&lt;&gt; 3B8&lt; 3C4&lt; PCI_X_AD22&lt; 2C8&lt;&gt; 3B4&lt; 3B8&lt; PCI_X_AD23&lt; 2C6&lt;&gt; 3B4&lt; 3B8&lt; PCI_X_AD24&lt; 2C8&lt;&gt; 3B4&lt; 3B8&lt; PCI_X_AD25&lt; 2C6&lt;&gt; 3B4&lt; 3B8&lt; PCI_X_AD27&lt; 2C8&lt;&gt; 3B4&lt; 3B8&lt; PCI_X_AD28&lt; 2C6&lt;&gt; 3B4&lt; 3B8&lt; PCI_X_AD29&lt; 2C8&lt;&gt; 3B4&lt; 3B8&lt; PCI_X_AD30&lt; 2C6&lt;&gt; 3B4&lt; 3B8&lt; PCI_X_AD31&lt; 2C8&lt;&gt; 3B4&lt; 3B8&lt; PCI_X_AD32&lt; 2A6&lt;&gt; 3B4&lt; 3B8&lt; PCI_X_AD33&lt; 2A6&lt;&gt; 3B4&lt; 3B8&lt; PCI_X_AD34&lt; 2A6&lt;&gt; 3B4&lt; 3B8&lt; PCI_X_AD35&lt; 2A8&lt;&gt; 3A8&lt; 3B4&lt; PCI_X_AD36&lt; 2A6&lt;&gt; 3A4&lt; 3A8&lt; PCI_X_AD37&lt; 2A8&lt;&gt; 3A4&lt; 3A8&lt; PCI_X_AD38&lt; 2A6&lt;&gt; 3A4&lt; 3A8&lt; PCI_X_AD39&lt; 2A8&lt;&gt; 3A4&lt; 3A8&lt; PCI_X_AD40&lt; 2A6&lt;&gt; 3A4&lt; 3A8&lt; PCI_X_AD41&lt; 2A8&lt;&gt; 3A4&lt; 3A8&lt; PCI_X_AD42&lt; 2A6&lt;&gt; 3A4&lt; 3A8&lt; PCI_X_AD43&lt; 2A8&lt;&gt; 3A4&lt; 3A8&lt; PCI_X_AD44&lt; 2A6&lt;&gt; 3D2&lt; 3D6&lt; PCI_X_AD45&lt; 2A8&lt;&gt; 3D2&lt; 3D6&lt; PCI_X_AD46&lt; 2A6&lt;&gt; 3D2&lt; 3D6&lt; PCI_X_AD47&lt; 2A8&lt;&gt; 3D2&lt; 3D6&lt; PCI_X_AD48&lt; 2A6&lt;&gt; 3D2&lt; 3D6&lt; PCI_X_AD49&lt; 2A8&lt;&gt; 3D2&lt; 3D6&lt; PCI_X_AD50&lt; 2A6&lt;&gt; 3D2&lt; 3D6&lt; PCI_X_AD51&lt; 2A8&lt;&gt; 3D2&lt; 3D6&lt; PCI_X_AD52&lt; 2A6&lt;&gt; 3D2&lt; 3D6&lt; PCI_X_AD53&lt; 2A8&lt;&gt; 3C2&lt; 3C6&lt; PCI_X_AD54&lt; 2A6&lt;&gt; 3C2&lt; 3C6&lt; PCI_X_AD55&lt; 2A8&lt;&gt; 3C2&lt; 3C6&lt; PCI_X_AD56&lt; 2A6&lt;&gt; 3C2&lt; 3C6&lt; PCI_X_AD57&lt; 2A8&lt;&gt; 3C2&lt; 3C6&lt; PCI_X_AD58&lt; 2A6&lt;&gt; 3C2&lt; 3C6&lt; PCI_X_AD59&lt; 2A8&lt;&gt; 3C2&lt; 3C6&lt; PCI_X_AD60&lt; 2A6&lt;&gt; 3C2&lt; 3C6&lt; PCI_X_AD61&lt; 288&lt;&gt; 3C2&lt; 3C6&lt; PCI_X_AD62&lt; 286&lt;&gt; 3C2&lt; 3C6&lt; PCI_X_AD63&lt; 288&lt;&gt; 3C2&lt; 3C6&lt; PCI_X_ACR64* 3A5&lt; 4B7&lt;&gt; 5C8&lt; PCI_X_AD0&lt; 3D7&lt; 4B6&lt;&gt; PCI_X_AD01..0 5C8&lt; PCI_X_AD1&lt; 3D7&lt; 4B7&lt;&gt; PCI_X_AD2&lt; 3D7&lt; 4B6&lt;&gt; PCI_X_AD3&lt; 3D7&lt; 4B7&lt;&gt; PCI_X_AD4&lt; 3D7&lt; 4B6&lt;&gt; PCI_X_AD5&lt; 3D7&lt; 4B7&lt;&gt; PCI_X_AD6&lt; 3D7&lt; 4B6&lt;&gt; PCI_X_AD7&lt; 3D7&lt; 4B7&lt;&gt; PCI_X_AD8&lt; 3D7&lt; 4B6&lt;&gt; PCI_X_AD9&lt; 3C7&lt; 4B6&lt;&gt; PCI_X_AD10&lt; 3C7&lt; 4B7&lt;&gt; PCI_X_AD11&lt; 3C7&lt; 4B6&lt;&gt; PCI_X_AD12&lt; 3C7&lt; 4B7&lt;&gt; PCI_X_AD13&lt; 3C7&lt; 4C6&lt;&gt; PCI_X_AD14&lt; 3C7&lt; 4C7&lt;&gt; PCI_X_AD15&lt; 3C7&lt; 4C6&lt;&gt; PCI_X_AD16&lt; 3C7&lt; 4C6&lt;&gt; PCI_X_AD17&lt; 3C7&lt; 4C7&lt;&gt; PCI_X_AD18&lt; 3C7&lt; 4C5&lt; 4C8&lt;&gt; PCI_X_AD19&lt; 3C7&lt; 4C7&lt;&gt; PCI_X_AD20&lt; 3C7&lt; 4C6&lt;&gt; PCI_X_AD21&lt; 3C7&lt; 4C7&lt;&gt; PCI_X_AD22&lt; 3B7&lt; 4C6&lt;&gt; PCI_X_AD23&lt; 3B7&lt; 4C7&lt;&gt; PCI_X_AD24&lt; 3B7&lt; 4C6&lt;&gt; PCI_X_AD25&lt; 3B7&lt; 4C7&lt;&gt; PCI_X_AD26&lt; 3B7&lt; 4C6&lt;&gt; PCI_X_AD27&lt; 3B7&lt; 4C7&lt;&gt; PCI_X_AD28&lt; 3B7&lt; 4C6&lt;&gt; PCI_X_AD29&lt; 3B7&lt; 4C7&lt;&gt; PCI_X_AD30&lt; 3B7&lt; 4D6&lt;&gt; PCI_X_AD31&lt; 3B7&lt; 4D7&lt;&gt; PCI_X_AD32&lt; 3B7&lt; 4A6&lt;&gt; PCI_X_AD33&lt; 3B7&lt; 4A7&lt;&gt; PCI_X_AD34&lt; 3B7&lt; 4A6&lt;&gt; PCI_X_AD35&lt; 3A7&lt; 4A7&lt;&gt; PCI_X_AD36&lt; 3A7&lt; 4A6&lt;&gt; PCI_X_AD37&lt; 3A7&lt; 4A7&lt;&gt; PCI_X_AD38&lt; 3A7&lt; 4A6&lt;&gt; PCI_X_AD39&lt; 3A7&lt; 4A7&lt;&gt; PCI_X_AD40&lt; 3A7&lt; 4A6&lt;&gt; PCI_X_AD41&lt; 3A7&lt; 4A7&lt;&gt; PCI_X_AD42&lt; 3A7&lt; 4A6&lt;&gt; PCI_X_AD43&lt; 3A7&lt; 4A7&lt;&gt; PCI_X_AD44&lt; 3D5&lt; 4A6&lt;&gt; PCI_X_AD45&lt; 3D5&lt; 4A7&lt;&gt; PCI_X_AD46&lt; 3D5&lt; 4A6&lt;&gt; PCI_X_AD47&lt; 3D5&lt; 4A7&lt;&gt; PCI_X_AD48&lt; 3D5&lt; 4A6&lt;&gt; PCI_X_AD49&lt; 3D5&lt; 4A7&lt;&gt; PCI_X_AD50&lt; 3D5&lt; 4A6&lt;&gt; PCI_X_AD51&lt; 3D5&lt; 4A7&lt;&gt; PCI_X_AD52&lt; 3D5&lt; 4A6&lt;&gt; PCI_X_AD53&lt; 3C5&lt; 4A7&lt;&gt; PCI_X_AD54&lt; 3C5&lt; 4A6&lt;&gt; PCI_X_AD55&lt; 3C5&lt; 4A7&lt;&gt; PCI_X_AD56&lt; 3C5&lt; 4A6&lt;&gt; PCI_X_AD57&lt; 3C5&lt; 4B7&lt;&gt; PCI_X_AD58&lt; 3C5&lt; 4B6&lt;&gt; PCI_X_AD59&lt; 3C5&lt; 4B7&lt;&gt; PCI_X_AD60&lt; 3C5&lt; 4B6&lt;&gt; PCI_X_AD61&lt; 3C5&lt; 4B7&lt;&gt; PCI_X_AD62&lt; 3C5&lt; 4B6&lt;&gt; PCI_X_AD63&lt; 3C5&lt; 4B7&lt;&gt; PCI_X_CBE0* 3C5&lt; 4B6&lt;&gt; 5B8&lt; PCI_X_CBE1* 3C5&lt; 4C7&lt;&gt; 5B8&lt; PCI_X_CBE2* 3B5&lt; 4C7&lt;&gt; 5B8&lt; PCI_X_CBE3* 3B5&lt; 4C7&lt;&gt; 5B8&lt; PCI_X_CBE4* 3B5&lt; 4B7&lt;&gt; 5B8&lt; PCI_X_CBE5* 3B5&lt; 4B6&lt;&gt; 5B8&lt; PCI_X_CBE6* 3B5&lt; 4B7&lt;&gt; 5B8&lt; PCI_X_CBE7* 3B5&lt; 4B6&lt;&gt; 5B8&lt; PCI_X_CLK 2D8&lt;&gt; 4D7&lt;&gt; 5B8&lt; PCI_X_DEVSEL* 3B5&lt; 4C7&lt;&gt; 5C8&lt; PCI_X_FRAME* 3B5&lt; 4C6&lt;&gt; 5C8&lt; PCI_X_GNT* 2D8&lt;&gt; 4D6&lt;&gt; PCI_X_IDSEL 4C6&lt;&gt; PCI_X_INT* 2D6&lt;&gt; 4D6&lt; PCI_X_IRQ* 4D6&lt;&gt; 4D6&lt;&gt; 4D7&lt;&gt; 4D7&lt;&gt; PCI_X_IRDY* 3B5&lt; 4C7&lt;&gt; 5C8&lt; PCI_X_LOCK* 3A5&lt; 4C7&lt;&gt; 5B8&lt; PCI_X_M66EN 3A5&lt; 4B7&lt;&gt; 5B8&lt; PCI_X_PAR 3B5&lt; 4C6&lt;&gt; 5C8&lt; PCI_X_PAR64 3A5&lt; 4B8&lt;&gt; 5C8&lt; PCI_X_PERR* 3B5&lt; 4C7&lt;&gt; 5C8&lt; PCI_X_PME* 3A5&lt; 4D6&lt;&gt; 5B8&lt; PCI_X_PRESSENT* 2D5&lt; 4C6&lt;&gt; PCI_X_RDY* 2D8&lt;&gt; 4D6&lt; PCI_X_REQ64* 3A1&lt; 4B2&lt;&gt; 5A8&lt; PCI_X_REQ6* 4D4&lt;&gt; PCI_X_SERR* 3A1&lt; 4C4&lt;&gt; 5B8&lt; PCI_X_STOP* 3B1&lt; 4C2&lt;&gt; 5B8&lt; PCI_X_TRDY* 3B1&lt; 4C2&lt;&gt; 5B8&lt; PCI_X_DEVSEL* 2C8&lt;&gt; 3B2&lt; 3B6&lt; 5D8&lt; PCI_X_FRAME* 2C6&lt;&gt; 3B2&lt; 3B6&lt; 5D8&lt; PCI_X_DMA31 2D6&lt;&gt; PCI_X_12C_SCL 2A4&lt; 2C6&lt;&gt; PCI_X_12C_SDA 2A4&lt;&gt; 2C6&lt;&gt; PCI_X_TRDY* 2C8&lt;&gt; 3B2&lt; 3B6&lt; 5D8&lt; PCI_X_LOCK* 2C8&lt;&gt; 3A2&lt; 3A6&lt; 5D8&lt; PCI_X_M66EN 2B8&lt;&gt; 3A2&lt; 3A6&lt; 5C8&lt; PCI_X_PAR 2C6&lt;&gt; 3B2&lt; 3B6&lt; 5D8&lt; PCI_X_PAR64 2B6&lt;&gt; 3A2&lt; 3A6&lt; 5D8&lt; PCI_X_PICXCAP 2A8&lt;&gt; 4C5&lt;&gt; 4C8&lt;&gt; PCI_X_PERR* 2C8&lt;&gt; 3B2&lt; 3B6&lt; 5D8&lt; PCI_X_PME* 2C6&lt;&gt; 3A2&lt; 3A6&lt; 5D8&lt; PCI_X_REQ64* 2B6&lt;&gt; 3A2&lt; 3A6&lt; 5D8&lt; PCI_X_SERR* 2D6&lt;&gt; 4D2&lt;&gt; 4D6&lt;&gt; PCI_X_SERR* 2C8&lt;&gt; 3A2&lt; 3A6&lt; 5D8&lt; PCI_X_STOP* 2C6&lt;&gt; 3B2&lt; 3B6&lt; 5D8&lt; PCI_X_TDR 2D8&lt;&gt; 4D7&lt;&gt; PCI_X_TDI 2D6&lt;&gt; 4D2&lt;&gt; PCI_X_TDO 2D6&lt;&gt; 4D7&lt;&gt; PCI_X_TMS 2D8&lt;&gt; 4D2&lt;&gt; 4D6&lt;&gt; PCI_X_TRDY* 2C6&lt;&gt; 3B2&lt; 3B6&lt; 5D8&lt; PCI_X_TRDY* 2D6&lt;&gt; 4D2&lt;&gt; 4D6&lt;&gt; SLOT2_PERRSWITCH 2A6&lt;&gt; 4A6&lt;&gt; SLOT2_PERRSWITCH 2A8&lt;&gt; 4A2&lt;&gt; </pre> |   |   |   |   |   |   |   | D |
| C |   |   |   |   |   |   |   |   | C |
| B |   |   |   |   |   |   |   |   | B |
| A |   |   |   |   |   |   |   |   | A |



\*\*\* Part Cross-Reference for the entire design \*\*\*

|      |                |   |
|------|----------------|---|
| C1   | CAP            | 4 |
| C2   | CAP            | 4 |
| C3   | CAP            | 2 |
| C4   | CAP            | 2 |
| C5   | CAP            | 2 |
| C6   | CAP            | 2 |
| C7   | CAP            | 2 |
| C8   | CAP            | 2 |
| C9   | CAP            | 2 |
| C10  | CAP            | 2 |
| C11  | CAP            | 2 |
| C12  | CAP            | 2 |
| C13  | CAP            | 2 |
| C14  | CAP            | 2 |
| C15  | CAP            | 2 |
| C16  | CAP            | 2 |
| C17  | CAP            | 2 |
| C18  | CAP            | 2 |
| C19  | CAP            | 2 |
| C20  | CAP            | 2 |
| C21  | CAP            | 2 |
| C22  | CAP            | 2 |
| C23  | CAP            | 2 |
| C24  | CAP            | 2 |
| C25  | CAP            | 2 |
| C26  | CAP            | 2 |
| C27  | CAP            | 2 |
| C28  | CAP            | 2 |
| C29  | CAP            | 2 |
| C30  | CAP            | 2 |
| C31  | CAP            | 2 |
| C32  | CAP            | 2 |
| C33  | CAP            | 2 |
| C34  | CAP            | 2 |
| C35  | CAP            | 2 |
| C36  | CAP            | 2 |
| C37  | CAP            | 2 |
| C38  | CAP            | 2 |
| C39  | CAP            | 2 |
| C40  | CAP            | 2 |
| C41  | CAP            | 2 |
| C42  | CAP            | 2 |
| C43  | CAP            | 2 |
| C44  | CAP            | 2 |
| C45  | CAP            | 2 |
| C46  | CAP            | 2 |
| C47  | CAP            | 2 |
| C48  | CAP            | 2 |
| C49  | CAP            | 2 |
| C50  | CAP            | 2 |
| C51  | CAP            | 4 |
| C52  | CAP            | 4 |
| C53  | CAP            | 2 |
| C54  | CAP            | 2 |
| C55  | CAP            | 2 |
| C56  | CAP            | 2 |
| C57  | CAP            | 2 |
| C58  | CAP            | 2 |
| C59  | CAP            | 2 |
| C60  | CAP            | 2 |
| C61  | CAP            | 2 |
| C62  | CAP            | 2 |
| J1   | CON_64_3_3V_BT | 4 |
| J2   | CON_64_3_3V_BT | 4 |
| J3   | CON_1800T_ADP  | 2 |
| R1   | RES            | 4 |
| R2   | RES            | 4 |
| R3   | RES            | 4 |
| R4   | RES            | 4 |
| R5   | RES            | 3 |
| R6   | RES            | 3 |
| R7   | RES            | 3 |
| R8   | RES            | 3 |
| R9   | RES            | 4 |
| R10  | RES            | 4 |
| R11  | RES            | 4 |
| R12  | RES            | 4 |
| RP1  | RPARK4P        | 3 |
| RP2  | RPARK4P        | 3 |
| RP3  | RPARK4P        | 3 |
| RP4  | RPARK4P        | 3 |
| RP5  | RPARK4P        | 3 |
| RP6  | RPARK4P        | 3 |
| RP7  | RPARK4P        | 3 |
| RP8  | RPARK4P        | 3 |
| RP9  | RPARK4P        | 3 |
| RP10 | RPARK4P        | 3 |
| RP11 | RPARK4P        | 3 |
| RP12 | RPARK4P        | 3 |
| RP13 | RPARK4P        | 3 |
| RP14 | RPARK4P        | 3 |
| RP15 | RPARK4P        | 3 |
| RP16 | RPARK4P        | 3 |
| RP17 | RPARK4P        | 3 |
| RP18 | RPARK4P        | 3 |
| RP19 | RPARK4P        | 3 |
| RP20 | RPARK4P        | 3 |
| RP21 | RPARK4P        | 3 |
| RP22 | RPARK4P        | 3 |
| RP23 | RPARK4P        | 3 |
| RP24 | RPARK4P        | 3 |
| RP25 | RPARK4P        | 3 |
| RP26 | RPARK4P        | 3 |
| RP27 | RPARK4P        | 3 |
| RP28 | RPARK4P        | 3 |
| RP29 | RPARK4P        | 3 |
| RP30 | RPARK4P        | 3 |
| RP31 | RPARK4P        | 3 |
| RP32 | RPARK4P        | 3 |
| RP33 | RPARK4P        | 3 |
| RP34 | RPARK4P        | 3 |
| RP35 | RPARK4P        | 3 |
| RP36 | RPARK4P        | 3 |
| RP37 | RPARK4P        | 3 |
| RP38 | RPARK4P        | 3 |
| RP39 | RPARK4P        | 3 |
| RP40 | RPARK4P        | 3 |
| RP41 | RPARK4P        | 3 |
| RP42 | RPARK4P        | 3 |
| RP43 | RPARK4P        | 3 |
| U2   | ADC_LMT5       | 2 |
| U3   | SN74LVCI932    | 2 |