

- 1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
- 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
- 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ZONE	ECN	DESCRIPTION OF CHANGE	CK APPD DATE	ENG APPD DATE
		502639	PVT Release	?	?

SCHEM, KILKENNY, M88

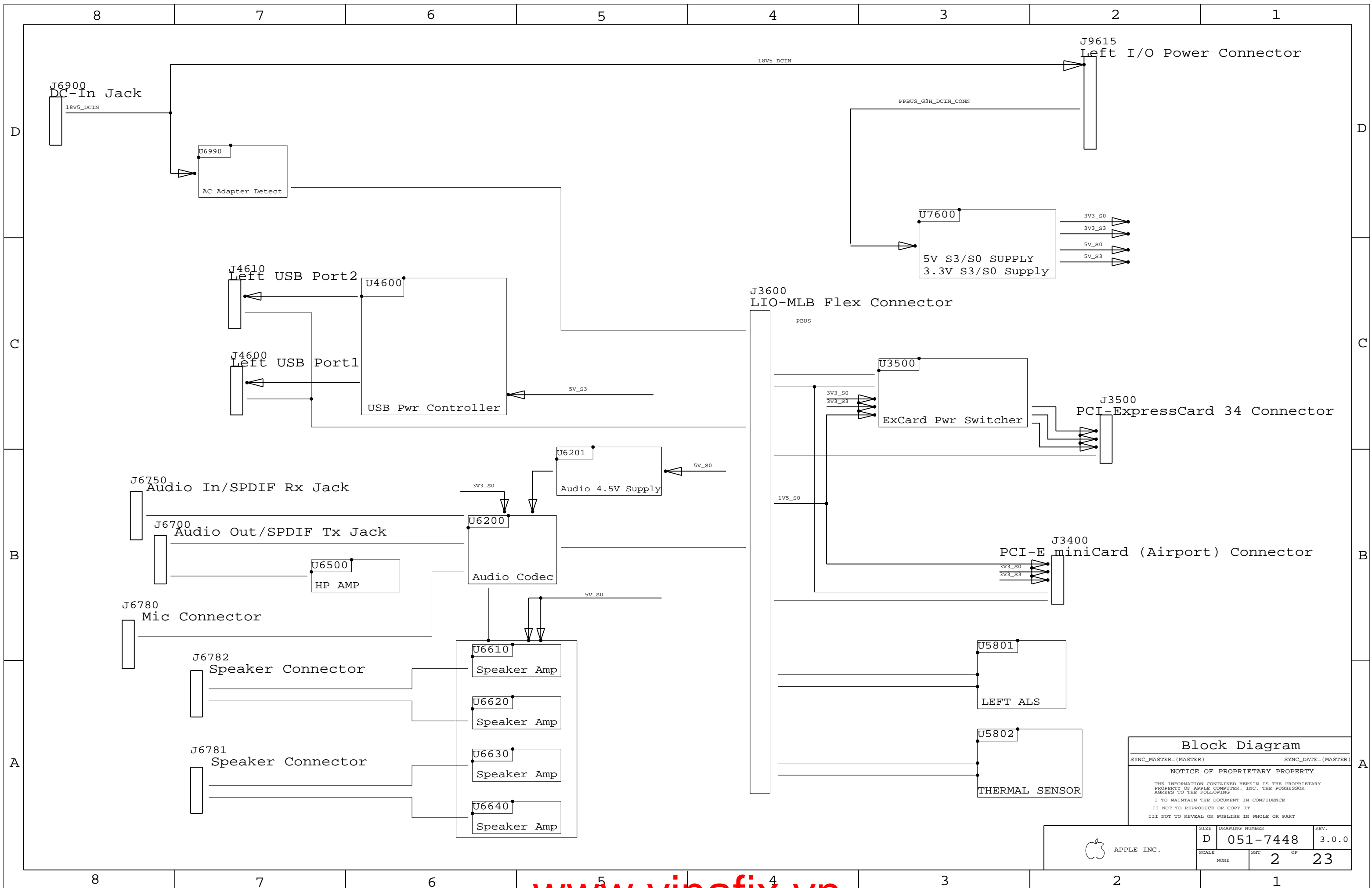
08/31/07

PDF PAGE	CSA PAGE	CONTENTS	SYNC MASTER	DATE
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2	2	Block Diagram	(MASTER)	(MASTER)
3	4	BOM CONFIGURATION	(MASTER)	(MASTER)
4	5	Version History	(MASTER)	(MASTER)
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18	76	5V/3.3V SUPPLY	M57_LIO	11/30/2006
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21	101		M88_AUDIO	08/30/2007
22	102	Cross Reference Page		
23	103	Cross Reference Page		

ALIASES RESOLVED

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-7448	1	SCHEM, KILKENNY, M88	SCH	CRITICAL	
820-2274	1	PCBF, KILKENNY, M88	PCB	CRITICAL	

DIMENSIONS ARE IN MILLIMETERS XX : _____ X.XX : _____ X.XXX : _____ ANGLES : _____ DO NOT SCALE DRAWING	METRIC	APPLE INC.
DRAPER <input type="checkbox"/> DESIGN CK <input type="checkbox"/> ENG APPD <input type="checkbox"/> MFG APPD <input type="checkbox"/> QA APPD <input type="checkbox"/> DESIGNER <input type="checkbox"/> RELEASE <input type="checkbox"/> SCALE NONE <input type="checkbox"/>	NOTICE OF PROPRIETARY PROPERTY <small>THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING</small> I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART	
 THIRD ANGLE PROJECTION	MATERIAL/FINISH NOTED AS APPLICABLE	SIZE D DRAWING NUMBER 051-7448 REV. 3.0.0 <small>SHT 1 OF 23</small>



Block Diagram

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7448	3.0.0
SCALE	SHT	OF	
NONE	2	23	

8

7

6

5

4

3

2

1

BOM NUMBER	BOM NAME	BOM OPTIONS
630-9095	PCBA, KILKENNY, M88	COMMON, ALTERNATE, ALS_NTC, OLD_MIC

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
353S1297	353S1717		U6990	TI Alt Src

BAR CODE LABEL / EEE#'S

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
000-0041	1	LBL, P/N LABEL, PCB	[EEE:Z3N]	CRITICAL	

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
815-8851	1	ALS SPACER, M9	SP6401	CRITICAL	

D

D

C

C


B

B

A

A

BOM CONFIGURATION	
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	D	051-7448	3.0.0
SCALE	SHT	OF	
NONE	3	23	

8

7

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1

Version History

Proto
 07/17/07 -- Left ALS: Changed TMP106 to TMP102 (TALP sensor)
 07/17/07 -- Left ALS: Added R5808 as NTC thermistor (with ALS_NTC BOMOPTION)
 07/17/07 -- DC-In: Moved J9615 to DC-In/PBUS page (deleted page 96)

D

D

C

C


B

B

A

A

Version History		
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SCALE	SHT	OF	
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8

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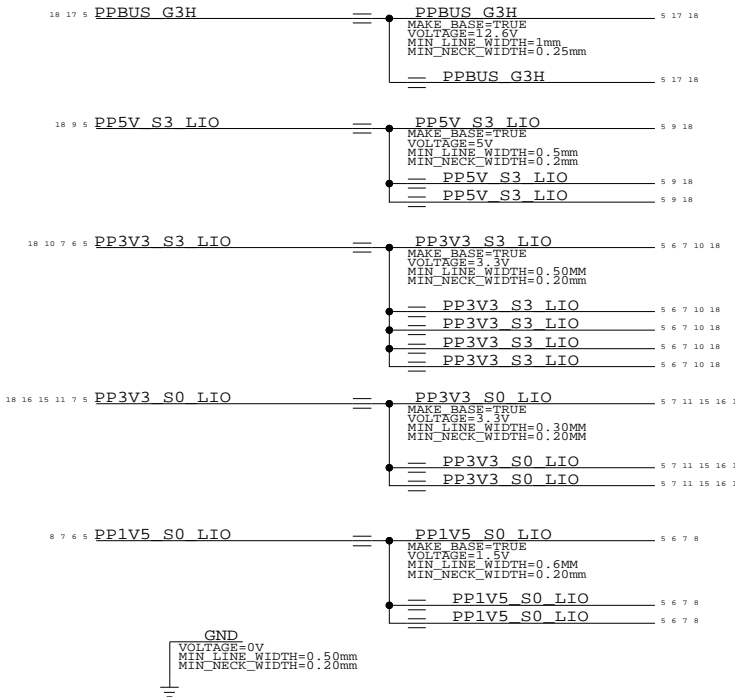
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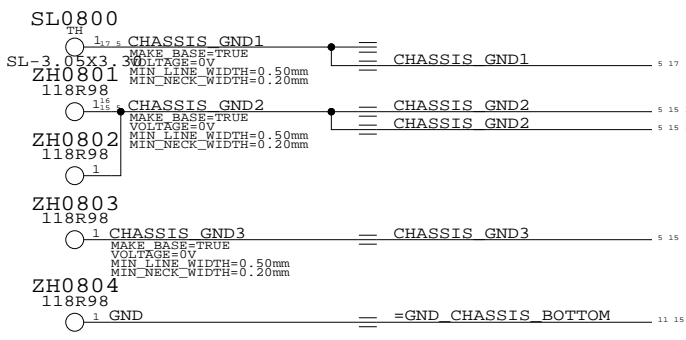
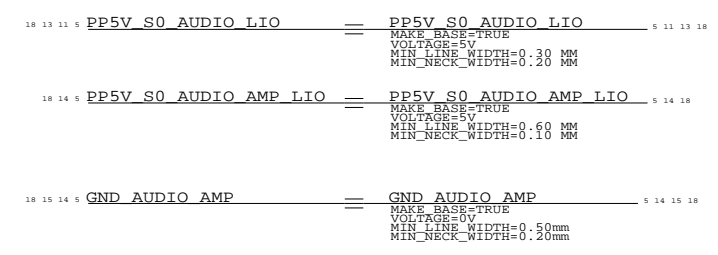
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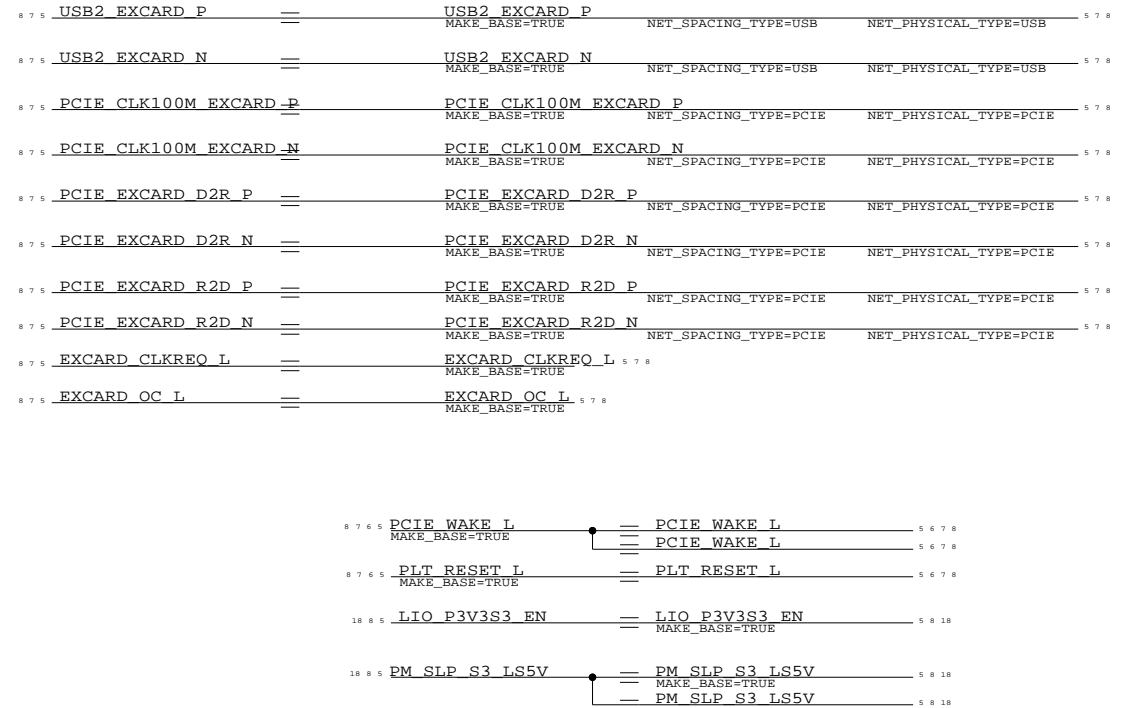
POWER & GROUNDS



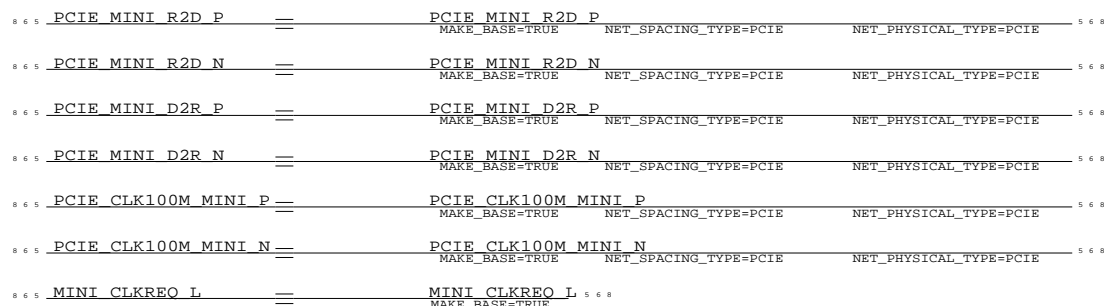
AUDIO POWER & GROUNDS



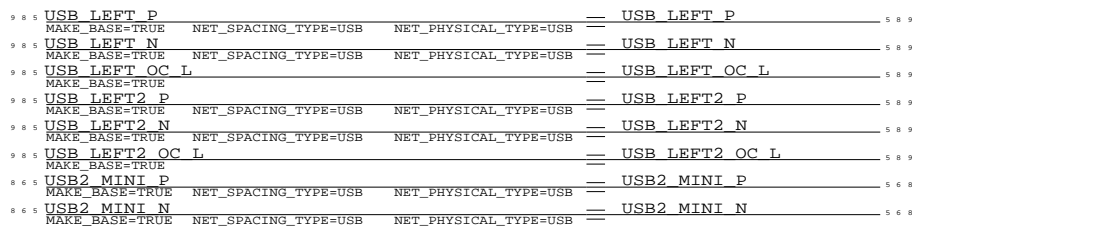
PCI-E EXPRESS CARD 34



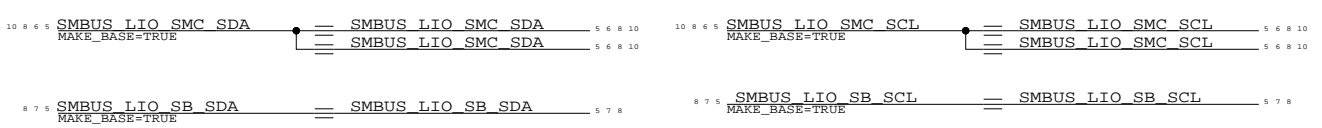
PCI-E MINICARD



USB



SMBUS



Aliases

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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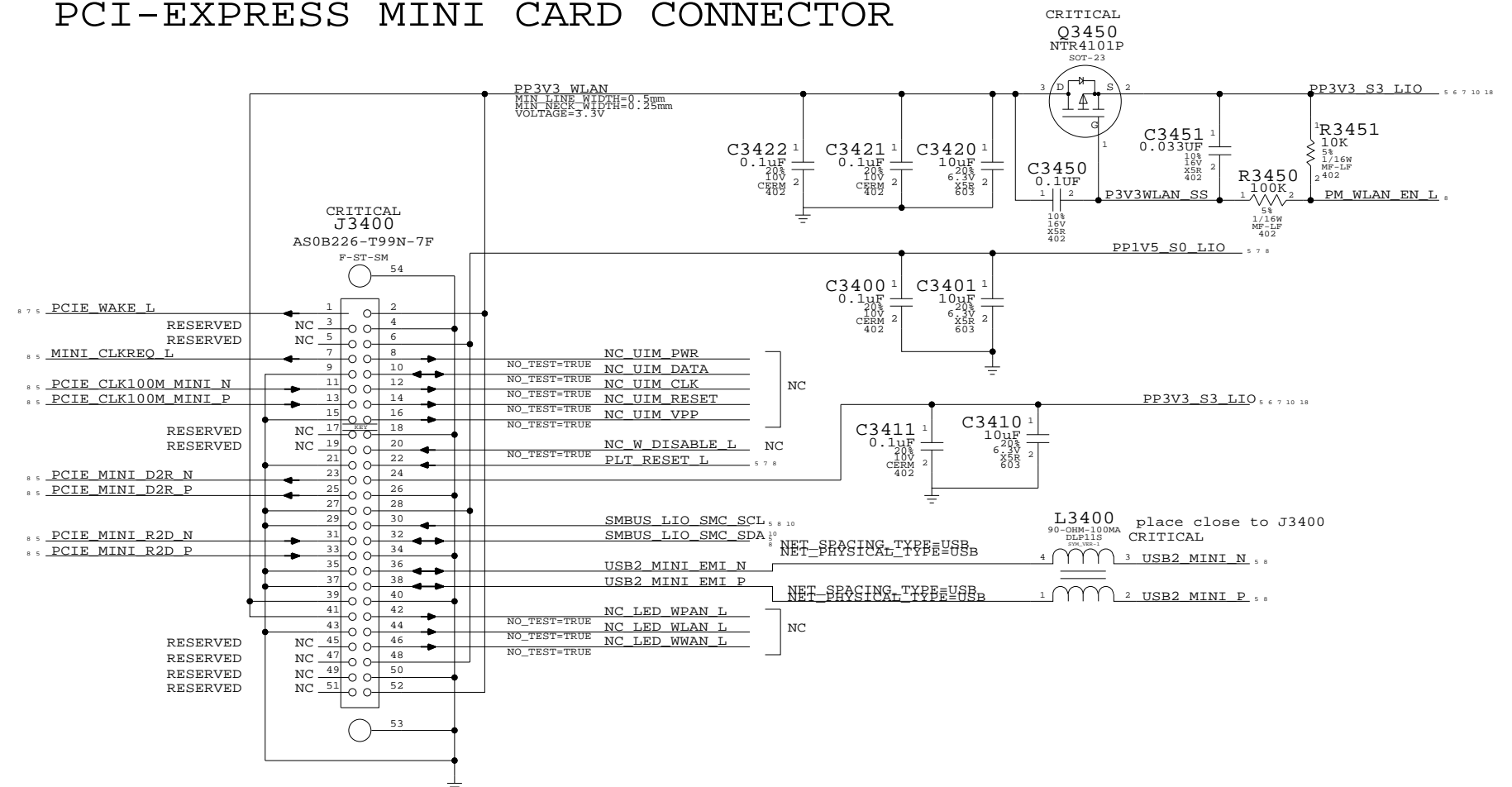
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APPLE INC.

SIZE	DRAWING NUMBER	REV.
D	051-7448	3.0.0
SCALE	SHT	OF
NONE	5	23

PCI-EXPRESS MINI CARD CONNECTOR



PCI-E MiniCard Connector

SYNC_MASTER=M75_LIO SYNC_DATE=04/10/2007

NOTICE OF PROPRIETARY PROPERTY

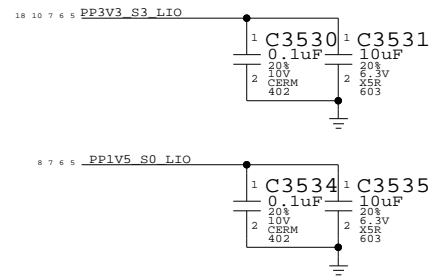
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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7448	3.0.0
SCALE	SHT 6 OF 23		
NONE			

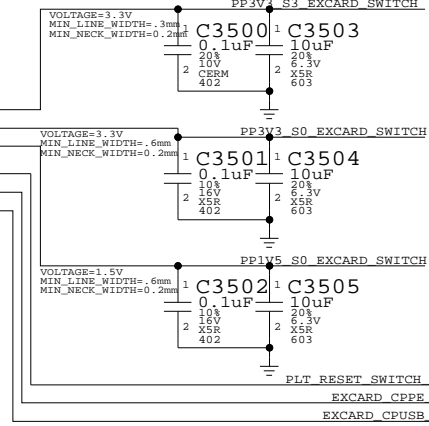
EXPRESSCARD/34 TOP MOUNT CONNECTOR

CARD CAGE CONNECTOR
CRITICAL
J3500
237-5300703903-2
P-RF-SM

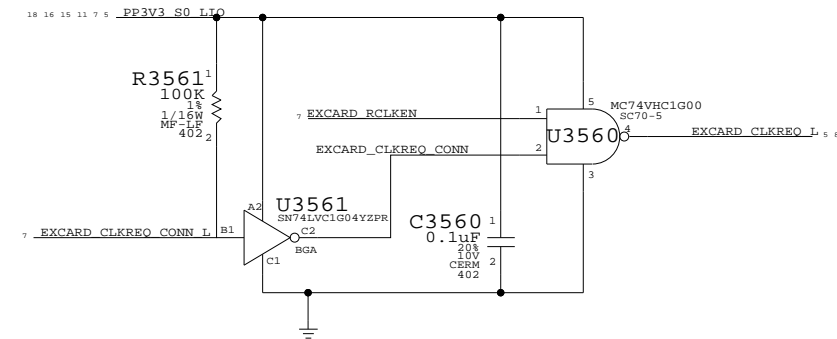
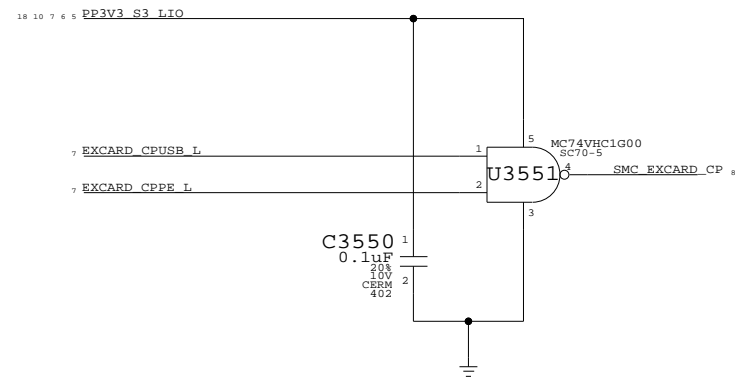
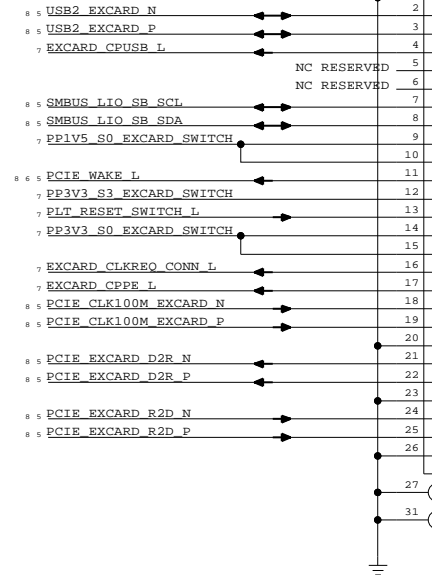
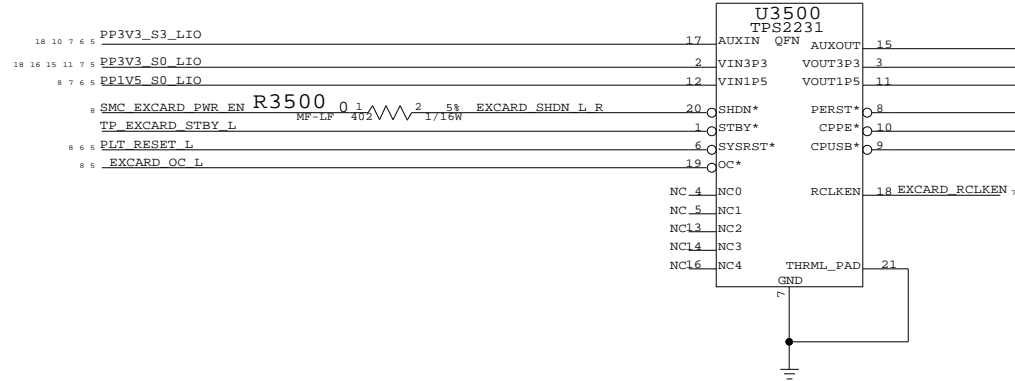
INPUT DECOUPLING



OUTPUT DECOUPLING



CRITICAL



ExpressCard Connector

SYNC_MASTER=M75_LIO SYNC_DATE=04/10/2007

NOTICE OF PROPRIETARY PROPERTY

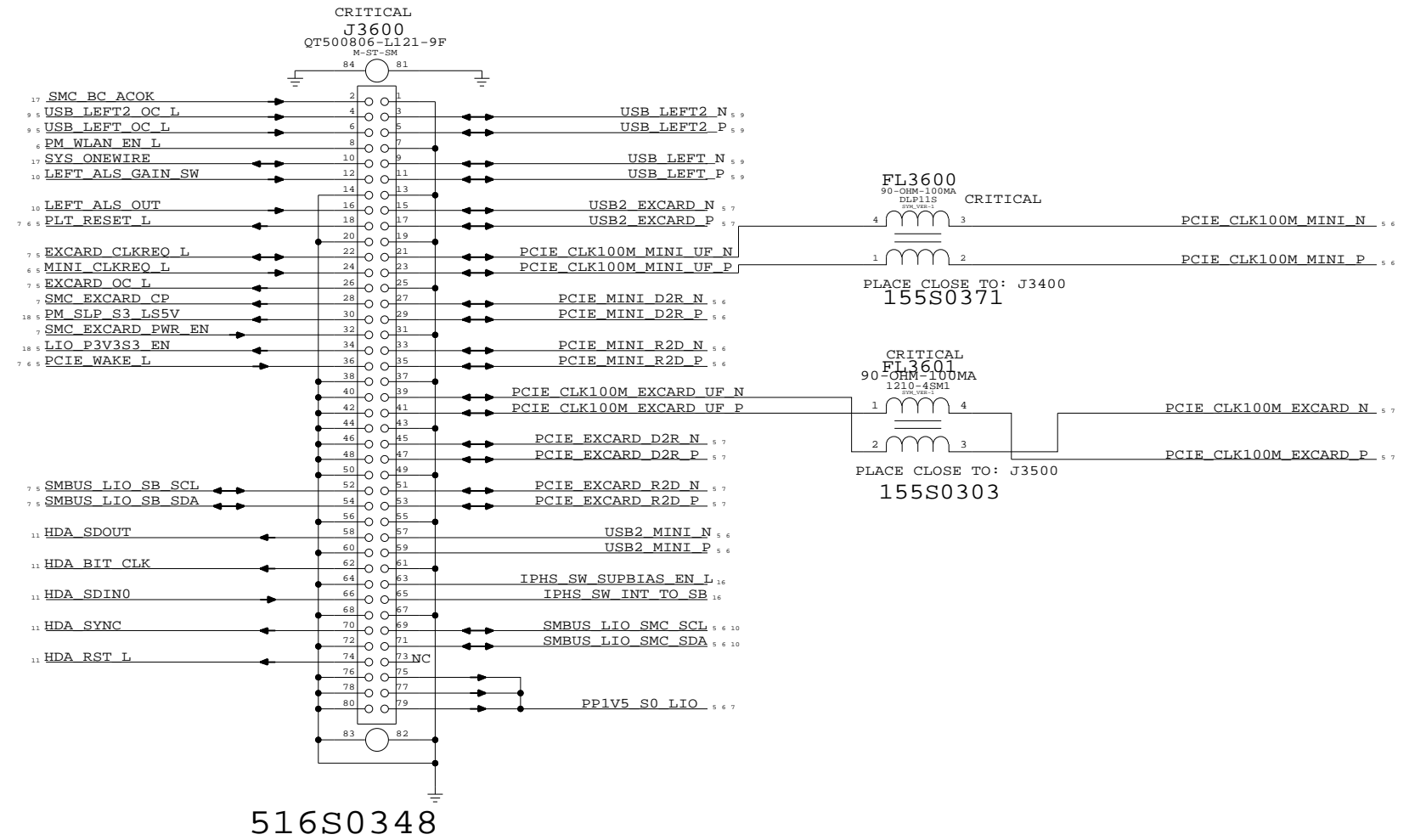
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SIZE	DRAWING NUMBER	REV.
D	051-7448	3.0.0
SCALE	SHT	OF
NONE	7	23

Left I/O Board Connector



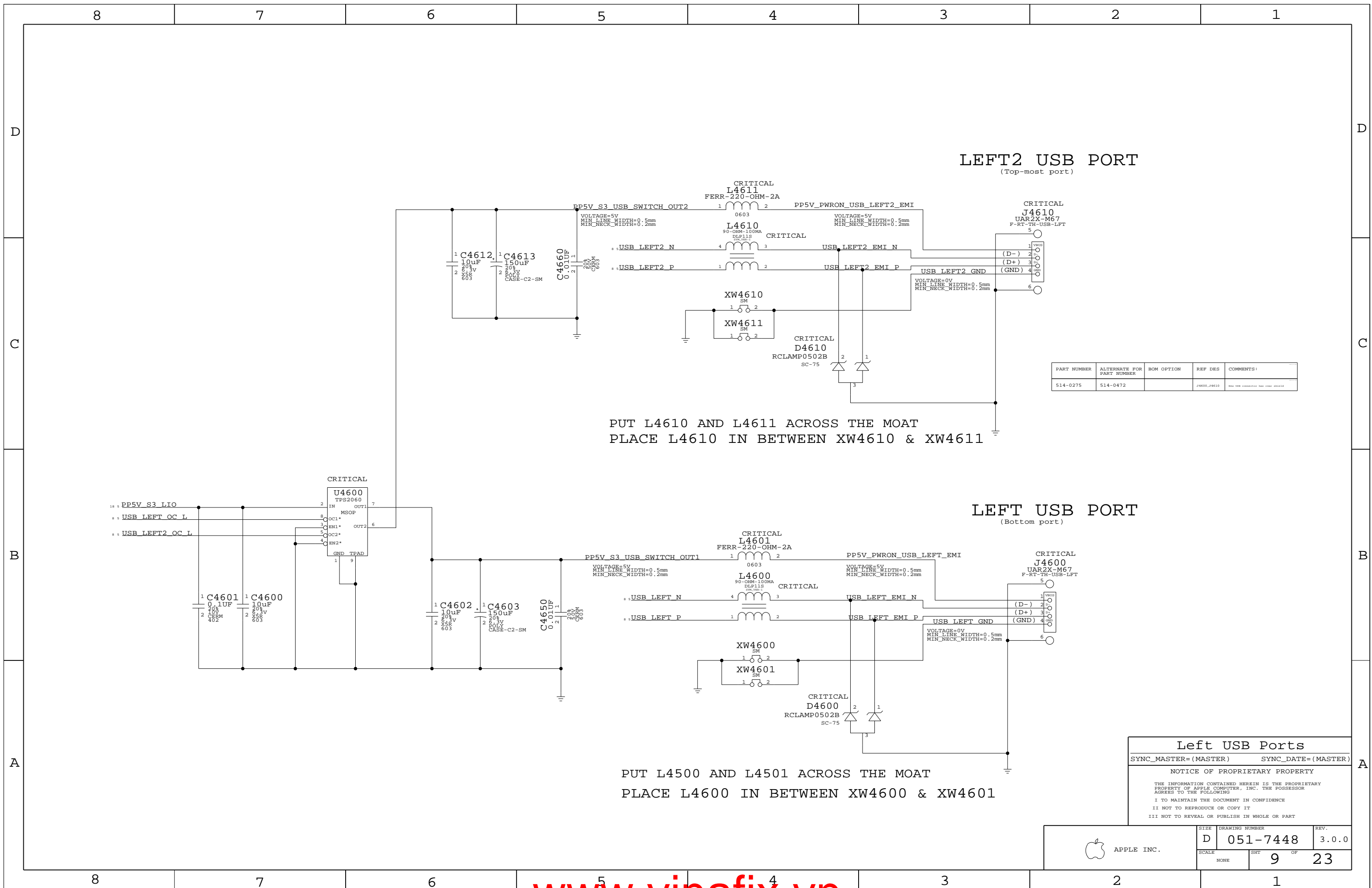
MLB I/O Board Connector

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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	D	051-7448	3.0.0
SCALE	SHT 8 OF 23		
NONE			



LEFT2 USB PORT
(Top-most port)

PUT L4610 AND L4611 ACROSS THE MOAT
PLACE L4610 IN BETWEEN XW4610 & XW4611

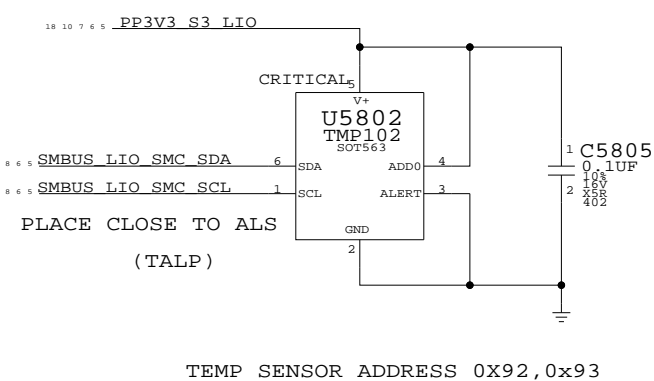
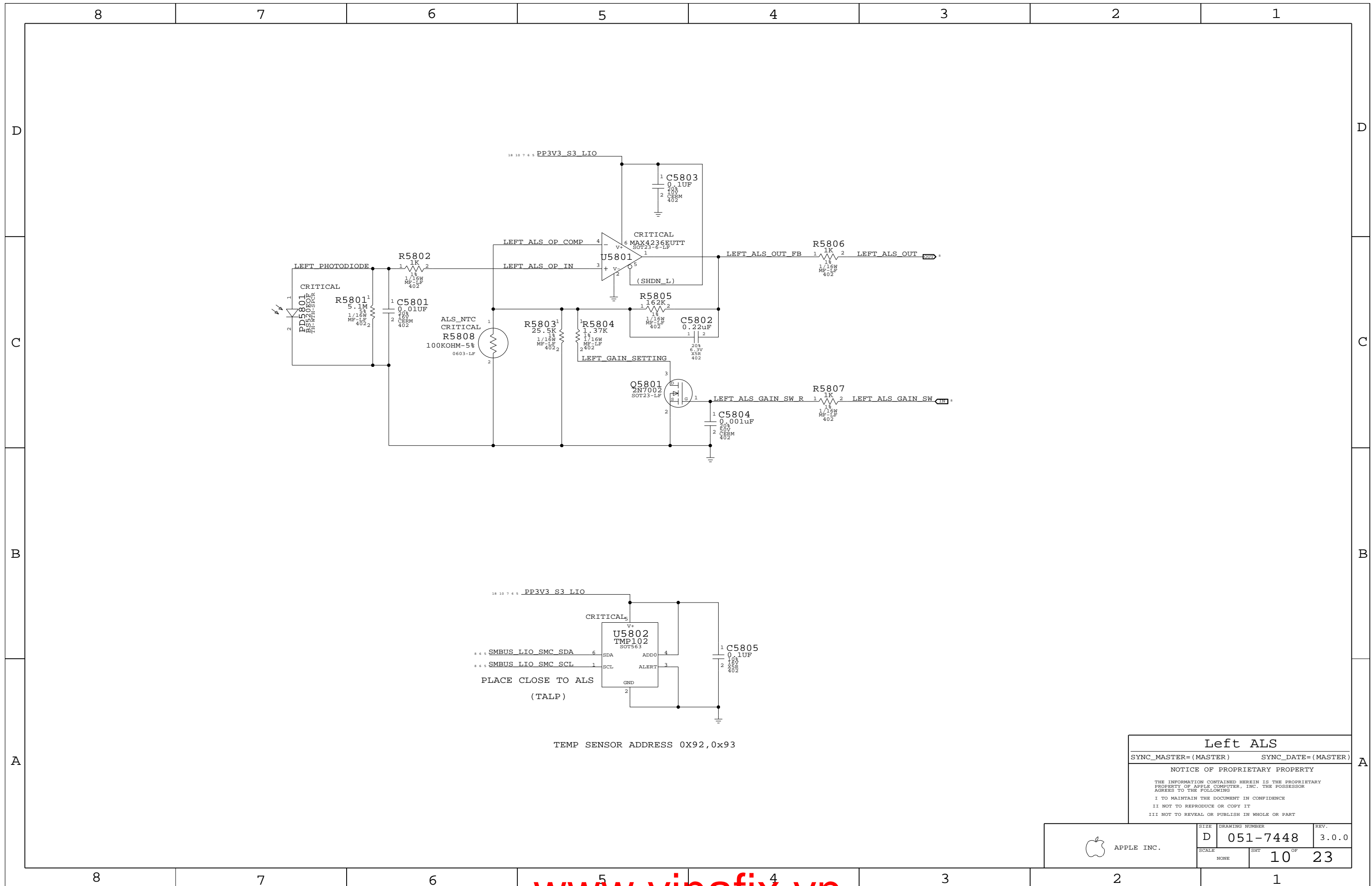
PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
514-0275	514-0472		J4600, J4610	NEW DES (CONVERTED FROM PART 0275)

LEFT USB PORT
(Bottom port)

PUT L4500 AND L4501 ACROSS THE MOAT
PLACE L4600 IN BETWEEN XW4600 & XW4601

Left USB Ports		
SYNC_MASTER=(MASTER)		SYNC_DATE=(MASTER)
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 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7448	3.0.0
SCALE	SHT	OF	
NONE	9	23	



Left ALS

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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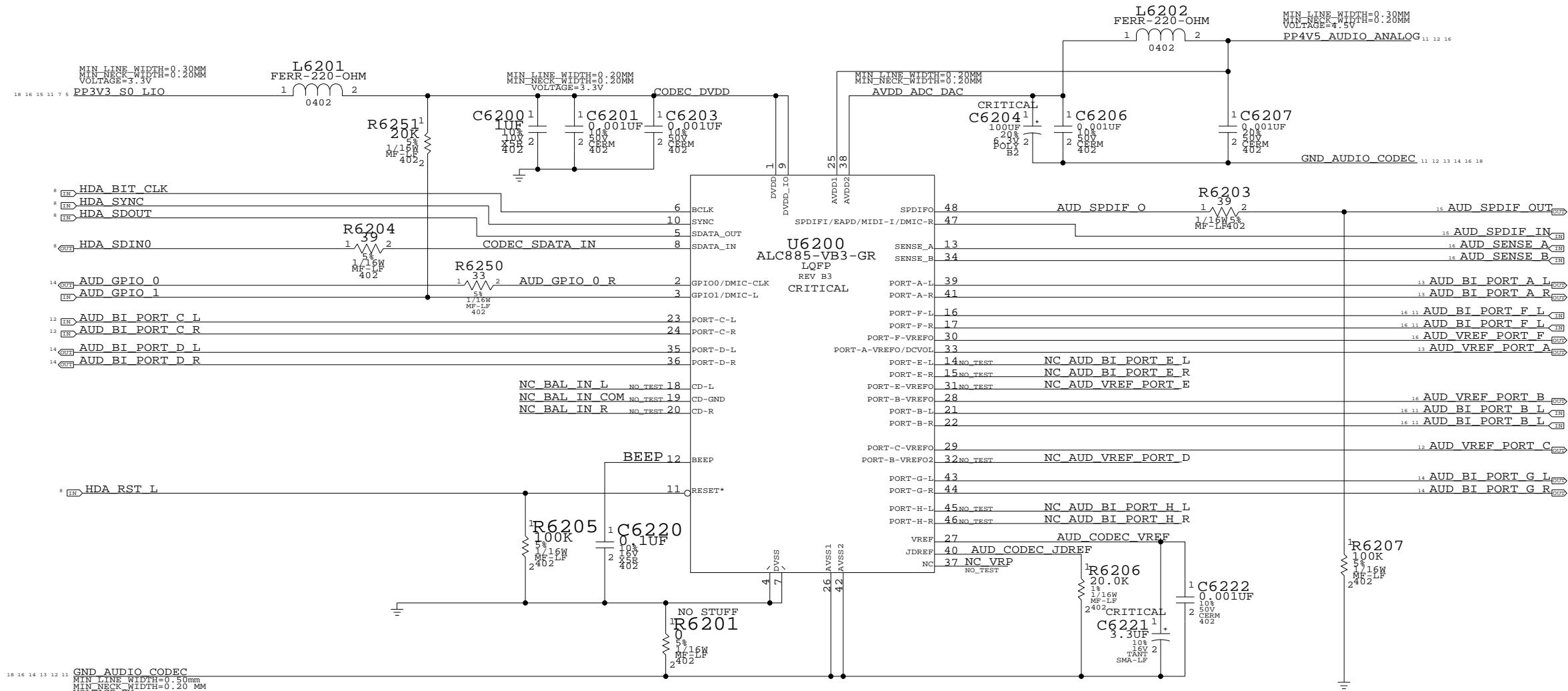
II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

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SCALE	SHT	OF	
NONE	10	23	

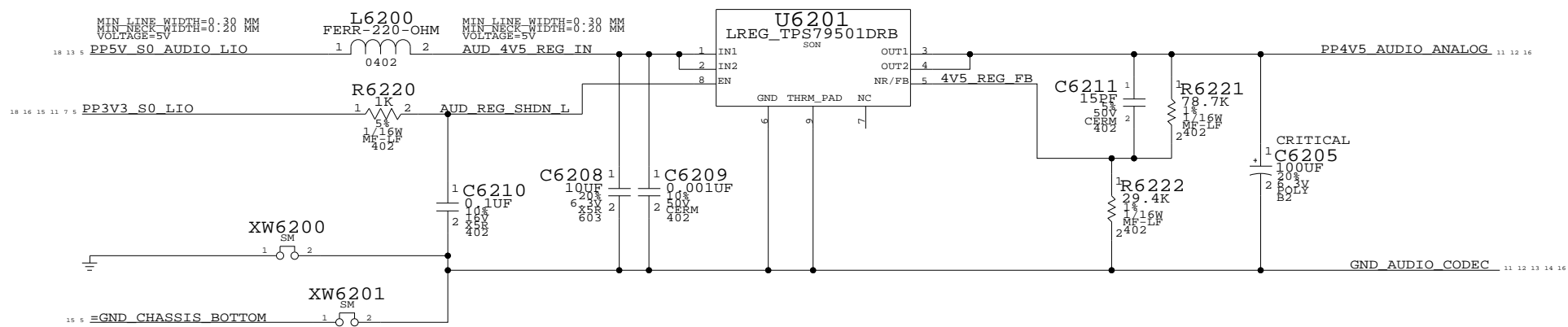
AUDIO CODEC

APPLE P/N 353S1527



AUDIO 4.5V REGULATOR

APPLE P/N 353S1576



AUDIO:CODEC

SYNC_MASTER=M88_AUDIO SYNC_DATE=08/30/2007

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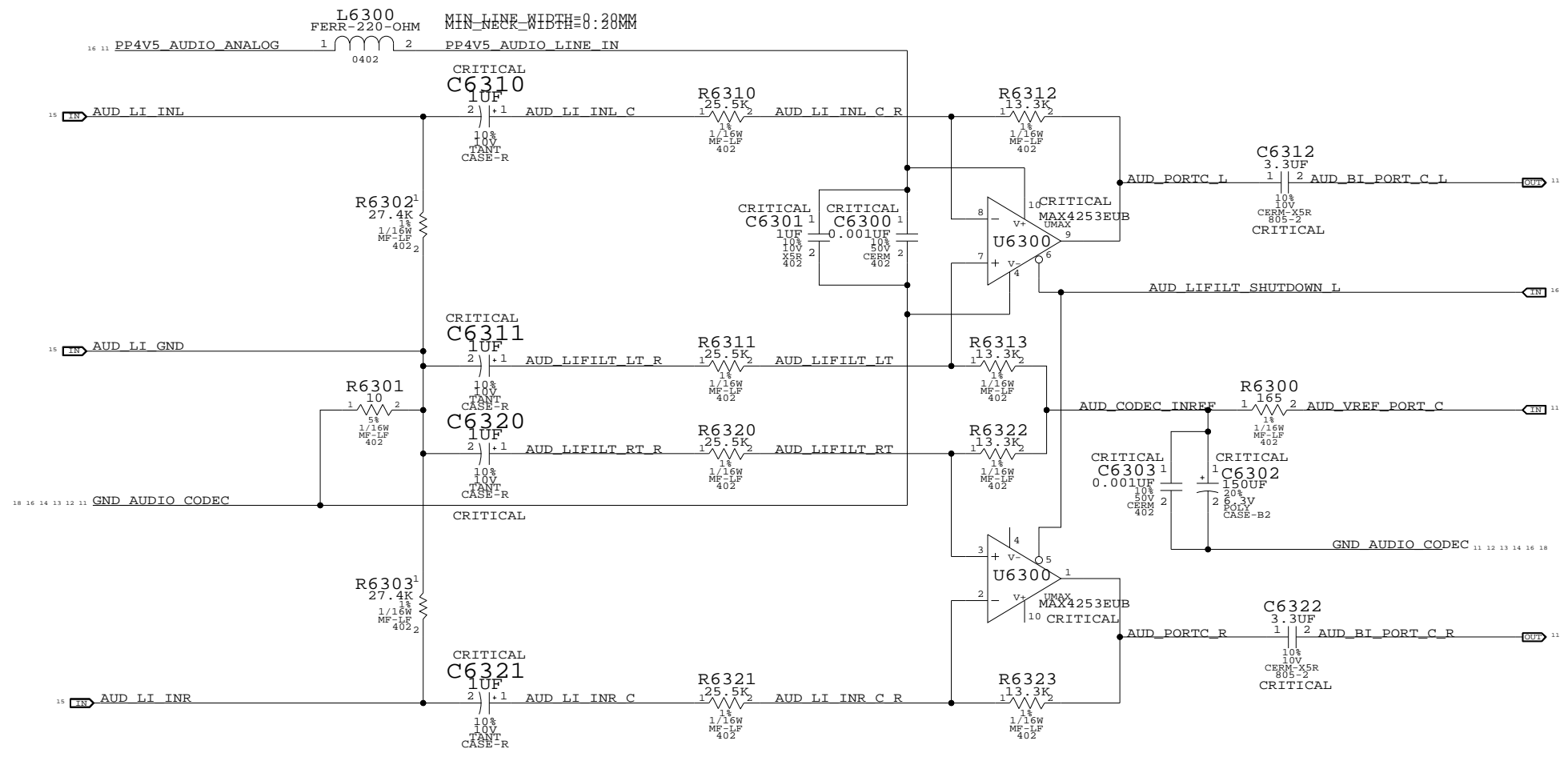
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APPLE INC.	SIZE D	DRAWING NUMBER 051-7448	REV. 3.0.0
	SCALE NONE	SHEET 11 OF 23	

Pseudo-Diff Line-In Filter
 GAIN = -5.4DB AV = 0.52
 FC = 6.24 HZ



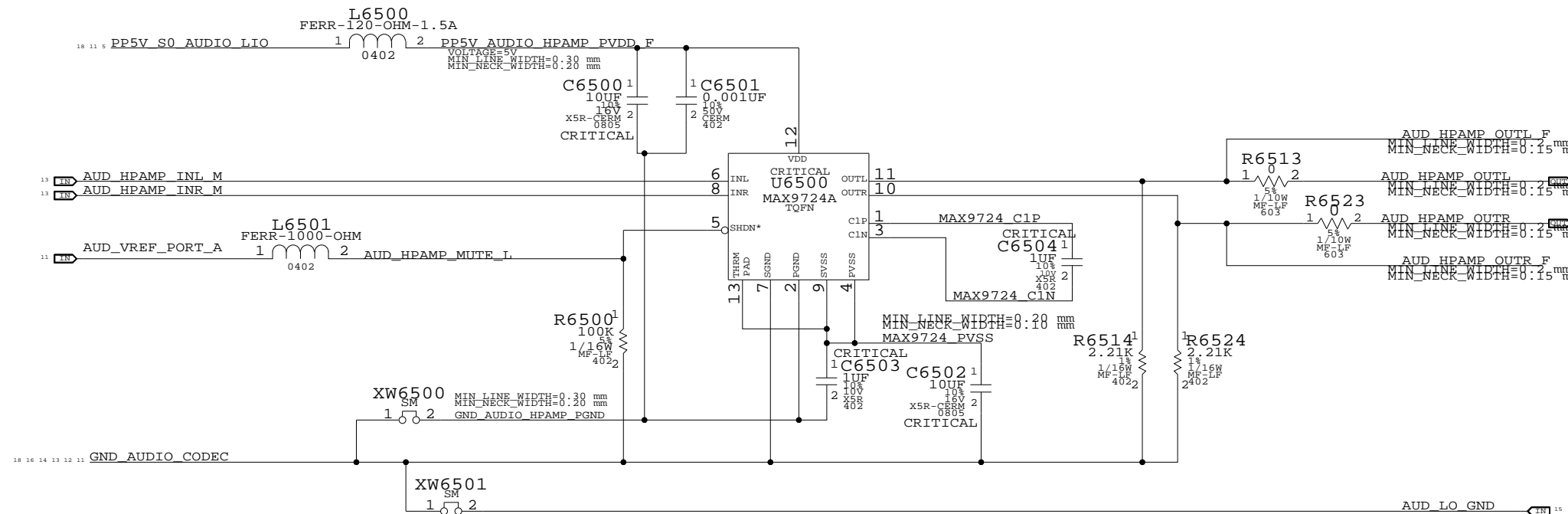
AUDIO: LINE IN
 SYNC_MASTER=M88_AUDIO SYNC_DATE=08/30/2007

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SCALE	SHT		OF
NONE	12		23

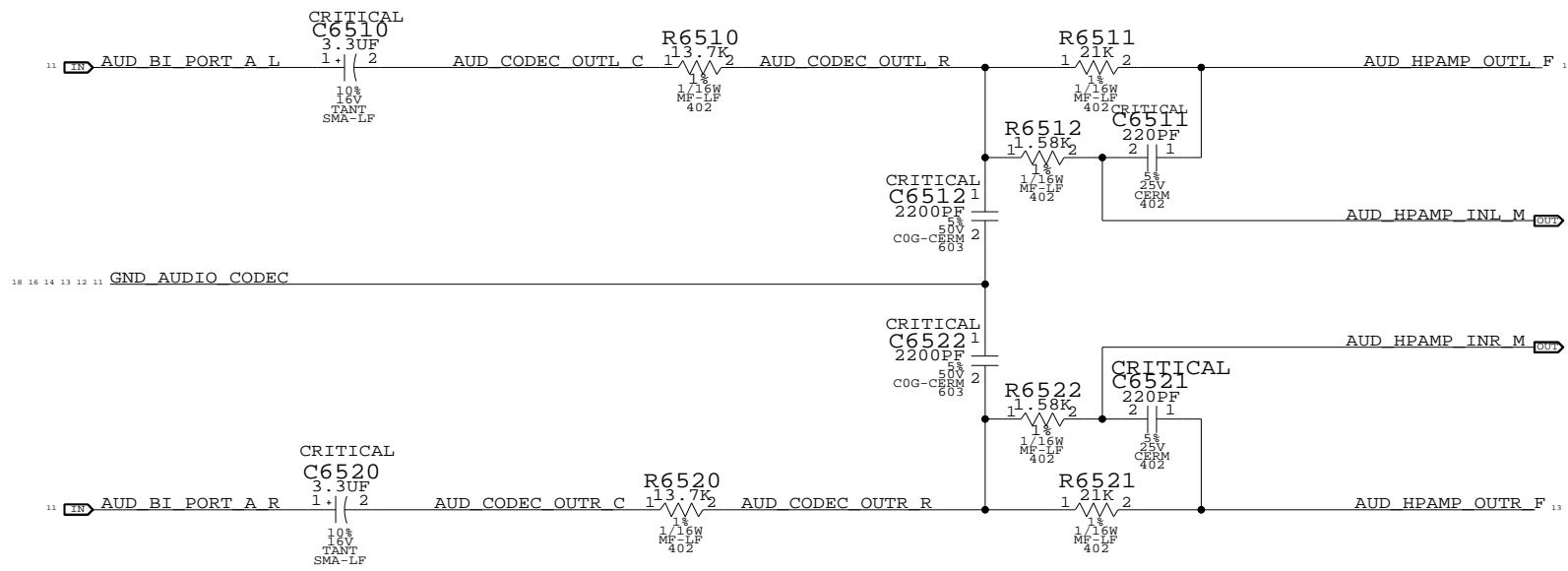
Headphone Amplifier (MAX9724A)

APN: 353S1637
VOLTAGE GAIN: 1.53



2nd Order DAC Filter

HP: 3.52 HZ LP: 39 KHZ

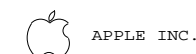


AUDIO: HEADPHONE AMP

SYNC_MASTER=M88_AUDIO SYNC_DATE=08/30/2007

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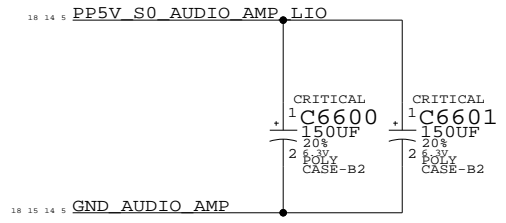
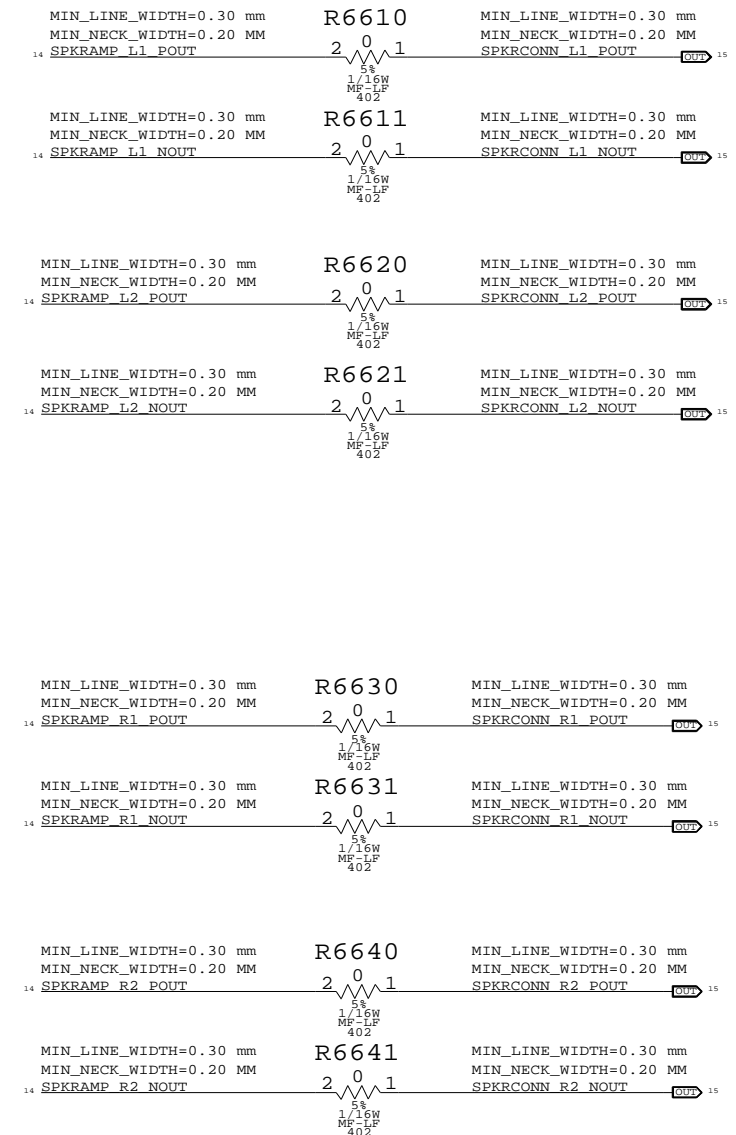
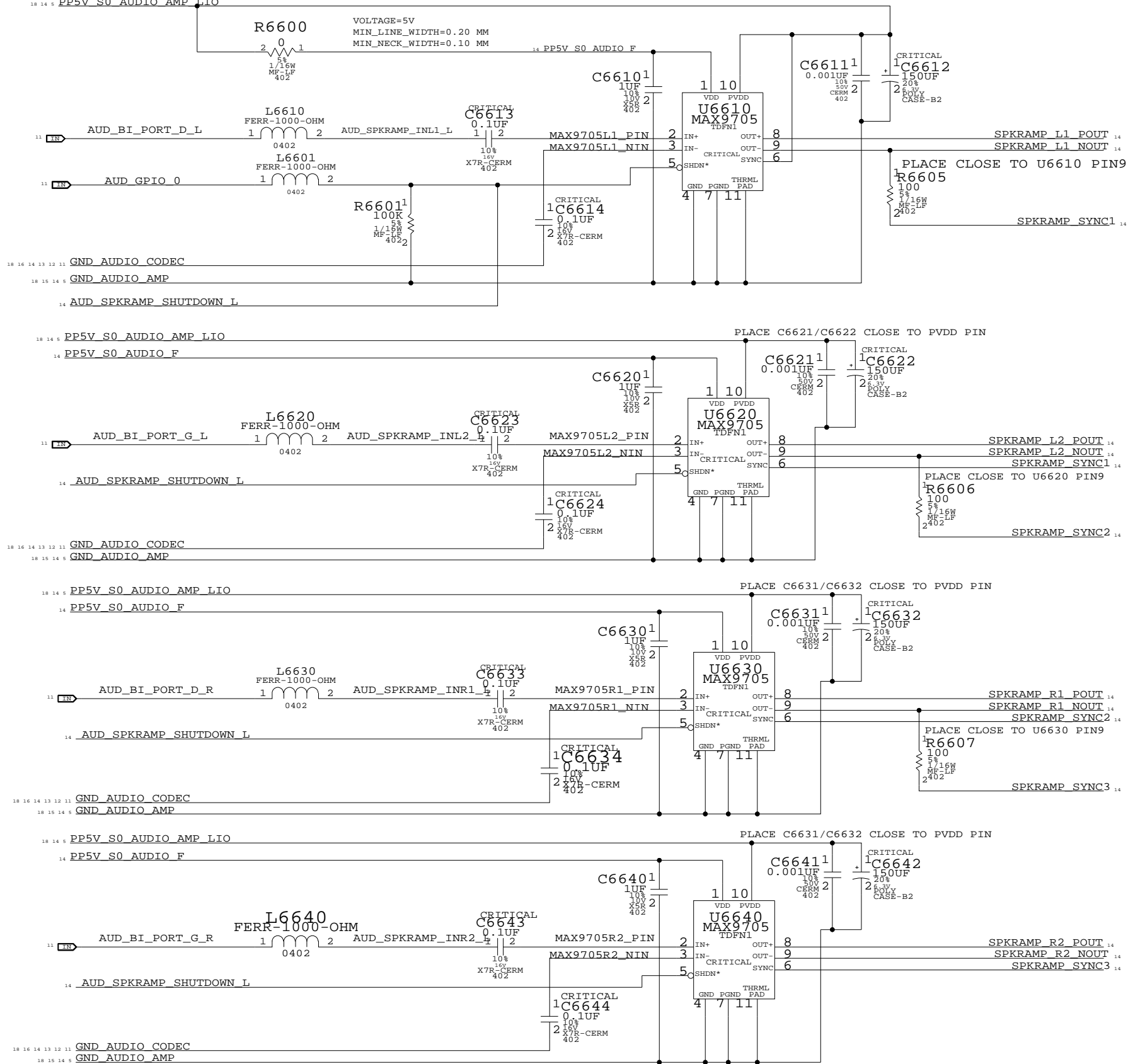
SIZE	DRAWING NUMBER	REV.
D	051-7448	3.0.0
SCALE	SHT	OF
NONE	13	23

2X MONO SPEAKER AMPLIFIERS (MAX9705B) GAIN = 12DB 79HZ < FC < 132HZ
 APN: 353S1595 TURN ON TIME: 30MS, TURN ON DELAY: 60MS

VOLTAGE=5V
 MIN_LINE_WIDTH=0.60 MM
 MIN_NECK_WIDTH=0.10 MM

PLACE C6611/C6612 CLOSE TO PVDD PIN

FANCY SPEAKER TESTPOINTS



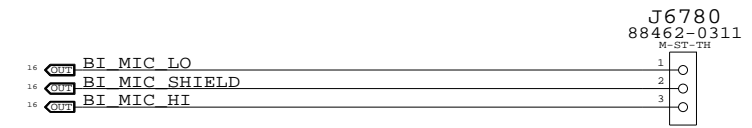
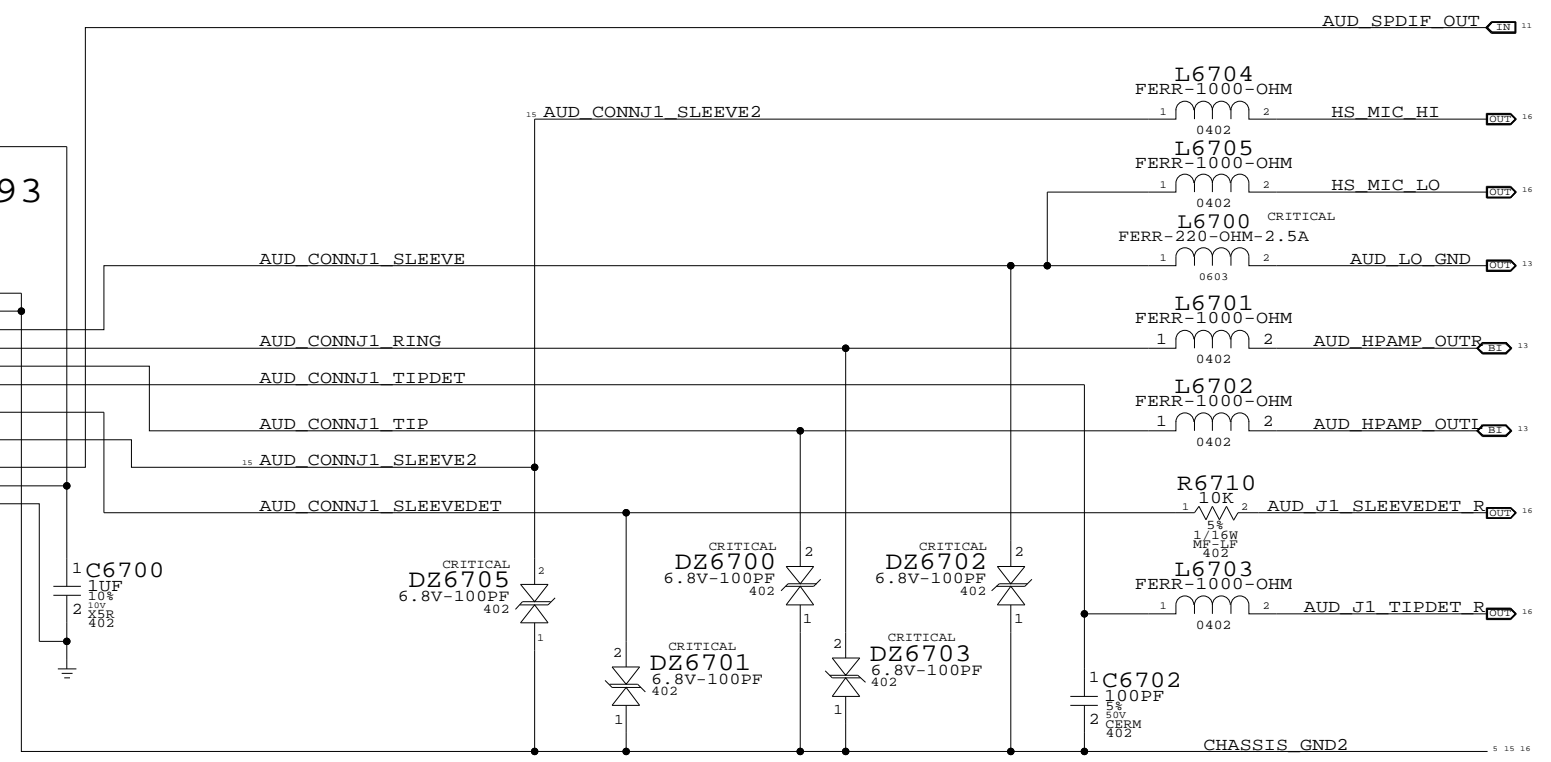
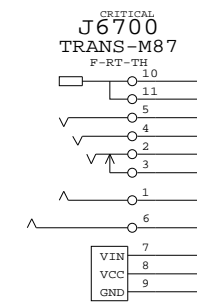
AUDIO: SPEAKER AMP
 SYNC_MASTER=M88_AUDIO SYNC_DATE=08/30/2007
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SCALE	SHT	14 OF 23	
NONE			

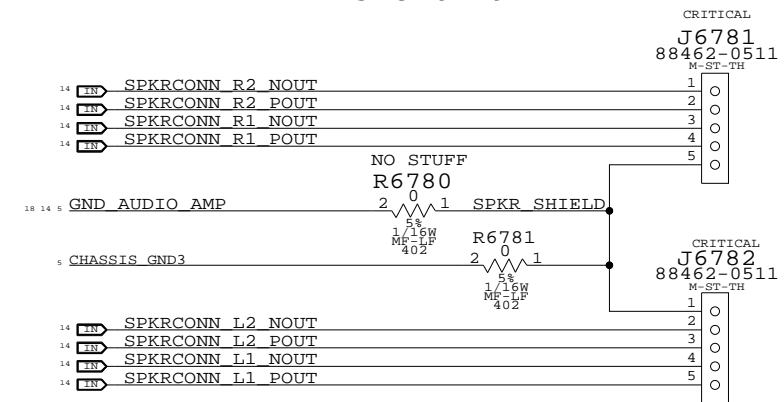
AUDIO JACK 1 LO/HP CONNECTOR, SPDIF TX

BUILT-IN MIC CONNECTOR
APN: 518-0230

18 16 15 11 7 5 PP3V3 S0 LIO
APN: 514-0493



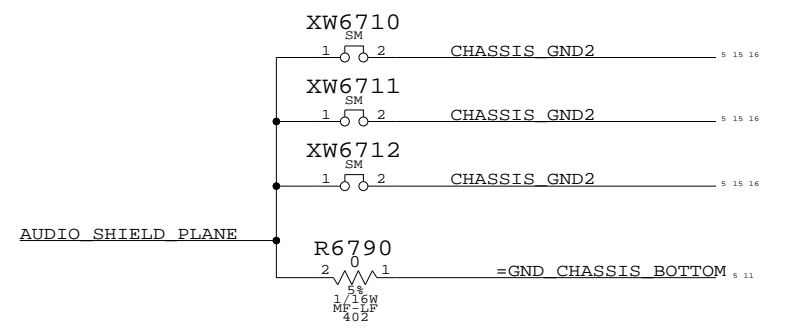
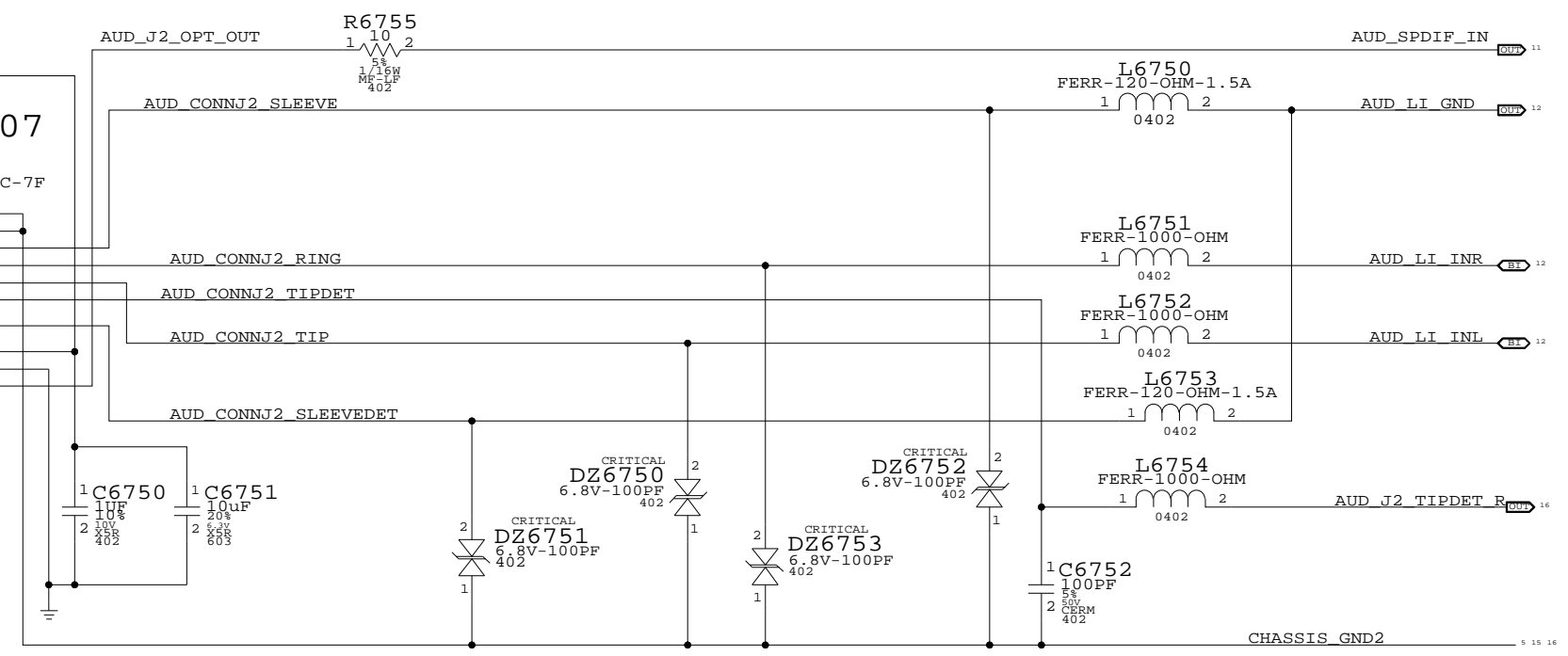
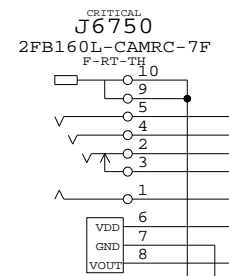
SPEAKER CONNECTORS
APN: 518-0229



AUDIO JACK 2 LINE IN CONNECTOR, SPDIF RX

AUDIO SHIELD
(FILLED SHAPE)

18 16 15 11 7 5 PP3V3 S0 LIO
APN: 514-0407



AUDIO: JACKS
SYNC_MASTER=M88_AUDIO SYNC_DATE=08/30/2007
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	D	051-7448	3.0.0
SCALE		SHT	
NONE		15 OF 23	

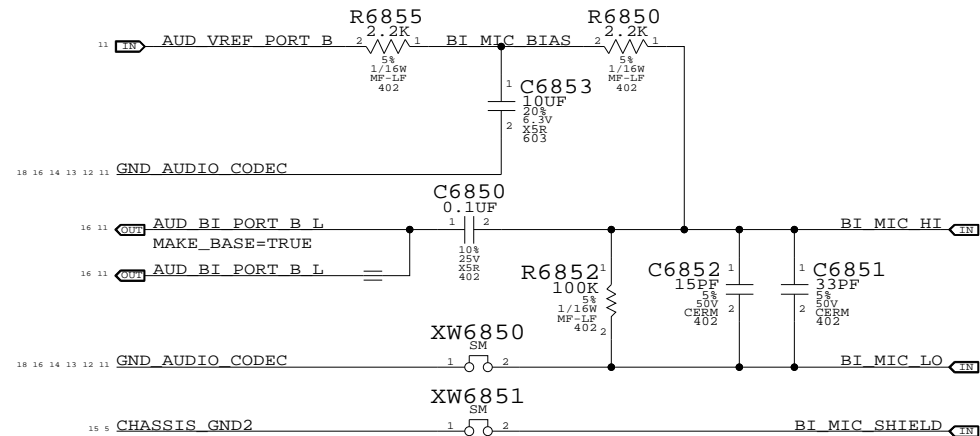
CODEC OUTPUT SIGNAL PATHS

FUNCTION	VOLUME	CONVERTER	PIN COMPLEX	MUTE CONTROL	DET ASSIGNMENT
HP/LINE OUT	0X0F (15)	0X05 (05)	0X15 (21,A)	VREF A (21)	0X15 (21,A)
SPEAKER 1	0X26 (38)	0X25 (37)	0X14 (20,D)	GPIO_0	N/A
SPEAKER 2	0X04 (14)	0X04 (04)	0X16 (22,G)	GPIO_0	N/A
SPDIF OUT	N/A	0X06 (06)	0X1E (30,SPDIFTX)	N/A	0X1B (27,E)

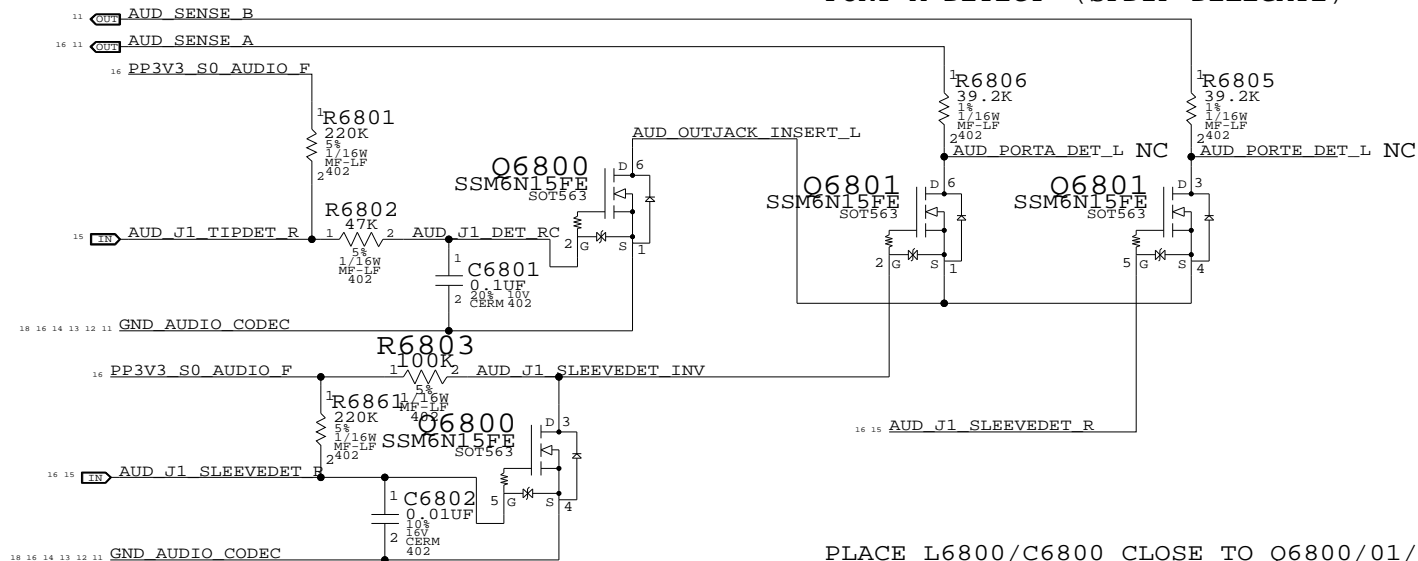
CODEC INPUT SIGNAL PATHS

FUNCTION	MIXER (INPUT)	CONVERTER	PIN COMPLEX	VREF	DET ASSIGNMENT
LINE IN	0X23 (35)	0X08 (08)	0X1A (26,C)	N/A	0X1A (26,C)
SPDIF IN	N/A	0X0A (10)	0X1F (31,SPDIFRX)	N/A	N/A
MIC (BUILT IN)	0X24 (36)	0X07 (07)	0X18 (24,B)	B (80%)	N/A
MIC (HEADSET)	0X24 (36)	0X07 (07)	0X19 (25,F)	F (80%)	SouthBridge

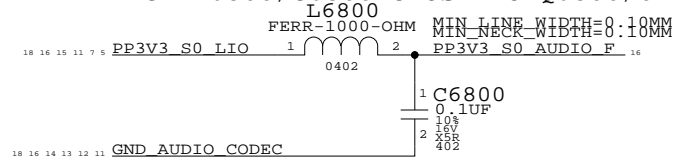
PORT B (BUILT-IN MIC)



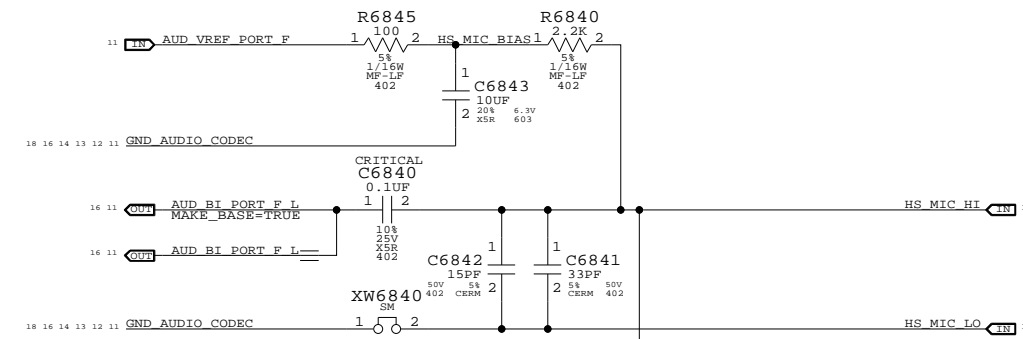
PORT A DETECT (SPDIF DELEGATE)



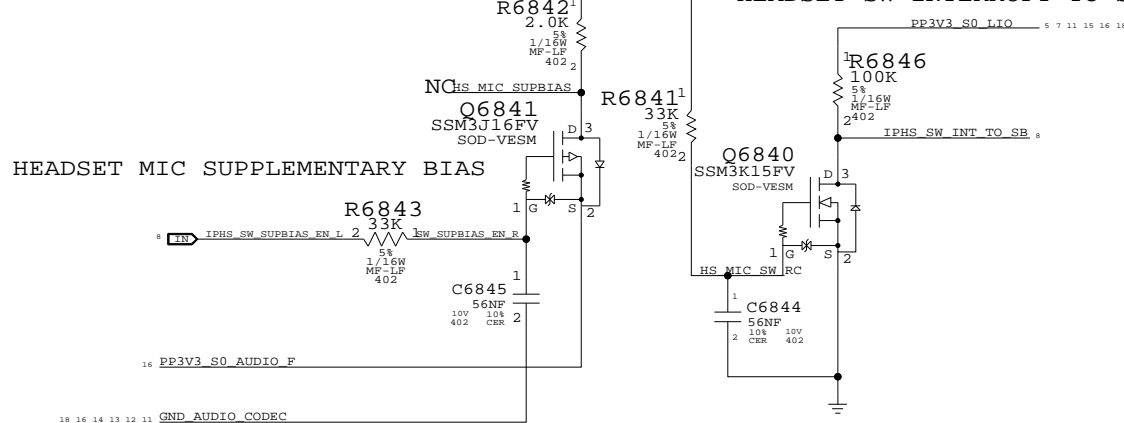
PLACE L6800/C6800 CLOSE TO Q6800/01/02/03



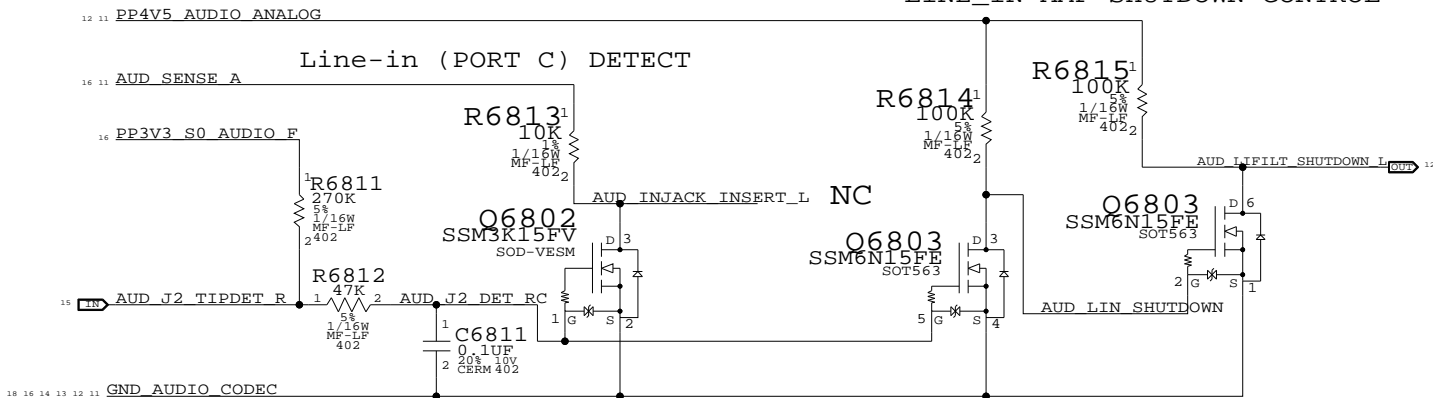
PORT F (HEADSET MIC)



HEADSET SW INTERRUPT TO SOUTHBRIDGE



LINE_IN AMP SHUTDOWN CONTROL



AUDIO: JACK TRANSLATORS

SYNC_MASTER=M88_AUDIO SYNC_DATE=08/30/2007

NOTICE OF PROPRIETARY PROPERTY

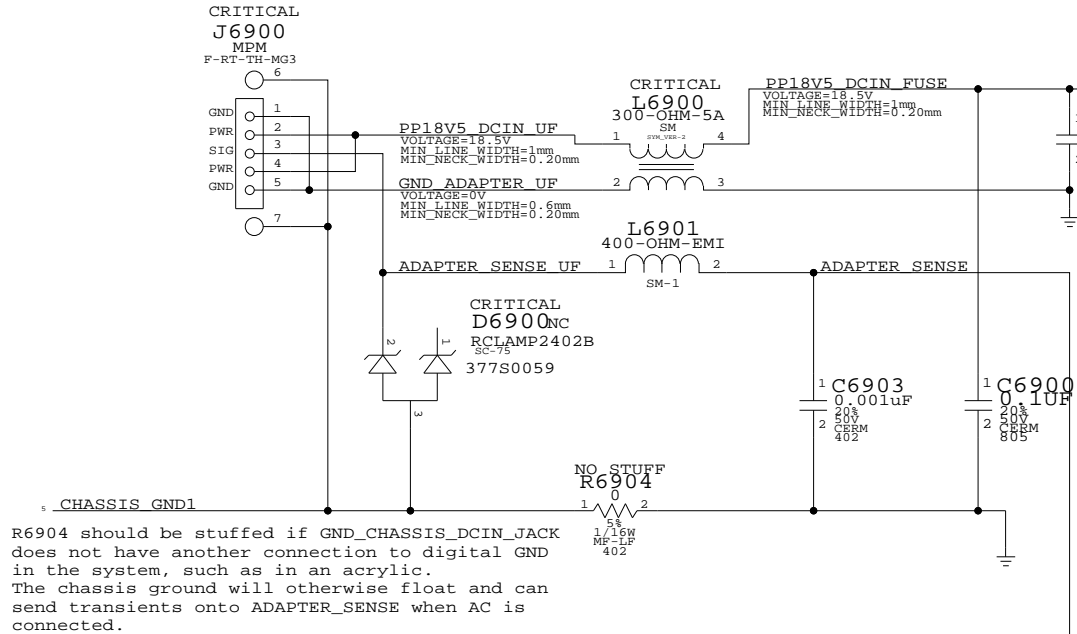
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APPLE INC.

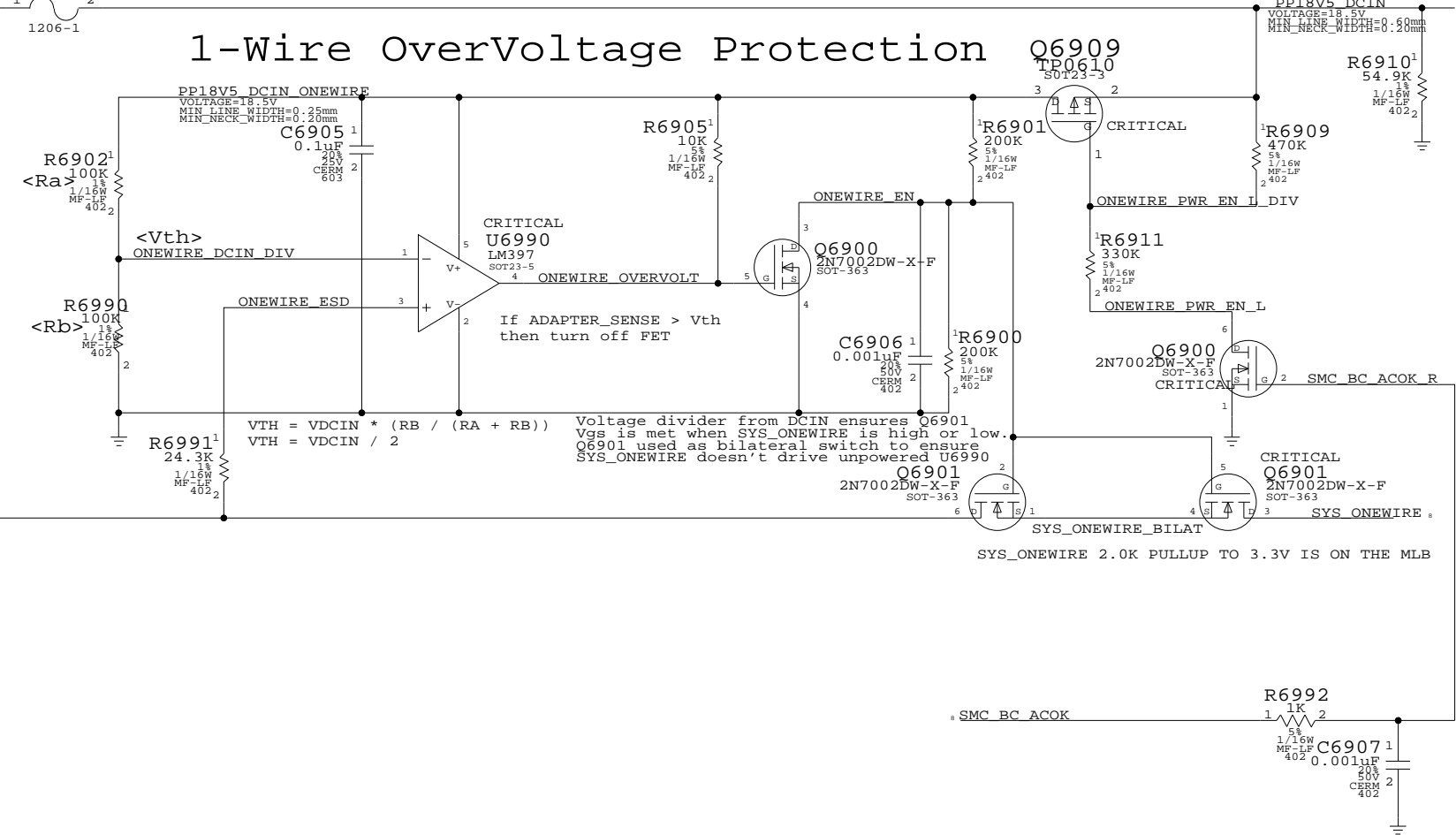
SIZE	DRAWING NUMBER	REV.
D	051-7448	3.0.0
SCALE	SHT	OF
NONE	16	23

DC Power Jack



R6904 should be stuffed if GND_CHASSIS_DCIN_JACK does not have another connection to digital GND in the system, such as in an acrylic. The chassis ground will otherwise float and can send transients onto ADAPTER_SENSE when AC is connected.

CRITICAL
F6900
6AMP-24V



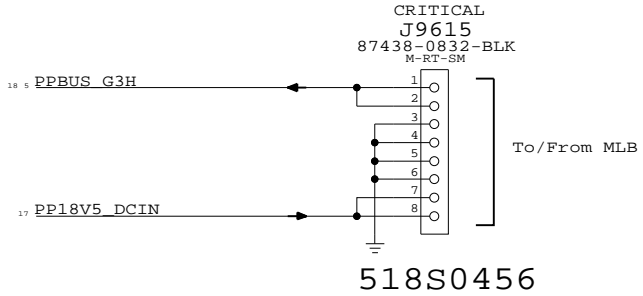
Q6909 restricts system load to 10K-70K window until adapter detects system and enables 16.5V output.

R6902¹
<Ra>
100K
1/16W
MF-LP
402 2

R6990¹
<Rb>
100K
1/16W
MF-LP
402 2

R6991¹
24.3K
1/16W
MF-LP
402 2

Left I/O Power Connector

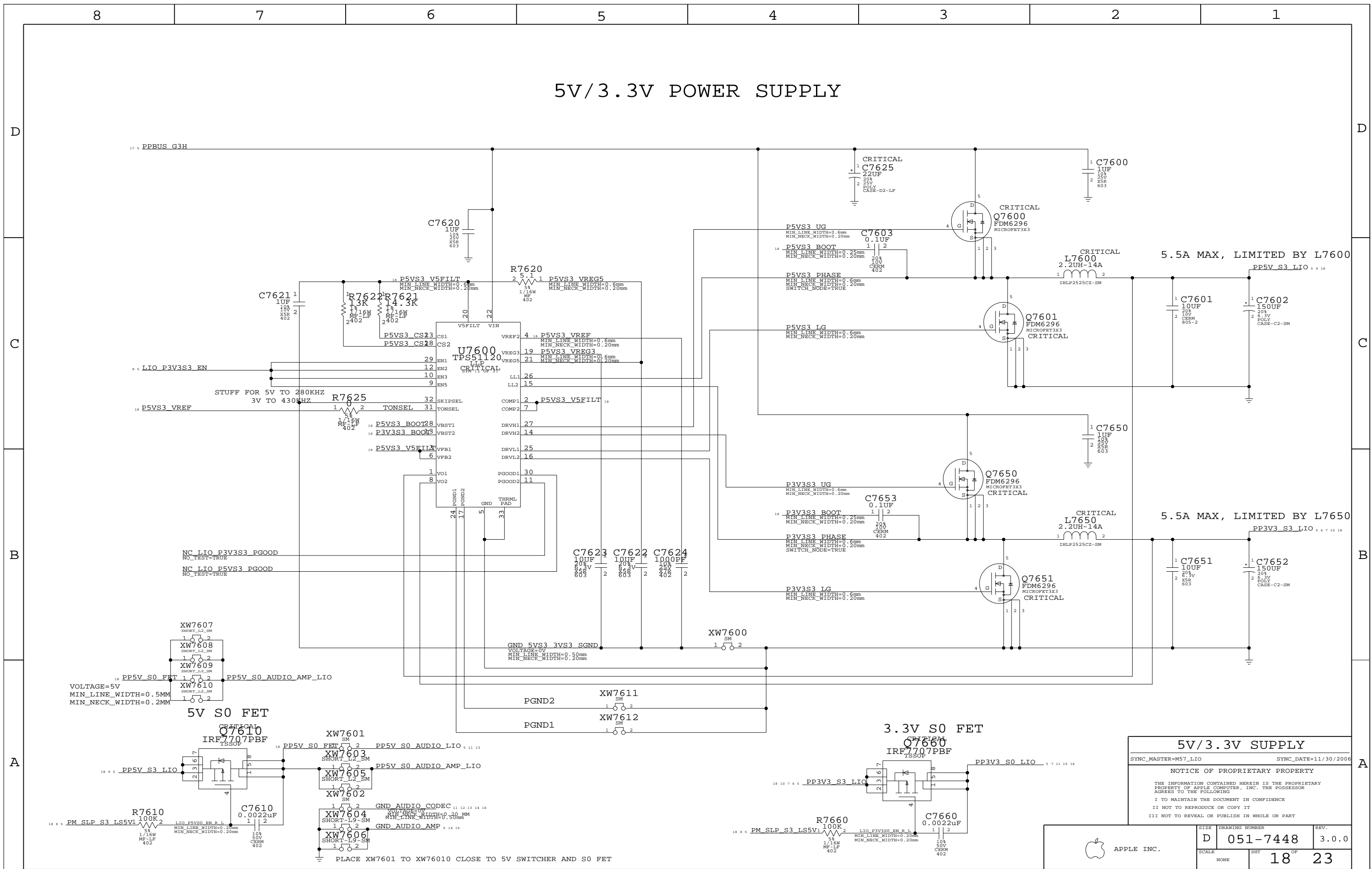


DC-In & PBUS Connectors

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)
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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7448	3.0.0
SCALE	NONE	SHT	17 OF 23

5V/3.3V POWER SUPPLY



5V/3.3V SUPPLY

SYNC_MASTER=M57_LIO SYNC_DATE=11/30/2006

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	D	051-7448	3.0.0
SCALE	SHEET		OF
NONE	18		23

	8	7	6	5	4	3	2	1
	<p>Title: Cref Part Report Design: lio Date: Jul 25 14:31:21 2007</p> <p>C3400 CAP_402 lio[6C3] C3401 CAP_603 lio[6C3] C3410 CAP_603 lio[6C3] C3411 CAP_402 lio[6C3] C3420 CAP_603 lio[6C3] C3421 CAP_402 lio[6C3] C3422 CAP_402 lio[6C3] C3450 CAP_402 lio[6C3] C3451 CAP_603 lio[6C2] C3500 CAP_402 lio[7C4] C3501 CAP_402 lio[7C4] C3502 CAP_402 lio[7C4] C3503 CAP_603 lio[7C4] C3504 CAP_603 lio[7C4] C3505 CAP_603 lio[7C4] C3530 CAP_402 lio[7C8] C3531 CAP_603 lio[7C7] C3534 CAP_402 lio[7C8] C3535 CAP_603 lio[7C7] C3550 CAP_402 lio[7A7] C3560 CAP_402 lio[7A3] C4600 CAP_603 lio[9B7] C4601 CAP_402 lio[9B7] C4602 CAP_603 lio[9B6] C4603 CAP_P_CASE-C2-SM lio[9B6] C4612 CAP_603 lio[9C6] C4613 CAP_P_CASE-C2-SM lio[9C6] C4650 CAP_603 lio[9B5] C4660 CAP_603 lio[9C5] C5801 CAP_402 lio[10C6] C5802 CAP_402 lio[10C4] C5803 CAP_402 lio[10D5] C5804 CAP_402 lio[10C4] C5805 CAP_402 lio[10A4] C6200 CAP_402 lio[11D6] C6201 CAP_402 lio[11D6] C6203 CAP_402 lio[11D6] C6204 CAP_P_CASE-A3-LF lio[11D4] C6205 CAP_P_CASE-A3-LF lio[11A4] C6206 CAP_402 lio[11D4] C6207 CAP_402 lio[11D3] C6208 CAP_603 lio[11A6] C6209 CAP_402 lio[11A5] C6210 CAP_402 lio[11A6] C6220 CAP_402 lio[11B6] C6221 CAP_P_SMA-LF lio[11B4] C6222 CAP_402 lio[11B4] C6300 CAP_402 lio[12C4] C6301 CAP_402 lio[12C4] C6302 CAP_P_CASE-B2 lio[12B3] C6303 CAP_402 lio[12B3] C6310 CAP_P_CASE-R lio[12C5] C6311 CAP_P_CASE-R lio[12C5] C6312 CAP_805-1 lio[12C3] C6320 CAP_P_CASE-R lio[12B5] C6321 CAP_P_CASE-R lio[12B5] C6322 CAP_805-1 lio[12B3] C6500 CAP_0805 lio[13D5] C6501 CAP_402 lio[13D5] C6502 CAP_0805 lio[13C4] C6503 CAP_402 lio[13C4] C6504 CAP_603 lio[13C3] C6510 CAP_P_SMA-LF lio[13B6] C6511 CAP_402 lio[13B4] C6512 CAP_603 lio[13B4] C6520 CAP_P_SMA-LF lio[13A6] C6521 CAP_402 lio[13A4] C6522 CAP_603 lio[13B4] C6600 CAP_P_CASE-B2 lio[14A3] C6601 CAP_P_CASE-B2 lio[14A2] C6610 CAP_402 lio[14D5] C6611 CAP_402 lio[14D4] C6612 CAP_P_CASE-B2 lio[14D4] C6613 CAP_402 lio[14D6] C6614 CAP_402 lio[14C6] C6620 CAP_402 lio[14C5] C6621 CAP_402 lio[14C5] C6622 CAP_P_CASE-B2 lio[14C4] C6623 CAP_402 lio[14C6] C6624 CAP_402 lio[14B6] C6630 CAP_402 lio[14B5] C6631 CAP_402 lio[14B5] C6632 CAP_P_CASE-B2 lio[14B4] C6633 CAP_402 lio[14B6] C6634 CAP_402 lio[14B6] C6640 CAP_402 lio[14A5] C6641 CAP_402 lio[14A5] C6642 CAP_P_CASE-B2 lio[14A4] C6643 CAP_402 lio[14A6] C6644 CAP_603 lio[14A6] C6700 CAP_402 lio[15C7] C6702 CAP_402 lio[15C5] C6750 CAP_402 lio[15A7] C6751 CAP_603 lio[15A7] C6752 CAP_402 lio[15A5] C6800 CAP_402 lio[16B2] C6801 CAP_402 lio[16C7] C6802 CAP_402 lio[16B7] C6811 CAP_402 lio[16A7] C6850 CAP_402 lio[16C2] C6851 CAP_402 lio[16C3] C6852 CAP_402 lio[16C2] C6853 CAP_603 lio[16D3] C6900 CAP_805 lio[17C6] C6903 CAP_402 lio[17C6] C6904 CAP_603 lio[17D6] C6905 CAP_603 lio[17D4] C6906 CAP_402 lio[17C2] C6907 CAP_402 lio[17B1] C7600 CAP_603 lio[18D2] C7601 CAP_805-2 lio[18C2] C7602 CAP_P_CASE-C2-SM lio[18C1] C7603 CAP_402 lio[18C3] C7610 CAP_402 lio[18A7] C7620 CAP_603 lio[18C6] C7621 CAP_402 lio[18C7] C7622 CAP_603 lio[18B5] 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lio[15C8] H2_F-RT-TH J6750 CON_F8RT_SPDIFRCVR_T lio[15B8] H2_F-RT-TH J6780 CON_M3ST_S_TH_M-ST-T lio[15D1] H J6781 CON_M3ST_S_TH_M-ST-T lio[15C1] H J6782 CON_M3ST_S_TH_M-ST-T lio[15C1] H J6900 CON_F5RT_S2MT_TH3_F- lio[17D8] RT-TH-MG3 J9615 CON_M8RT_S_SM_M-RT-S lio[17B5] M L3400 FILTER_4P_1210-4SM1 lio[6B2] L4600 FILTER_4P_1210-4SM1 lio[9B4] L4601 IND_0603 lio[9B4] L4610 FILTER_4P_1210-4SM1 lio[9C4] L4611 IND_0603 lio[9D4] L6200 IND_0402 lio[11A6] L6201 IND_0402 lio[11D7] L6202 IND_0402 lio[11D4] L6300 IND_0402 lio[12C6] L6500 IND_0402 lio[13D6] L6501 IND_0402 lio[13C6] L6601 IND_0402 lio[14C7] L6610 IND_0402 lio[14D7] L6620 IND_0402 lio[14C7] L6630 IND_0402 lio[14B7] L6640 IND_0402 lio[14A7] L6700 FILTER_2P_0603 lio[15D4] L6701 IND_0402 lio[15C4] L6702 IND_0402 lio[15C4] L6703 IND_0402 lio[15C4] L6750 IND_0402 lio[15B4] L6751 IND_0402 lio[15B4] L6752 IND_0402 lio[15A4] L6753 IND_0402 lio[15A4] L6754 IND_0402 lio[15A4] L6800 IND_0402 lio[16B2] L6900 FILTER_4P_SM lio[17D7] L6901 IND_SM-1 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THERMISTOR_0603-LF lio[10C6] R6200 RES_402 lio[11A7] R6201 RES_402 lio[11B6] R6202 RES_402 lio[11A6] R6203 RES_402 lio[11C3] R6204 RES_402 lio[11C7] R6205 RES_402 lio[11B6] R6206 RES_402 lio[11B4] R6207 RES_402 lio[11B3] R6220 RES_402 lio[11A6] R6221 RES_402 lio[11A4]</p>	<p>R6222 RES_402 lio[11A4] R6250 RES_402 lio[11B1] R6251 RES_402 lio[11B1] R6300 RES_402 lio[12B3] R6301 RES_402 lio[12B6] R6302 RES_402 lio[12C5] R6303 RES_402 lio[12B5] R6310 RES_402 lio[12C4] R6311 RES_402 lio[12C4] R6312 RES_402 lio[12C4] R6313 RES_402 lio[12C4] R6320 RES_402 lio[12B4] R6321 RES_402 lio[12B4] R6322 RES_402 lio[12B4] R6323 RES_402 lio[12B4] R6500 RES_402 lio[13C5] R6510 RES_402 lio[13B5] R6511 RES_402 lio[13B4] R6512 RES_402 lio[13B4] R6513 RES_603 lio[13D3] R6514 RES_402 lio[13C3] R6520 RES_402 lio[13A5] R6521 RES_402 lio[13A4] R6522 RES_402 lio[13A4] R6523 RES_603 lio[13C2] R6524 RES_402 lio[13C3] R6600 RES_402 lio[14D7] R6601 RES_402 lio[14C7] R6605 RES_402 lio[14C4] R6606 RES_402 lio[14B4] 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XW4601 SHORT_SM lio[9A4] XW4610 SHORT_SM lio[9C4] XW4611 SHORT_SM lio[9C4] XW6500 SHORT_SM lio[13C6] XW6501 SHORT_SM lio[13C5] XW6710 SHORT_SM lio[15B1] XW6711 SHORT_SM lio[15B2] XW6712 SHORT_SM lio[15B2] XW6800 SHORT_SM lio[16C3] XW7600 SHORT_SM lio[18A4] XW7601 SHORT_SM lio[18A7] XW7602 SHORT_SM lio[18A7] XW7603 SHORT_LAYER_2_SHORT- lio[18A7] L2_SM XW7604 SHORT_LAYER_9_SHORT- lio[18A7]</p>	<p>L9-SM XW7605 SHORT_LAYER_2_SHORT- lio[18A7] L2_SM XW7606 SHORT_LAYER_9_SHORT- lio[18A7] L2_SM XW7607 SHORT_LAYER_2_SHORT- lio[18A7] L2_SM XW7608 SHORT_LAYER_2_SHORT- lio[18A7] L2_SM XW7609 SHORT_LAYER_2_SHORT- lio[18A7] L2_SM XW7610 SHORT_LAYER_2_SHORT- lio[18A7] L2_SM XW7611 SHORT_SM lio[18A5] XW7612 SHORT_SM lio[18A5] ZH0801 MTHOLE lio[5A8] ZH0802 MTHOLE lio[5A8] ZH0803 MTHOLE lio[5A8] ZH0804 MTHOLE lio[5A8]</p>				
	8	7	6	5	4	3	2	1

CHANGE LIST

MAY 03 2006:FIRST RELEASE
MAY 04 2006:REMOVED U7350
MAY 10 2006:CONNECT XW7200.1 TO GND AUDIO PWR INSTEAD OF 5V. REPLACED C7301/02/04/52/53/55 WITH ZAP DIODES
MAY 25 2006:REPLACED R6801 TO R7000, R7200 WITH 155S0138. CHANGED C6803 TO 22UF ROHS COMPLIANT CAP
MAY 31 2006:CHANGE U6800 TO 353S1458, DC SCREENED VERSION. CHANGED NETNAME "=GND_AUDIO_PWR" TO "=GND_AUDIO_AMP"
JUNE 1 2006:CHANGED NETNAME "=PP5V_S0_AUDIO_PWR" TO "=PP5V_S0_AUDIO_AMP"
JUNE 1 2006:ADDED NETNAMES TO NC PINS ON U6800. ADDED "NO_TEST=TRUE" ATTRIBUTES TO NC NETS.
JUNE 28 2006:ADDED CRITICAL TO U6800. REMOVE "?" FROM BOMOPTION TABLE

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APPLE INC.	SIZE	D	DRAWING NUMBER	051-7448	REV.	3.0.0
	SCALE	NONE	SHT	21	OF	23

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	<p>Title: Basenet Report Design: lio Date: Aug 31 16:07:47 2007</p> <p>Base nets and synonyms for lio.lib.LIO(lio.lib.lio(sch_l))</p> <table border="1"> <thead> <tr> <th>Base Signal</th> <th>Synonyms</th> <th>Location(Zone)[dir]</th> </tr> </thead> <tbody> <tr><td>4V5_REG_FB</td><td>4V5_REG_FB - @lio.lib.LIO</td><td>11A5</td></tr> <tr><td>ADAPTER_SENSE</td><td>ADAPTER_SENSE @lio.lib.LIO</td><td>1706</td></tr> <tr><td>ADAPTER_SENSE_UP</td><td>ADAPTER_SENSE_UP - @lio.lib.LIO</td><td>1707</td></tr> <tr><td>AUDIO_SHIELD_PLANE</td><td>AUDIO_SHIELD_PLANE - @lio.lib.LIO</td><td>1583</td></tr> <tr><td>AUD_4V5_REG_IN</td><td>AUD_4V5_REG_IN - @lio.lib.LIO</td><td>11A6</td></tr> <tr><td>AUD_BI_PORT_A_L</td><td>AUD_BI_PORT_A_L - @lio.lib.LIO</td><td>11C3 13B7</td></tr> <tr><td>AUD_BI_PORT_A_R</td><td>AUD_BI_PORT_A_R - @lio.lib.LIO</td><td>11C3 13A7</td></tr> <tr><td>AUD_BI_PORT_B_L</td><td>AUD_BI_PORT_B_L - @lio.lib.LIO</td><td>11C3 11C3 16C3 16D3</td></tr> <tr><td>AUD_BI_PORT_C_L</td><td>AUD_BI_PORT_C_L - @lio.lib.LIO</td><td>11C8 12C2</td></tr> <tr><td>AUD_BI_PORT_C_R</td><td>AUD_BI_PORT_C_R - @lio.lib.LIO</td><td>11C8 12B2</td></tr> <tr><td>AUD_BI_PORT_D_L</td><td>AUD_BI_PORT_D_L - @lio.lib.LIO</td><td>11C8 14D8</td></tr> <tr><td>AUD_BI_PORT_D_R</td><td>AUD_BI_PORT_D_R - @lio.lib.LIO</td><td>11C8 14B8</td></tr> <tr><td>AUD_BI_PORT_F_L</td><td>AUD_BI_PORT_F_L - @lio.lib.LIO</td><td>11C3 11C3 16B4 16B4</td></tr> <tr><td>AUD_BI_PORT_G_L</td><td>AUD_BI_PORT_G_L - @lio.lib.LIO</td><td>11C3 14C8</td></tr> <tr><td>AUD_BI_PORT_G_R</td><td>AUD_BI_PORT_G_R - @lio.lib.LIO</td><td>11B3 14A8</td></tr> <tr><td>AUD_CODEC_INREF</td><td>AUD_CODEC_INREF - @lio.lib.LIO</td><td>12B3</td></tr> <tr><td>AUD_CODEC_JDREF</td><td>AUD_CODEC_JDREF - @lio.lib.LIO</td><td>11B4</td></tr> <tr><td>AUD_CODEC_OUTL_C</td><td>AUD_CODEC_OUTL_C - @lio.lib.LIO</td><td>13B6</td></tr> <tr><td>AUD_CODEC_OUTL_R</td><td>AUD_CODEC_OUTL_R - @lio.lib.LIO</td><td>13B5</td></tr> <tr><td>AUD_CODEC_OUTR_C</td><td>AUD_CODEC_OUTR_C - @lio.lib.LIO</td><td>13A6</td></tr> <tr><td>AUD_CODEC_OUTR_R</td><td>AUD_CODEC_OUTR_R - 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@lio.lib.LIO</td><td>16C5</td></tr> <tr><td>AUD_REG_SHDN_L</td><td>AUD_REG_SHDN_L - @lio.lib.LIO</td><td>11A6</td></tr> <tr><td>AUD_SENSE_A</td><td>AUD_SENSE_A - @lio.lib.LIO</td><td>11C2 16A8 16C8</td></tr> <tr><td>AUD_SENSE_B</td><td>AUD_SENSE_B - @lio.lib.LIO</td><td>11C2 16C8</td></tr> <tr><td>AUD_SPDIF_IN</td><td>AUD_SPDIF_IN - @lio.lib.LIO</td><td>11C3 15B3</td></tr> <tr><td>AUD_SPDIF_0</td><td>AUD_SPDIF_0 - @lio.lib.LIO</td><td>11C4</td></tr> <tr><td>AUD_SPDIF_OUT</td><td>AUD_SPDIF_OUT - @lio.lib.LIO</td><td>11C3 15D3</td></tr> <tr><td>AUD_SPKRAMP_INL1_L</td><td>AUD_SPKRAMP_INL1_L - @lio.lib.LIO</td><td>14A7</td></tr> <tr><td>AUD_SPKRAMP_INL2_L</td><td>AUD_SPKRAMP_INL2_L - @lio.lib.LIO</td><td>14C7</td></tr> <tr><td>AUD_SPKRAMP_INR1_L</td><td>AUD_SPKRAMP_INR1_L - @lio.lib.LIO</td><td>14B6</td></tr> <tr><td>AUD_SPKRAMP_INR2_L</td><td>AUD_SPKRAMP_INR2_L - @lio.lib.LIO</td><td>14A6</td></tr> <tr><td>AUD_SPKRAMP_SHUTDOWN</td><td>AUD_SPKRAMP_SHUTDOWN_L - @lio.lib.LIO</td><td>14A8 14B8 14B8 14C8</td></tr> <tr><td>AUD_VREF_PORT_A</td><td>AUD_VREF_PORT_A - @lio.lib.LIO</td><td>11C3 13C7</td></tr> <tr><td>AUD_VREF_PORT_B</td><td>AUD_VREF_PORT_B - @lio.lib.LIO</td><td>11C3 16D3</td></tr> <tr><td>AUD_VREF_PORT_C</td><td>AUD_VREF_PORT_C - @lio.lib.LIO</td><td>11C3 12B2</td></tr> <tr><td>AUD_VREF_PORT_F</td><td>AUD_VREF_PORT_F - @lio.lib.LIO</td><td>11C3 16C4</td></tr> <tr><td>AVDD_ADC_DAC</td><td>AVDD_ADC_DAC - @lio.lib.LIO</td><td>11D5</td></tr> <tr><td>BEEP</td><td>BEEP - @lio.lib.LIO</td><td>11C6</td></tr> <tr><td>BI_MIC_BIAS</td><td>BI_MIC_BIAS - @lio.lib.LIO</td><td>16D3</td></tr> <tr><td>BI_MIC_HI</td><td>BI_MIC_HI - @lio.lib.LIO</td><td>15D3 16D1</td></tr> <tr><td>BI_MIC_LO</td><td>BI_MIC_LO - @lio.lib.LIO</td><td>15D3 16C1</td></tr> <tr><td>BI_MIC_SHIELD</td><td>BI_MIC_SHIELD - @lio.lib.LIO</td><td>15D3 16C1</td></tr> <tr><td>CHASSIS_GND1</td><td>CHASSIS_GND1 - @lio.lib.LIO</td><td>5A6 5B8 17C8</td></tr> <tr><td>CHASSIS_GND2</td><td>CHASSIS_GND2 - @lio.lib.LIO</td><td>5A6 5A6 5A8 15A3 15B1</td></tr> <tr><td>CHASSIS_GND3</td><td>CHASSIS_GND3 - @lio.lib.LIO</td><td>5A6 5A8 15C3</td></tr> <tr><td>CODEC_DVDD</td><td>CODEC_DVDD - @lio.lib.LIO</td><td>11D6</td></tr> <tr><td>CODEC_SDATA_IN</td><td>CODEC_SDATA_IN - @lio.lib.LIO</td><td>11C6</td></tr> <tr><td>EXCARD_CLKREQ_CONN</td><td>EXCARD_CLKREQ_CONN - @lio.lib.LIO</td><td>7B3</td></tr> <tr><td>EXCARD_CLKREQ_CONN_L</td><td>EXCARD_CLKREQ_CONN_L - @lio.lib.LIO</td><td>7A4 7C3</td></tr> <tr><td>EXCARD_CLKREQ_L</td><td>EXCARD_CLKREQ_L - @lio.lib.LIO</td><td>5C3 5C5 7B2 8C6</td></tr> <tr><td>EXCARD_CFFE_L</td><td>EXCARD_CFFE_L - @lio.lib.LIO</td><td>7A8 7C3 7C3</td></tr> <tr><td>EXCARD_CPUSB_L</td><td>EXCARD_CPUSB_L - @lio.lib.LIO</td><td>7A8 7C3 7C3</td></tr> <tr><td>EXCARD_OC_L</td><td>EXCARD_OC_L - @lio.lib.LIO</td><td>5C4 5C5 7C7 8C6</td></tr> <tr><td>EXCARD_RCLKEN</td><td>EXCARD_RCLKEN - @lio.lib.LIO</td><td>7B3 7C5</td></tr> <tr><td>EXCARD_SHDN_L_R</td><td>EXCARD_SHDN_L_R - @lio.lib.LIO</td><td>7C6</td></tr> <tr><td>GND_SV33_SV33_SGND</td><td>GND_SV33_SV33_SGND - @lio.lib.LIO</td><td>16A6</td></tr> <tr><td>GND_ADAPTER_UP</td><td>GND_ADAPTER_UP - @lio.lib.LIO</td><td>17D7</td></tr> <tr><td>GND_AUDIO_AMP</td><td>GND_AUDIO_AMP - @lio.lib.LIO</td><td>5B6 5B8 14A3 14A8 14A8</td></tr> <tr><td>GND_AUDIO_CODEC</td><td>GND_AUDIO_CODEC - @lio.lib.LIO</td><td>14B8 14C8 15C3 18A6</td></tr> <tr><td>GND_AUDIO_CODEC</td><td>GND_AUDIO_CODEC - @lio.lib.LIO</td><td>11A3 11B8 11D3 12B2 12B7</td></tr> <tr><td>GND_AUDIO_CODEC</td><td>GND_AUDIO_CODEC - @lio.lib.LIO</td><td>13A7 13C7 14A8 14A8 14B8</td></tr> <tr><td>GND_AUDIO_CODEC</td><td>GND_AUDIO_CODEC - @lio.lib.LIO</td><td>14C8 16A4 16A8 16B4 16B6</td></tr> <tr><td>GND_AUDIO_CODEC</td><td>GND_AUDIO_CODEC - @lio.lib.LIO</td><td>16B8 16C3 16C4 16C8 16D3</td></tr> <tr><td>GND_AUDIO_CODEC</td><td>GND_AUDIO_CODEC - @lio.lib.LIO</td><td>18A6</td></tr> <tr><td>GND_AUDIO_HPAMP_PGND</td><td>GND_AUDIO_HPAMP_PGND - @lio.lib.LIO</td><td>13C6</td></tr> </tbody> </table>	Base Signal	Synonyms	Location(Zone)[dir]	4V5_REG_FB	4V5_REG_FB - @lio.lib.LIO	11A5	ADAPTER_SENSE	ADAPTER_SENSE @lio.lib.LIO	1706	ADAPTER_SENSE_UP	ADAPTER_SENSE_UP - @lio.lib.LIO	1707	AUDIO_SHIELD_PLANE	AUDIO_SHIELD_PLANE - @lio.lib.LIO	1583	AUD_4V5_REG_IN	AUD_4V5_REG_IN - @lio.lib.LIO	11A6	AUD_BI_PORT_A_L	AUD_BI_PORT_A_L - @lio.lib.LIO	11C3 13B7	AUD_BI_PORT_A_R	AUD_BI_PORT_A_R - @lio.lib.LIO	11C3 13A7	AUD_BI_PORT_B_L	AUD_BI_PORT_B_L - @lio.lib.LIO	11C3 11C3 16C3 16D3	AUD_BI_PORT_C_L	AUD_BI_PORT_C_L - @lio.lib.LIO	11C8 12C2	AUD_BI_PORT_C_R	AUD_BI_PORT_C_R - @lio.lib.LIO	11C8 12B2	AUD_BI_PORT_D_L	AUD_BI_PORT_D_L - @lio.lib.LIO	11C8 14D8	AUD_BI_PORT_D_R	AUD_BI_PORT_D_R - @lio.lib.LIO	11C8 14B8	AUD_BI_PORT_F_L	AUD_BI_PORT_F_L - @lio.lib.LIO	11C3 11C3 16B4 16B4	AUD_BI_PORT_G_L	AUD_BI_PORT_G_L - @lio.lib.LIO	11C3 14C8	AUD_BI_PORT_G_R	AUD_BI_PORT_G_R - @lio.lib.LIO	11B3 14A8	AUD_CODEC_INREF	AUD_CODEC_INREF - @lio.lib.LIO	12B3	AUD_CODEC_JDREF	AUD_CODEC_JDREF - @lio.lib.LIO	11B4	AUD_CODEC_OUTL_C	AUD_CODEC_OUTL_C - @lio.lib.LIO	13B6	AUD_CODEC_OUTL_R	AUD_CODEC_OUTL_R - @lio.lib.LIO	13B5	AUD_CODEC_OUTR_C	AUD_CODEC_OUTR_C - @lio.lib.LIO	13A6	AUD_CODEC_OUTR_R	AUD_CODEC_OUTR_R - @lio.lib.LIO	13A5	AUD_CODEC_VREF	AUD_CODEC_VREF - @lio.lib.LIO	11B4	AUD_CONNJ1_RING	AUD_CONNJ1_RING - @lio.lib.LIO	15C7	AUD_CONNJ1_SLEEVE	AUD_CONNJ1_SLEEVE - @lio.lib.LIO	15C7	AUD_CONNJ1_SLEEVE2	AUD_CONNJ1_SLEEVE2 - @lio.lib.LIO	15C7 15D6	AUD_CONNJ1_SLEEVEDET	AUD_CONNJ1_SLEEVEDET - @lio.lib.LIO	15C7	AUD_CONNJ1_TIP	AUD_CONNJ1_TIP - @lio.lib.LIO	15C7	AUD_CONNJ1_TIPDET	AUD_CONNJ1_TIPDET - @lio.lib.LIO	15C7	AUD_CONNJ2_RING	AUD_CONNJ2_RING - @lio.lib.LIO	15A7	AUD_CONNJ2_SLEEVE	AUD_CONNJ2_SLEEVE - @lio.lib.LIO	15B7	AUD_CONNJ2_SLEEVEDET	AUD_CONNJ2_SLEEVEDET - @lio.lib.LIO	15A7	AUD_CONNJ2_TIP	AUD_CONNJ2_TIP - @lio.lib.LIO	15A7	AUD_CONNJ2_TIPDET	AUD_CONNJ2_TIPDET - @lio.lib.LIO	15A7	AUD_GPIO_0	AUD_GPIO_0 - @lio.lib.LIO	11C8 14C8	AUD_GPIO_0_R	AUD_GPIO_0_R - @lio.lib.LIO	11C6	AUD_GPIO_1	AUD_GPIO_1 - @lio.lib.LIO	11C8	AUD_HPAMP_INL_M	AUD_HPAMP_INL_M - @lio.lib.LIO	13B3 13C7	AUD_HPAMP_INR_M	AUD_HPAMP_INR_M - @lio.lib.LIO	13A3 13C7	AUD_HPAMP_MUTE_L	AUD_HPAMP_MUTE_L - @lio.lib.LIO	13C6	AUD_HPAMP_OUTL_F	AUD_HPAMP_OUTL_F - @lio.lib.LIO	13C2 15C3	AUD_HPAMP_OUTR_F	AUD_HPAMP_OUTR_F - @lio.lib.LIO	13B3 13C2	AUD_HPAMP_OUTR_F	AUD_HPAMP_OUTR_F - @lio.lib.LIO	13C2 15C3	AUD_HPAMP_OUTR_F	AUD_HPAMP_OUTR_F - @lio.lib.LIO	13A3 13C2	AUD_INJACK_INSERT_L	AUD_INJACK_INSERT_L - @lio.lib.LIO	16A6	AUD_J1_DET_RC	AUD_J1_DET_RC - @lio.lib.LIO	16C7	AUD_J1_SLEEVEDET_INV	AUD_J1_SLEEVEDET_INV - @lio.lib.LIO	16C7	AUD_J1_SLEEVEDET_R	AUD_J1_SLEEVEDET_R - @lio.lib.LIO	15C3 16B6 16B8	AUD_J1_TIPDET_R	AUD_J1_TIPDET_R - @lio.lib.LIO	15C3 16C8	AUD_J2_DET_RC	AUD_J2_DET_RC - @lio.lib.LIO	16A7	AUD_J2_OPT_DET	AUD_J2_OPT_DET - @lio.lib.LIO	15B7	AUD_J2_TIPDET_R	AUD_J2_TIPDET_R - @lio.lib.LIO	15A3 16A8	AUD_LIFILT_LT	AUD_LIFILT_LT - @lio.lib.LIO	12C4	AUD_LIFILT_RT	AUD_LIFILT_RT - @lio.lib.LIO	12C5	AUD_LIFILT_RT	AUD_LIFILT_RT - @lio.lib.LIO	12B4	AUD_LIFILT_RT_R	AUD_LIFILT_RT_R - @lio.lib.LIO	12B5	AUD_LIFILT_SHUTDOWN	AUD_LIFILT_SHUTDOWN_L - @lio.lib.LIO	12C2 16A4	AUD_LIN_SHUTDOWN	AUD_LIN_SHUTDOWN - @lio.lib.LIO	16A5	AUD_L1_GND	AUD_L1_GND - @lio.lib.LIO	12C7 15B3	AUD_L1_INL_C	AUD_L1_INL_C - @lio.lib.LIO	12C5 15A3	AUD_L1_INL_C_R	AUD_L1_INL_C_R - @lio.lib.LIO	12C4	AUD_L1_INR	AUD_L1_INR - @lio.lib.LIO	12B7 15A3	AUD_L1_INR_C	AUD_L1_INR_C - @lio.lib.LIO	12B5	AUD_L1_INR_C_R	AUD_L1_INR_C_R - @lio.lib.LIO	12B4	AUD_L1_GND	AUD_L1_GND - @lio.lib.LIO	13B2 15C3	AUD_OUTJACK_INSERT_L	AUD_OUTJACK_INSERT_L - @lio.lib.LIO	16C6	AUD_PORTA_DET_L	AUD_PORTA_DET_L - @lio.lib.LIO	16C5	AUD_PORTC_L	AUD_PORTC_L - @lio.lib.LIO	12B3	AUD_PORTC_R	AUD_PORTC_R - @lio.lib.LIO	12B3	AUD_PORTE_DET_L	AUD_PORTE_DET_L - @lio.lib.LIO	16C5	AUD_REG_SHDN_L	AUD_REG_SHDN_L - @lio.lib.LIO	11A6	AUD_SENSE_A	AUD_SENSE_A - @lio.lib.LIO	11C2 16A8 16C8	AUD_SENSE_B	AUD_SENSE_B - @lio.lib.LIO	11C2 16C8	AUD_SPDIF_IN	AUD_SPDIF_IN - @lio.lib.LIO	11C3 15B3	AUD_SPDIF_0	AUD_SPDIF_0 - @lio.lib.LIO	11C4	AUD_SPDIF_OUT	AUD_SPDIF_OUT - @lio.lib.LIO	11C3 15D3	AUD_SPKRAMP_INL1_L	AUD_SPKRAMP_INL1_L - @lio.lib.LIO	14A7	AUD_SPKRAMP_INL2_L	AUD_SPKRAMP_INL2_L - @lio.lib.LIO	14C7	AUD_SPKRAMP_INR1_L	AUD_SPKRAMP_INR1_L - @lio.lib.LIO	14B6	AUD_SPKRAMP_INR2_L	AUD_SPKRAMP_INR2_L - @lio.lib.LIO	14A6	AUD_SPKRAMP_SHUTDOWN	AUD_SPKRAMP_SHUTDOWN_L - @lio.lib.LIO	14A8 14B8 14B8 14C8	AUD_VREF_PORT_A	AUD_VREF_PORT_A - @lio.lib.LIO	11C3 13C7	AUD_VREF_PORT_B	AUD_VREF_PORT_B - @lio.lib.LIO	11C3 16D3	AUD_VREF_PORT_C	AUD_VREF_PORT_C - @lio.lib.LIO	11C3 12B2	AUD_VREF_PORT_F	AUD_VREF_PORT_F - @lio.lib.LIO	11C3 16C4	AVDD_ADC_DAC	AVDD_ADC_DAC - @lio.lib.LIO	11D5	BEEP	BEEP - @lio.lib.LIO	11C6	BI_MIC_BIAS	BI_MIC_BIAS - @lio.lib.LIO	16D3	BI_MIC_HI	BI_MIC_HI - @lio.lib.LIO	15D3 16D1	BI_MIC_LO	BI_MIC_LO - @lio.lib.LIO	15D3 16C1	BI_MIC_SHIELD	BI_MIC_SHIELD - @lio.lib.LIO	15D3 16C1	CHASSIS_GND1	CHASSIS_GND1 - @lio.lib.LIO	5A6 5B8 17C8	CHASSIS_GND2	CHASSIS_GND2 - @lio.lib.LIO	5A6 5A6 5A8 15A3 15B1	CHASSIS_GND3	CHASSIS_GND3 - @lio.lib.LIO	5A6 5A8 15C3	CODEC_DVDD	CODEC_DVDD - @lio.lib.LIO	11D6	CODEC_SDATA_IN	CODEC_SDATA_IN - @lio.lib.LIO	11C6	EXCARD_CLKREQ_CONN	EXCARD_CLKREQ_CONN - @lio.lib.LIO	7B3	EXCARD_CLKREQ_CONN_L	EXCARD_CLKREQ_CONN_L - @lio.lib.LIO	7A4 7C3	EXCARD_CLKREQ_L	EXCARD_CLKREQ_L - @lio.lib.LIO	5C3 5C5 7B2 8C6	EXCARD_CFFE_L	EXCARD_CFFE_L - @lio.lib.LIO	7A8 7C3 7C3	EXCARD_CPUSB_L	EXCARD_CPUSB_L - @lio.lib.LIO	7A8 7C3 7C3	EXCARD_OC_L	EXCARD_OC_L - @lio.lib.LIO	5C4 5C5 7C7 8C6	EXCARD_RCLKEN	EXCARD_RCLKEN - @lio.lib.LIO	7B3 7C5	EXCARD_SHDN_L_R	EXCARD_SHDN_L_R - @lio.lib.LIO	7C6	GND_SV33_SV33_SGND	GND_SV33_SV33_SGND - @lio.lib.LIO	16A6	GND_ADAPTER_UP	GND_ADAPTER_UP - @lio.lib.LIO	17D7	GND_AUDIO_AMP	GND_AUDIO_AMP - @lio.lib.LIO	5B6 5B8 14A3 14A8 14A8	GND_AUDIO_CODEC	GND_AUDIO_CODEC - @lio.lib.LIO	14B8 14C8 15C3 18A6	GND_AUDIO_CODEC	GND_AUDIO_CODEC - @lio.lib.LIO	11A3 11B8 11D3 12B2 12B7	GND_AUDIO_CODEC	GND_AUDIO_CODEC - @lio.lib.LIO	13A7 13C7 14A8 14A8 14B8	GND_AUDIO_CODEC	GND_AUDIO_CODEC - @lio.lib.LIO	14C8 16A4 16A8 16B4 16B6	GND_AUDIO_CODEC	GND_AUDIO_CODEC - @lio.lib.LIO	16B8 16C3 16C4 16C8 16D3	GND_AUDIO_CODEC	GND_AUDIO_CODEC - @lio.lib.LIO	18A6	GND_AUDIO_HPAMP_PGND	GND_AUDIO_HPAMP_PGND - @lio.lib.LIO	13C6	<table border="1"> <tbody> <tr><td>HDA_BIT_CLK</td><td>HDA_BIT_CLK - @lio.lib.LIO</td><td>8B6 11C8</td></tr> <tr><td>HDA_RST_L</td><td>HDA_RST_L - @lio.lib.LIO</td><td>8B6 11B8</td></tr> <tr><td>HDA_SDINO</td><td>HDA_SDINO - @lio.lib.LIO</td><td>8B6 11C8</td></tr> <tr><td>HDA_SDOUT</td><td>HDA_SDOUT - @lio.lib.LIO</td><td>8B6 11C8</td></tr> <tr><td>HDA_SYNC</td><td>HDA_SYNC - @lio.lib.LIO</td><td>8B6 11C8</td></tr> <tr><td>HS_MIC_BIAS</td><td>HS_MIC_BIAS - @lio.lib.LIO</td><td>16C3</td></tr> <tr><td>HS_MIC_HI</td><td>HS_MIC_HI - @lio.lib.LIO</td><td>15D3 16B1</td></tr> <tr><td>HS_MIC_LO</td><td>HS_MIC_LO - @lio.lib.LIO</td><td>15D3 16B1</td></tr> <tr><td>HS_MIC_SUPBIAS</td><td>HS_MIC_SUPBIAS - @lio.lib.LIO</td><td>14B6</td></tr> <tr><td>HS_MIC_SW_RC</td><td>HS_MIC_SW_RC - @lio.lib.LIO</td><td>16A2</td></tr> <tr><td>IPHS_SW_INT_TO_SB</td><td>IPHS_SW_INT_TO_SB - @lio.lib.LIO</td><td>8B3 16B1</td></tr> <tr><td>IPHS_SW_SUPBIAS_EN_L</td><td>IPHS_SW_SUPBIAS_EN_L - @lio.lib.LIO</td><td>8B3 16B4</td></tr> <tr><td>LEFT_ALS_GAIN_SW</td><td>LEFT_ALS_GAIN_SW - @lio.lib.LIO</td><td>8C6 10C3</td></tr> <tr><td>LEFT_ALS_GAIN_SW_R</td><td>LEFT_ALS_GAIN_SW_R - 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@lio.lib.LIO</td><td>14C6</td></tr> <tr><td>MAX9705L2_PIN</td><td>MAX9705L2_PIN - @lio.lib.LIO</td><td>14C6</td></tr> <tr><td>MAX9705R1_NIN</td><td>MAX9705R1_NIN - @lio.lib.LIO</td><td>14B6</td></tr> <tr><td>MAX9705R1_PIN</td><td>MAX9705R1_PIN - @lio.lib.LIO</td><td>14B6</td></tr> <tr><td>MAX9705R2_NIN</td><td>MAX9705R2_NIN - @lio.lib.LIO</td><td>14A6</td></tr> <tr><td>MAX9705R2_PIN</td><td>MAX9705R2_PIN - @lio.lib.LIO</td><td>14A6</td></tr> <tr><td>MAX9724_CIN</td><td>MAX9724_CIN - @lio.lib.LIO</td><td>13C4</td></tr> <tr><td>MAX9724_C1P</td><td>MAX9724_C1P - @lio.lib.LIO</td><td>13C4</td></tr> <tr><td>MAX9724_FVSS</td><td>MAX9724_FVSS - @lio.lib.LIO</td><td>13C4</td></tr> <tr><td>MINI_CLKREQ_L</td><td>MINI_CLKREQ_L - @lio.lib.LIO</td><td>5B4 5B5 6C6 8C6</td></tr> <tr><td>NC_AUD_BI_PORT_E_L</td><td>NC_AUD_BI_PORT_E_L - @lio.lib.LIO</td><td>11C4</td></tr> <tr><td>NC_AUD_BI_PORT_E_R</td><td>NC_AUD_BI_PORT_E_R - @lio.lib.LIO</td><td>11C4</td></tr> <tr><td>NC_AUD_BI_PORT_M_L</td><td>NC_AUD_BI_PORT_M_L - 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@lio.lib.LIO</td><td>18C5</td></tr> <tr><td>P5V83_VREG5</td><td>P5V83_VREG5 - @lio.lib.LIO</td><td>18C5</td></tr> <tr><td>PCIE_CLK100M_EXCARD_N</td><td>PCIE_CLK100M_EXCARD_N - @lio.lib.LIO</td><td>5D2 5D5 7C3 8B1</td></tr> <tr><td>PCIE_CLK100M_EXCARD_P</td><td>PCIE_CLK100M_EXCARD_P - @lio.lib.LIO</td><td>5D2 5D5 7C3 8B1</td></tr> <tr><td>PCIE_CLK100M_EXCARD_UP_N</td><td>PCIE_CLK100M_EXCARD_UP_N - @lio.lib.LIO</td><td>8C4</td></tr> <tr><td>PCIE_CLK100M_EXCARD_UP_P</td><td>PCIE_CLK100M_EXCARD_UP_P - @lio.lib.LIO</td><td>8B4</td></tr> <tr><td>PCIE_CLK100M_MINI_N</td><td>PCIE_CLK100M_MINI_N - @lio.lib.LIO</td><td>5B2 5B5 6C6 8C1</td></tr> <tr><td>PCIE_CLK100M_MINI_P</td><td>PCIE_CLK100M_MINI_P - @lio.lib.LIO</td><td>5B2 5B5 6C6 8C1</td></tr> <tr><td>PCIE_CLK100M_MINI_UP_N</td><td>PCIE_CLK100M_MINI_UP_N - @lio.lib.LIO</td><td>8C4</td></tr> <tr><td>PCIE_CLK100M_MINI_UP_P</td><td>PCIE_CLK100M_MINI_UP_P - @lio.lib.LIO</td><td>8C4</td></tr> <tr><td>PCIE_EXCARD_D2R_N</td><td>PCIE_EXCARD_D2R_N - @lio.lib.LIO</td><td>5D2 5D5 7C3 8B3</td></tr> <tr><td>PCIE_EXCARD_D2R_P</td><td>PCIE_EXCARD_D2R_P - @lio.lib.LIO</td><td>5D2 5D5 7C3 8B3</td></tr> <tr><td>PCIE_EXCARD_R2D_N</td><td>PCIE_EXCARD_R2D_N - @lio.lib.LIO</td><td>5D2 5D5 7C3 8B3</td></tr> <tr><td>PCIE_EXCARD_R2D_P</td><td>PCIE_EXCARD_R2D_P - @lio.lib.LIO</td><td>5D2 5D5 7C3 8B3</td></tr> <tr><td>PCIE_MINI_D2R_N</td><td>PCIE_MINI_D2R_N - @lio.lib.LIO</td><td>5B2 5B5 6C6 8C3</td></tr> <tr><td>PCIE_MINI_D2R_P</td><td>PCIE_MINI_D2R_P - @lio.lib.LIO</td><td>5B2 5B5 6C6 8C3</td></tr> <tr><td>PCIE_MINI_R2D_N</td><td>PCIE_MINI_R2D_N - @lio.lib.LIO</td><td>5B2 5B5 6B6 8C3</td></tr> <tr><td>PCIE_MINI_R2D_P</td><td>PCIE_MINI_R2D_P - @lio.lib.LIO</td><td>5C2 5C5 6B6 8C3</td></tr> <tr><td>PCIE_WAKE_L</td><td>PCIE_WAKE_L - @lio.lib.LIO</td><td>5C3 5C3 5C4 6C6 7C3 8C6</td></tr> <tr><td>PGND1</td><td>PGND1 - @lio.lib.LIO</td><td>18A5</td></tr> <tr><td>PGND2</td><td>PGND2 - @lio.lib.LIO</td><td>18A5</td></tr> <tr><td>PLT_RESET_L</td><td>PLT_RESET_L - @lio.lib.LIO</td><td>5C3 5C4 6C3 7C7 8C6</td></tr> <tr><td>PLT_RESET_SWITCH_L</td><td>PLT_RESET_SWITCH_L - @lio.lib.LIO</td><td>7C3 7C3</td></tr> <tr><td>PM_SLP_S3_L5V5</td><td>PM_SLP_S3_L5V5 - @lio.lib.LIO</td><td>5C3 5C3 5C4 8C6 18A4 18A8</td></tr> <tr><td>PM_WLAN_EN_L</td><td>PM_WLAN_EN_L - @lio.lib.LIO</td><td>6C1 8C6</td></tr> <tr><td>PP1V5_S0_EXCARD_SWIT</td><td>PP1V5_S0_EXCARD_SWIT - @lio.lib.LIO</td><td>7C3 7C3</td></tr> <tr><td>PP1V5_S0_LIO</td><td>PP1V5_S0_LIO - @lio.lib.LIO</td><td>5C6 5C6 5C6 5C8 6C2 7C7 7C8 8B3</td></tr> <tr><td>PP3V3_S0_AUDIO_F</td><td>PP3V3_S0_AUDIO_F - @lio.lib.LIO</td><td>16A4 16A8 16B5 16C8 16C8</td></tr> <tr><td>PP3V3_S0_EXCARD_SWIT</td><td>PP3V3_S0_EXCARD_SWIT - @lio.lib.LIO</td><td>7C3 7C3</td></tr> <tr><td>PP3V3_S0_LIO</td><td>PP3V3_S0_LIO - @lio.lib.LIO</td><td>5C6 5C6 5C6 5C8 7B4 7C7 11A7 11D6 15B8 15D8 16B1</td></tr> <tr><td>PP3V3_S3_EXCARD_SWIT</td><td>PP3V3_S3_EXCARD_SWIT - @lio.lib.LIO</td><td>7C3 7C3</td></tr> <tr><td>PP3V3_S3_LIO</td><td>PP3V3_S3_LIO - @lio.lib.LIO</td><td>5C6 5C6 5D6 5D6 5D6 5D8 6C2 6D1 7B8 7C7 7C8 10B5 10D5 18A4 18B1</td></tr> <tr><td>PP3V3_WLAN</td><td>PP3V3_WLAN - @lio.lib.LIO</td><td>6D4</td></tr> <tr><td>PP4V5_AUDIO_ANALOG</td><td>PP4V5_AUDIO_ANALOG - @lio.lib.LIO</td><td>11A3 11D3 12C6 16A8</td></tr> <tr><td>PP4V5_AUDIO_LINE_IN</td><td>PP4V5_AUDIO_LINE_IN - @lio.lib.LIO</td><td>12C5</td></tr> </tbody> </table>	HDA_BIT_CLK	HDA_BIT_CLK - @lio.lib.LIO	8B6 11C8	HDA_RST_L	HDA_RST_L - @lio.lib.LIO	8B6 11B8	HDA_SDINO	HDA_SDINO - @lio.lib.LIO	8B6 11C8	HDA_SDOUT	HDA_SDOUT - @lio.lib.LIO	8B6 11C8	HDA_SYNC	HDA_SYNC - @lio.lib.LIO	8B6 11C8	HS_MIC_BIAS	HS_MIC_BIAS - @lio.lib.LIO	16C3	HS_MIC_HI	HS_MIC_HI - @lio.lib.LIO	15D3 16B1	HS_MIC_LO	HS_MIC_LO - @lio.lib.LIO	15D3 16B1	HS_MIC_SUPBIAS	HS_MIC_SUPBIAS - @lio.lib.LIO	14B6	HS_MIC_SW_RC	HS_MIC_SW_RC - @lio.lib.LIO	16A2	IPHS_SW_INT_TO_SB	IPHS_SW_INT_TO_SB - @lio.lib.LIO	8B3 16B1	IPHS_SW_SUPBIAS_EN_L	IPHS_SW_SUPBIAS_EN_L - @lio.lib.LIO	8B3 16B4	LEFT_ALS_GAIN_SW	LEFT_ALS_GAIN_SW - @lio.lib.LIO	8C6 10C3	LEFT_ALS_GAIN_SW_R	LEFT_ALS_GAIN_SW_R - @lio.lib.LIO	10C4	LEFT_ALS_OP_COMP	LEFT_ALS_OP_COMP - @lio.lib.LIO	10C6	LEFT_ALS_OP_IN	LEFT_ALS_OP_IN - @lio.lib.LIO	10C6	LEFT_ALS_OUT	LEFT_ALS_OUT - @lio.lib.LIO	8C6 10C3	LEFT_ALS_OUT_FB	LEFT_ALS_OUT_FB - @lio.lib.LIO	10C4	LEFT_GAIN_SETTING	LEFT_GAIN_SETTING - @lio.lib.LIO	10C5	LEFT_PHOTODIODE	LEFT_PHOTODIODE - @lio.lib.LIO	10C7	LIO_P3V3S0_EN_R_L	LIO_P3V3S0_EN_R_L - @lio.lib.LIO	18A3	LIO_P3V3S3_EN	LIO_P3V3S3_EN - @lio.lib.LIO	5C3 5C4 8C6 18C8	LIO_P5V80_EN_R_L	LIO_P5V80_EN_R_L - @lio.lib.LIO	18A8	MAX9705L1_NIN	MAX9705L1_NIN - @lio.lib.LIO	14D6	MAX9705L1_PIN	MAX9705L1_PIN - @lio.lib.LIO	14D6	MAX9705L2_NIN	MAX9705L2_NIN - @lio.lib.LIO	14C6	MAX9705L2_PIN	MAX9705L2_PIN - @lio.lib.LIO	14C6	MAX9705R1_NIN	MAX9705R1_NIN - @lio.lib.LIO	14B6	MAX9705R1_PIN	MAX9705R1_PIN - @lio.lib.LIO	14B6	MAX9705R2_NIN	MAX9705R2_NIN - @lio.lib.LIO	14A6	MAX9705R2_PIN	MAX9705R2_PIN - @lio.lib.LIO	14A6	MAX9724_CIN	MAX9724_CIN - @lio.lib.LIO	13C4	MAX9724_C1P	MAX9724_C1P - @lio.lib.LIO	13C4	MAX9724_FVSS	MAX9724_FVSS - @lio.lib.LIO	13C4	MINI_CLKREQ_L	MINI_CLKREQ_L - @lio.lib.LIO	5B4 5B5 6C6 8C6	NC_AUD_BI_PORT_E_L	NC_AUD_BI_PORT_E_L - @lio.lib.LIO	11C4	NC_AUD_BI_PORT_E_R	NC_AUD_BI_PORT_E_R - @lio.lib.LIO	11C4	NC_AUD_BI_PORT_M_L	NC_AUD_BI_PORT_M_L - @lio.lib.LIO	11B4	NC_AUD_BI_PORT_M_R	NC_AUD_BI_PORT_M_R - @lio.lib.LIO	11B4	NC_AUD_VREF_PORT_D	NC_AUD_VREF_PORT_D - @lio.lib.LIO	11C4	NC_AUD_VREF_PORT_E	NC_AUD_VREF_PORT_E - @lio.lib.LIO	11C4	NC_BAL_IN_COM	NC_BAL_IN_COM - @lio.lib.LIO	11C6	NC_BAL_IN_L	NC_BAL_IN_L - @lio.lib.LIO	11C6	NC_BAL_IN_R	NC_BAL_IN_R - @lio.lib.LIO	11C6	NC_LED_WLAN_L	NC_LED_WLAN_L - @lio.lib.LIO	6B3	NC_LED_WLAN_R	NC_LED_WLAN_R - @lio.lib.LIO	6B3	NC_LED_WLAN_SS	NC_LED_WLAN_SS - @lio.lib.LIO	6B3	NC_LIO_P3V3S3_PG00D	NC_LIO_P3V3S3_PG00D - @lio.lib.LIO	18B7	NC_LIO_P5V80_PG00D	NC_LIO_P5V80_PG00D - @lio.lib.LIO	18B7	NC_UIM_CLK	NC_UIM_CLK - @lio.lib.LIO	6C3	NC_UIM_DATA	NC_UIM_DATA - @lio.lib.LIO	6C3	NC_UIM_PWR	NC_UIM_PWR - @lio.lib.LIO	6C3	NC_UIM_RESET	NC_UIM_RESET - @lio.lib.LIO	6C3	NC_UIM_VFP	NC_UIM_VFP - @lio.lib.LIO	6C3	NC_VFP	NC_VFP - @lio.lib.LIO	11B4	NC_W_DISABLE_L	NC_W_DISABLE_L - @lio.lib.LIO	6C3	ONEMIRE_D2IN_DIV	ONEMIRE_D2IN_DIV - @lio.lib.LIO	17C5	ONEMIRE_EN	ONEMIRE_EN - @lio.lib.LIO	17D3	ONEMIRE_ESD	ONEMIRE_ESD - @lio.lib.LIO	17C4	ONEMIRE_OVERRVOLT	ONEMIRE_OVERRVOLT - @lio.lib.LIO	17C4	ONEMIRE_PWR_EN_L	ONEMIRE_PWR_EN_L - @lio.lib.LIO	17C2	ONEMIRE_PWR_EN_L_DIV	ONEMIRE_PWR_EN_L_DIV - @lio.lib.LIO	17D2	P3V3S3_BOOT	P3V3S3_BOOT - @lio.lib.LIO	18B4 18B6	P3V3S3_LG	P3V3S3_LG - @lio.lib.LIO	18B4	P3V3S3_PHASE	P3V3S3_PHASE - @lio.lib.LIO	18B4	P3V3S3_UG	P3V3S3_UG - @lio.lib.LIO	18B4	P3V3WLAN_SS	P3V3WLAN_SS - @lio.lib.LIO	6C3	P5V83_BOOT	P5V83_BOOT - @lio.lib.LIO	18C4 18C6	P5V83_CS1	P5V83_CS1 - @lio.lib.LIO	18C6	P5V83_CS2	P5V83_CS2 - @lio.lib.LIO	18C6	P5V83_LG	P5V83_LG - @lio.lib.LIO	18C4	P5V83_PHASE	P5V83_PHASE - @lio.lib.LIO	18C4	P5V83_UG	P5V83_UG - @lio.lib.LIO	18C4	P5V83_V5FILT	P5V83_V5FILT - @lio.lib.LIO	18B6 18C5 18C6	P5V83_VREF	P5V83_VREF - @lio.lib.LIO	18C5 18C8	P5V83_VREG3	P5V83_VREG3 - @lio.lib.LIO	18C5	P5V83_VREG5	P5V83_VREG5 - @lio.lib.LIO	18C5	PCIE_CLK100M_EXCARD_N	PCIE_CLK100M_EXCARD_N - @lio.lib.LIO	5D2 5D5 7C3 8B1	PCIE_CLK100M_EXCARD_P	PCIE_CLK100M_EXCARD_P - @lio.lib.LIO	5D2 5D5 7C3 8B1	PCIE_CLK100M_EXCARD_UP_N	PCIE_CLK100M_EXCARD_UP_N - @lio.lib.LIO	8C4	PCIE_CLK100M_EXCARD_UP_P	PCIE_CLK100M_EXCARD_UP_P - @lio.lib.LIO	8B4	PCIE_CLK100M_MINI_N	PCIE_CLK100M_MINI_N - @lio.lib.LIO	5B2 5B5 6C6 8C1	PCIE_CLK100M_MINI_P	PCIE_CLK100M_MINI_P - @lio.lib.LIO	5B2 5B5 6C6 8C1	PCIE_CLK100M_MINI_UP_N	PCIE_CLK100M_MINI_UP_N - @lio.lib.LIO	8C4	PCIE_CLK100M_MINI_UP_P	PCIE_CLK100M_MINI_UP_P - @lio.lib.LIO	8C4	PCIE_EXCARD_D2R_N	PCIE_EXCARD_D2R_N - @lio.lib.LIO	5D2 5D5 7C3 8B3	PCIE_EXCARD_D2R_P	PCIE_EXCARD_D2R_P - @lio.lib.LIO	5D2 5D5 7C3 8B3	PCIE_EXCARD_R2D_N	PCIE_EXCARD_R2D_N - @lio.lib.LIO	5D2 5D5 7C3 8B3	PCIE_EXCARD_R2D_P	PCIE_EXCARD_R2D_P - @lio.lib.LIO	5D2 5D5 7C3 8B3	PCIE_MINI_D2R_N	PCIE_MINI_D2R_N - @lio.lib.LIO	5B2 5B5 6C6 8C3	PCIE_MINI_D2R_P	PCIE_MINI_D2R_P - @lio.lib.LIO	5B2 5B5 6C6 8C3	PCIE_MINI_R2D_N	PCIE_MINI_R2D_N - @lio.lib.LIO	5B2 5B5 6B6 8C3	PCIE_MINI_R2D_P	PCIE_MINI_R2D_P - @lio.lib.LIO	5C2 5C5 6B6 8C3	PCIE_WAKE_L	PCIE_WAKE_L - @lio.lib.LIO	5C3 5C3 5C4 6C6 7C3 8C6	PGND1	PGND1 - @lio.lib.LIO	18A5	PGND2	PGND2 - @lio.lib.LIO	18A5	PLT_RESET_L	PLT_RESET_L - @lio.lib.LIO	5C3 5C4 6C3 7C7 8C6	PLT_RESET_SWITCH_L	PLT_RESET_SWITCH_L - @lio.lib.LIO	7C3 7C3	PM_SLP_S3_L5V5	PM_SLP_S3_L5V5 - @lio.lib.LIO	5C3 5C3 5C4 8C6 18A4 18A8	PM_WLAN_EN_L	PM_WLAN_EN_L - @lio.lib.LIO	6C1 8C6	PP1V5_S0_EXCARD_SWIT	PP1V5_S0_EXCARD_SWIT - @lio.lib.LIO	7C3 7C3	PP1V5_S0_LIO	PP1V5_S0_LIO - @lio.lib.LIO	5C6 5C6 5C6 5C8 6C2 7C7 7C8 8B3	PP3V3_S0_AUDIO_F	PP3V3_S0_AUDIO_F - @lio.lib.LIO	16A4 16A8 16B5 16C8 16C8	PP3V3_S0_EXCARD_SWIT	PP3V3_S0_EXCARD_SWIT - @lio.lib.LIO	7C3 7C3	PP3V3_S0_LIO	PP3V3_S0_LIO - @lio.lib.LIO	5C6 5C6 5C6 5C8 7B4 7C7 11A7 11D6 15B8 15D8 16B1	PP3V3_S3_EXCARD_SWIT	PP3V3_S3_EXCARD_SWIT - @lio.lib.LIO	7C3 7C3	PP3V3_S3_LIO	PP3V3_S3_LIO - @lio.lib.LIO	5C6 5C6 5D6 5D6 5D6 5D8 6C2 6D1 7B8 7C7 7C8 10B5 10D5 18A4 18B1	PP3V3_WLAN	PP3V3_WLAN - @lio.lib.LIO	6D4	PP4V5_AUDIO_ANALOG	PP4V5_AUDIO_ANALOG - @lio.lib.LIO	11A3 11D3 12C6 16A8	PP4V5_AUDIO_LINE_IN	PP4V5_AUDIO_LINE_IN - @lio.lib.LIO	12C5	<table border="1"> <tbody> <tr><td>PP5V_AUDIO_HPAMP_PVD</td><td>PP5V_AUDIO_HPAMP_PVD - @lio.lib.LIO</td><td>13D6</td></tr> <tr><td>PP5V_AUDIO_HPAMP_PVD_F</td><td>PP5V_AUDIO_HPAMP_PVD_F - @lio.lib.LIO</td><td>13D6</td></tr> <tr><td>D_F</td><td>D_F - @lio.lib.LIO</td><td>13D6</td></tr> <tr><td>PP5V_PWRON_USB_LEFT2</td><td>PP5V_PWRON_USB_LEFT2 - @lio.lib.LIO</td><td>9D4</td></tr> <tr><td>PP5V_PWRON_USB_LEFT2_EMI</td><td>PP5V_PWRON_USB_LEFT2_EMI - @lio.lib.LIO</td><td>9D4</td></tr> <tr><td>PP5V_PWRON_USB_LEFT2_EMI</td><td>PP5V_PWRON_USB_LEFT2_EMI - @lio.lib.LIO</td><td>9B4</td></tr> <tr><td>PP5V_S0_AUDIO_AMP_LI</td><td>PP5V_S0_AUDIO_AMP_LI - @lio.lib.LIO</td><td>5B6 5B8 14A3 14A8 14B8</td></tr> <tr><td>PP5V_S0_AUDIO_AMP_LI</td><td>PP5V_S0_AUDIO_AMP_LI - @lio.lib.LIO</td><td>14C8 14D8 18A6 18A7</td></tr> <tr><td>PP5V_S0_AUDIO_F</td><td>PP5V_S0_AUDIO_F - @lio.lib.LIO</td><td>14A8 14B8 14C8 14D6</td></tr> <tr><td>PP5V_S0_AUDIO_LIO</td><td>PP5V_S0_AUDIO_LIO - @lio.lib.LIO</td><td>5B6 5B8 11A7 13D7 18A6</td></tr> <tr><td>PP5V_S0_FET</td><td>PP5V_S0_FET - @lio.lib.LIO</td><td>18A7 18A8</td></tr> <tr><td>PP5V_S3_LIO</td><td>PP5V_S3_LIO - @lio.lib.LIO</td><td>5D6 5D6 5D6 5D8 9B8 18A8</td></tr> <tr><td>PP5V_S3_USB_SWITCH_O</td><td>PP5V_S3_USB_SWITCH_OUT1 - @lio.lib.LIO</td><td>18C1</td></tr> <tr><td>PP5V_S3_USB_SWITCH_O</td><td>PP5V_S3_USB_SWITCH_OUT2 - @lio.lib.LIO</td><td>9B5</td></tr> <tr><td>PP5V_S3_USB_SWITCH_O</td><td>PP5V_S3_USB_SWITCH_OUT2 - @lio.lib.LIO</td><td>9D5</td></tr> <tr><td>PP18V5_D2IN</td><td>PP18V5_D2IN - @lio.lib.LIO</td><td>17A6 17D1</td></tr> <tr><td>PP18V5_D2IN_FUSE</td><td>PP18V5_D2IN_FUSE - @lio.lib.LIO</td><td>17D6</td></tr> <tr><td>PP18V5_D2IN_ONEMIRE</td><td>PP18V5_D2IN_ONEMIRE - @lio.lib.LIO</td><td>17D5</td></tr> <tr><td>PP18V5_D2IN_UP</td><td>PP18V5_D2IN_UP - @lio.lib.LIO</td><td>17D7</td></tr> <tr><td>PPBUS_G3H</td><td>PPBUS_G3H - @lio.lib.LIO</td><td>5D6 5D6 5D8 17B6 18D8</td></tr> <tr><td>SMBUS_LIO_SB_SCL</td><td>SMBUS_LIO_SB_SCL - @lio.lib.LIO</td><td>5A2 5A3 7C3 8B6</td></tr> <tr><td>SMBUS_LIO_SB_SDA</td><td>SMBUS_LIO_SB_SDA - @lio.lib.LIO</td><td>5A4 5A5 7C3 8B6</td></tr> <tr><td>SMBUS_LIO_SMC_SCL</td><td>SMBUS_LIO_SMC_SCL - @lio.lib.LIO</td><td>5A2 5A2 5A3 6B3 8B3 10A6</td></tr> <tr><td>SMBUS_LIO_SMC_SDA</td><td>SMBUS_LIO_SMC_SDA - @lio.lib.LIO</td><td>5A4 5A4 5A5 6B3 8B3 10A6</td></tr> <tr><td>SMC_BC_ACCK</td><td>SMC_BC_ACCK - @lio.lib.LIO</td><td>8C6 17B2</td></tr> <tr><td>SMC_BC_ACCK_R</td><td>SMC_BC_ACCK_R - @lio.lib.LIO</td><td>17C1</td></tr> <tr><td>SMC_EXCARD_CP</td><td>SMC_EXCARD_CP - @lio.lib.LIO</td><td>7A6 8C6</td></tr> <tr><td>SMC_EXCARD_PWR_EN</td><td>SMC_EXCARD_PWR_EN - @lio.lib.LIO</td><td>7C7 8C6</td></tr> <tr><td>SPKRAMP_L1_NOUT</td><td>SPKRAMP_L1_NOUT - @lio.lib.LIO</td><td>14D3 14D3</td></tr> <tr><td>SPKRAMP_L1_POUT</td><td>SPKRAMP_L1_POUT - @lio.lib.LIO</td><td>14D3 14D3</td></tr> <tr><td>SPKRAMP_L2_NOUT</td><td>SPKRAMP_L2_NOUT - @lio.lib.LIO</td><td>14C3 14C3</td></tr> <tr><td>SPKRAMP_L2_POUT</td><td>SPKRAMP_L2_POUT - @lio.lib.LIO</td><td>14C3 14C3</td></tr> <tr><td>SPKRAMP_R1_NOUT</td><td>SPKRAMP_R1_NOUT - @lio.lib.LIO</td><td>14B3 14B3</td></tr> <tr><td>SPKRAMP_R1_POUT</td><td>SPKRAMP_R1_POUT - @lio.lib.LIO</td><td>14B3 14B3</td></tr> <tr><td>SPKRAMP_R2_NOUT</td><td>SPKRAMP_R2_NOUT - @lio.lib.LIO</td><td>14A3 14B3</td></tr> <tr><td>SPKRAMP_R2_POUT</td><td>SPKRAMP_R2_POUT - @lio.lib.LIO</td><td>14A3 14B3</td></tr> <tr><td>SPKRAMP_SYNC1</td><td>SPKRAMP</td></tr></tbody></table>	PP5V_AUDIO_HPAMP_PVD	PP5V_AUDIO_HPAMP_PVD - @lio.lib.LIO	13D6	PP5V_AUDIO_HPAMP_PVD_F	PP5V_AUDIO_HPAMP_PVD_F - @lio.lib.LIO	13D6	D_F	D_F - @lio.lib.LIO	13D6	PP5V_PWRON_USB_LEFT2	PP5V_PWRON_USB_LEFT2 - @lio.lib.LIO	9D4	PP5V_PWRON_USB_LEFT2_EMI	PP5V_PWRON_USB_LEFT2_EMI - @lio.lib.LIO	9D4	PP5V_PWRON_USB_LEFT2_EMI	PP5V_PWRON_USB_LEFT2_EMI - @lio.lib.LIO	9B4	PP5V_S0_AUDIO_AMP_LI	PP5V_S0_AUDIO_AMP_LI - @lio.lib.LIO	5B6 5B8 14A3 14A8 14B8	PP5V_S0_AUDIO_AMP_LI	PP5V_S0_AUDIO_AMP_LI - @lio.lib.LIO	14C8 14D8 18A6 18A7	PP5V_S0_AUDIO_F	PP5V_S0_AUDIO_F - @lio.lib.LIO	14A8 14B8 14C8 14D6	PP5V_S0_AUDIO_LIO	PP5V_S0_AUDIO_LIO - @lio.lib.LIO	5B6 5B8 11A7 13D7 18A6	PP5V_S0_FET	PP5V_S0_FET - @lio.lib.LIO	18A7 18A8	PP5V_S3_LIO	PP5V_S3_LIO - @lio.lib.LIO	5D6 5D6 5D6 5D8 9B8 18A8	PP5V_S3_USB_SWITCH_O	PP5V_S3_USB_SWITCH_OUT1 - @lio.lib.LIO	18C1	PP5V_S3_USB_SWITCH_O	PP5V_S3_USB_SWITCH_OUT2 - @lio.lib.LIO	9B5	PP5V_S3_USB_SWITCH_O	PP5V_S3_USB_SWITCH_OUT2 - @lio.lib.LIO	9D5	PP18V5_D2IN	PP18V5_D2IN - @lio.lib.LIO	17A6 17D1	PP18V5_D2IN_FUSE	PP18V5_D2IN_FUSE - @lio.lib.LIO	17D6	PP18V5_D2IN_ONEMIRE	PP18V5_D2IN_ONEMIRE - @lio.lib.LIO	17D5	PP18V5_D2IN_UP	PP18V5_D2IN_UP - @lio.lib.LIO	17D7	PPBUS_G3H	PPBUS_G3H - @lio.lib.LIO	5D6 5D6 5D8 17B6 18D8	SMBUS_LIO_SB_SCL	SMBUS_LIO_SB_SCL - @lio.lib.LIO	5A2 5A3 7C3 8B6	SMBUS_LIO_SB_SDA	SMBUS_LIO_SB_SDA - @lio.lib.LIO	5A4 5A5 7C3 8B6	SMBUS_LIO_SMC_SCL	SMBUS_LIO_SMC_SCL - @lio.lib.LIO	5A2 5A2 5A3 6B3 8B3 10A6	SMBUS_LIO_SMC_SDA	SMBUS_LIO_SMC_SDA - @lio.lib.LIO	5A4 5A4 5A5 6B3 8B3 10A6	SMC_BC_ACCK	SMC_BC_ACCK - @lio.lib.LIO	8C6 17B2	SMC_BC_ACCK_R	SMC_BC_ACCK_R - @lio.lib.LIO	17C1	SMC_EXCARD_CP	SMC_EXCARD_CP - @lio.lib.LIO	7A6 8C6	SMC_EXCARD_PWR_EN	SMC_EXCARD_PWR_EN - @lio.lib.LIO	7C7 8C6	SPKRAMP_L1_NOUT	SPKRAMP_L1_NOUT - @lio.lib.LIO	14D3 14D3	SPKRAMP_L1_POUT	SPKRAMP_L1_POUT - @lio.lib.LIO	14D3 14D3	SPKRAMP_L2_NOUT	SPKRAMP_L2_NOUT - @lio.lib.LIO	14C3 14C3	SPKRAMP_L2_POUT	SPKRAMP_L2_POUT - @lio.lib.LIO	14C3 14C3	SPKRAMP_R1_NOUT	SPKRAMP_R1_NOUT - @lio.lib.LIO	14B3 14B3	SPKRAMP_R1_POUT	SPKRAMP_R1_POUT - @lio.lib.LIO	14B3 14B3	SPKRAMP_R2_NOUT	SPKRAMP_R2_NOUT - @lio.lib.LIO	14A3 14B3	SPKRAMP_R2_POUT	SPKRAMP_R2_POUT - @lio.lib.LIO	14A3 14B3	SPKRAMP_SYNC1	SPKRAMP
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