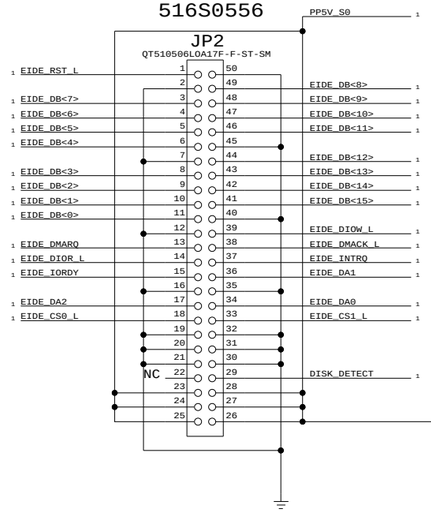
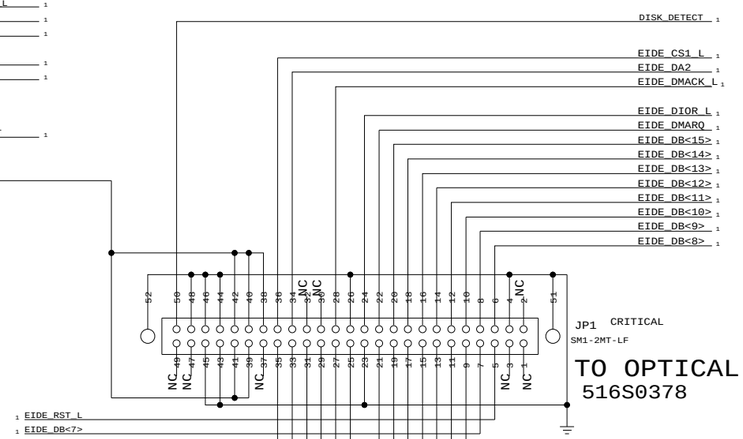
1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ZONE	ECN	DESCRIPTION OF CHANGE	CK APPD / DATE	ENG APPD / DATE
01		528515	ENGINEERING RELEASED	08/28/07	?

SCHEM,FLEX,ODD,M88
EVT
08/22/07

TO MLB
516S0556





SIGNAL CONSTRAINTS

GROUP	SIG_NAME	MATCHED_DELAY	MIN_LINE_WIDTH	NET_SPACING_TYPE
	EIDE_DB<15..0>	OPT:::500	MIN_LINE_WIDTH=0.13MM	0.13MM SPACING
	EIDE_DIOR_L	OPT:::500	MIN_LINE_WIDTH=0.13MM	0.13MM SPACING
	EIDE_DIOW_L	OPT:::500	MIN_LINE_WIDTH=0.13MM	0.13MM SPACING
	EIDE_RST_L	OPT:::500	MIN_LINE_WIDTH=0.13MM	0.13MM SPACING
	PP5V_S0	VOLTAGE=5V	MIN_LINE_WIDTH=3.05MM	0.25MM SPACING

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-7501	1	SCHEM,FLEX,ODD,M88	SCH1	CRITICAL	?
821-0599	1	PCBF,FLEX,ODD,M88	PCB1	CRITICAL	?
632-0637	1	PCBA,FLEX,ODD,M88	BOM1	X	OMIT
056-2228	1	PCB DSGN GD,FLEX,ODD,M76	MCO1	CRITICAL	?

DIMENSIONS ARE IN MILLIMETERS
XX ±
X.XX ±
X.XXX ±
ANGLES ±
DO NOT SCALE DRAWING

THIRD ANGLE PROJECTION

METRIC

DRAFTER | DESIGN CK
ENG APPD | MFG APPD
QA APPD | DESIGNER
RELEASE | SCALE NONE

MATERIAL/FINISH NOTED AS APPLICABLE

SIZE D

APPLE INC.

TITLE
SCHEM,FLEX,ODD,M88

DRAWING NUMBER 051-7501 REV. 01

SHT 1 OF 3

Title: Basenet Report
Design: flex_odd
Date: Dec 14 11:07:57 2006

Base nets and synonyms for flex_odd_lib.FLEX_ODD(@flex_odd_lib.flex_odd(sch_1))

Base Signal	Synonyms	Location([Zone][dir])
DISK_DETECT	DISK_DETECT - @flex_odd_lib.FLEX_ODD	1C3 1C5
EIDE_CS0_L	EIDE_CS0_L - @flex_odd_lib.FLEX_ODD	1A5 1C6
EIDE_CS1_L	EIDE_CS1_L - @flex_odd_lib.FLEX_ODD	1C3 1C5
EIDE_DA0	EIDE_DA0 - @flex_odd_lib.FLEX_ODD	1B5 1C5
EIDE_DA1	EIDE_DA1 - @flex_odd_lib.FLEX_ODD	1B5 1C5
EIDE_DA2	EIDE_DA2 - @flex_odd_lib.FLEX_ODD	1C3 1C6
EIDE_DB<0>	EIDE_DB<0> - @flex_odd_lib.FLEX_ODD	1B5 1C6
EIDE_DB<15..0>	EIDE_DB<15..0> - @flex_odd_lib.FLEX_ODD	1B1
EIDE_DB<1>	EIDE_DB<1> - @flex_odd_lib.FLEX_ODD	1B5 1C6
EIDE_DB<2>	EIDE_DB<2> - @flex_odd_lib.FLEX_ODD	1B5 1C6
EIDE_DB<3>	EIDE_DB<3> - @flex_odd_lib.FLEX_ODD	1B5 1C6
EIDE_DB<4>	EIDE_DB<4> - @flex_odd_lib.FLEX_ODD	1B5 1C6
EIDE_DB<5>	EIDE_DB<5> - @flex_odd_lib.FLEX_ODD	1B5 1C6
EIDE_DB<6>	EIDE_DB<6> - @flex_odd_lib.FLEX_ODD	1B5 1C6
EIDE_DB<7>	EIDE_DB<7> - @flex_odd_lib.FLEX_ODD	1B5 1D6
EIDE_DB<8>	EIDE_DB<8> - @flex_odd_lib.FLEX_ODD	1C3 1D5
EIDE_DB<9>	EIDE_DB<9> - @flex_odd_lib.FLEX_ODD	1C3 1D5
EIDE_DB<10>	EIDE_DB<10> - @flex_odd_lib.FLEX_ODD	1C3 1C5
EIDE_DB<11>	EIDE_DB<11> - @flex_odd_lib.FLEX_ODD	1C3 1C5
EIDE_DB<12>	EIDE_DB<12> - @flex_odd_lib.FLEX_ODD	1C3 1C5
EIDE_DB<13>	EIDE_DB<13> - @flex_odd_lib.FLEX_ODD	1C3 1C5
EIDE_DB<14>	EIDE_DB<14> - @flex_odd_lib.FLEX_ODD	1C3 1C5
EIDE_DB<15>	EIDE_DB<15> - @flex_odd_lib.FLEX_ODD	1C3 1C5
EIDE_DIOR_L	EIDE_DIOR_L - @flex_odd_lib.FLEX_ODD	1B1 1C3 1C6
EIDE_DIOW_L	EIDE_DIOW_L - @flex_odd_lib.FLEX_ODD	1A1 1B5 1C5
EIDE_DMACK_L	EIDE_DMACK_L - @flex_odd_lib.FLEX_ODD	1C3 1C5
EIDE_DMARQ	EIDE_DMARQ - @flex_odd_lib.FLEX_ODD	1C3 1C6
EIDE_INTRQ	EIDE_INTRQ - @flex_odd_lib.FLEX_ODD	1B5 1C5
EIDE_IORDY	EIDE_IORDY - @flex_odd_lib.FLEX_ODD	1B5 1C6
EIDE_RST_L	EIDE_RST_L - @flex_odd_lib.FLEX_ODD	1A1 1B5 1D6
PP5V_S0	PP5V_S0 - @flex_odd_lib.FLEX_ODD	1A1 1D5

Title: Cref Part Report
Design: flex_odd
Date: Dec 14 11:07:57 2006

JP1 CON_50SM_KX14_SM1-2M flex_odd[1B4]
T-LF
JP2 CON_F50SM_5MMC_QT510 flex_odd[1C5]
506L0317F-F-ST-SM