SCHEM, PCB, LIO, K78

EVT 1, 2011-04-18

Page Contents Sync date
1 Table of Contents 8/1/2011
2 System Block Diagram 8/1/2011
3 BOM Test 8/1/2011
4 Power Alliances 8/1/2011
5 MCO, External USB Connectors 8/1/2011
6 LIO Connectors 8/1/2011
7 AUDIO: CODEC/REGULATOR 8/1/2011
8 AUDIO: HEADPHONE FILTER 8/1/2011
9 AUDIO: SPEAKER AND 8/1/2011
10 AUDIO: JACK 8/1/2011
11 AUDIO: JACK TRANSLATORS 8/1/2011
12 AUX CONNECTOR 8/1/2011
13 MCP Constraints 2 8/1/2011

Alternate Parts

Schematic / PCB #'s

PRODUCT SAFETY REQUIREMENTS:

- UL Recognition: Min. 130-C Temp. Rating and V-0 Flammability Rating per UL 796 & UL 94.
- PCBs to be silk screened with UL/CUL Recognition Mark, Manufacturer's UL File Number, Min. 130-C Temp. Rating and V-0 Flammability Rating.
Functional Test Points
### Pogo pins

- **APN: 870-2015 ZS8031**
  - 1.02IA-SN0K-SILVER-K99
  - Place under audio connector (bottom side)
- **APN: 870-2015 ZS8043**
  - 1.02IA-SN0K-SILVER-K99
  - Place near MPM near edge of board (bottom side)
- **APN: 870-2015 ZS8048**
  - 1.02IA-SN0K-SILVER-K99
  - Place near MPM near middle of board (bottom side)
- **APN: 998-5725**
  - Place near MPM near edge of board (bottom side)

### ESD Boss

- **APN: 998-5725**
  - 1.02IA-SN0K-SILVER-K99
  - Place near MPM near edge of board (bottom side)

---

**Power Aliases**

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Description</th>
<th>Reference</th>
<th>Critical</th>
<th>Ref</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>PP18V5_G3H</td>
<td>PP18V5_DCIN_CONN</td>
<td>1.5V</td>
<td>MAKE_BASE = TRUE</td>
<td>VOLTAGE = 18.5V</td>
<td>MIN_NECK_WIDTH = 0.2MM</td>
</tr>
<tr>
<td>PP3V3_S0</td>
<td>PP3V3_S0_AUDIO</td>
<td>3.3V</td>
<td>MAKE_BASE = TRUE</td>
<td>VOLTAGE = 3.42V</td>
<td>MIN_NECK_WIDTH = 0.3MM</td>
</tr>
<tr>
<td>PP5V_S3</td>
<td>PP5V_S3_AUDIO_AMP</td>
<td>5V</td>
<td>MAKE_BASE = TRUE</td>
<td>VOLTAGE = 5V</td>
<td>MIN_NECK_WIDTH = 0.2MM</td>
</tr>
<tr>
<td>PP3V42_G3H</td>
<td>PP3V42_G3H_AUDIO_UF</td>
<td>3.42V</td>
<td>MAKE_BASE = TRUE</td>
<td>VOLTAGE = 3.42V</td>
<td>MIN_NECK_WIDTH = 0.3MM</td>
</tr>
<tr>
<td>PP3V42_G3H</td>
<td>PP3V42_G3H_ONEWIRE</td>
<td>3.42V</td>
<td>MAKE_BASE = TRUE</td>
<td>VOLTAGE = 3.42V</td>
<td>MIN_NECK_WIDTH = 0.3MM</td>
</tr>
<tr>
<td>PP3V3R1V5_S0_AUDIO</td>
<td>PP1V5_S0_AUDIO</td>
<td>3.3V</td>
<td>MAKE_BASE = TRUE</td>
<td>VOLTAGE = 3.3V</td>
<td>MIN_NECK_WIDTH = 0.3MM</td>
</tr>
<tr>
<td>PP3V3_S0</td>
<td>PP3V3_S0_TEMP_SNR</td>
<td>3.3V</td>
<td>MAKE_BASE = TRUE</td>
<td>VOLTAGE = 3.3V</td>
<td>MIN_NECK_WIDTH = 0.3MM</td>
</tr>
<tr>
<td>PP3V3R1V5_S0_AUDIO</td>
<td>PP1V5_S0_AUDIO</td>
<td>3.3V</td>
<td>MAKE_BASE = TRUE</td>
<td>VOLTAGE = 3.3V</td>
<td>MIN_NECK_WIDTH = 0.3MM</td>
</tr>
<tr>
<td>PP5V_S3</td>
<td>PP5V_S3_REG</td>
<td>5V</td>
<td>MAKE_BASE = TRUE</td>
<td>VOLTAGE = 5V</td>
<td>MIN_NECK_WIDTH = 0.2MM</td>
</tr>
<tr>
<td>PP3V3_S0</td>
<td>PP3V3_S0_AUDIO_DIG</td>
<td>3.3V</td>
<td>MAKE_BASE = TRUE</td>
<td>VOLTAGE = 3.3V</td>
<td>MIN_NECK_WIDTH = 0.3MM</td>
</tr>
<tr>
<td>PP5V_S3</td>
<td>PP5V_S3_EXTUSB</td>
<td>5V</td>
<td>MAKE_BASE = TRUE</td>
<td>VOLTAGE = 5V</td>
<td>MIN_NECK_WIDTH = 0.2MM</td>
</tr>
<tr>
<td>PP5V_S3</td>
<td>PP5V_S3_CAMERA</td>
<td>5V</td>
<td>MAKE_BASE = TRUE</td>
<td>VOLTAGE = 5V</td>
<td>MIN_NECK_WIDTH = 0.2MM</td>
</tr>
</tbody>
</table>

---

[Viewing online at www.vinafix.vn](http://www.vinafix.vn)