

EEE PC 701 PCB version

GPI37	GPI38	GPI39	PCB version
0	0	0	
0	0	0	
0	0	1	
0	0	1	
0	1	0	
0	1	0	
0	1	1	
0	1	1	
1	0	0	
1	0	0	
1	0	1	
1	0	1	
1	1	0	
1	1	0	
1	1	1	
1	1	1	

USB

USB 0	USB Conn
USB 1	USB Conn
USB 2	NA
USB 3	NA
USB 4	Card Reader
USB 5	Minicard
USB 6	Bluetooth
USB 7	Camera

PCIE

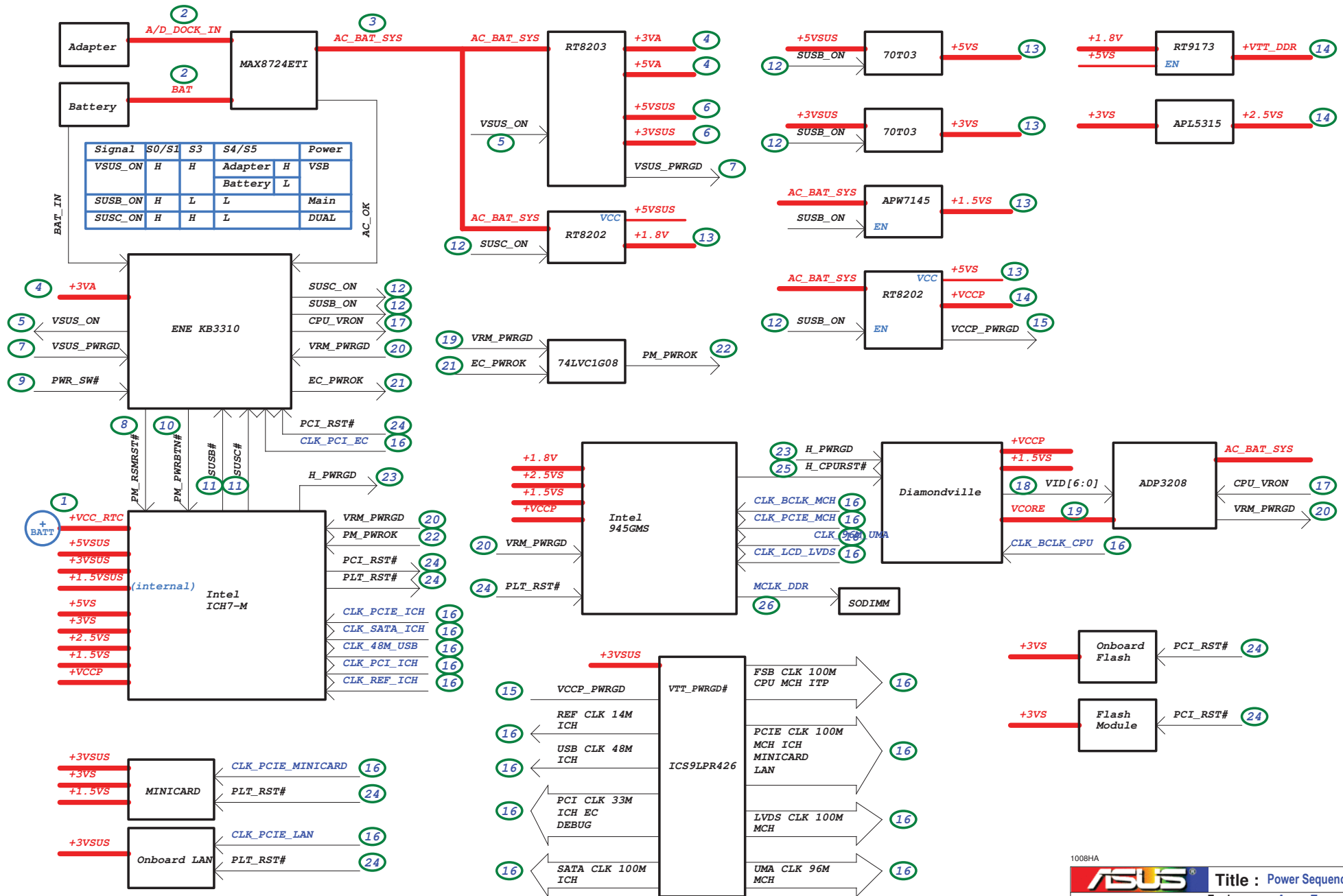
PCIE 1	NC
PCIE 2	LAN
PCIE 3	Minicard
PCIE 4	SSD

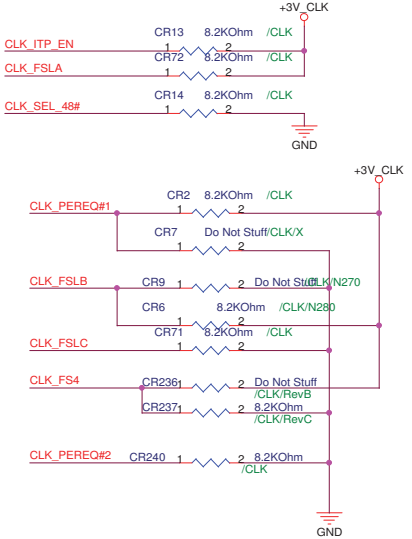
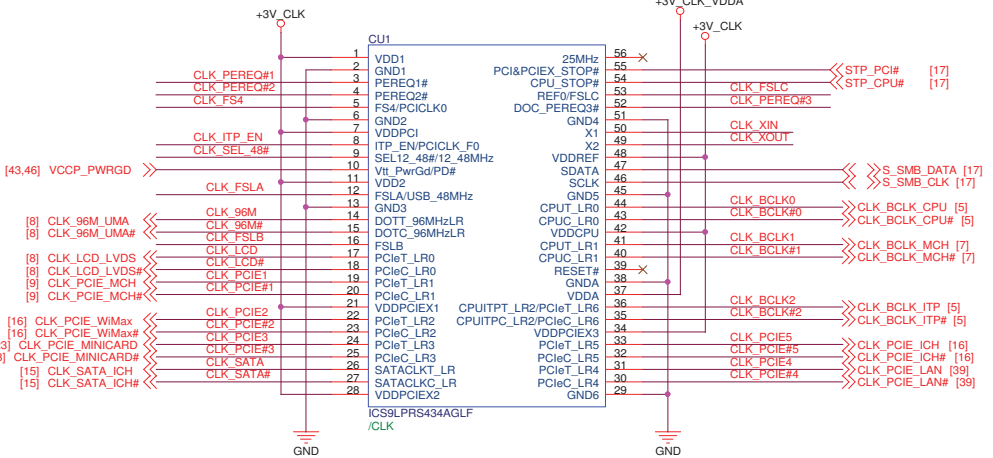
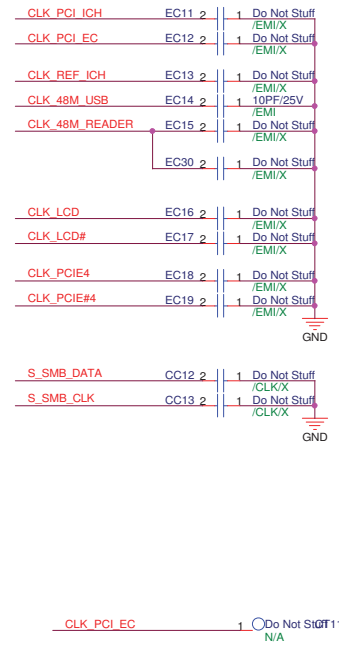
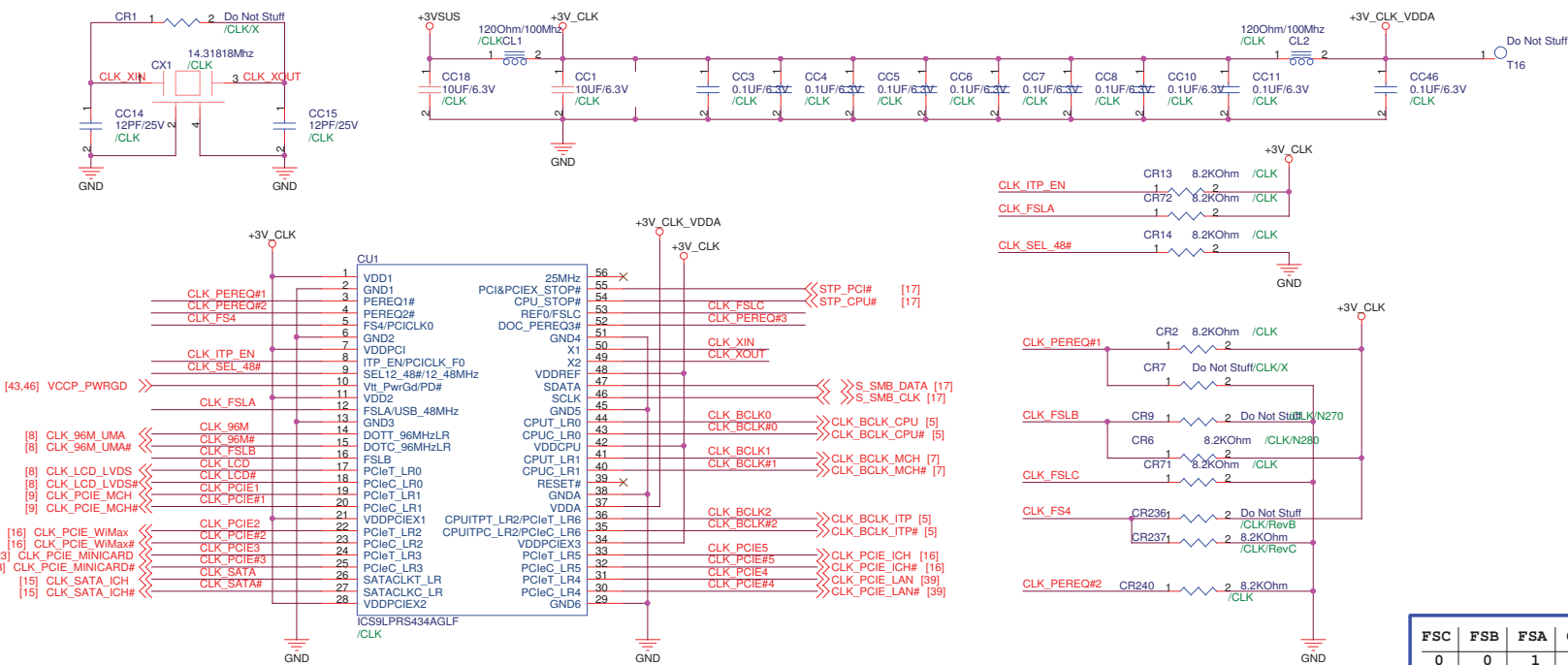
Azalia

ACZ_SDIN0	CODEC
ACZ_SDIN1	NC
ACZ_SDIN2	NC

1008HA

		Title : System Setting
ASUSTek Computer INC.		Engineer: Aaron Tsao
Size A3	Project Name 1008HA	Rev 1.3G
Date: Friday, March 27, 2009		Sheet 2 of 49

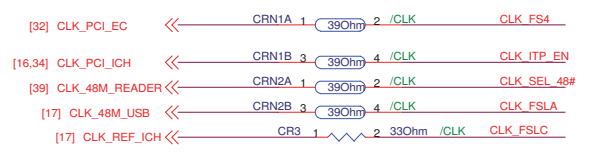
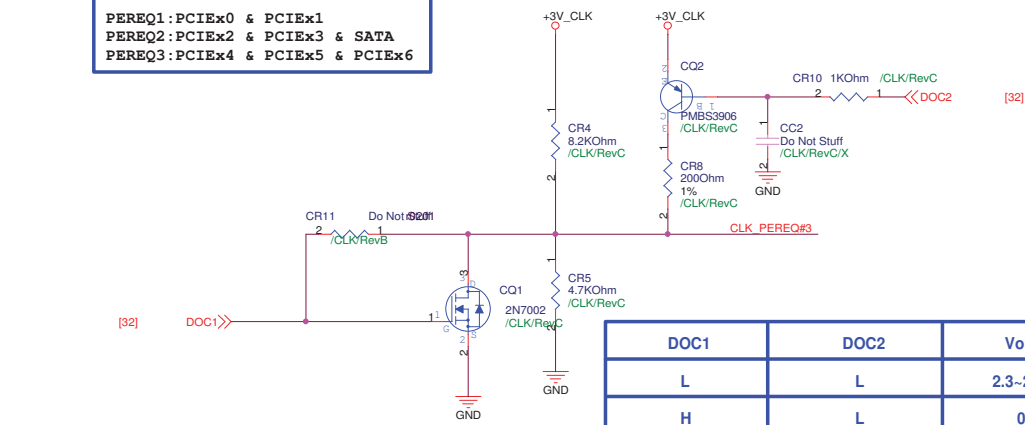




FSC	FSB	FSA	CPU	PCIE	SATA
0	0	1	133	100	100
1	0	1	100	100	100

Selected through the I2c
 1:Disable
 0:Enable

PEREQ1:PCIEx0 & PCIEx1
 PEREQ2:PCIEx2 & PCIEx3 & SATA
 PEREQ3:PCIEx4 & PCIEx5 & PCIEx6



DOC1	DOC2	Voltage	Status
L	L	2.3~2.52V	Performance
H	L	0V	Power Saving
H	H	0V	Power Saving
L	H	1.1~1.2V	Normal

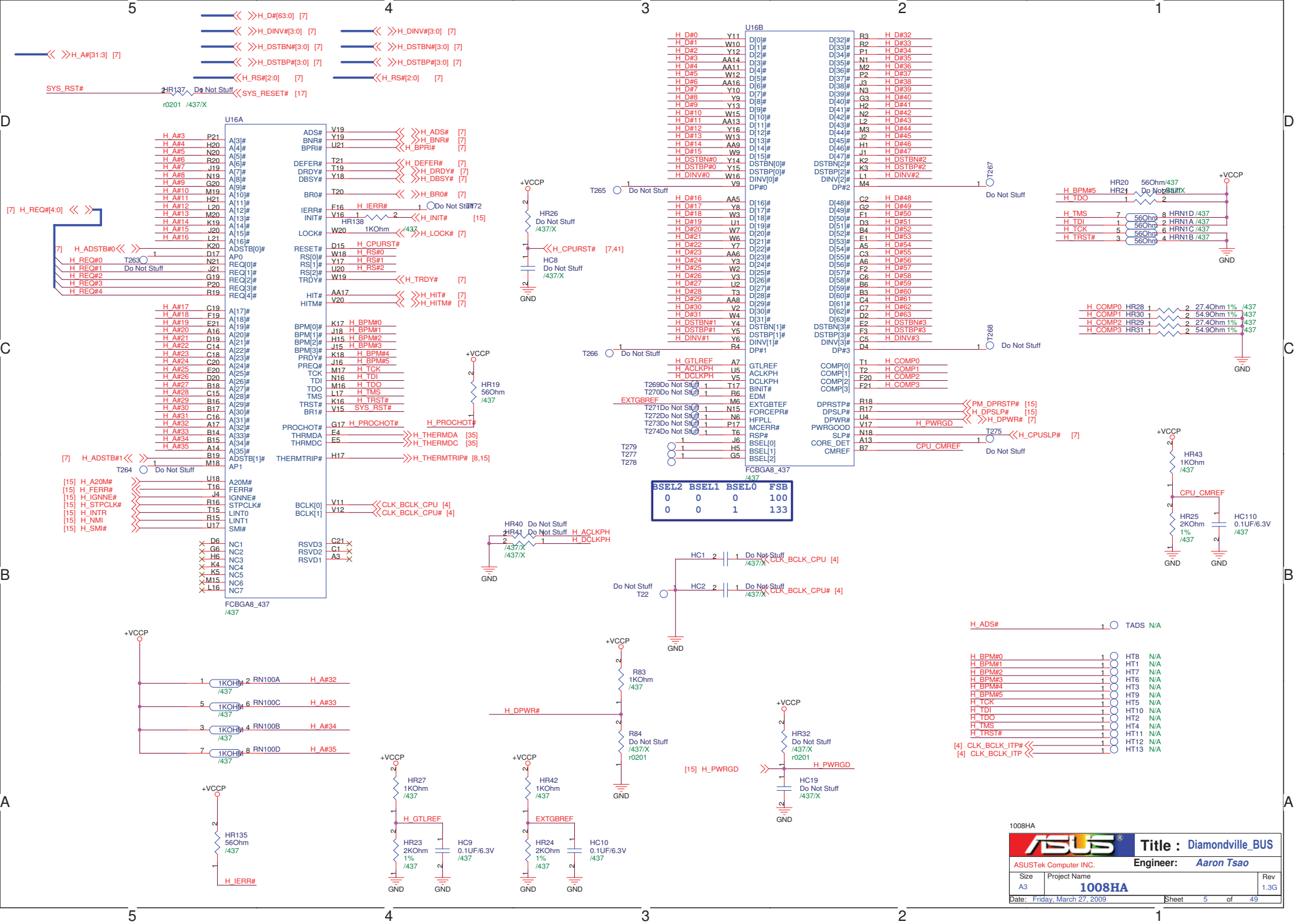
1008HA

Title :Clock ICS9LPRS427
Engineer: Aaron Tsao

ASUSTek Computer INC.

Size	Project Name
A3	1008HA

Date: Friday, March 27, 2009 Sheet 4 of 49 Rev 1.3G



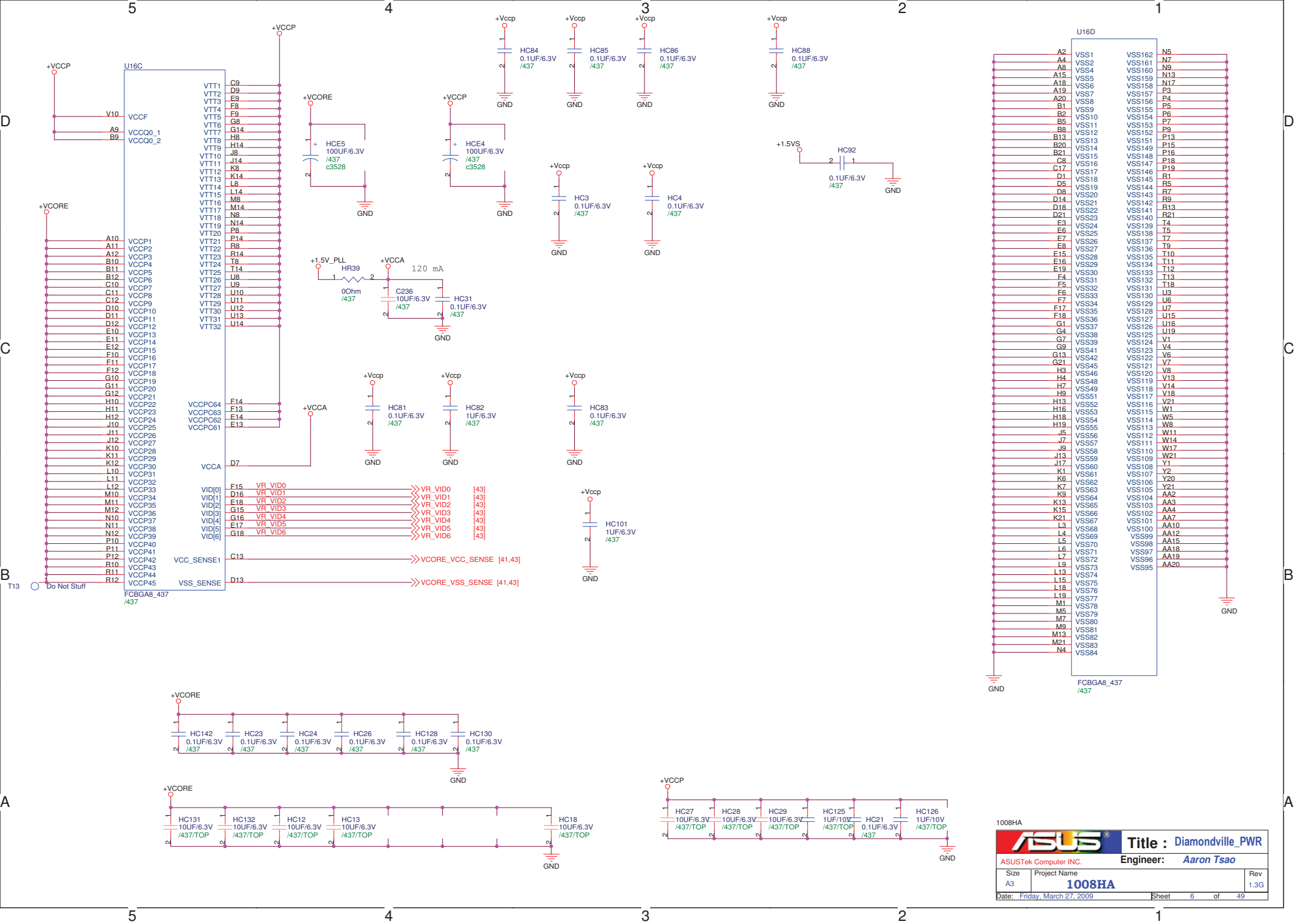
1008HA

ASUS Title : Diamondville_BUS

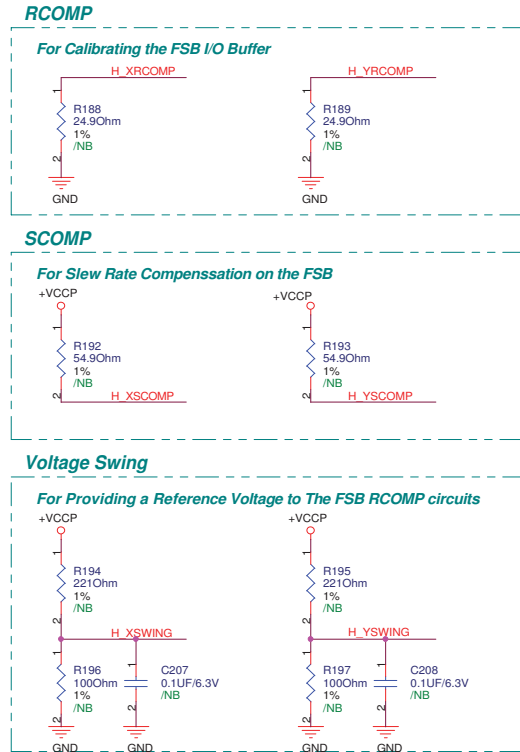
ASUSTek Computer INC. Engineer: Aaron Tsao

Size A3 Project Name 1008HA Rev 1.3G

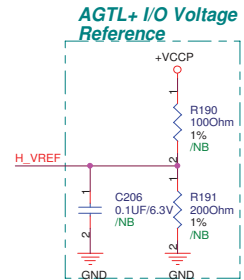
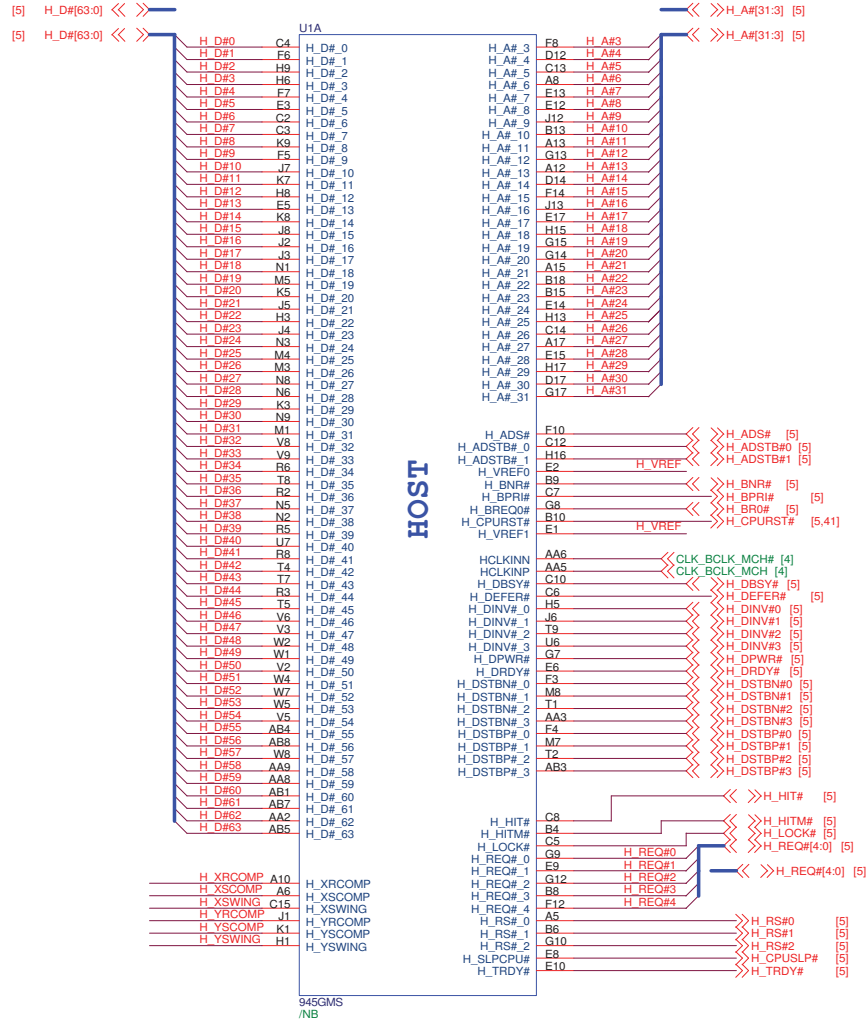
Date: Friday, March 27, 2009 Sheet 5 of 49



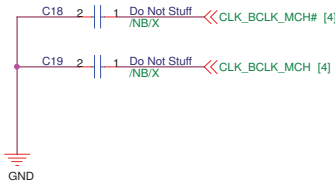
Power:
+VCCP



Signal voltage level =
0.3125*VCCP
Trace should be 10 mil wide
with 20 mil spacing



Layout Note:
0.1uF should be placed 100mils or
less from GMCH pin.



1008HA

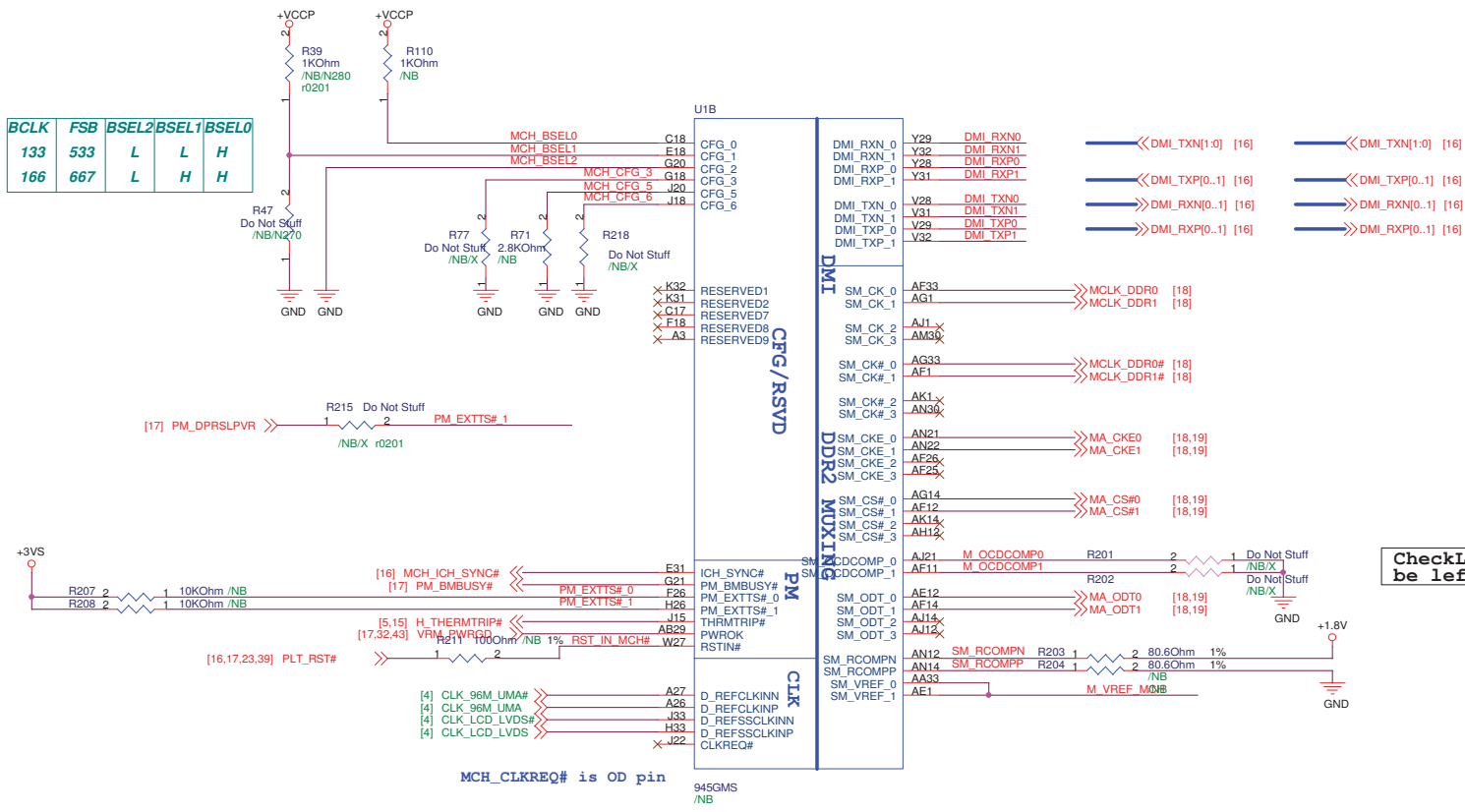
ASUS Title : NB-945GMS(HOST)

ASUSTek COMPUTER INC. Engineer: Aaron Tsao

Size: A3 Project Name: 1008HA Rev: 1.3G

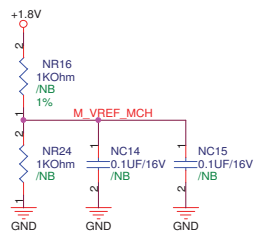
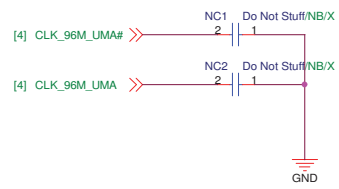
Date: Friday, March 27, 2009 Sheet: 7 of 49

BCLK	FSB	BSEL2	BSEL1	BSEL0
133	533	L	L	H
166	667	L	H	H



CheckList notes : Can be left as NC

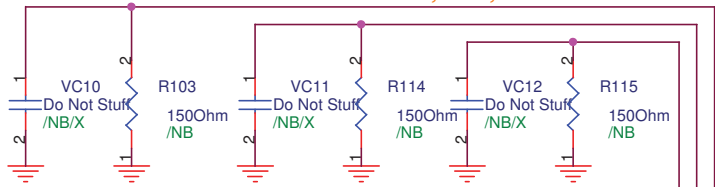
MCH_CLKREQ# is OD pin



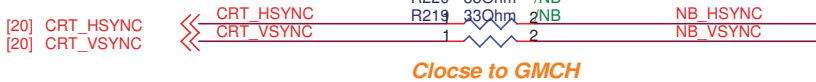
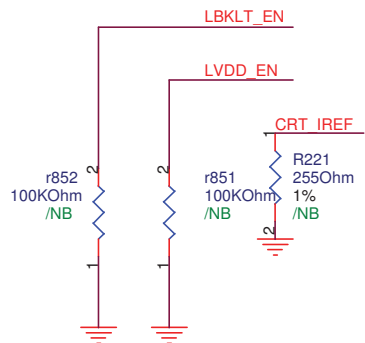
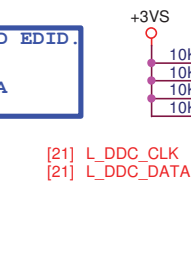
1008HA

ASUS		Title : NB-945GMS(DMI & CFG)
ASUSTeK COMPUTER INC.		Engineer: Aaron Tsao
Size A3	Project Name 1008HA	Rev 1.3G
Date: Friday, March 27, 2009		Sheet 8 of 49

**Close to GMCH
R103,R114,R115**



IF USE NB READ EDID.
MUST CONNECT
L_DDC_CLK&DATA

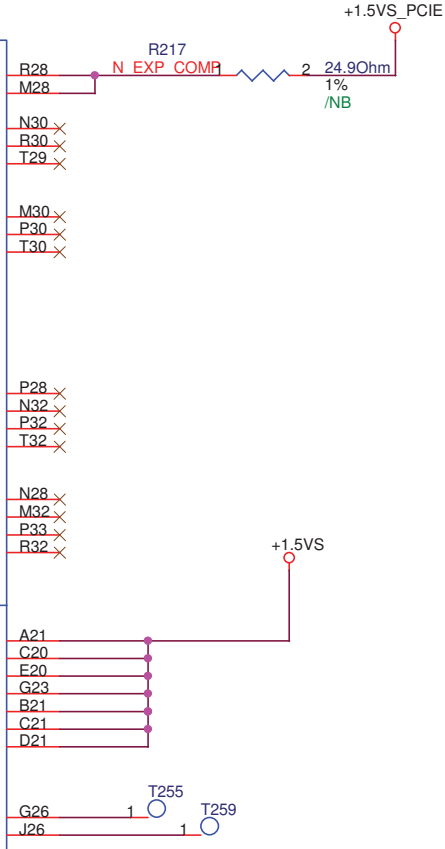


Close to GMCH

- [4] CLK_PCIE_MCH#
- [4] CLK_PCIE_MCH
- [20] DDC_CLK
- [20] DDC_DATA
- [20] CRT_BLUE
- [20] CRT_GREEN
- [20] CRT_RED
- [21] LBKLT_CTRL
- [21] LBKLT_EN
- [21] LA_CLKN
- [21] LA_CLKP
- [21] LA_DATAN0
- [21] LA_DATAN1
- [21] LA_DATAN2
- [21] LA_DATAP0
- [21] LA_DATAP1
- [21] LA_DATAP2
- [20] CRT_HSYNC
- [20] CRT_VSYNC

U1F

MISC	SDVO_CTRLDATA SDVO_CTRLCLK G_CLKINN G_CLKINP
VGA	CRT_DDC_CLK CRT_DDC_DATA CRT_BLUE CRT_BLUE# CRT_GREEN CRT_GREEN# CRT_RED CRT_RED# CRT_VSYNC CRT_VSYNC# CRT_HSYNC CRT_HSYNC# CRT_IREF
SDVO	L_BKLTCTL L_BKLTEN L_CLKCTLA L_CTLBDATA L_DDC_CLK L_DDC_DATA L_VDDEN L_IBG L_VBG L_VREFH L_VREFL
LVDS	LA_CLKN LA_CLKP LA_DATAN_0 LA_DATAN_1 LA_DATAN_2 LA_DATAP_0 LA_DATAP_1 LA_DATAP_2 LB_DATAN_0 LB_DATAN_1 LB_DATAN_2 LB_DATAP_0 LB_DATAP_1 LB_DATAP_2



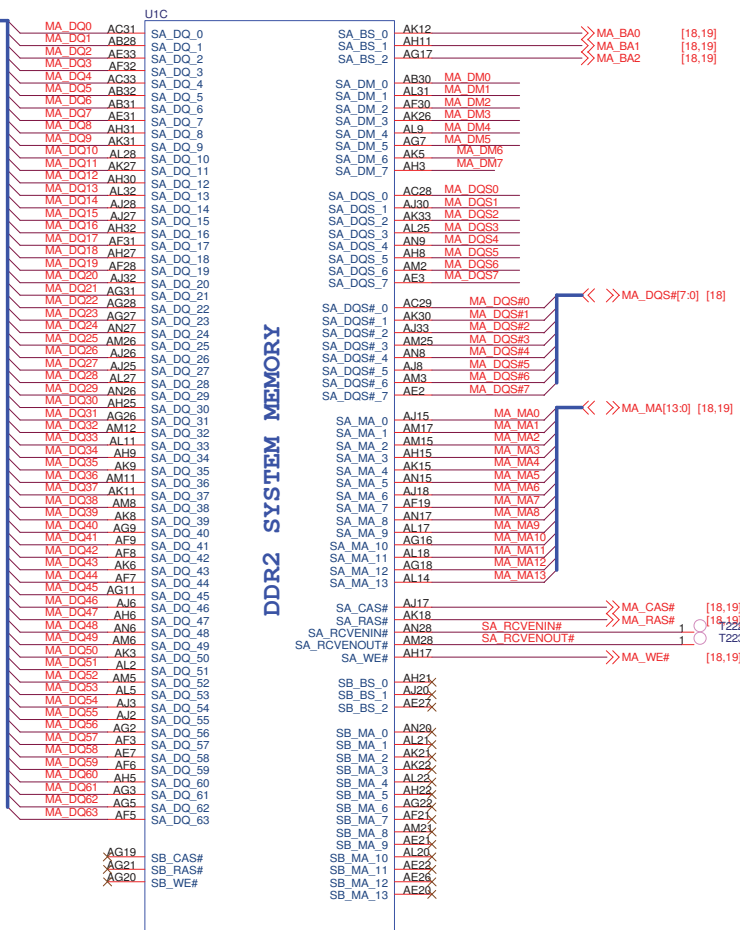
945GMS
/NB

1008HA

		Title : NB-945GMS(GRAPHIC)
ASUSTeK COMPUTER INC.		Engineer: Aaron Tsao
Size A4	Project Name 1008HA	Rev 1.3G
Date: Friday, March 27, 2009		Sheet 9 of 49

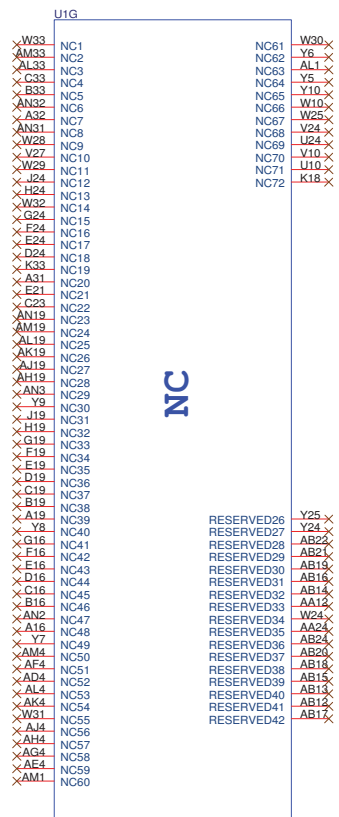
[18] MA_DQ[63:0] << >>
 [18] MA_DM[7:0] << >>

[18] MA_DQ[63:0] << >>
 [18] MA_DQ[63:0] << >>



DDR2 SYSTEM MEMORY

945GMS /NB



945GMS /NB

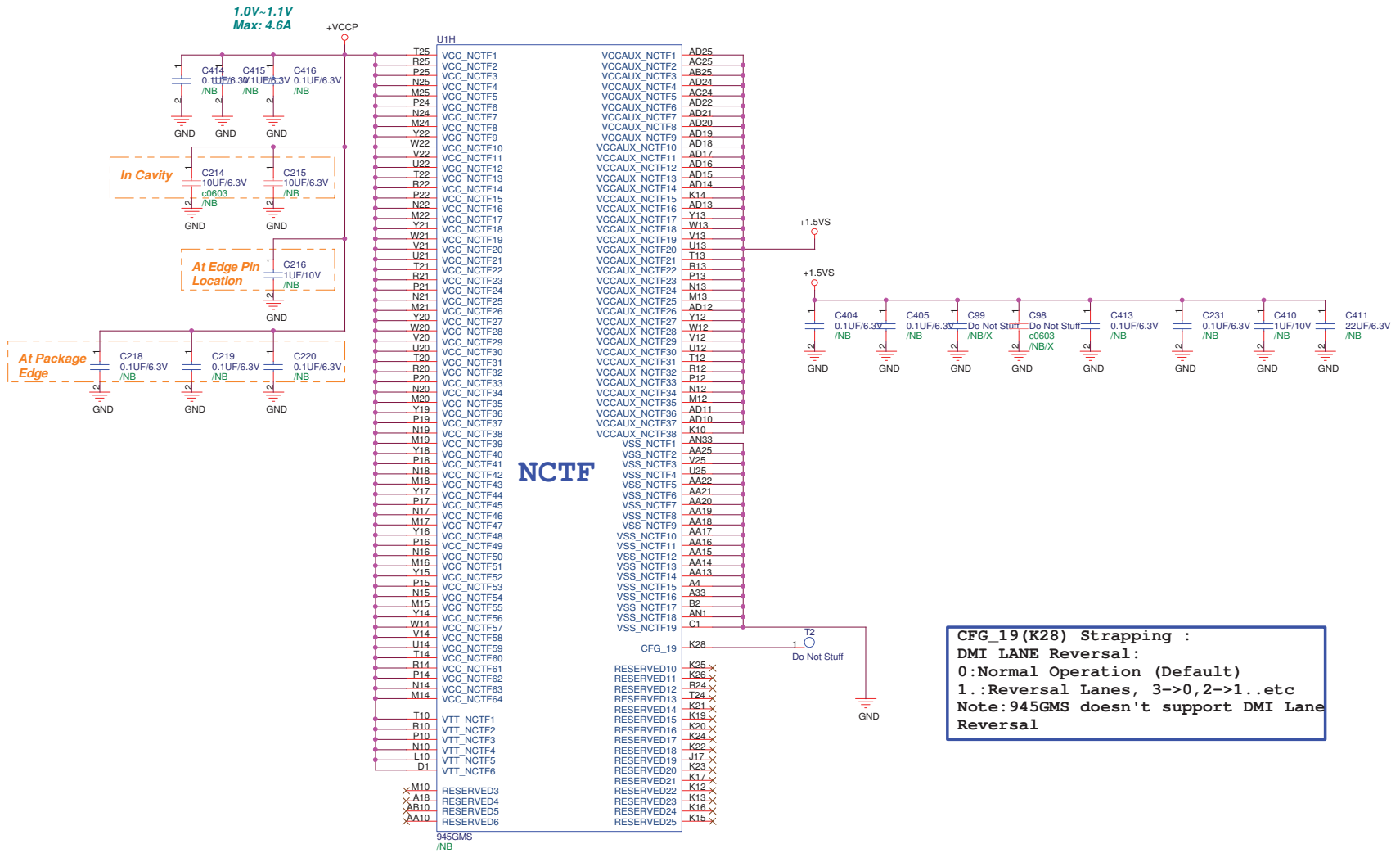
1008HA

ASUS Title : NB-945GMS(DDR2)

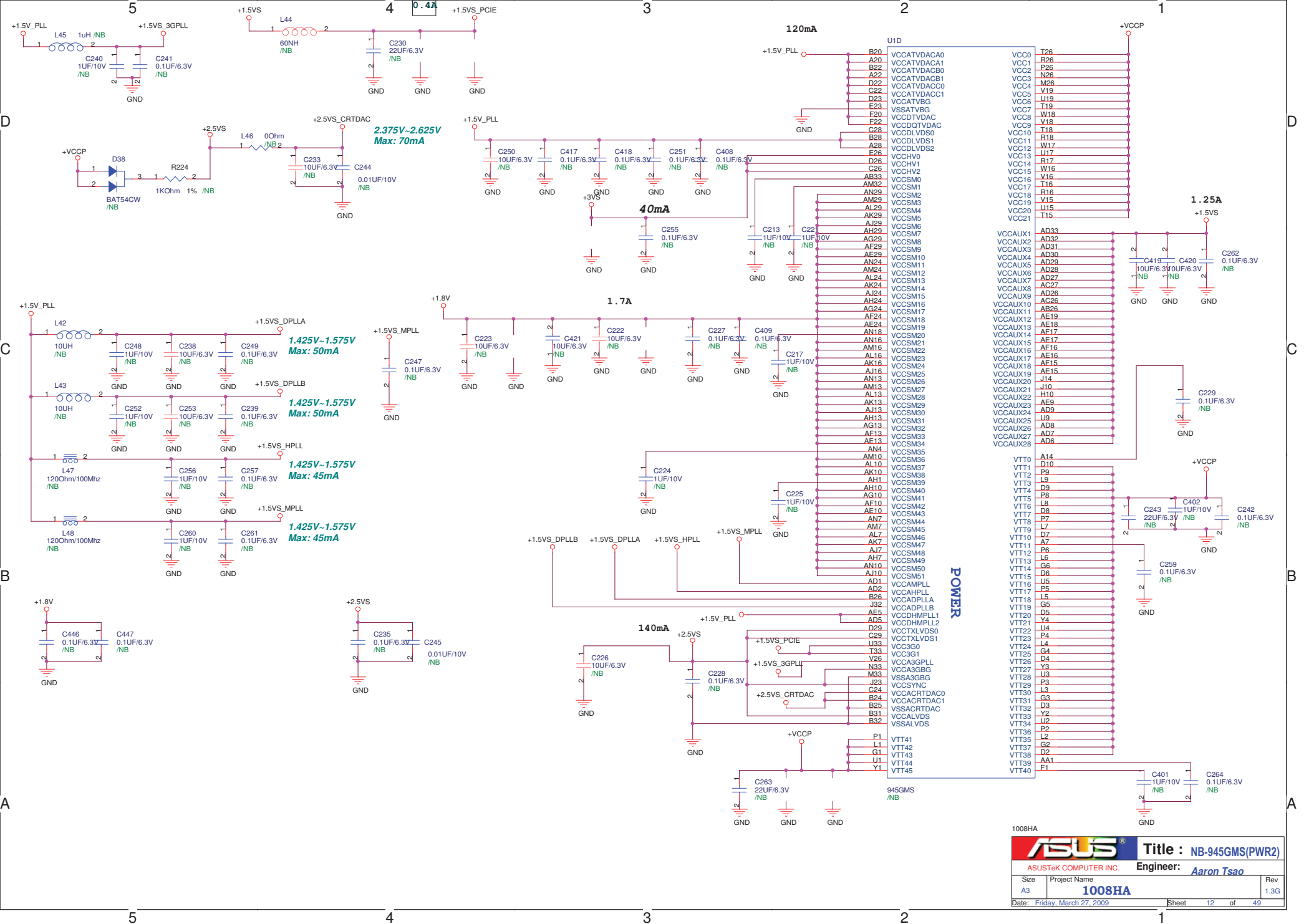
ASUSTeK COMPUTER INC. Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 10 of 49

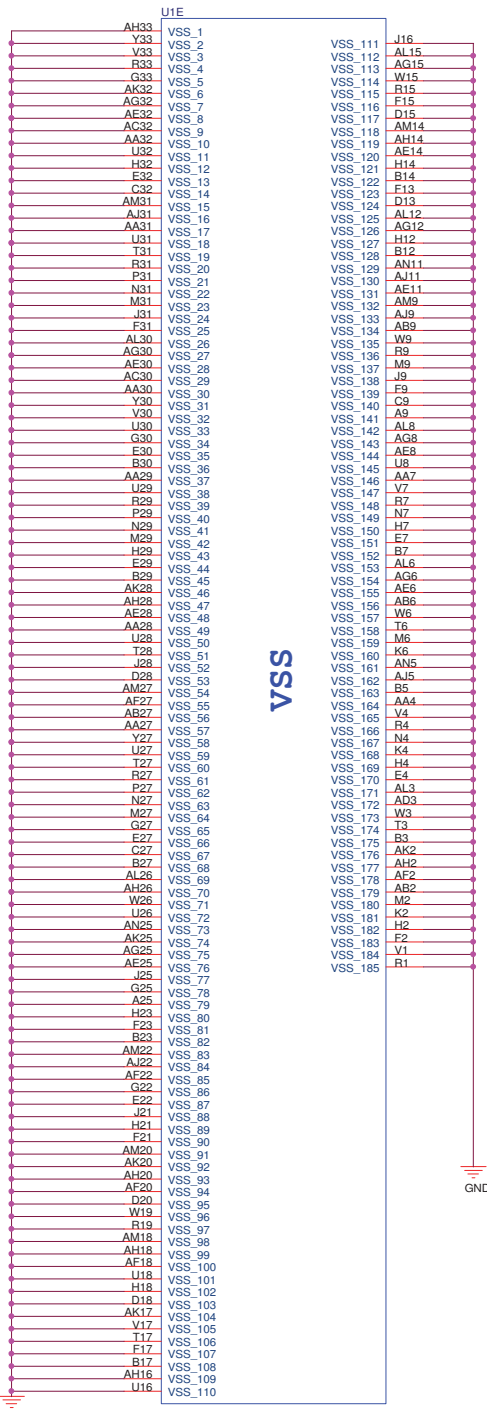


CFG_19(K28) Strapping :
DMI LANE Reversal:
 0:Normal Operation (Default)
 1.:Reversal Lanes, 3->0,2->1..etc
 Note:945GMS doesn't support DMI Lane Reversal



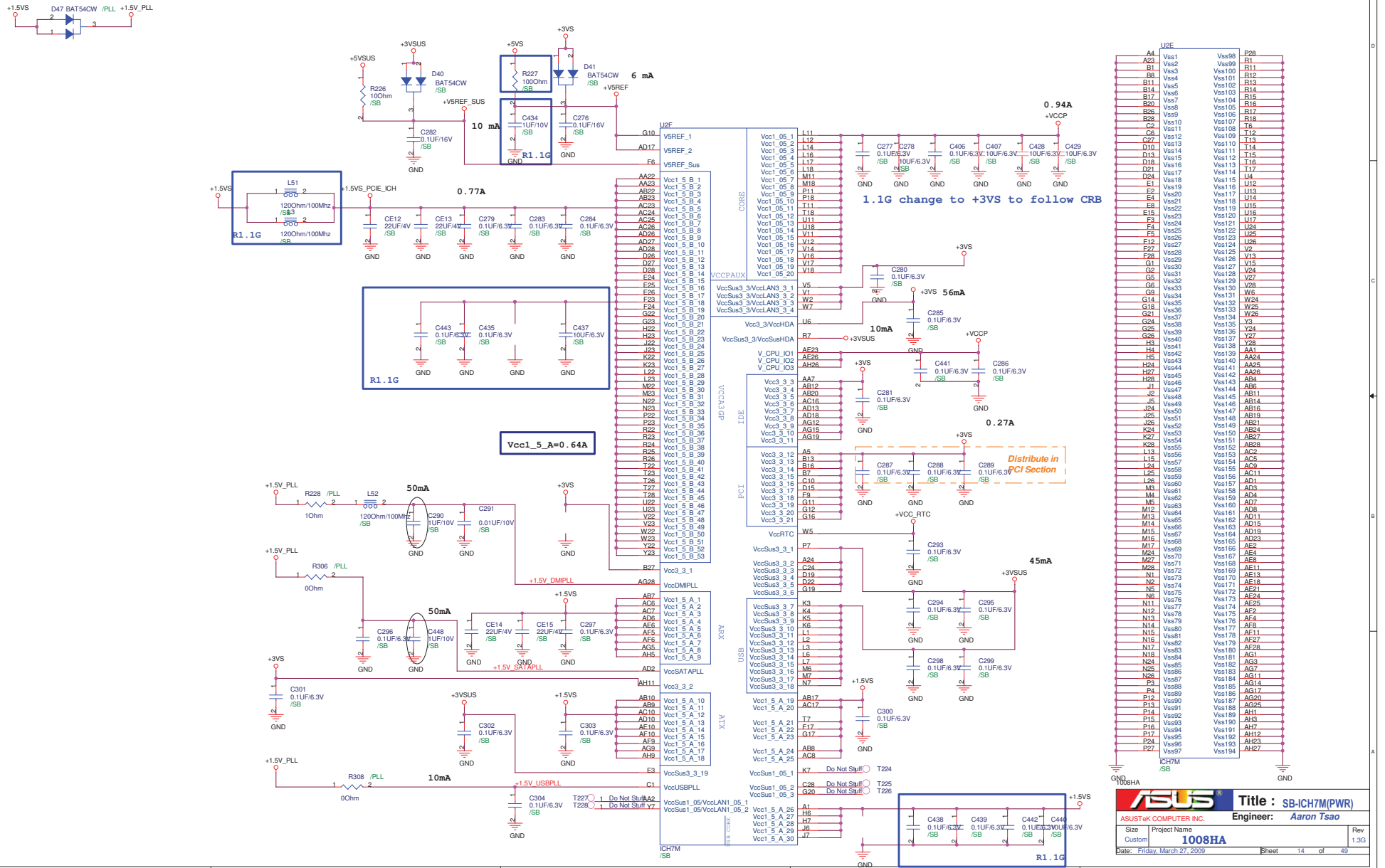
<http://laptop-motherboard-schematic.blogspot.com/>

		Title : NB-945GMS(PWR2)	
ASUSTeK COMPUTER INC.		Engineer: Aaron Tsao	
Size	Project Name	Rev	
A3	1008HA	1.3G	
Date:	Friday, March 27, 2009	Sheet	12 of 49

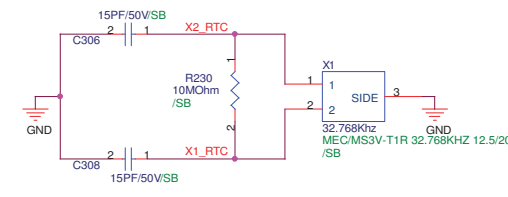
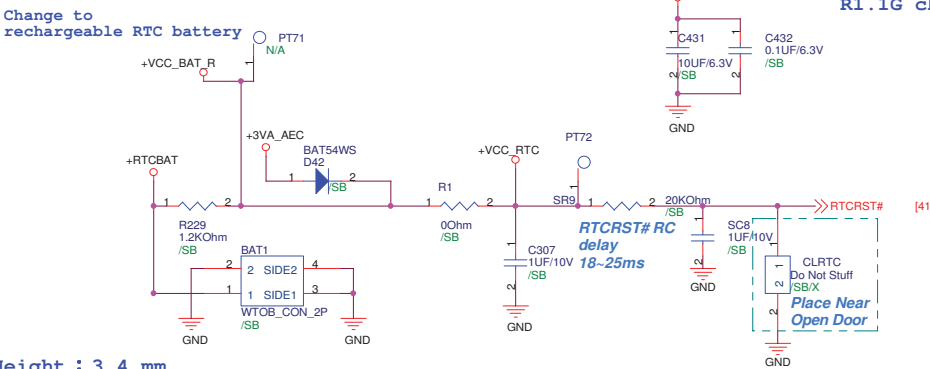


1008HA

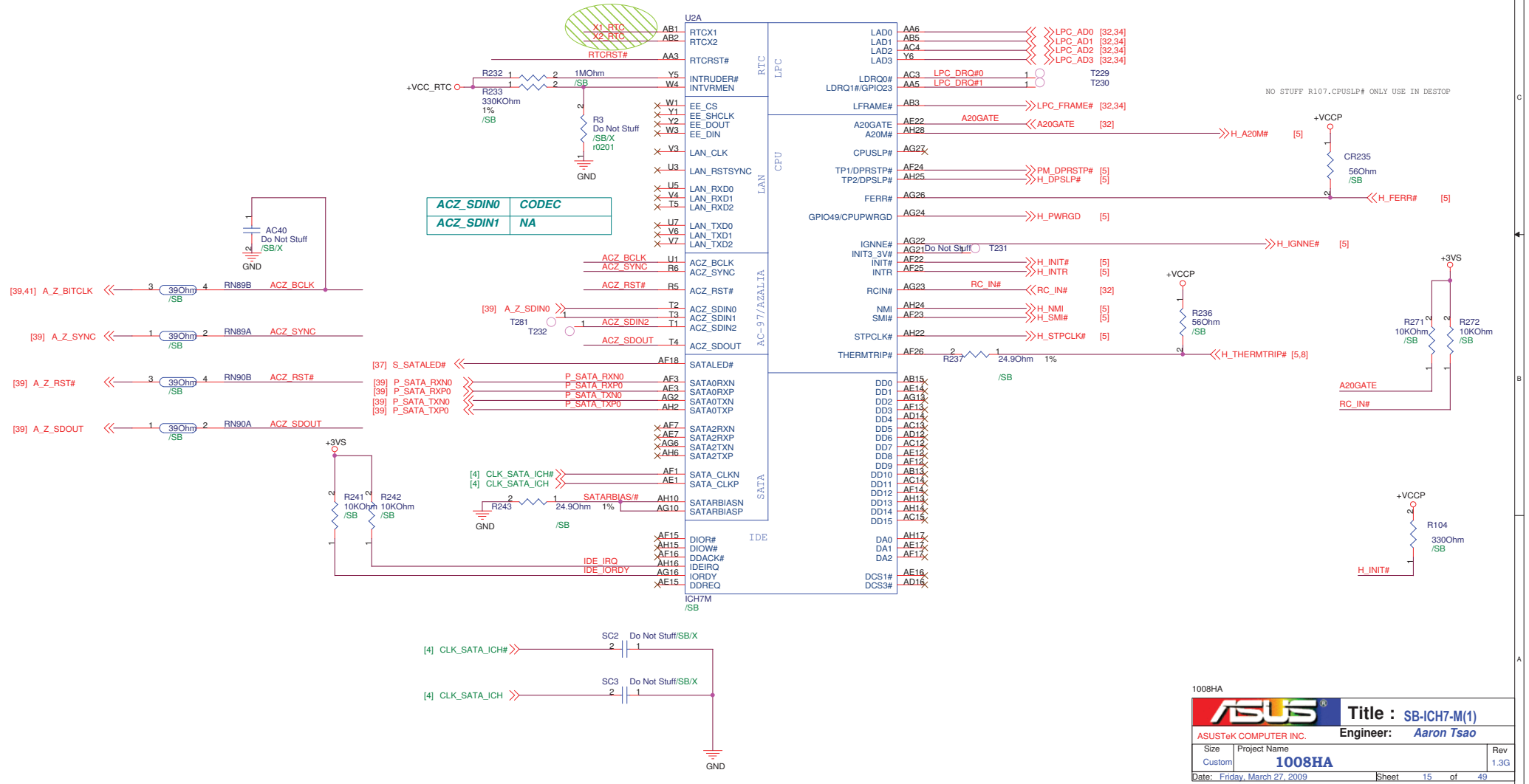
ASUS		Title : NB-945PMS(GND)	
ASUSTeK COMPUTER INC.		Engineer: Aaron Tsao	
Size	Project Name		Rev
A3	1008HA		1.3G
Date: Friday, March 27, 2009		Sheet	13 of 49



R1.1G change +3VA net to +3VA_AEC



Height : 3.4 mm

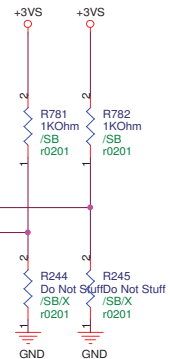
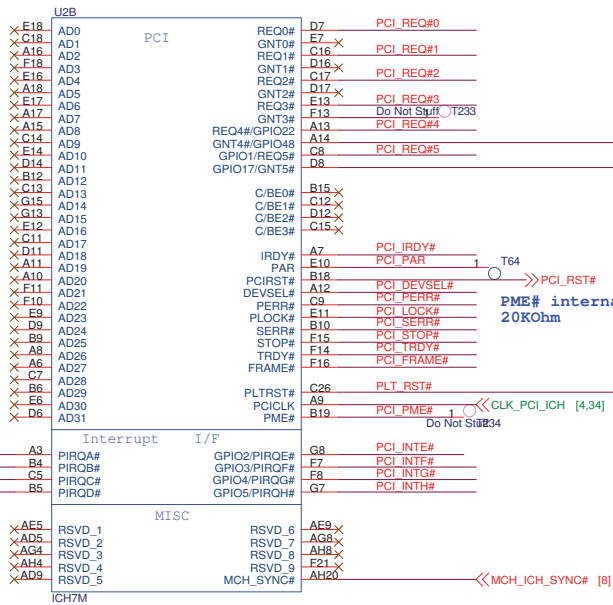
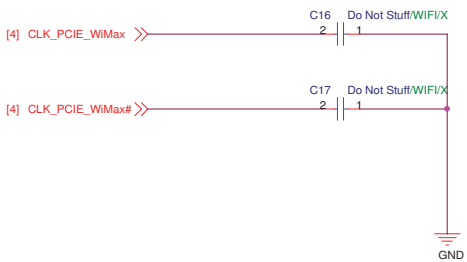
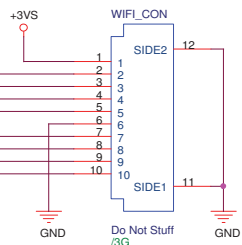
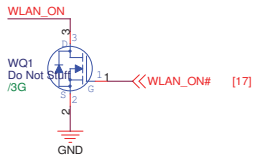


1008HA

Title : SB-ICH7-M(1)

ASUSTeK COMPUTER INC. Engineer: **Aaron Tsao**

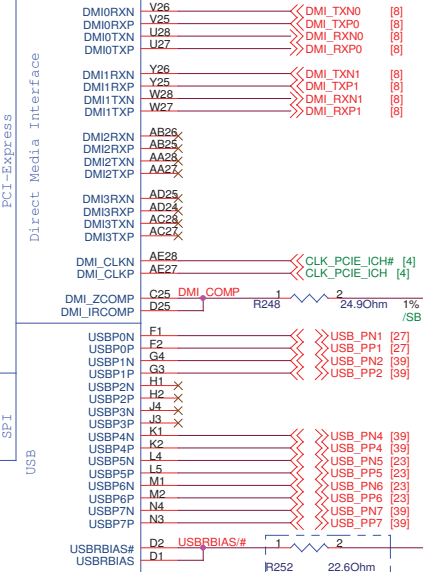
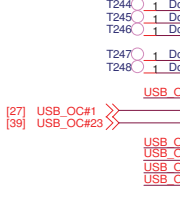
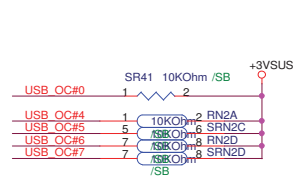
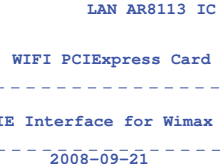
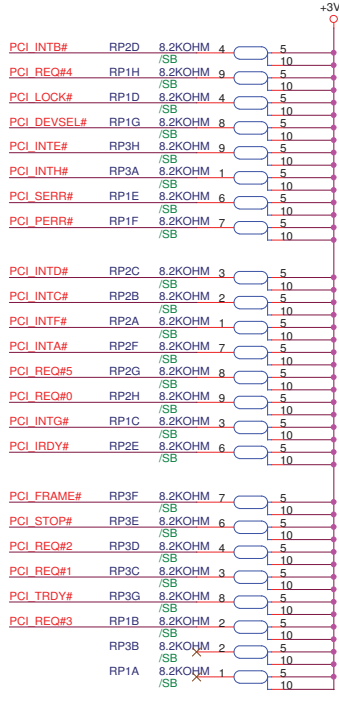
Size	Project Name	Rev
Custom	1008HA	1.3G
Date: Friday, March 27, 2009	Sheet	15 of 49



ICH7 Boot BIOS Select

	GNT#5	GNT#4
LPC	H	H
PCI	H	L
SPI	L	H

Buffer to Reduce Loading on PLT_RST#



- USB 0 USB Conn
- USB 1 USB Conn
- USB 2 NA
- USB 3 NA
- USB 4 Card Reader
- USB 5 WIFI
- USB 6 Bluetooth
- USB 7 Camera

CRB & Checklist

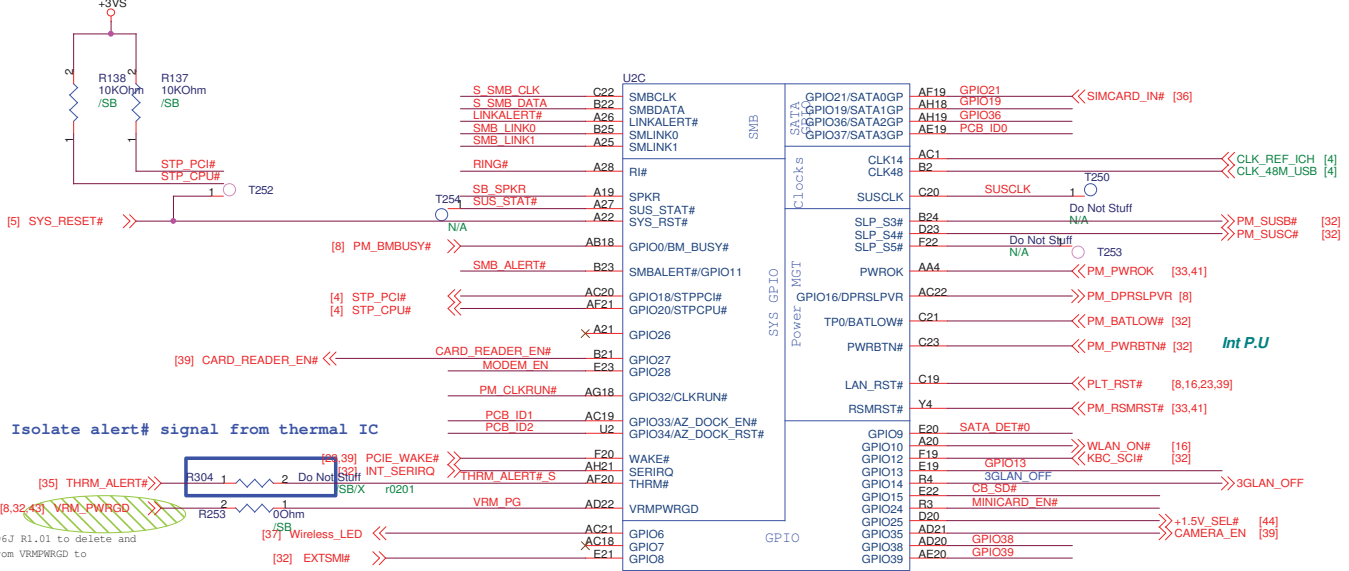
1008HA

ASUS Title: SB-ICH7M(2)

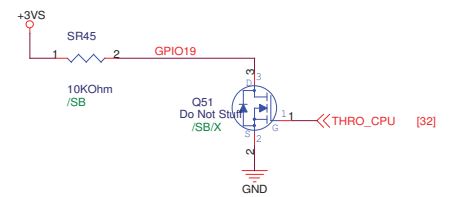
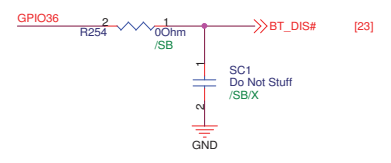
ASUSTeK COMPUTER INC. Engineer: Aaron Tsao

Size Project Name
Custom 1008HA

Date: Friday, March 27, 2009 Sheet 16 of 49



05/12/30, refer 296J R1.01 to delete and change net name from VRMPWRGD to VRM_PWRGD.

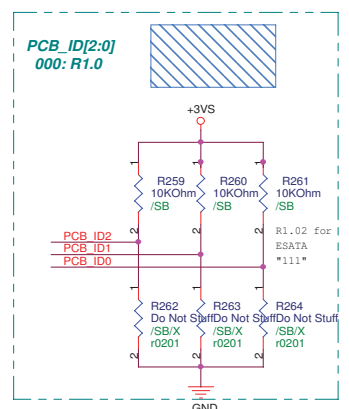
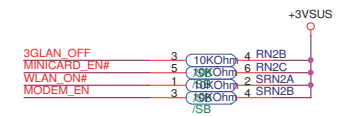


GPIO25 Internal PU 20K For +1.5V DIMM Power

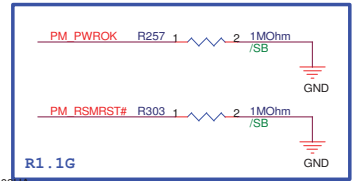
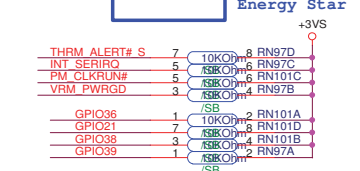
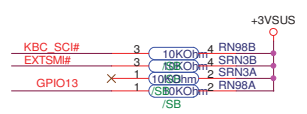
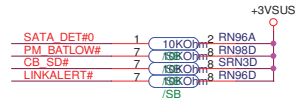
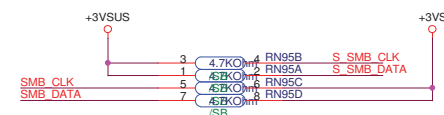
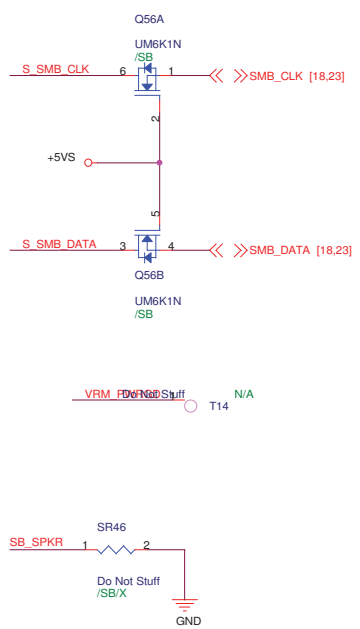


S SMB_CLK <<< S SMB_CLK [4]
S SMB_DATA <<< S SMB_DATA [4]

WLAN_LED	WLAN	BT
High	v	v
High	v	x
High	x	v
Low	x	x





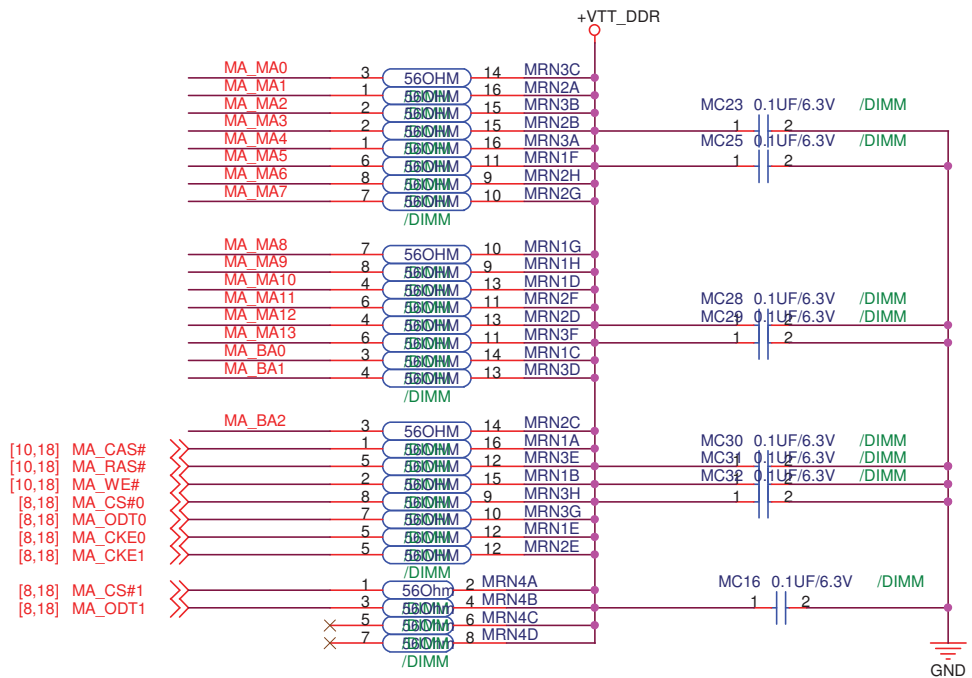
PCB_VID3 : PROJECT CODE




1008HA

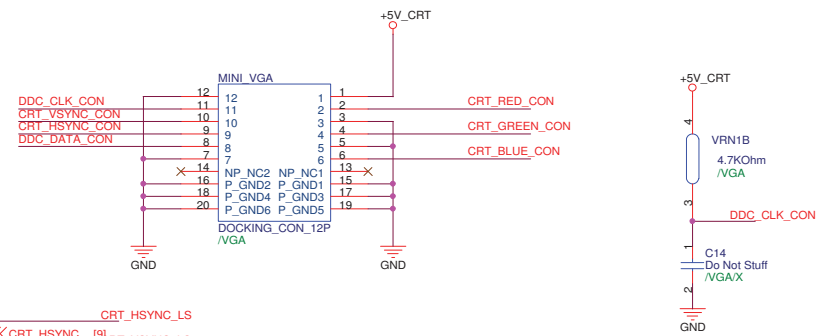
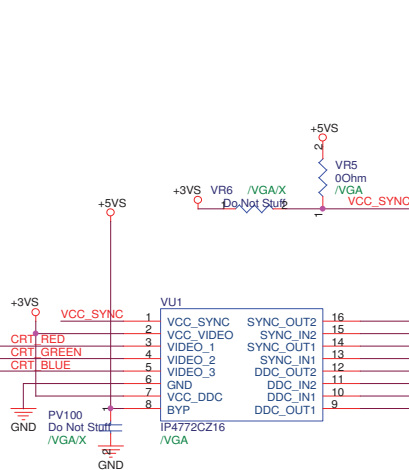
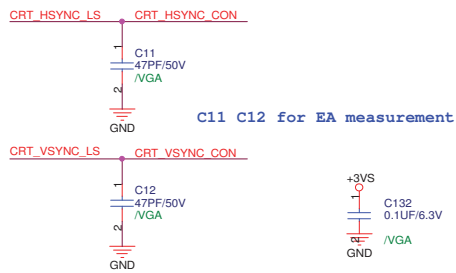
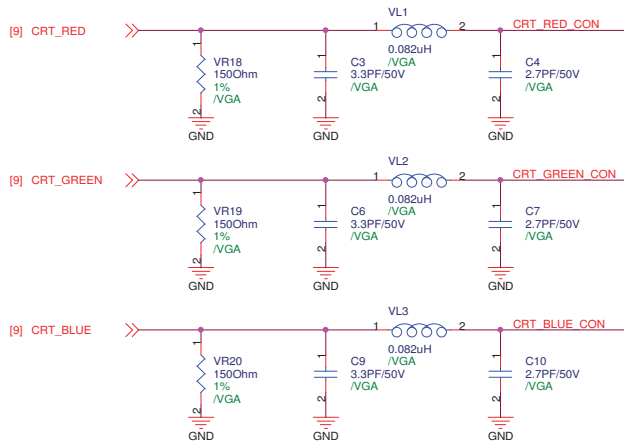
Title : SB-ICH7M(3)
ASUSTeK COMPUTER INC Engineer: Aaron Tsao
Size Project Name
Custom 1008HA
Date: Friday, March 27, 2009 Sheet 17 of 49

 <<MA_MA[13:0] [10,18]
 <<MA_BA[2:0] [10,18]



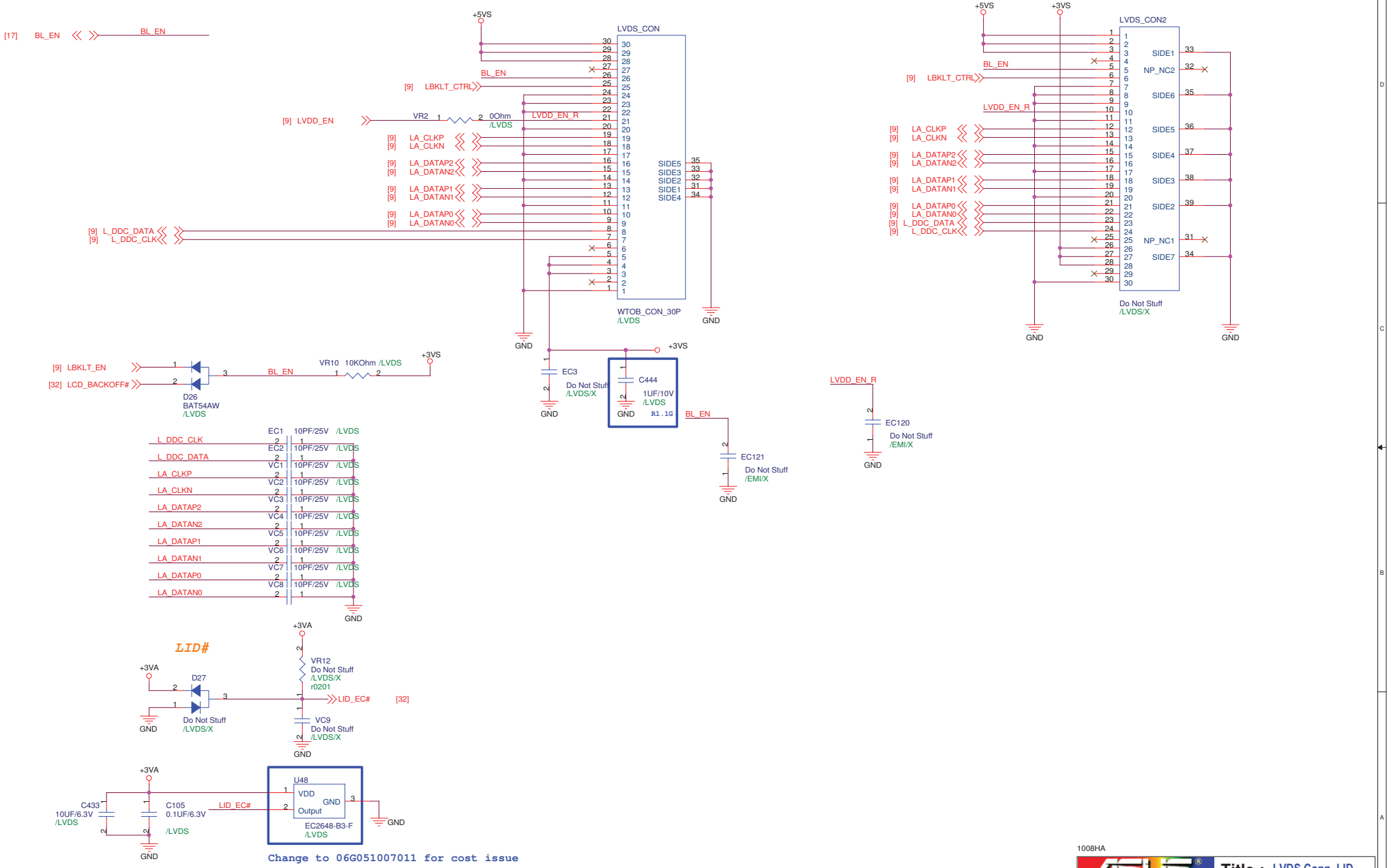
1008HA

		Title : DDR2_Termination	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A4	Project Name 1008HA		Rev 1.3G
Date: Friday, March 27, 2009		Sheet 19 of 49	



1008HA

ASUS		Title : Onboard VGA	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A3	Project Name 1008HA	Date: Friday, March 27, 2009	Rev 1.3G
Sheet 20 of 49			

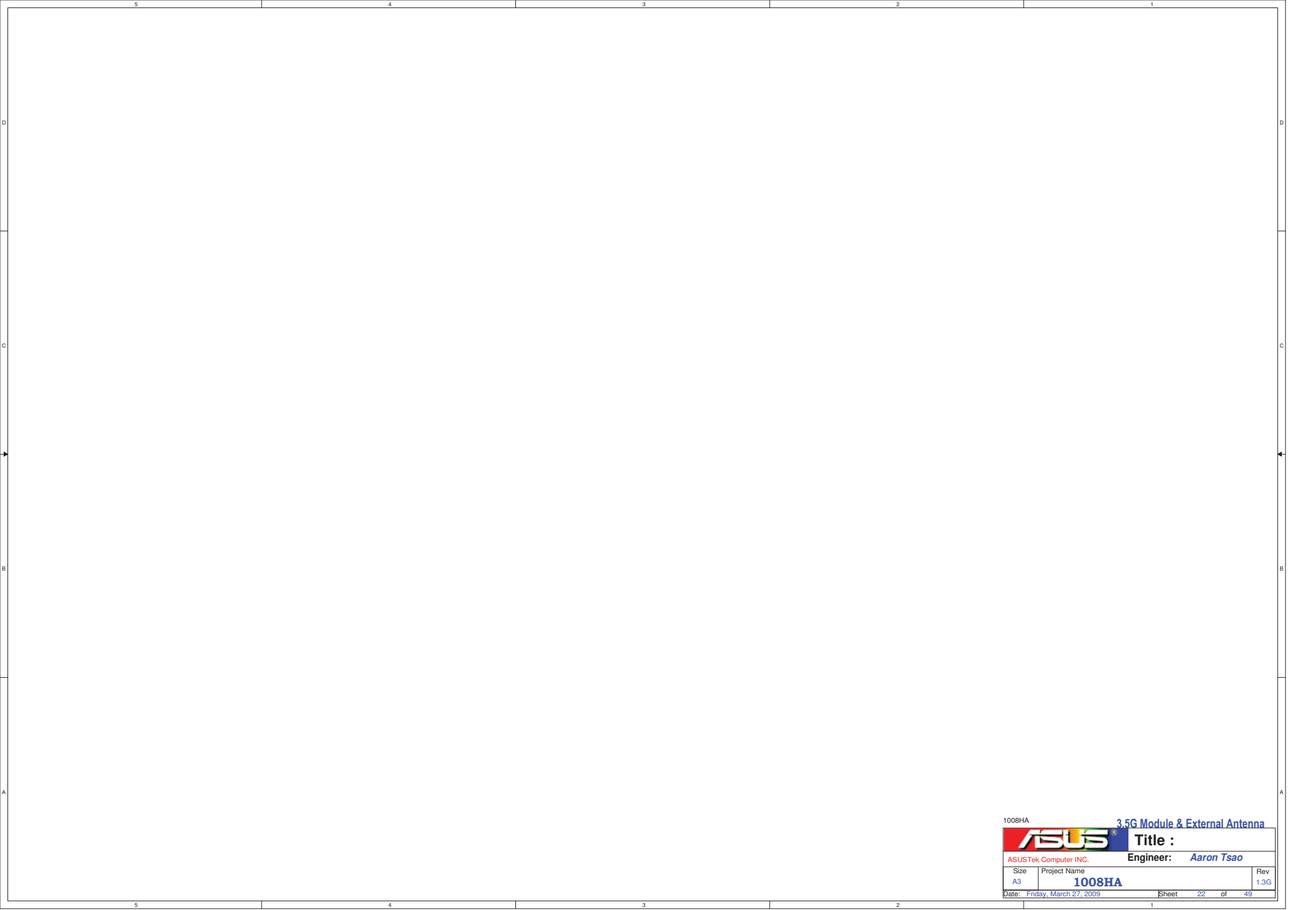


1008HA

ASUS		Title : LVDS Conn_LID	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A3	Project Name 1008HA	Rev 1.3G	
Date: Friday, March 27, 2009		Sheet 21	of 49

LID_EC#

<http://laptop-motherboard-schematic.blogspot.com/>



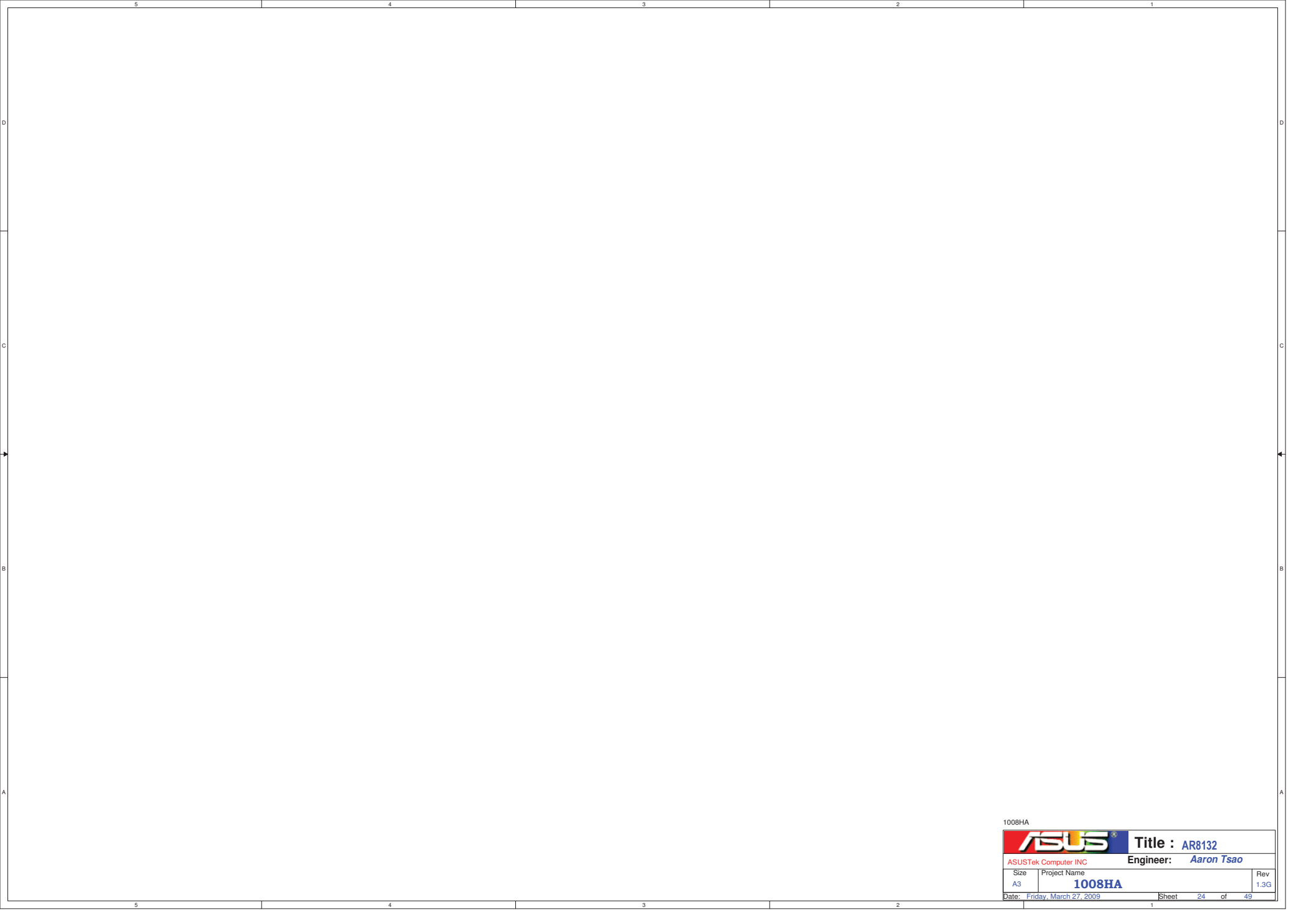
1008HA **3.5G Module & External Antenna**

ASUS Title :


ASUSTek Computer INC. Engineer: *Aaron Tsao*

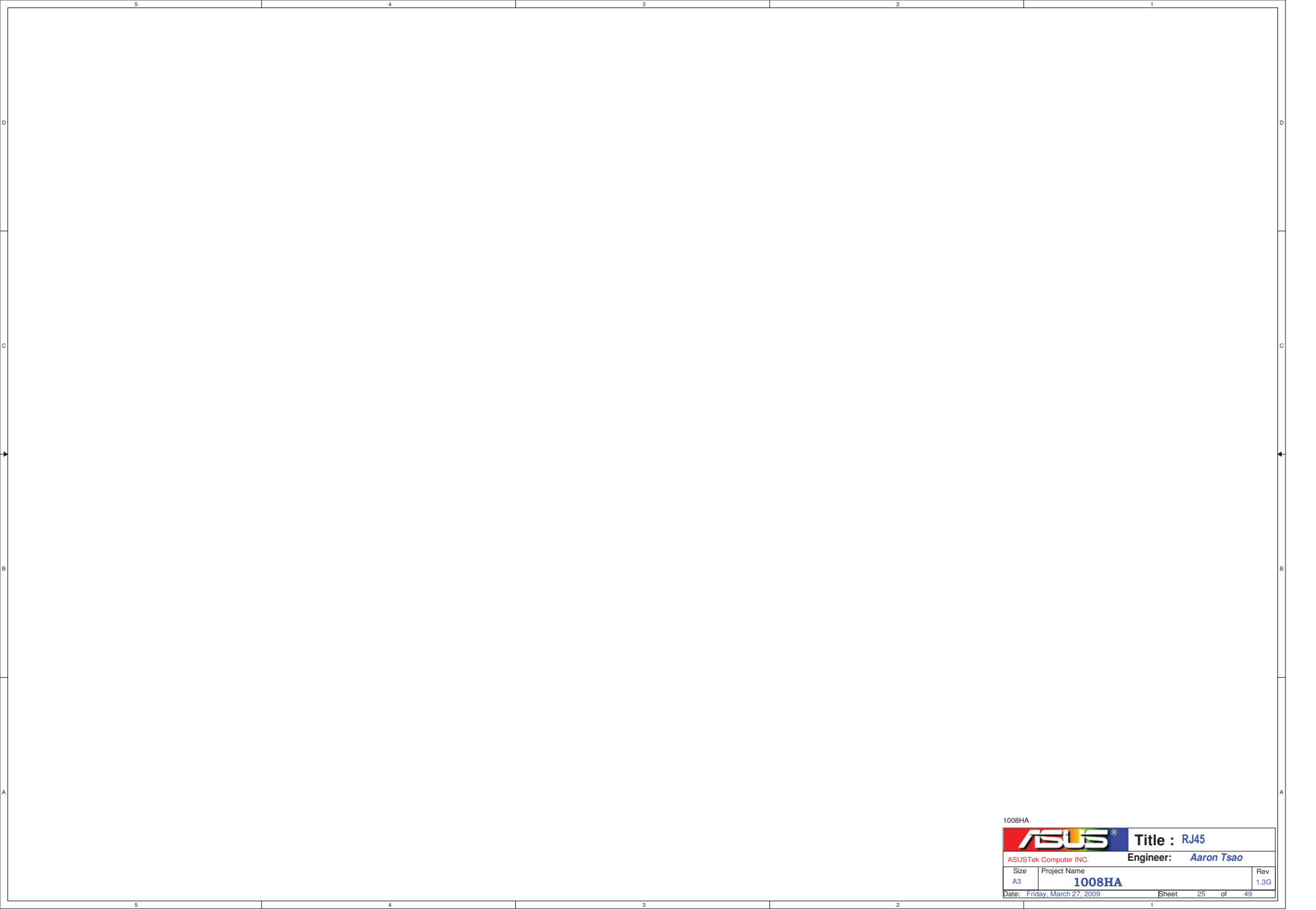
Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 22 of 49



1008HA

		Title : AR8132	
ASUSTek Computer INC		Engineer: Aaron Tsao	
Size	Project Name		Rev
A3	1008HA		1.3G
Date: Friday, March 27, 2009		Sheet	24 of 49




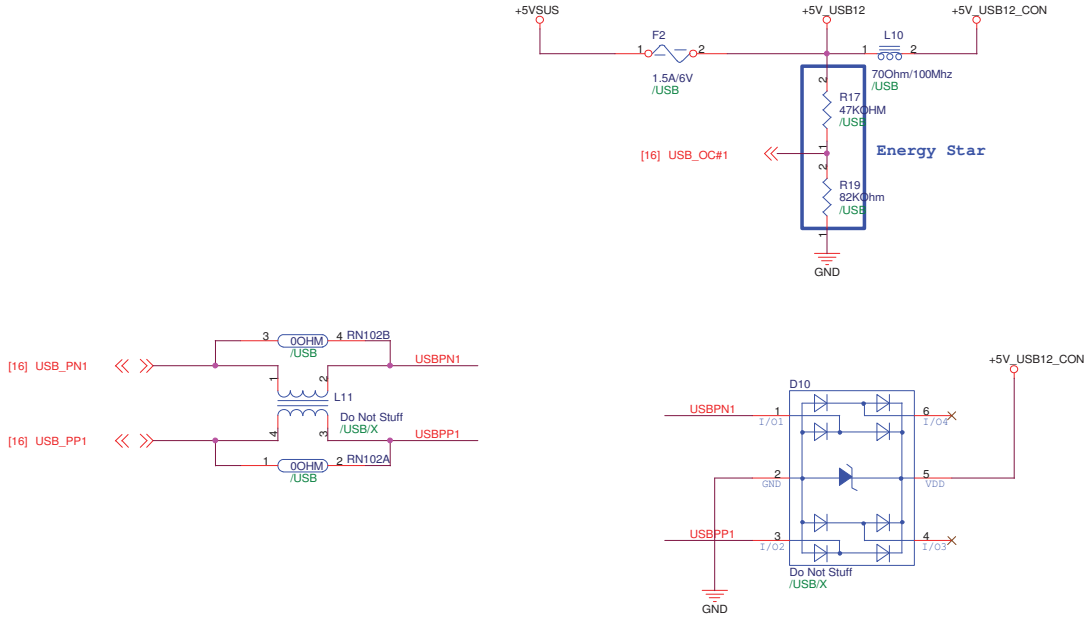
1008HA

		Title : RJ45	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size	Project Name		Rev
A3	1008HA		1.3G
Date: Friday, March 27, 2009		Sheet	25 of 49

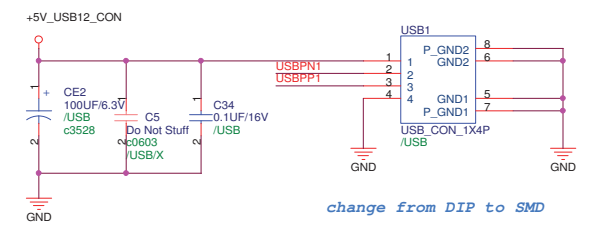
Naming Rule:
IC: IU?
R: IR?
C: IC?
L: IL?

1008HA

		Title : G-Sensor	
ASUSTek Computer INC.		Engineer: <i>Aaron Tsao</i>	
Size	Project Name	Rev	
A3	1008HA	1.3G	
Date: Friday, March 27, 2009		Sheet	26 of 49

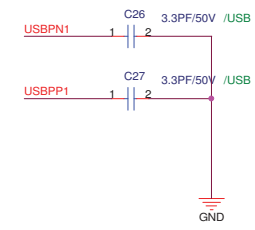


1.1G change USB con. to 12G131030042



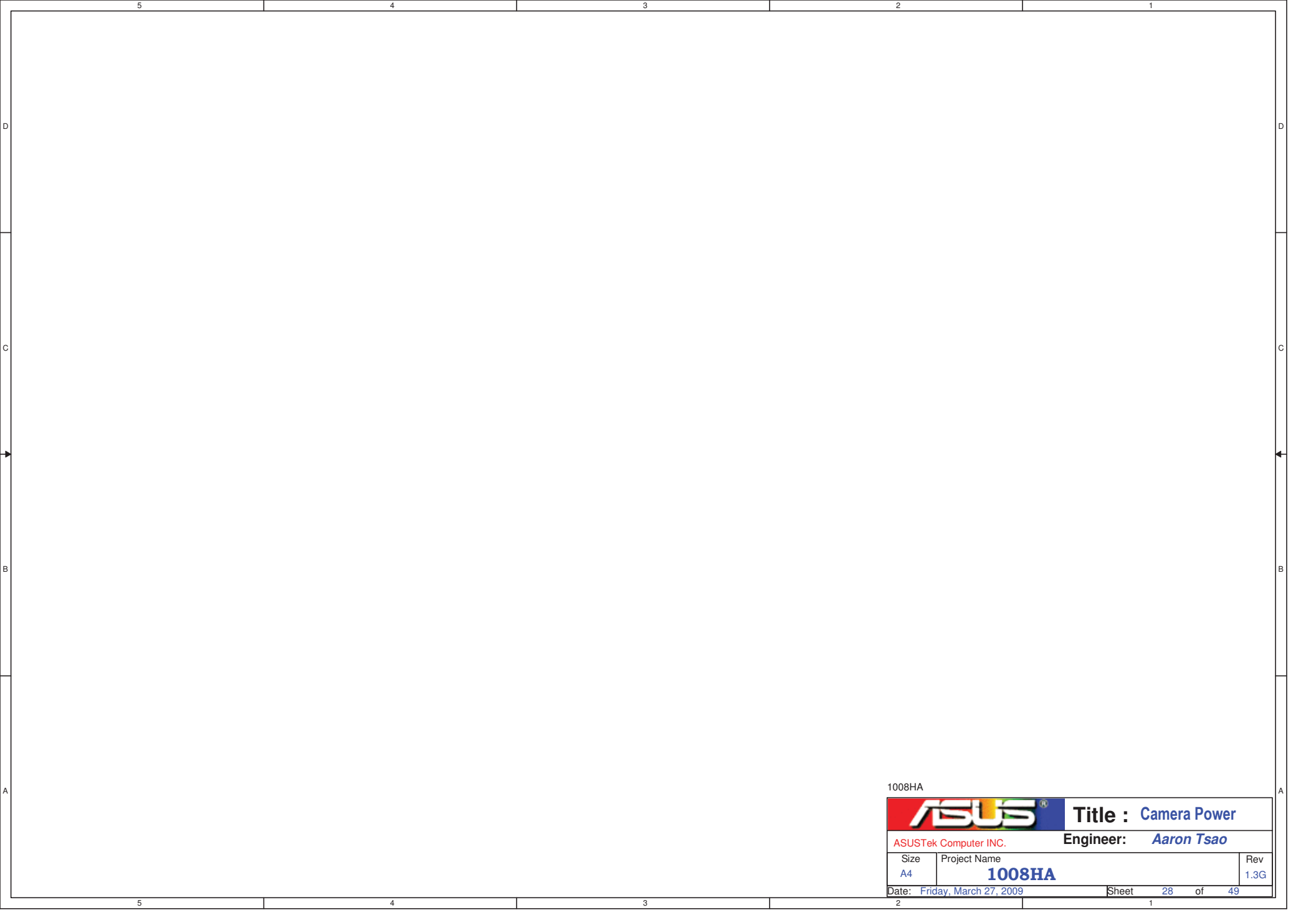
change from DIP to SMD

1.1G change CE2 CE3 CE4 to POSCAP, 100uF/6.3V




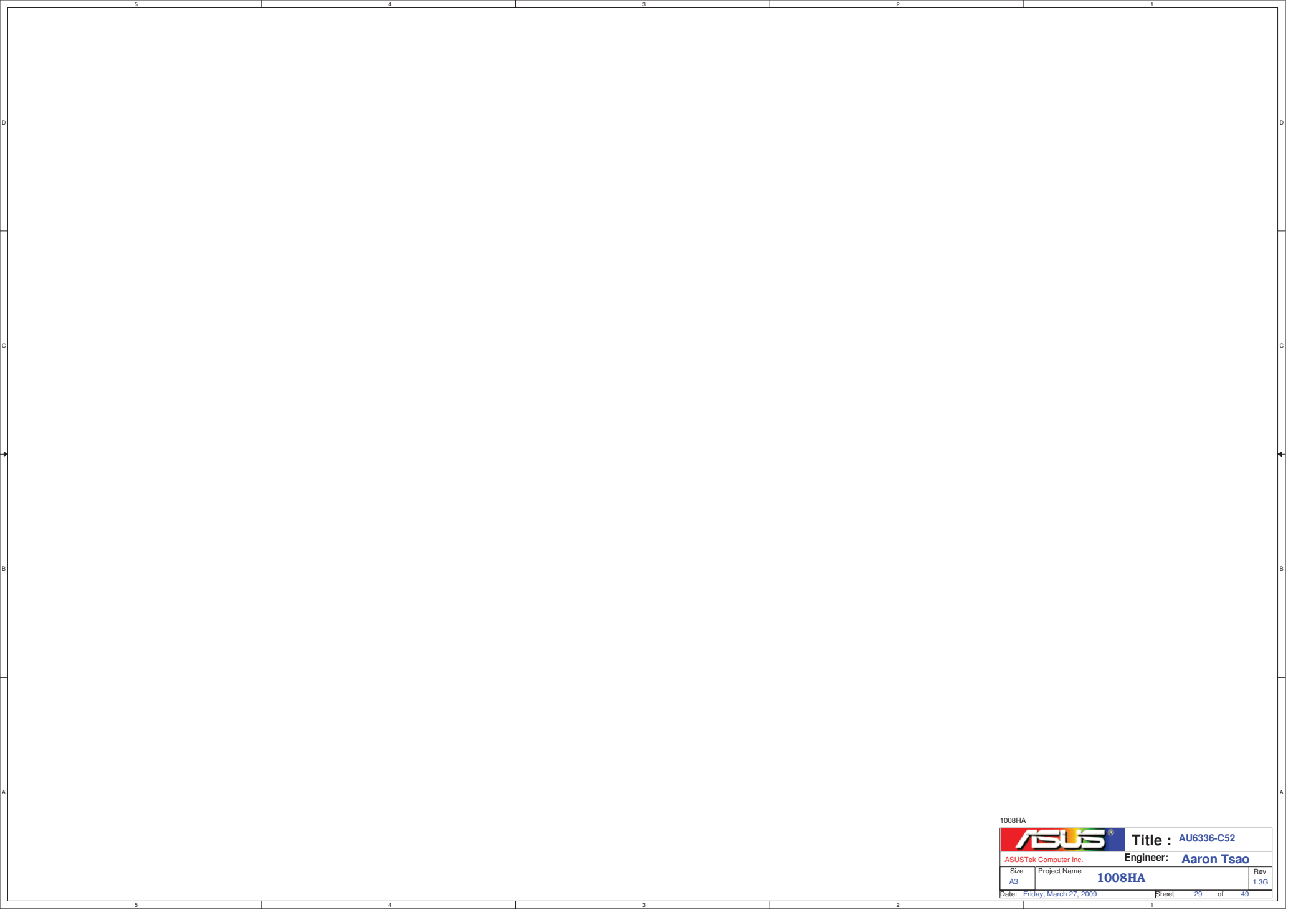
1008HA

		Title : USB Port	
ASUSTek Computer INC.		Engineer: <i>Aaron Tsao</i>	
Size A3	Project Name 1008HA	Rev 1.3G	
Date: Friday, March 27, 2009	Sheet 27	of 49	




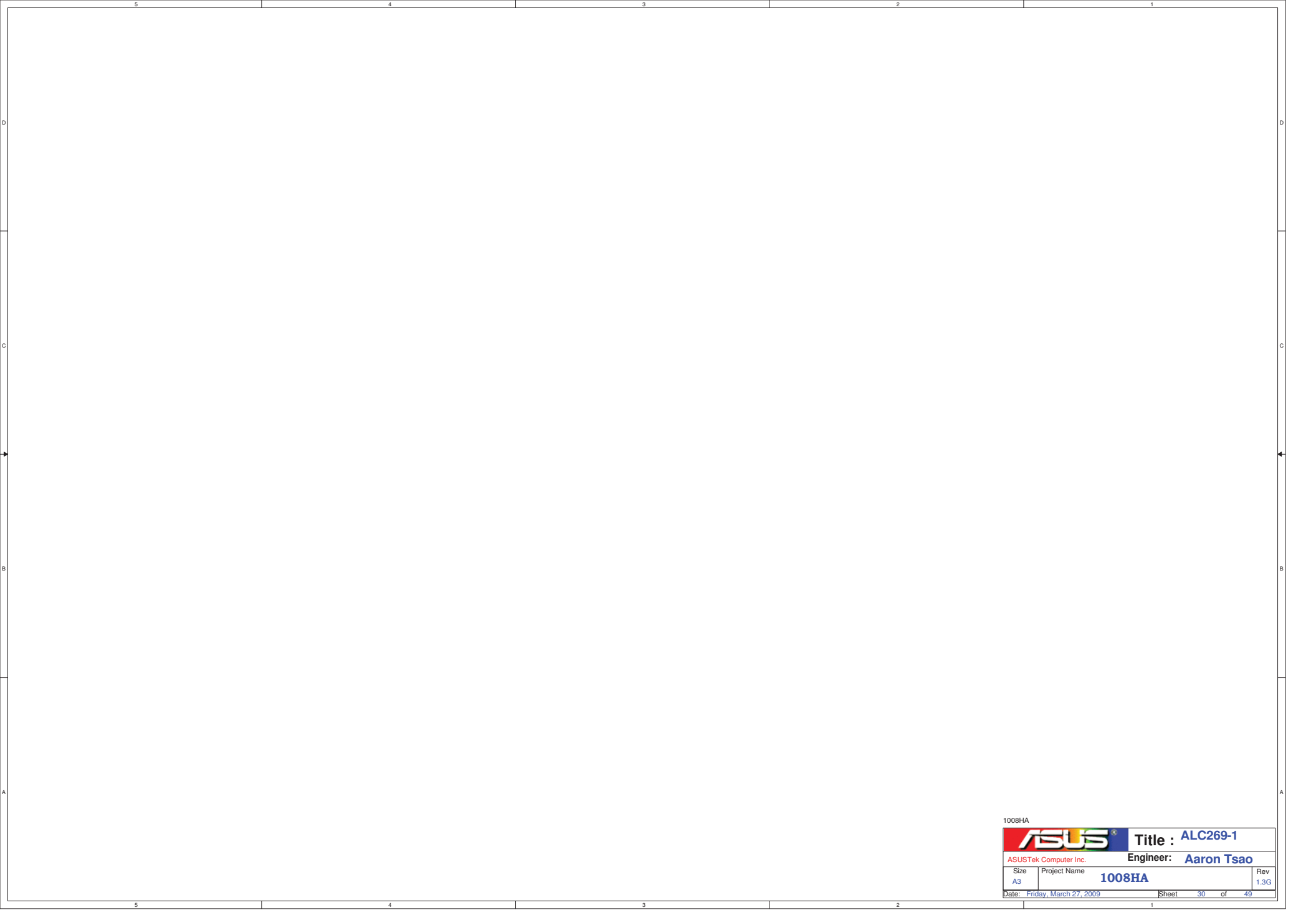
1008HA

		Title : Camera Power
ASUSTek Computer INC.		Engineer: Aaron Tsao
Size A4	Project Name 1008HA	Rev 1.3G
Date: Friday, March 27, 2009		Sheet 28 of 49




1008HA

		Title : AU6336-C52	
ASUSTek Computer Inc.		Engineer: Aaron Tsao	
Size	Project Name	Rev	
A3	1008HA	1.3G	
Date: Friday, March 27, 2009	Sheet	29	of 49



1008HA


		Title : ALC269-1	
ASUSTek Computer Inc.		Engineer: Aaron Tsao	
Size A3	Project Name 1008HA	Rev 1.3G	
Date: Friday, March 27, 2009		Sheet	30 of 49

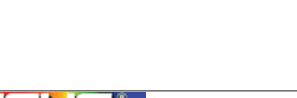
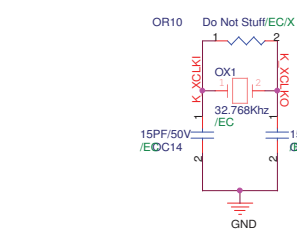
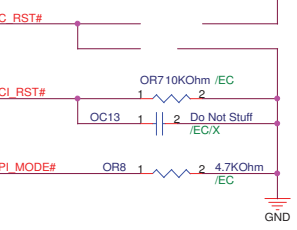
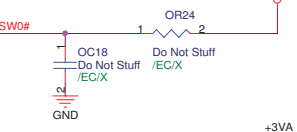
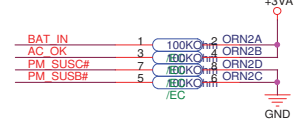
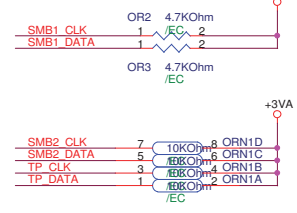
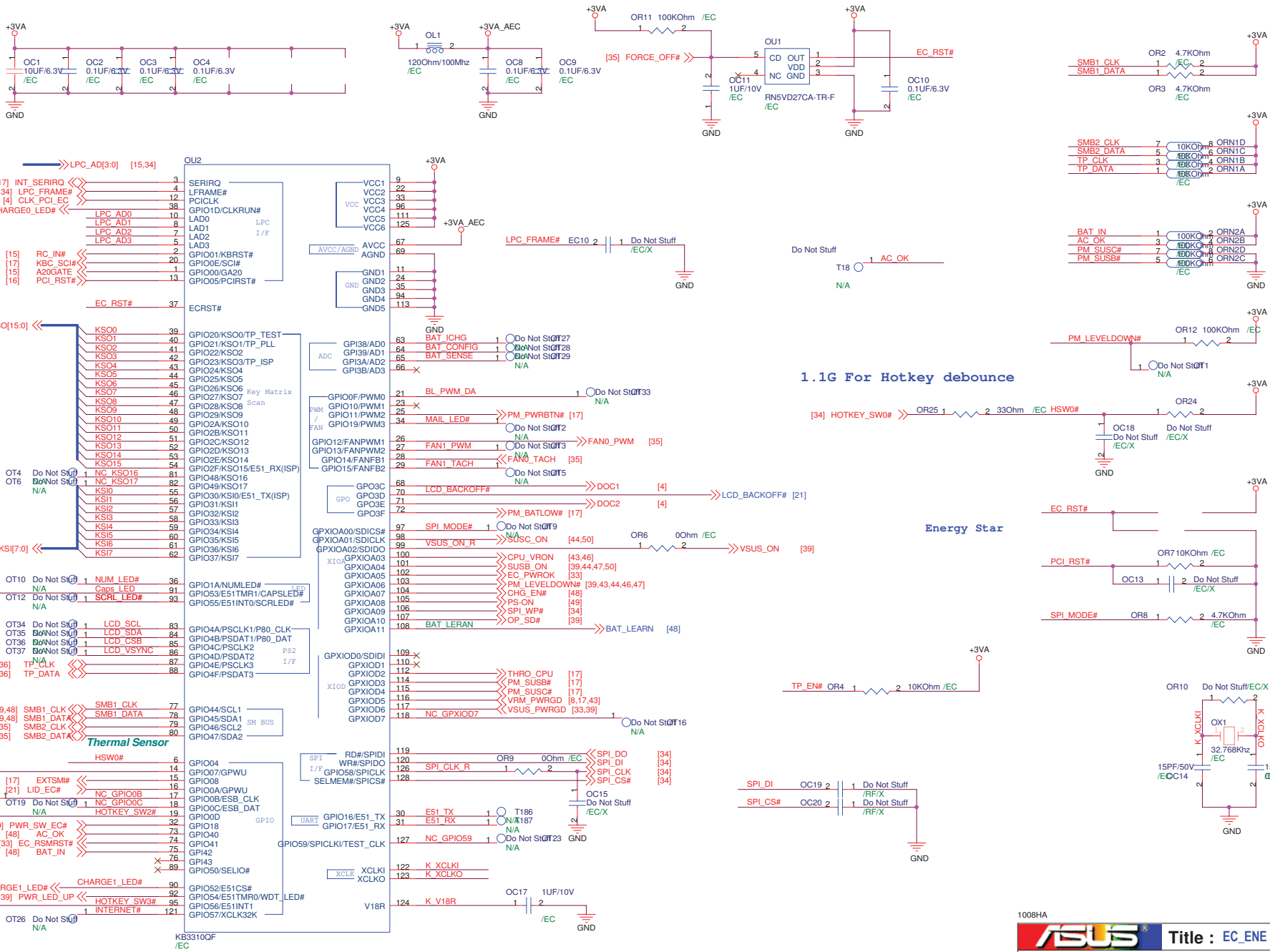
1.1G add PWR LED and Charge LED

DMIC Cable length should be less 30cm

Change R291 R292 R293 to 510 Ohm

1008HA

		Title : ALC269-2	
ASUSTek Computer Inc.		Engineer: Aaron Tsao	
Size	Project Name	Rev	
A3	1008HA	1.3G	
Date: Friday, March 27, 2009	Sheet	31	of 49

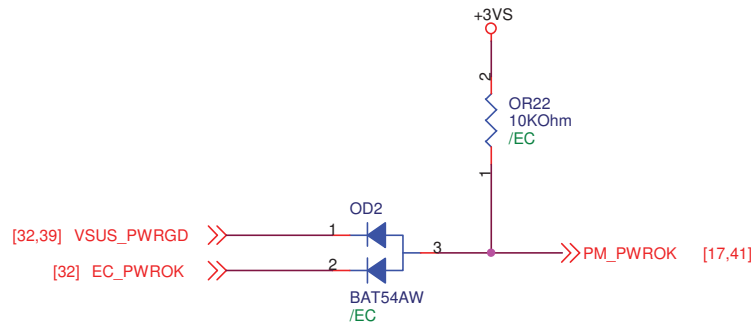
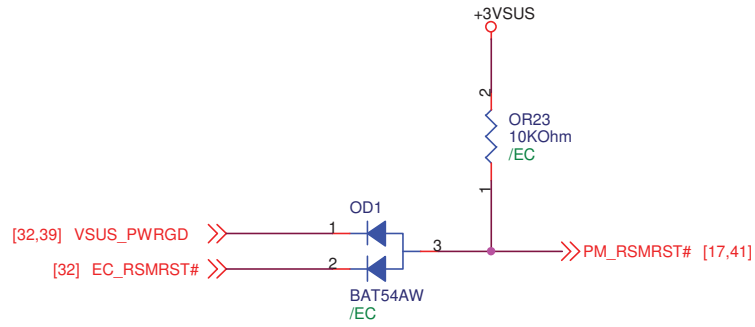


1.1G For Hotkey debounce

Energy Star

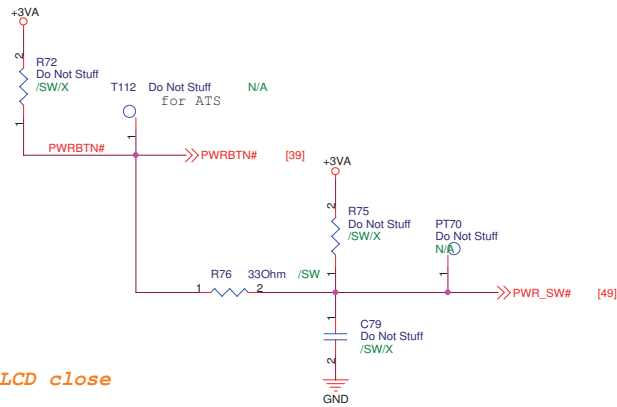
OR25 1.1G For Hotkey debounce
HOTKEY_SWO# - HOTKEY_SW3# internal PU

1008HA		ASUS Title : EC_ENE KB3310	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A3	Project Name	1008HA	
Date: Friday, March 27, 2009	Sheet 32	of 49	

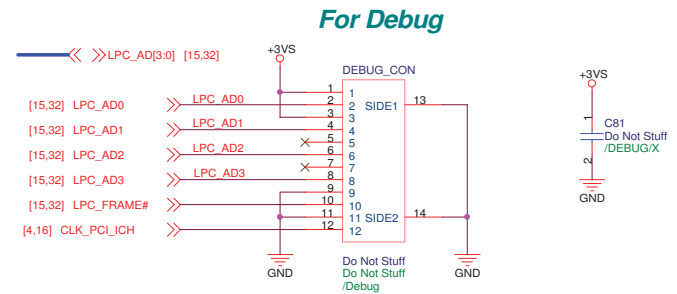


1008HA

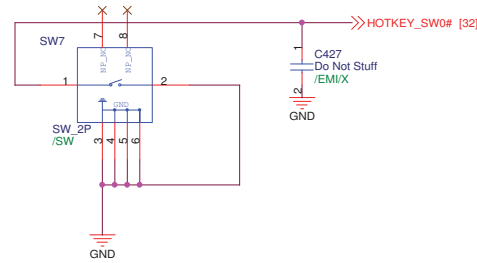
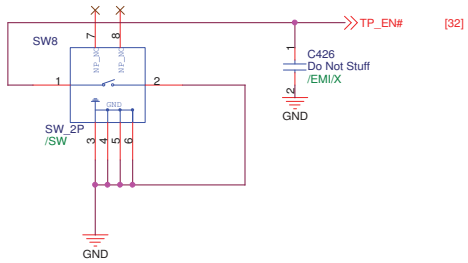
		Title : EC
ASUSTek Computer INC.		Engineer: Aaron Tsao
Size A4	Project Name 1008HA	Rev 1.3G
Date: Friday, March 27, 2009		Sheet 33 of 49



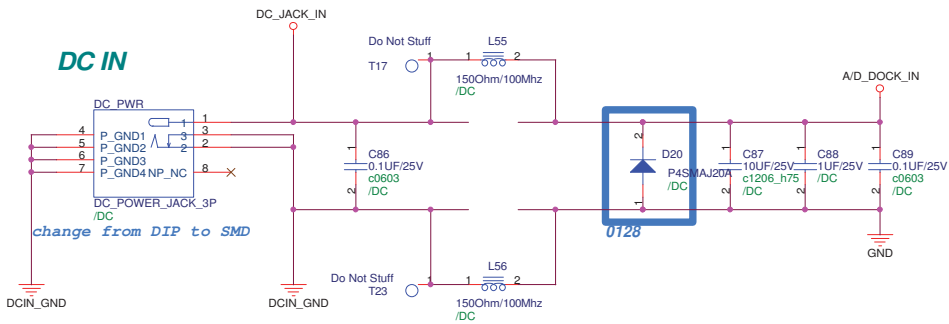
prevent system power on when LCD close



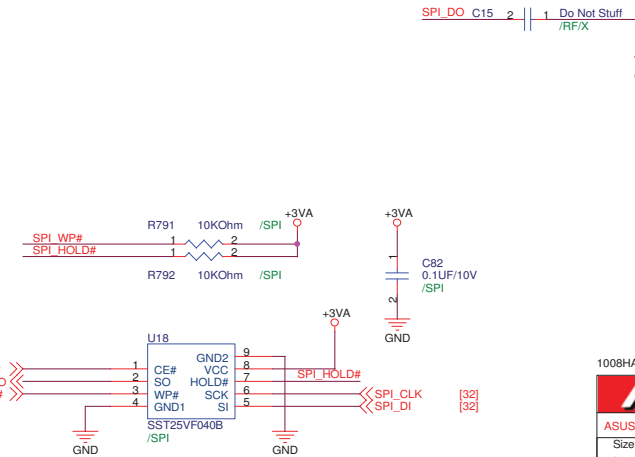
Debug Card cable use Z96 Touch Pad cable, P/N:
 14G124110126, 14G124110120, 14G124110121
 14G124110124, 14G124110125



DC IN

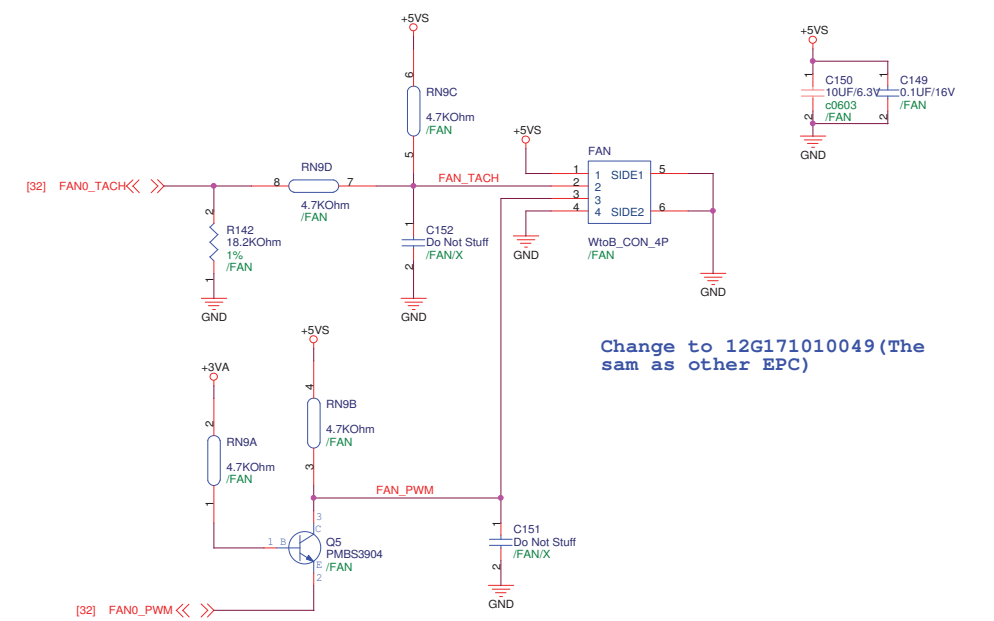
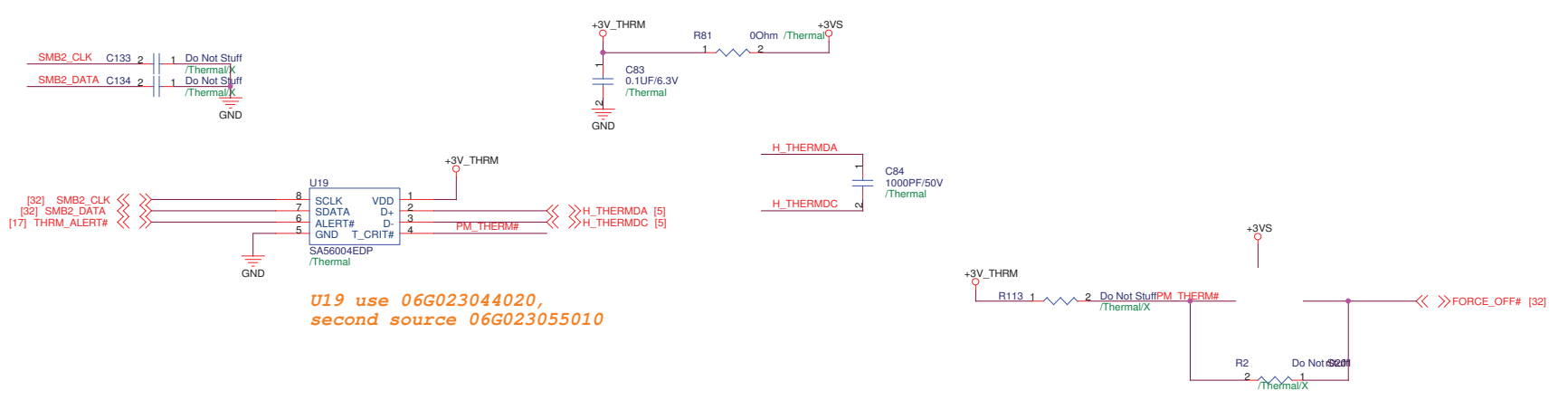


change from DIP to SMD



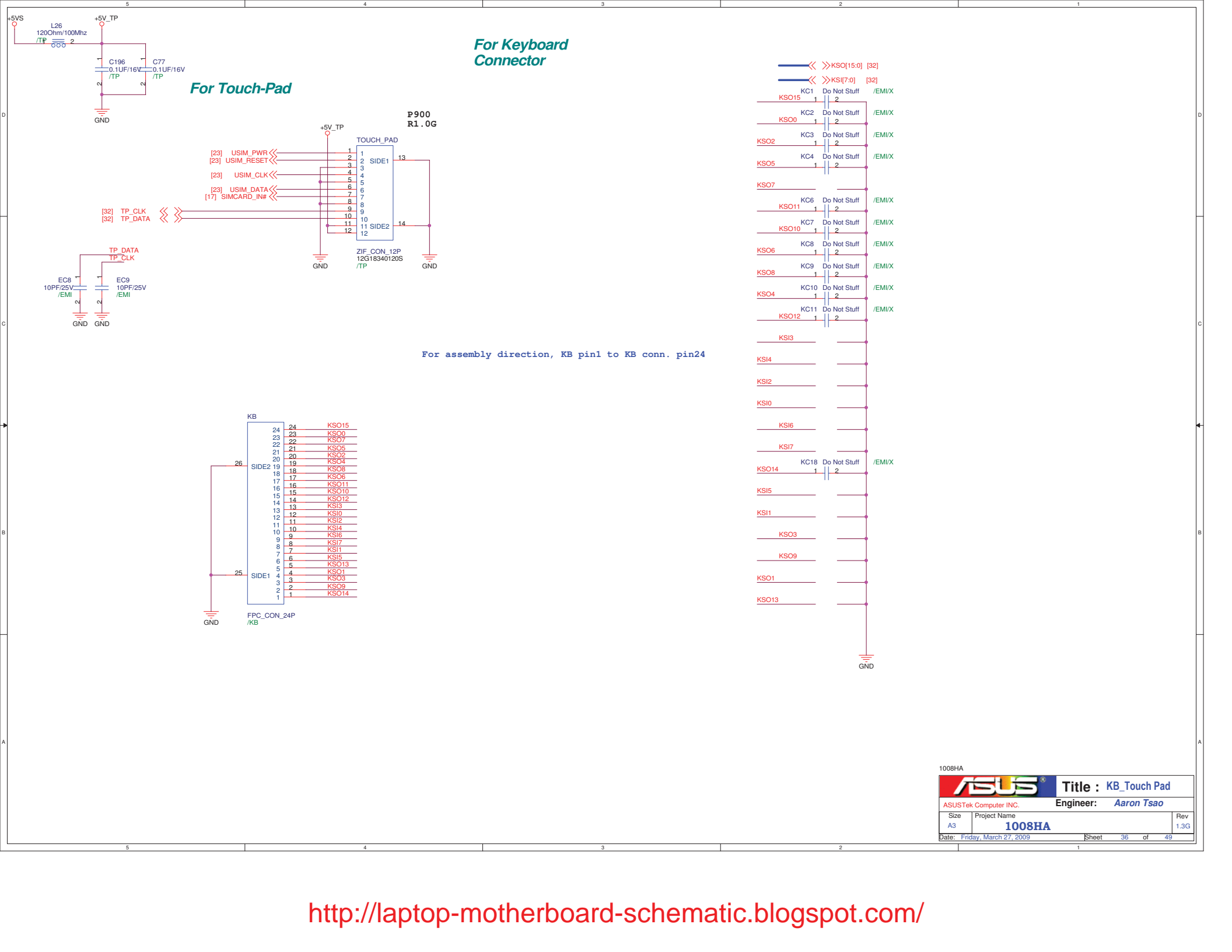
1008HA

Title :_34_Switch_SPI_ITP_Debug
ASUS Computer INC. **Engineer:** Aaron Tsao
 Size A3 Project Name **1008HA** Rev 1.3G
 Date: Friday, March 27, 2009 Sheet 34 of 49



1008HA

ASUS		Title : Thermal Sensor_FAN	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A3	Project Name 1008HA	Rev 1.3G	
Date: Friday, March 27, 2009		Sheet 35 of 49	

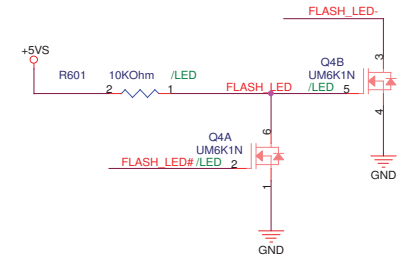
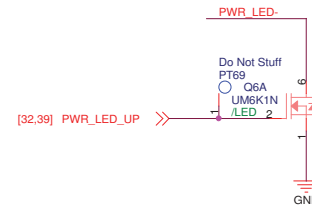
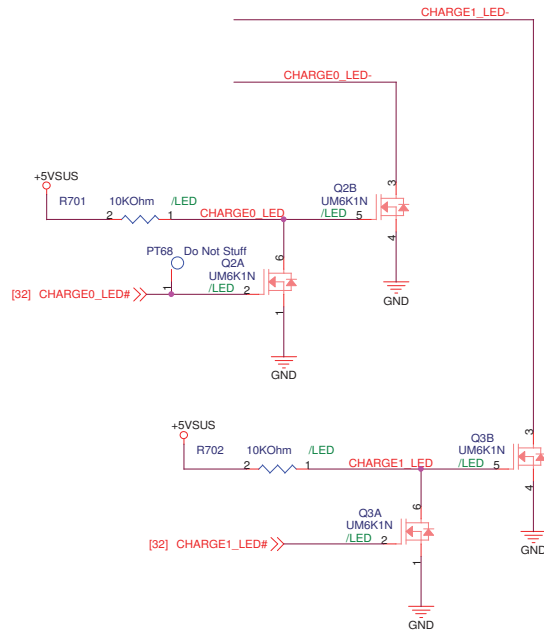


1.1G change to EVERLIGHT

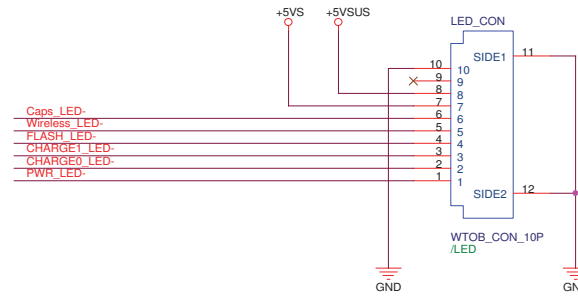
for CHARGE LED
Height : 0.55mm

for POWER LED
White

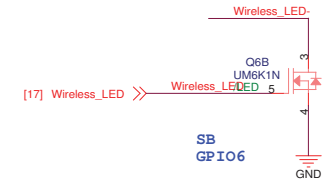
for FLASH LED
White



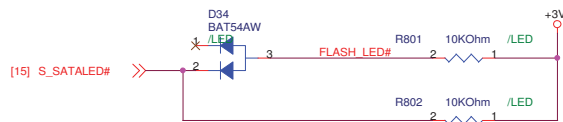
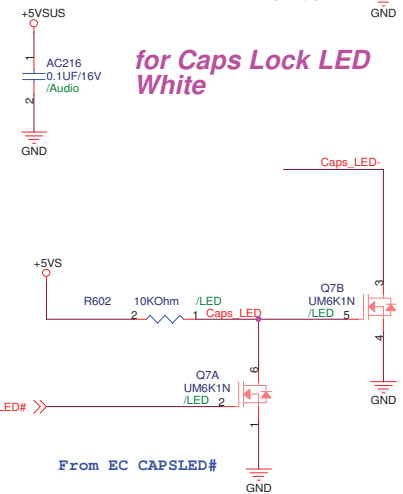
Change LED resistor to 510 Ohm, about 4mA



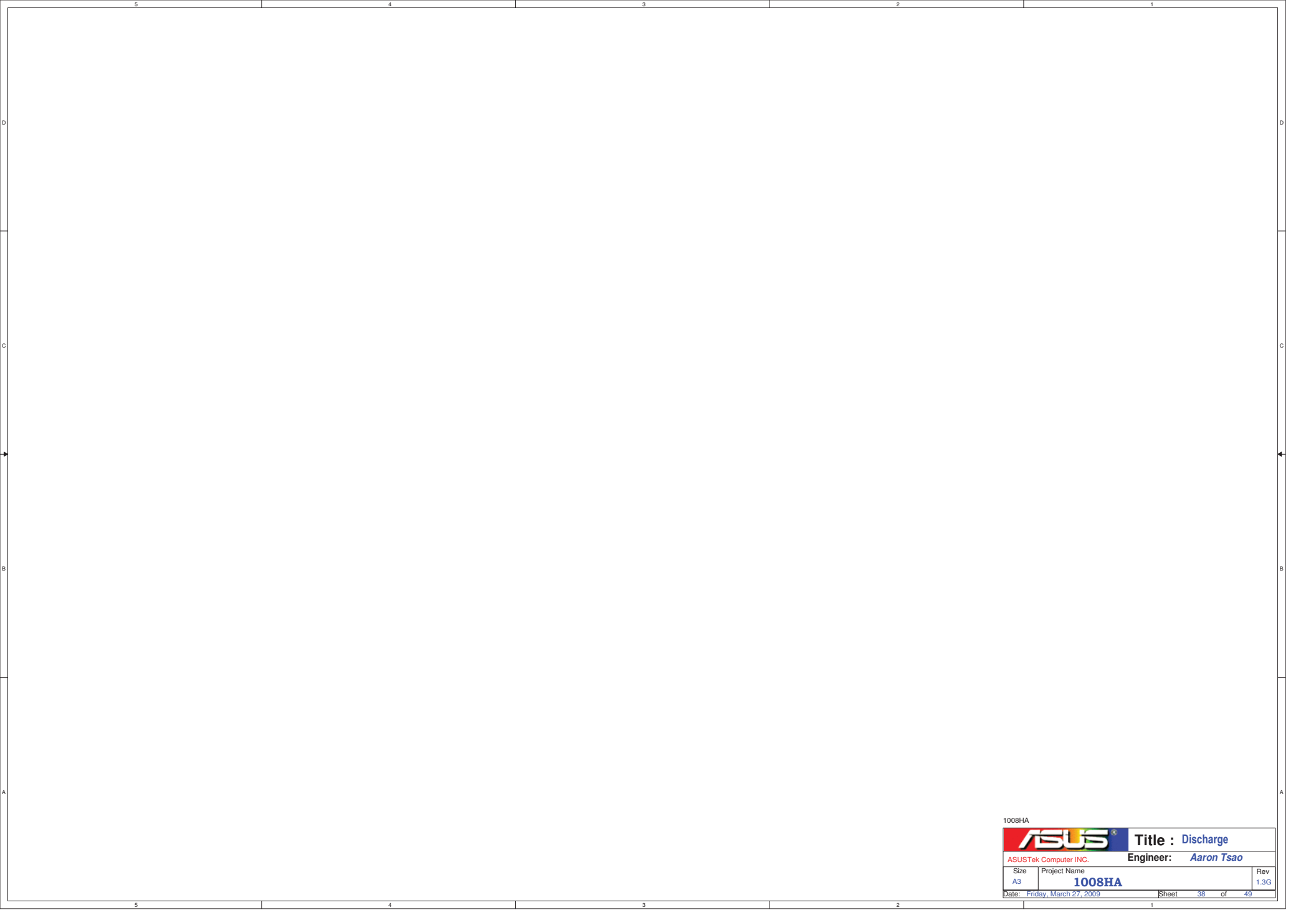
for WIFI/BlueTooth LED
White




for Caps Lock LED
White



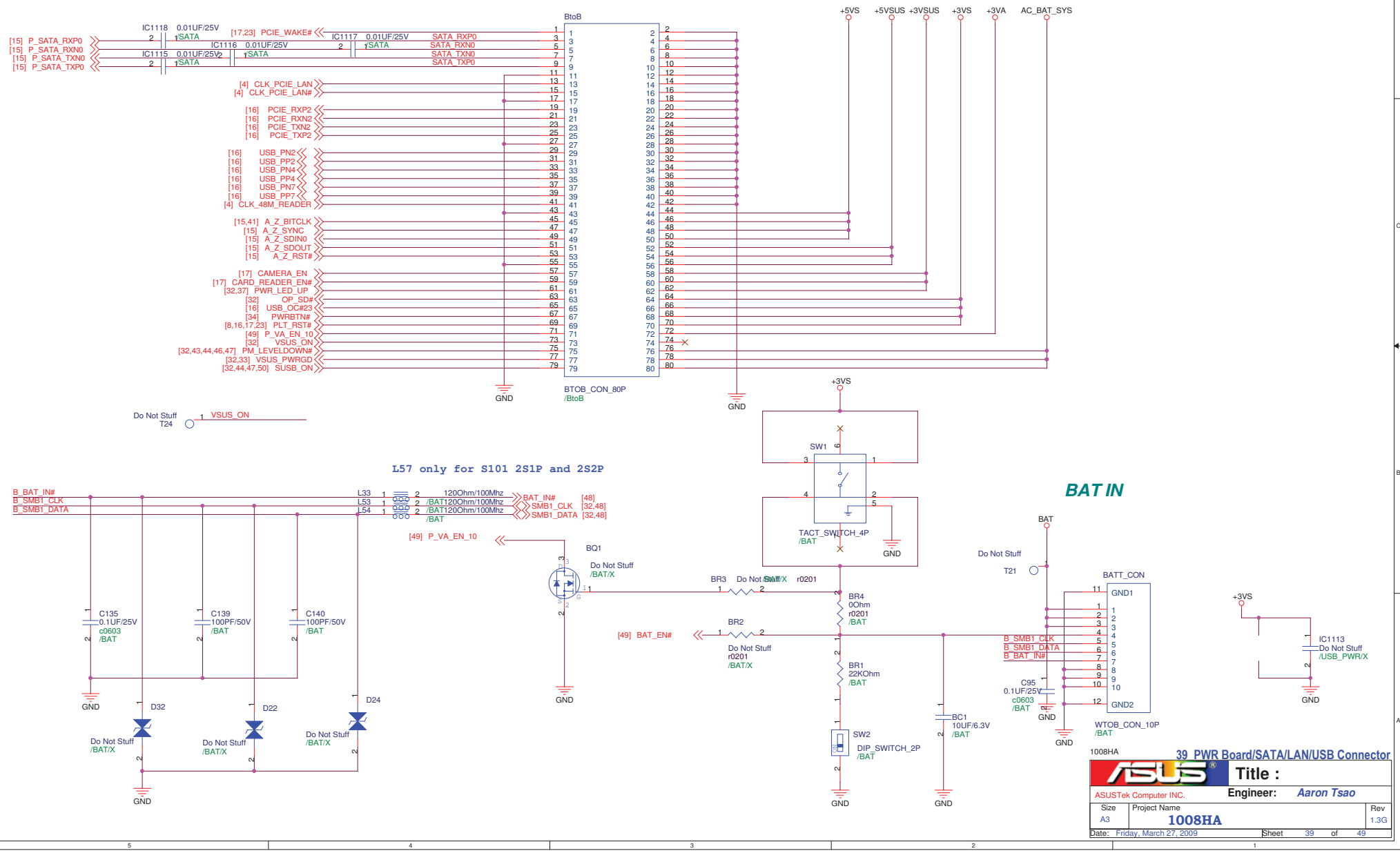
1008HA		ASUS		Title : LED	
ASUSTek Computer INC.		Engineer: <i>Kenneth_Hung</i>			
Size	Project Name			Rev	
A3	1008HA			1.3G	
Date: Friday, March 27, 2009	Sheet		37		of 50



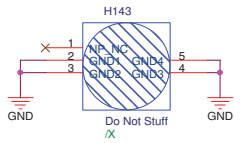
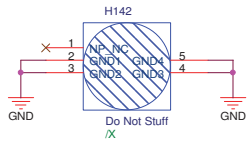
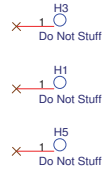
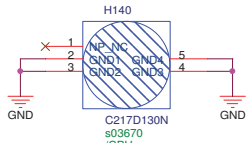
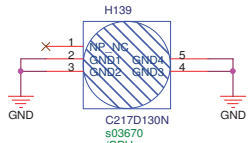
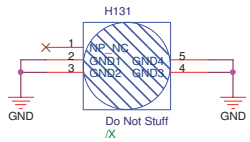
1008HA

		Title : Discharge
ASUSTek Computer INC.		Engineer: Aaron Tsao
Size	Project Name	Rev
A3	1008HA	1.3G
Date: Friday, March 27, 2009		Sheet 38 of 49

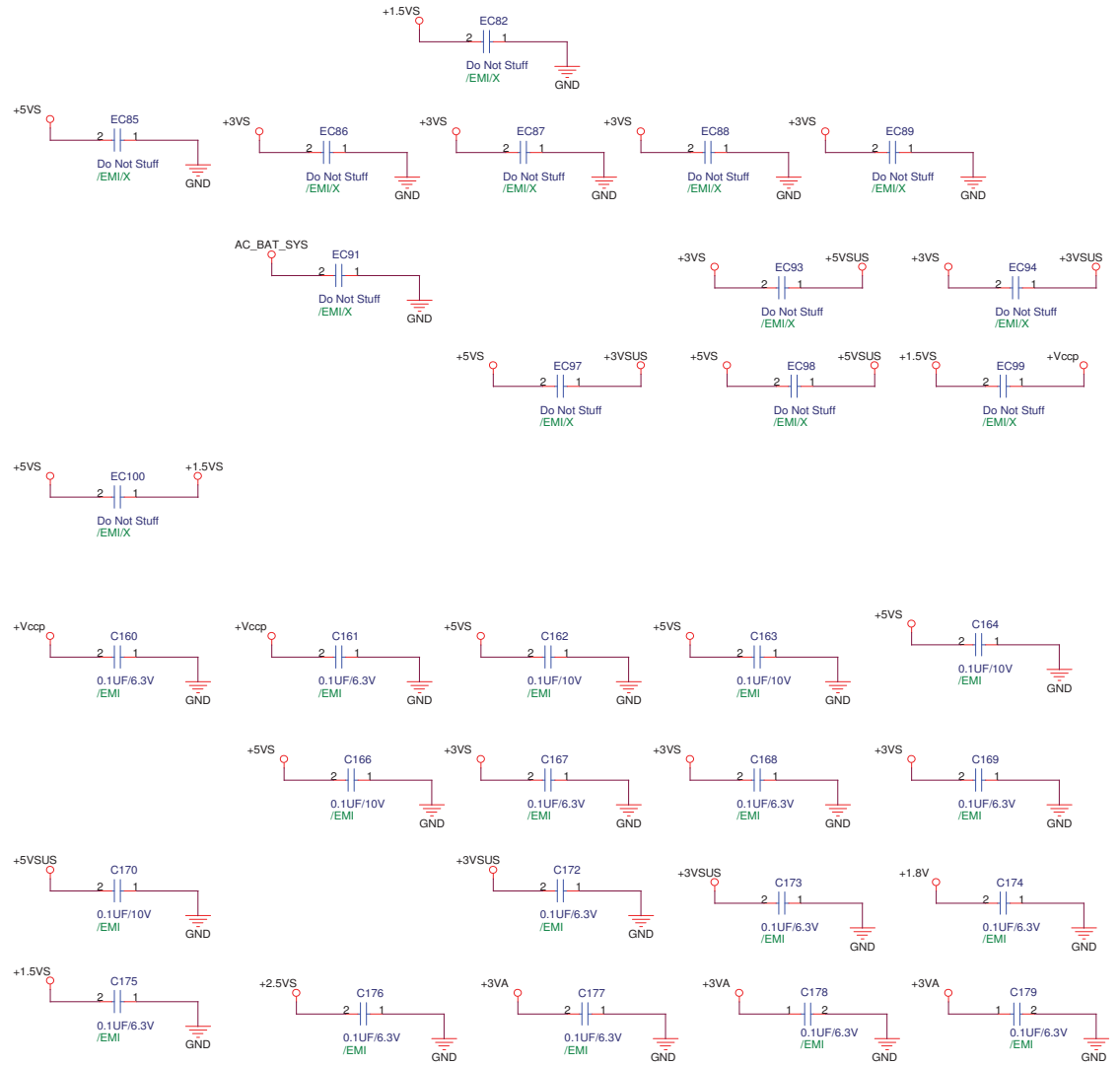
PWR Board/SATA/LAN/USB Connector



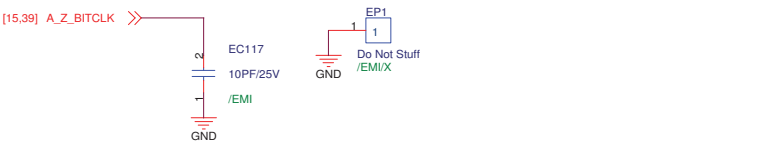
ASUS 39 PWR Board/SATA/LAN/USB Connector
Title :
 ASUSTek Computer INC. **Engineer: Aaron Tsao**
 Size Project Name
 A3 **1008HA**
 Date: Friday, March 27, 2009 Sheet 39 of 49 Rev 1.3G



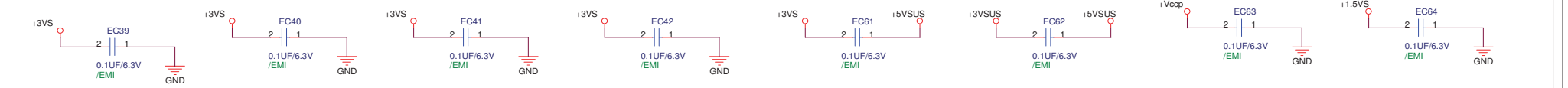
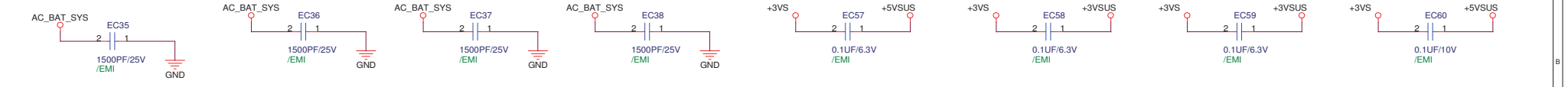
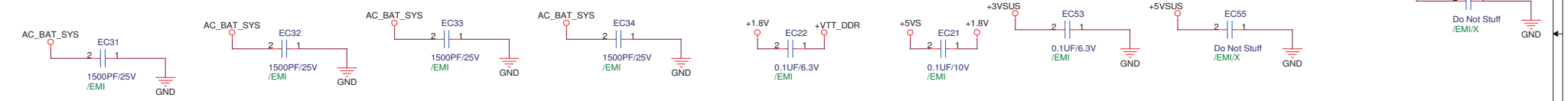
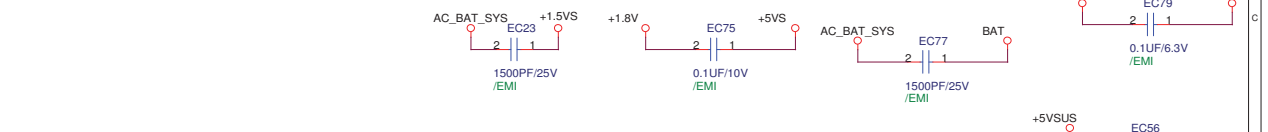
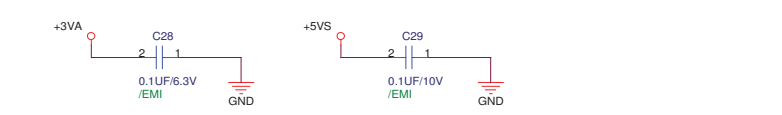
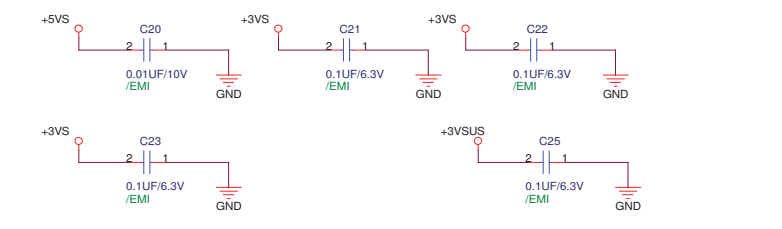
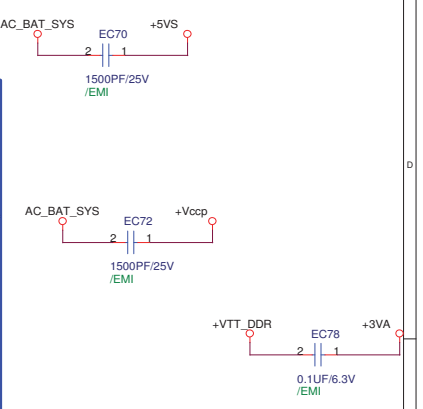
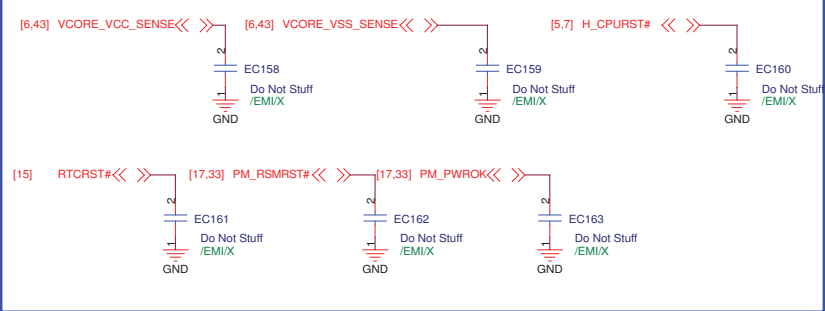
R1.1G Change



1008HA		ASUS		Title : Srew Hole	
ASUSTek Computer INC.		Engineer: Aaron Tsao			
Size A3	Project Name 1008HA	Date: Friday, March 27, 2009		Sheet 40	of 49
				Rev 1.3G	

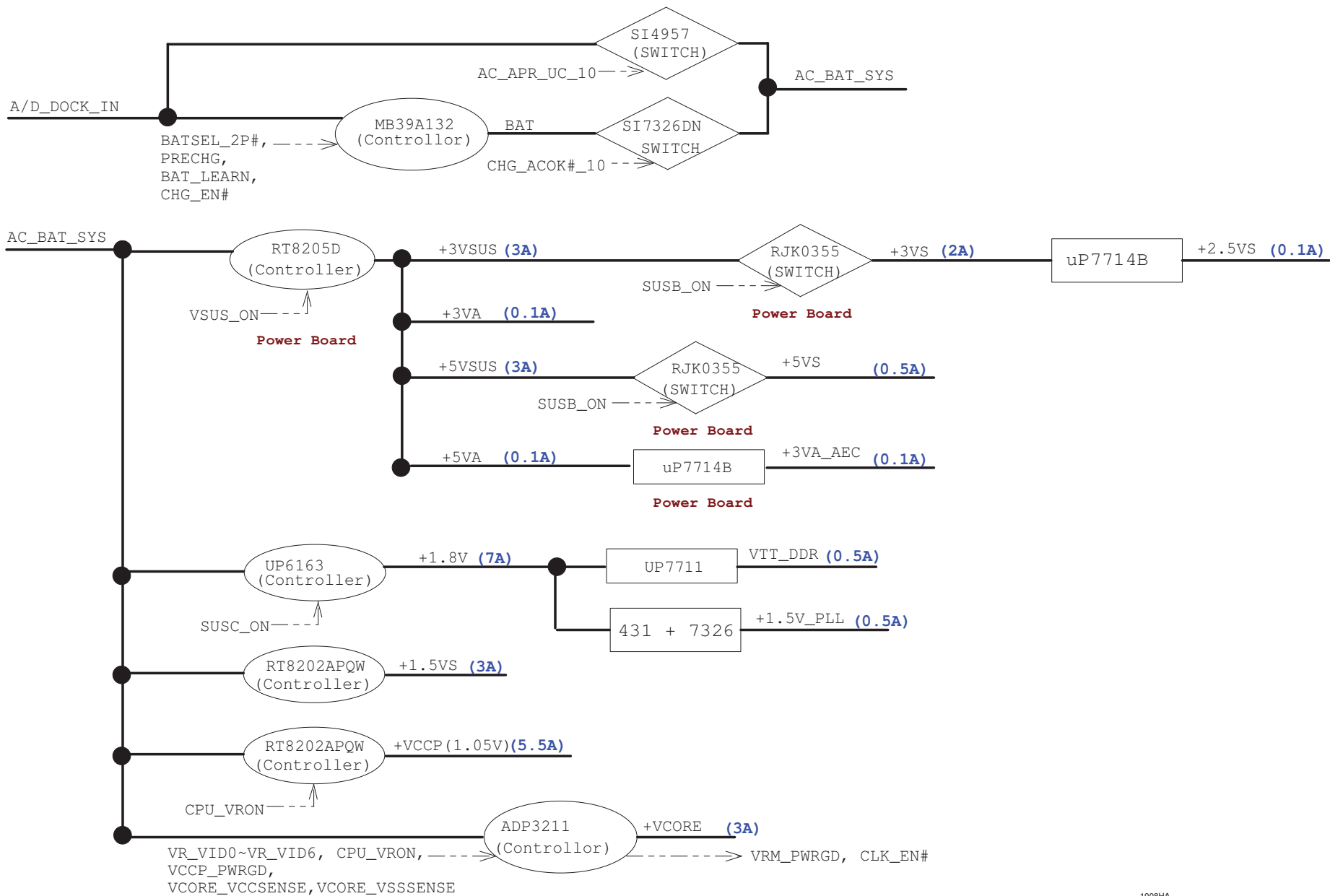


1.3G For ESD



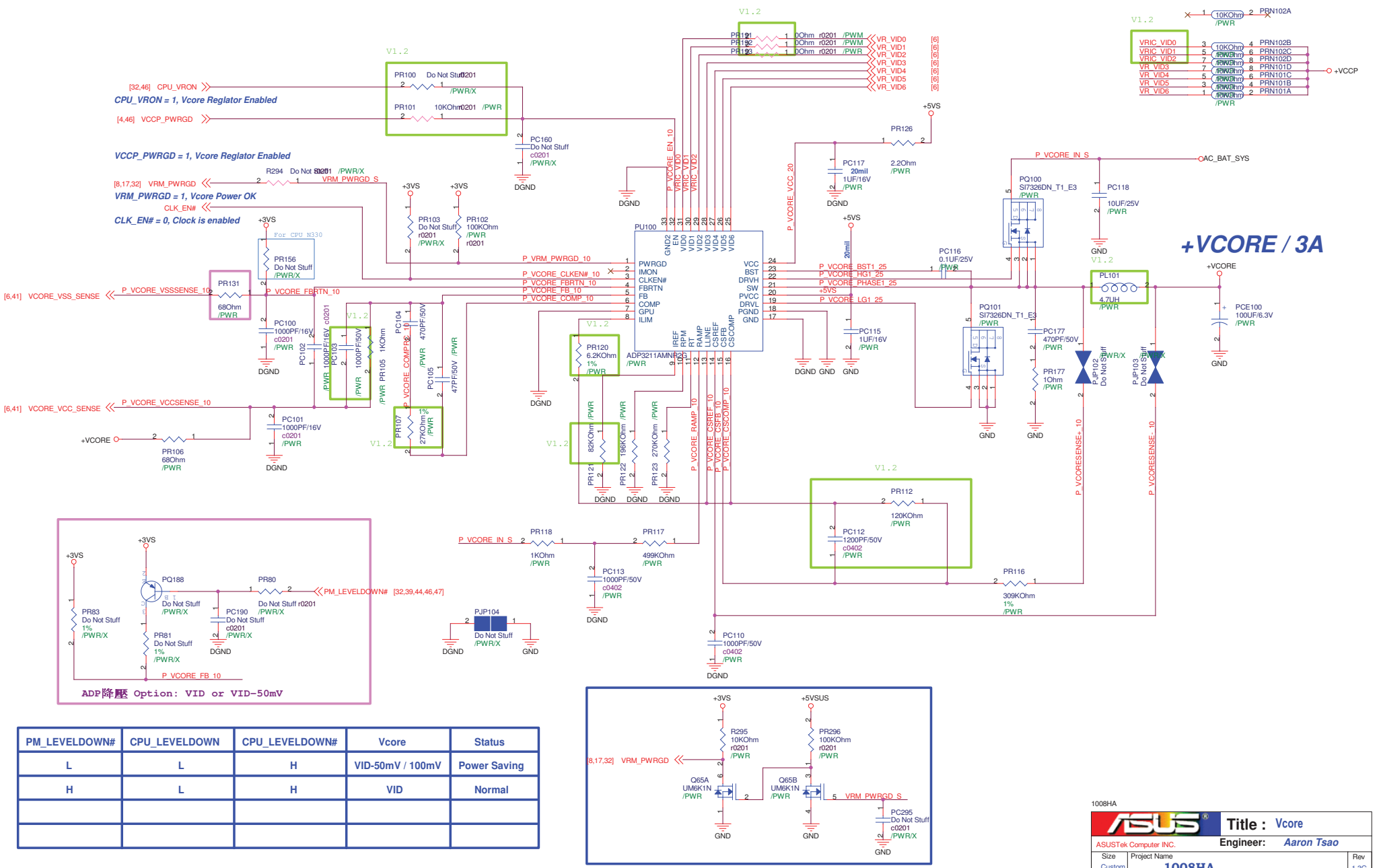
1008HA

ASUS		Title : EMI	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A3	Project Name 1008HA	Date: Friday, March 27, 2009	Rev 1.3G
		Sheet 41 of 49	

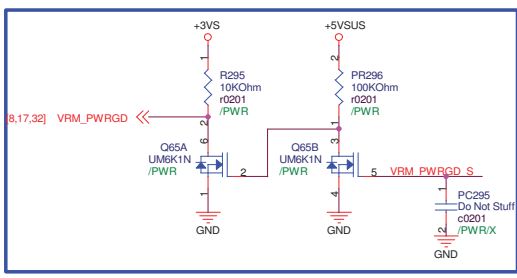


1008HA

ASUS		Title : Power Flow	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A3	Project Name 1008HA	Rev 1.3G	
Date: Friday, March 27, 2009	Sheet 42 of 49		



PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Vcore	Status
L	L	H	VID-50mV / 100mV	Power Saving
H	L	H	VID	Normal



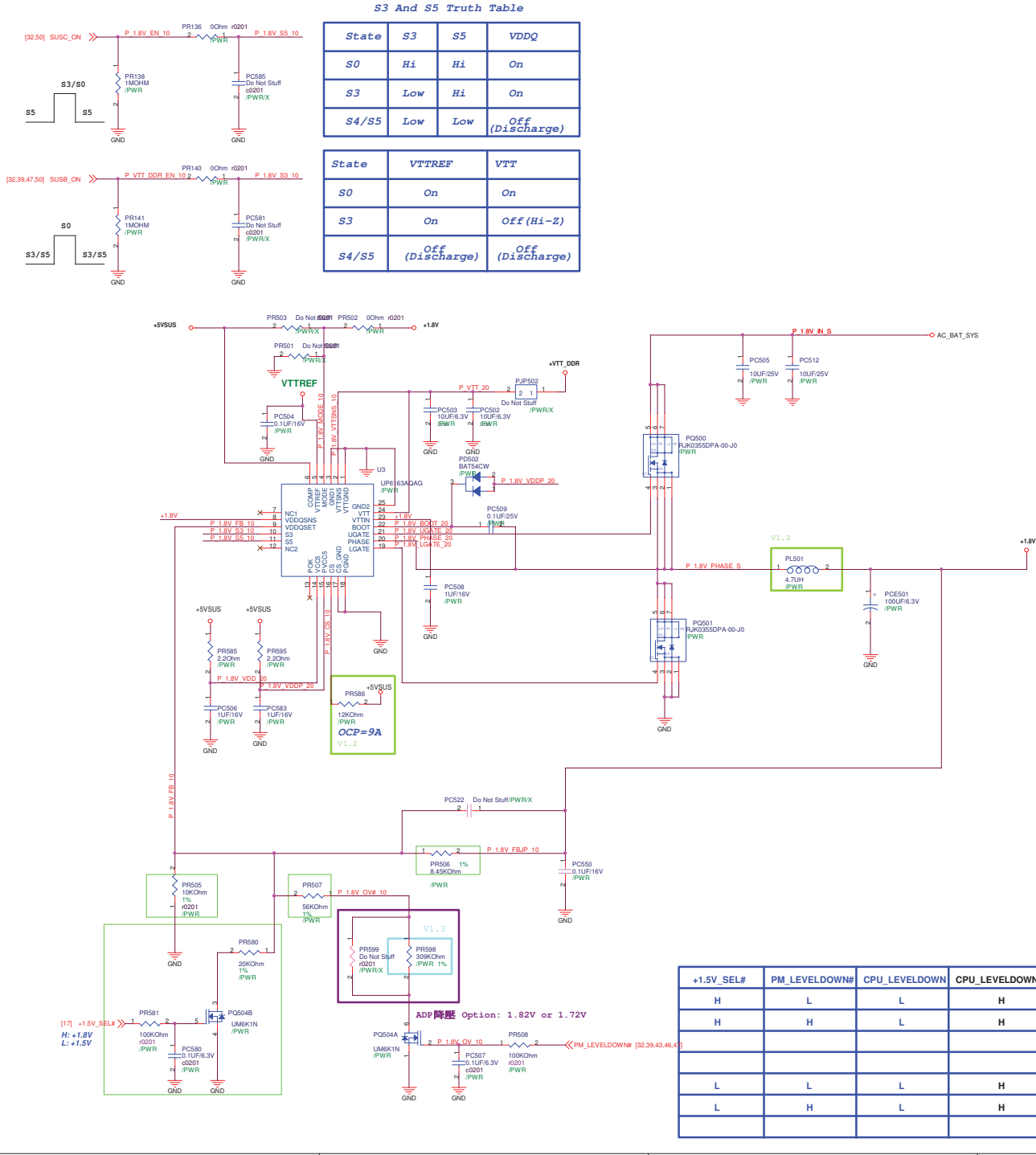
1008HA

Title : Vcore

ASUSTek Computer INC. **Engineer: Aaron Tsao**

Size: Custom **1008HA** Rev: 1.3G

Date: Friday, March 27, 2009 Sheet: 43 of 51



S3 And S5 Truth Table

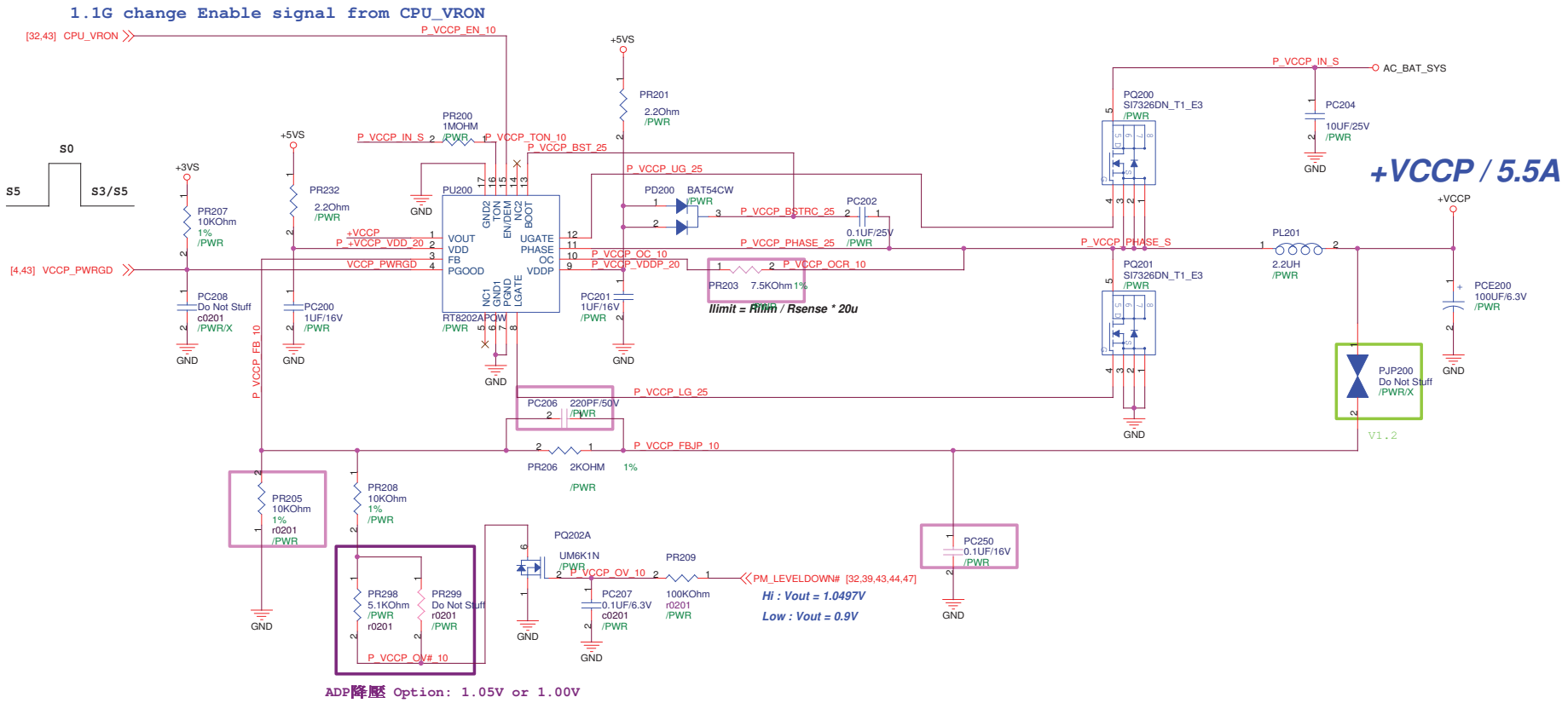
State	S3	S5	VDDQ
S0	Hi	Hi	On
S3	Low	Hi	On
S4/S5	Low	Low	(Off (Discharge))

State	VTTREF	VTT
S0	On	On
S3	On	Off (Hi-Z)
S4/S5	(Off (Discharge))	(Off (Discharge))

- Controller**
- Voltage & Current:**
+1.8V@3A
 - Frequency:**
PR127=1Mohm Fosc=250KHz
 - OCP:**
PR181=22Kohm ->10A
 - POR:**
POR Hysteresis =20mV
V on =3.9-4.5V
 - UVP:**
Vout*70%
 - OVP:**
Vout*115%
 - Enable Voltage:**
V rising = 2V
V falling = 0.8V
 - Soft start time:**
Tss=512us
 - Phase selection:**
/X
 - Inrush Current:**
C total = 110 uF
I inrush= 0.39 A

- Power stage**
- I/P Current:**
 $I_{in} = V_o \cdot I_o / (0.8 \cdot V_{in}) = 0.94A$
 - Ripple Current:**
 $I_{rip} = 1.78A$
 $I_{spec} = 2A \times 1 \text{ pcs}$
 - Dynamic:**
 $I_{peak} = 5A$
ESR = 18 mohm
 $\Delta V = 90mV$
 - Inductor Spec:**
 $I_{sat} = 10A$
 $I_{dc} = 5.5A$
DCR=37 mohm
 - MOSFET Spec:**
H-side MOSFET: AP4800AGM
Rds(ON)= 21 mohm (Vgs=4.5 V)
I cont = 9.6 A (T=25 °C)
I peak = 40 A
L-side MOSFET: RAP4800AGM
Rds(ON)= 21 mohm (Vgs=4.5 V)
I cont = 9.6 A (T=25 °C)
I peak = 40 A

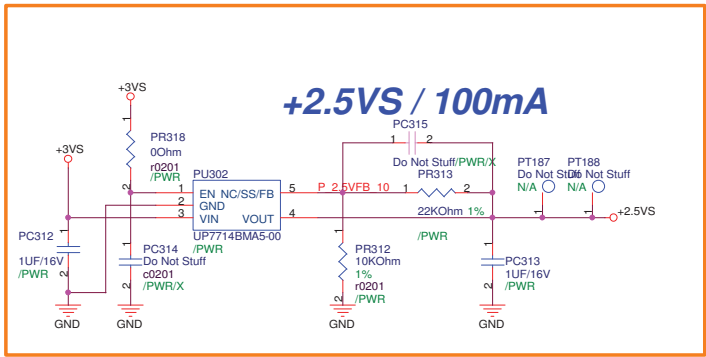
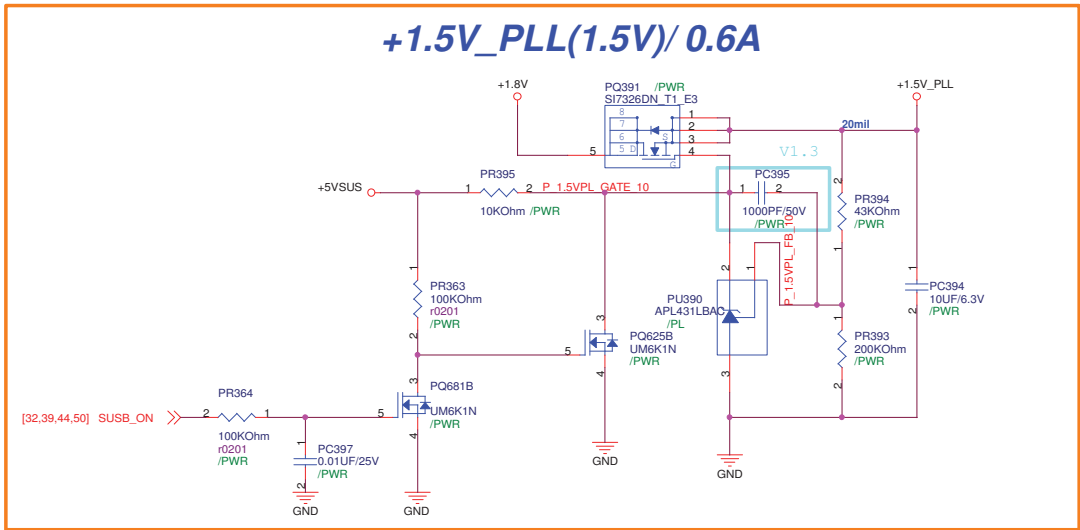
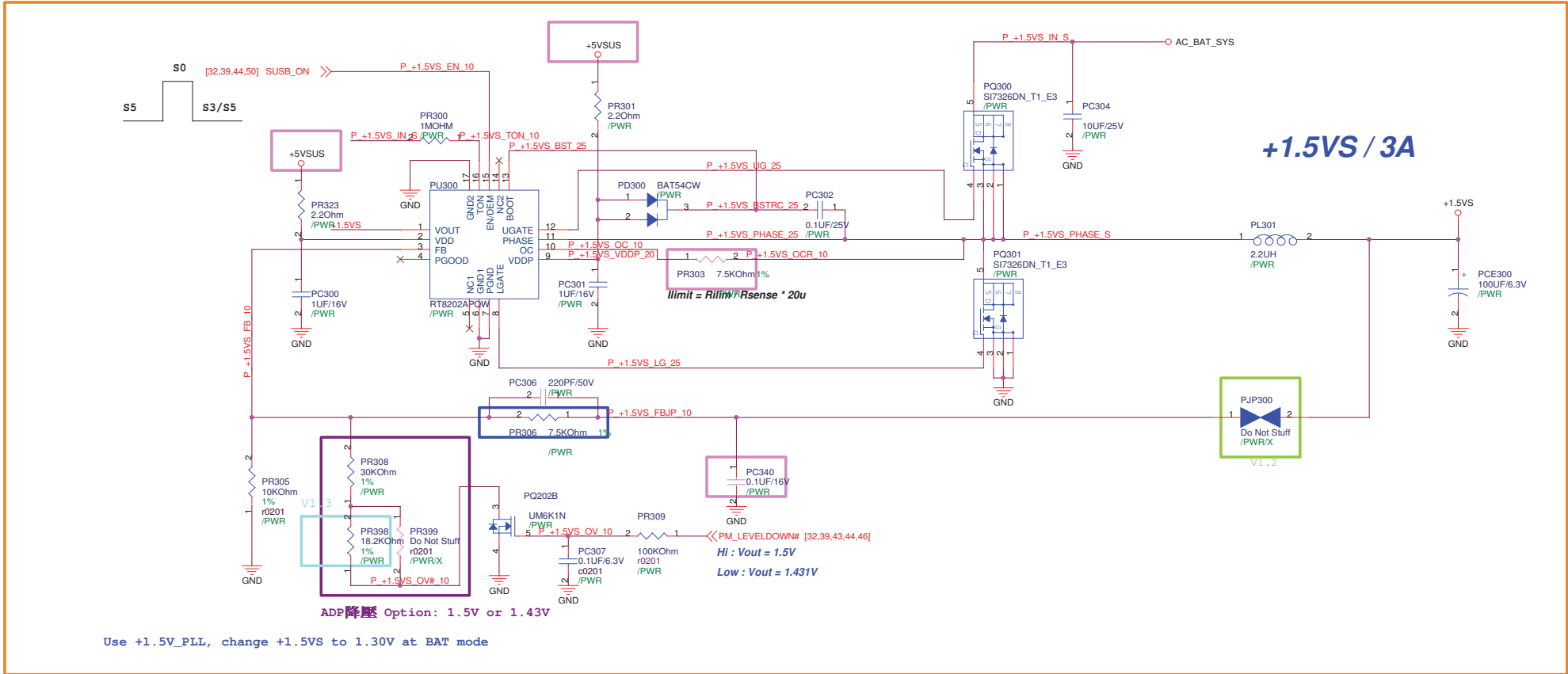
+1.5V_SEL#	PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
H	L	L	H	1.72V	Power Saving
H	H	L	H	1.82V	Normal
L	L	L	H	1.4V	Power Saving
L	H	L	H	1.5V	Normal



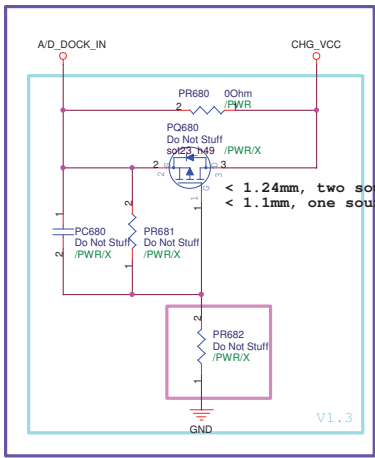
PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
L	L	H	0.9V	Power Saving
H	L	H	1.05V	Normal

1008HA

ASUS		Title : VCCP	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A3	Project Name 1008HA	Rev 1.3G	
Date: Friday, March 27, 2009		Sheet 46 of 51	



1008HA		ASUS		Title : +1.5VS & +2.5VS	
ASUSTek Computer INC.		Engineer: Aaron Tsao			
Size	Project Name				Rev
A3	1008HA				1.3G
Date: Friday, March 27, 2009	Sheet		47 of 51		



VREF = 5.0V
 fosc(KHz) = 17000 / RT (KOhm)
 Soft start: ts(s) = 0.13 * CS (uF)
 VTH of -IN1: 5V / 62 * (100+62) = 13.06V
 VTH of ACIN: 1.25V / 25 * (185+25) = 10.5V
 Change PR607 and PR608 value

Battery Cell Selection :
 BAT_LEARN = 1, Battery discharges
 BAT_ID = 1, 2 Cells; Vadj2 = 0.998V
 => Icharge = 1.477A
 BAT_ID = 0, 4/6 Cells; Vadj2 = 1.648V
 => Icharge = 2.517A

Pre-Charging Mode :
 Precharging current = 150mA
 Vadj2 = 168.75mV

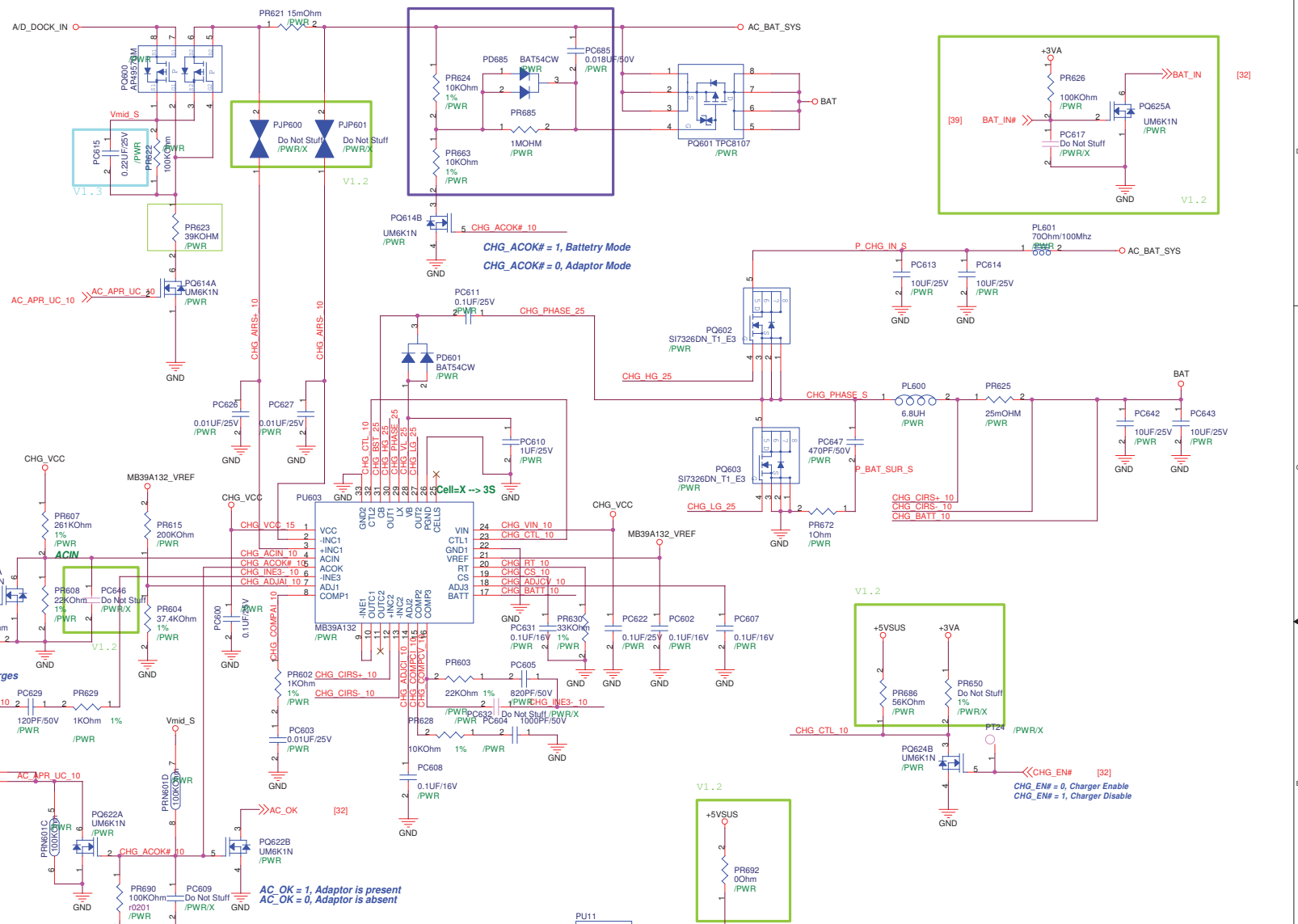
Adaptor Max. Current :
 Ilimit = 1.9A; 36.1W (19V/40W)

ACIN Threshold = 1.25V
 Adaptor > 16V, System Powered by Adaptor
 Adaptor < 16V, System Powered by Battery

Battery Charging Voltage :
 Vadj3 > 4.1V ==> Vbat = 4.2V /cell
 2.2V > Vadj3 > 1.1V ==> Vbat = 2 * Vadj3 /cell

Battery Charging Current :
 4.4V > Vadj2 >= 0V ==>
 Ichg = (Vadj2 - 0.075) / (25 * Rs)

Input Adaptor Max. Current Limit :
 Ilimit_current = (Vadj1 - 0.075) / (25 * Rs)



1004HN Charging Current :

	2P#	Icharge
	1	1.49A
	0	2.5A

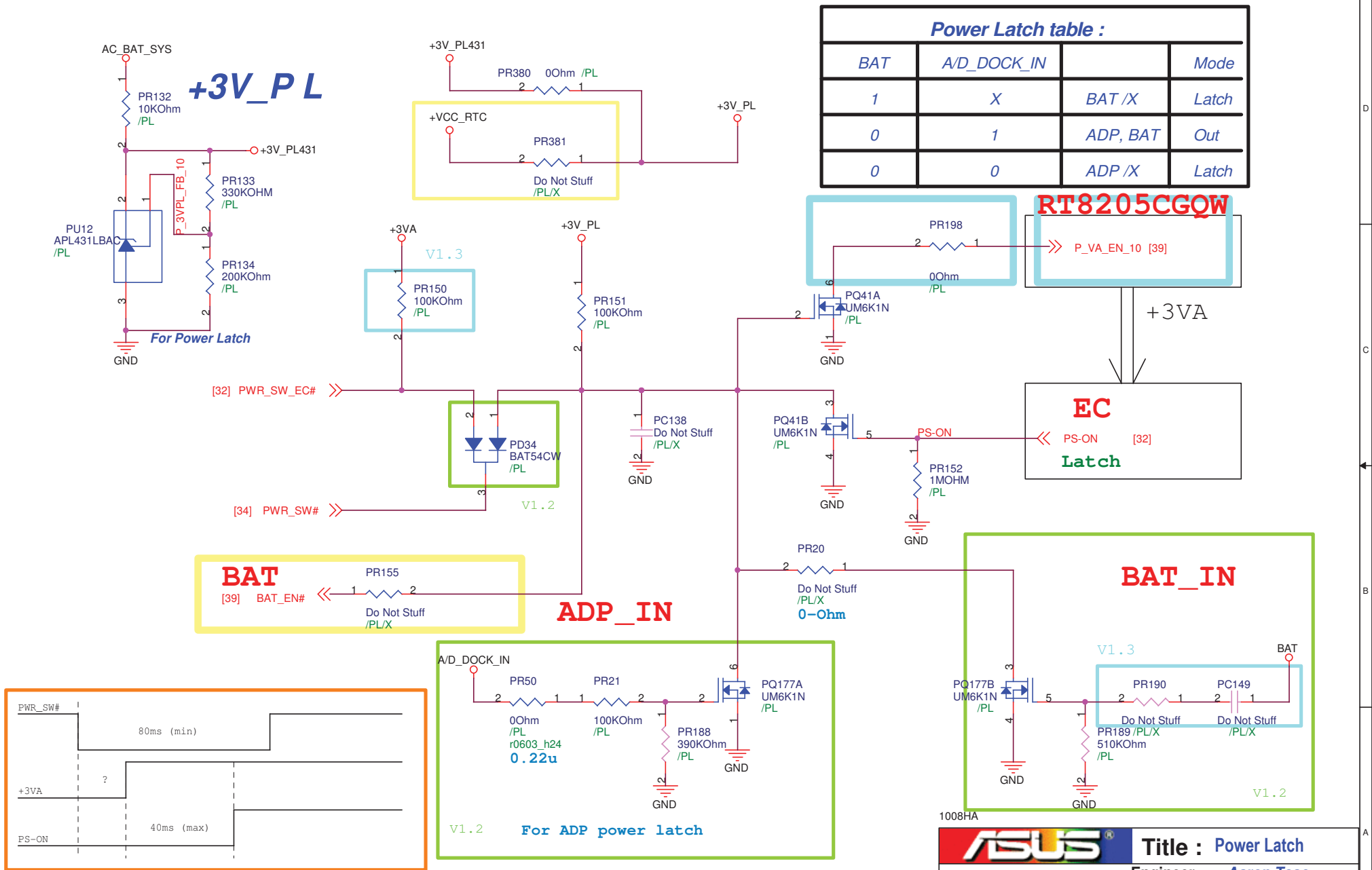
Standard Charging Current :

	4P#	2P#	Icharge
	1	0	0.557A
	0	1	1.598A
	0	0	2.768A

1008HA

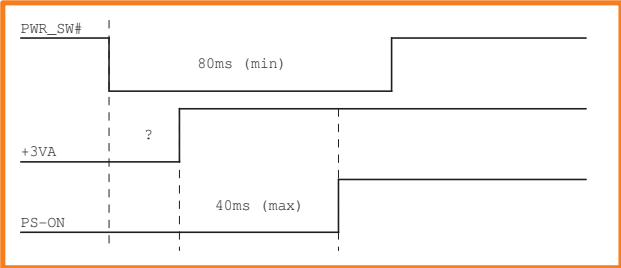
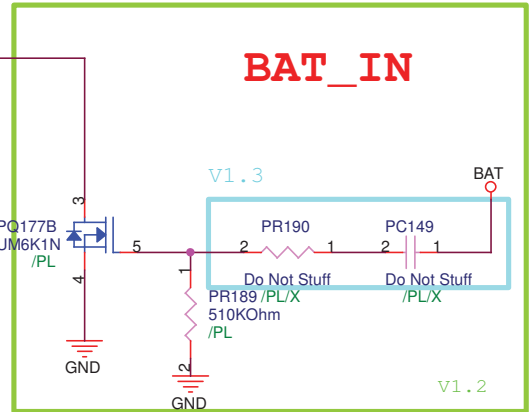
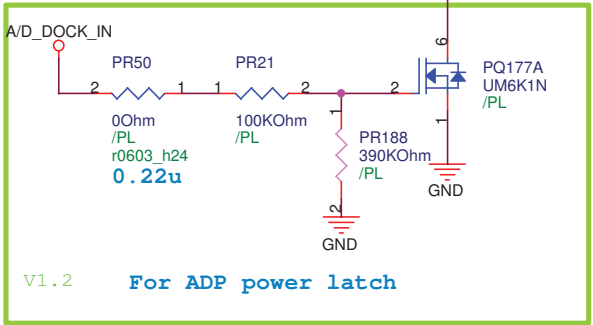
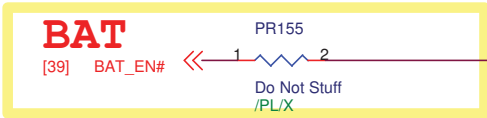
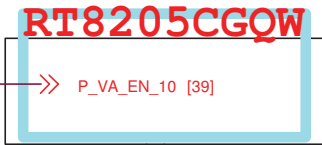
ASUS Title : CHARGER
 ASUSTek Computer INC. Engineer: Aaron Tsao

Size	Project Name	Rev
Custom	1008HA	1.3G
Date: Friday, March 27, 2009	Sheet	48 of 55



Power Latch table :

BAT	A/D_DOCK_IN	Mode	
1	X	BAT /X	Latch
0	1	ADP, BAT	Out
0	0	ADP /X	Latch



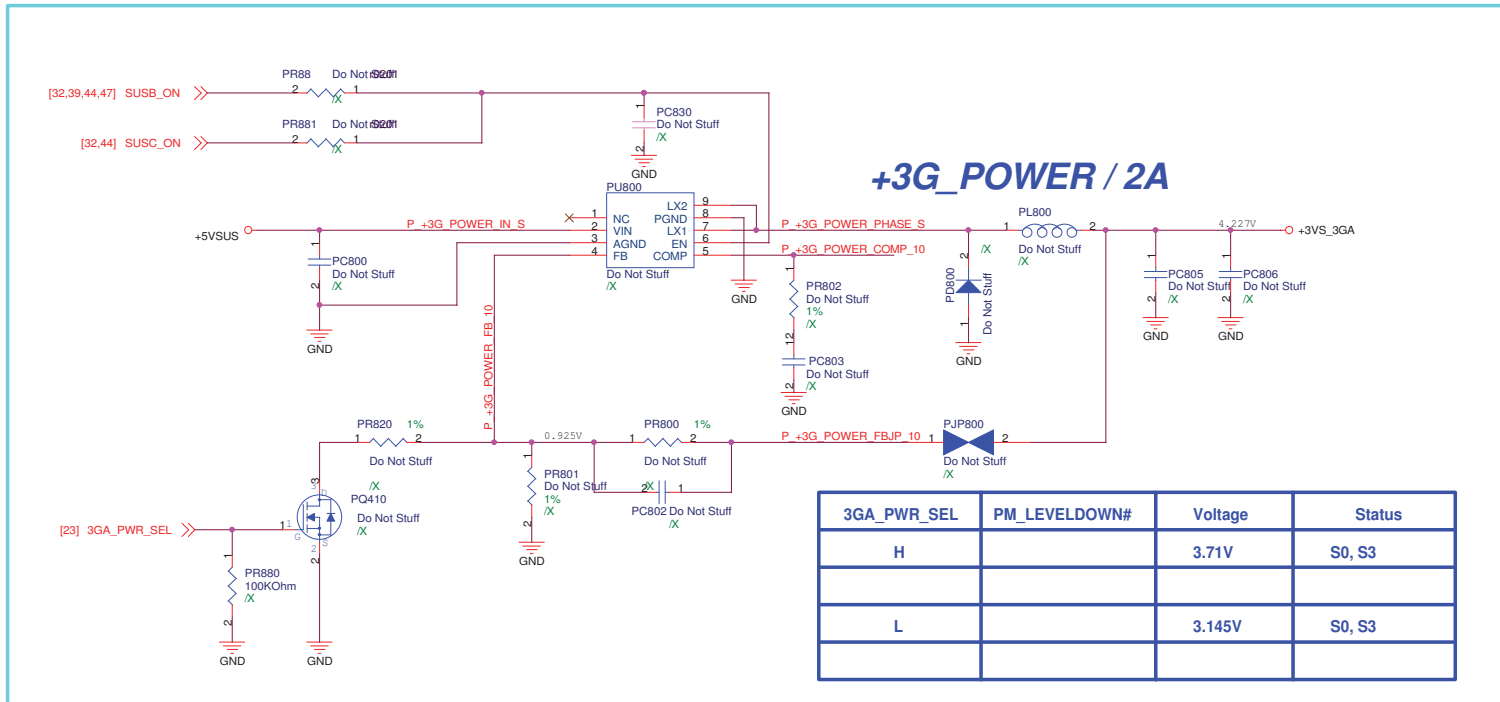
1008HA

ASUS Title : Power Latch

ASUSTek Computer INC. Engineer: Aaron Tsao

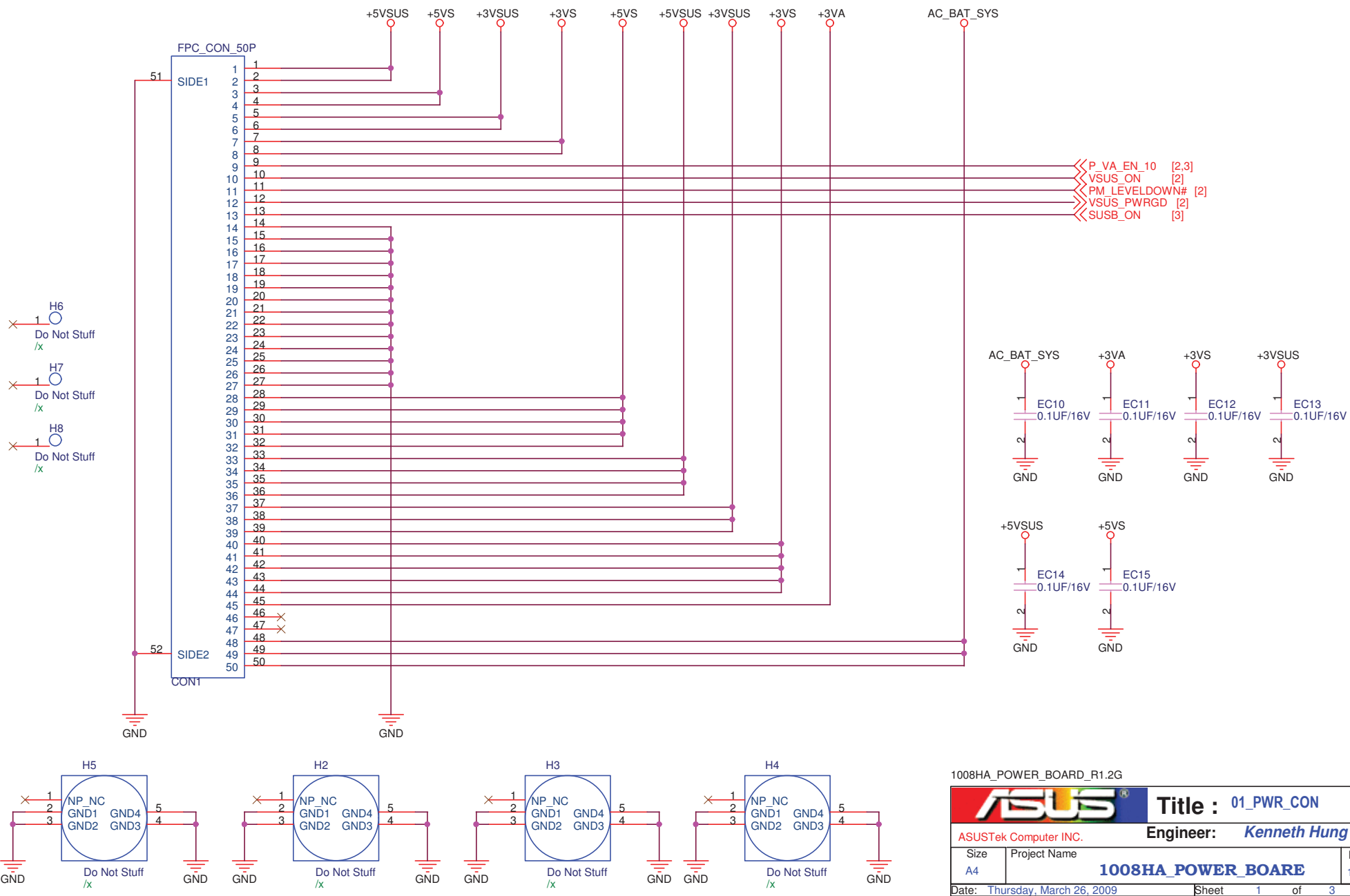
Size	Project Name	Rev
A4	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 49 of 56



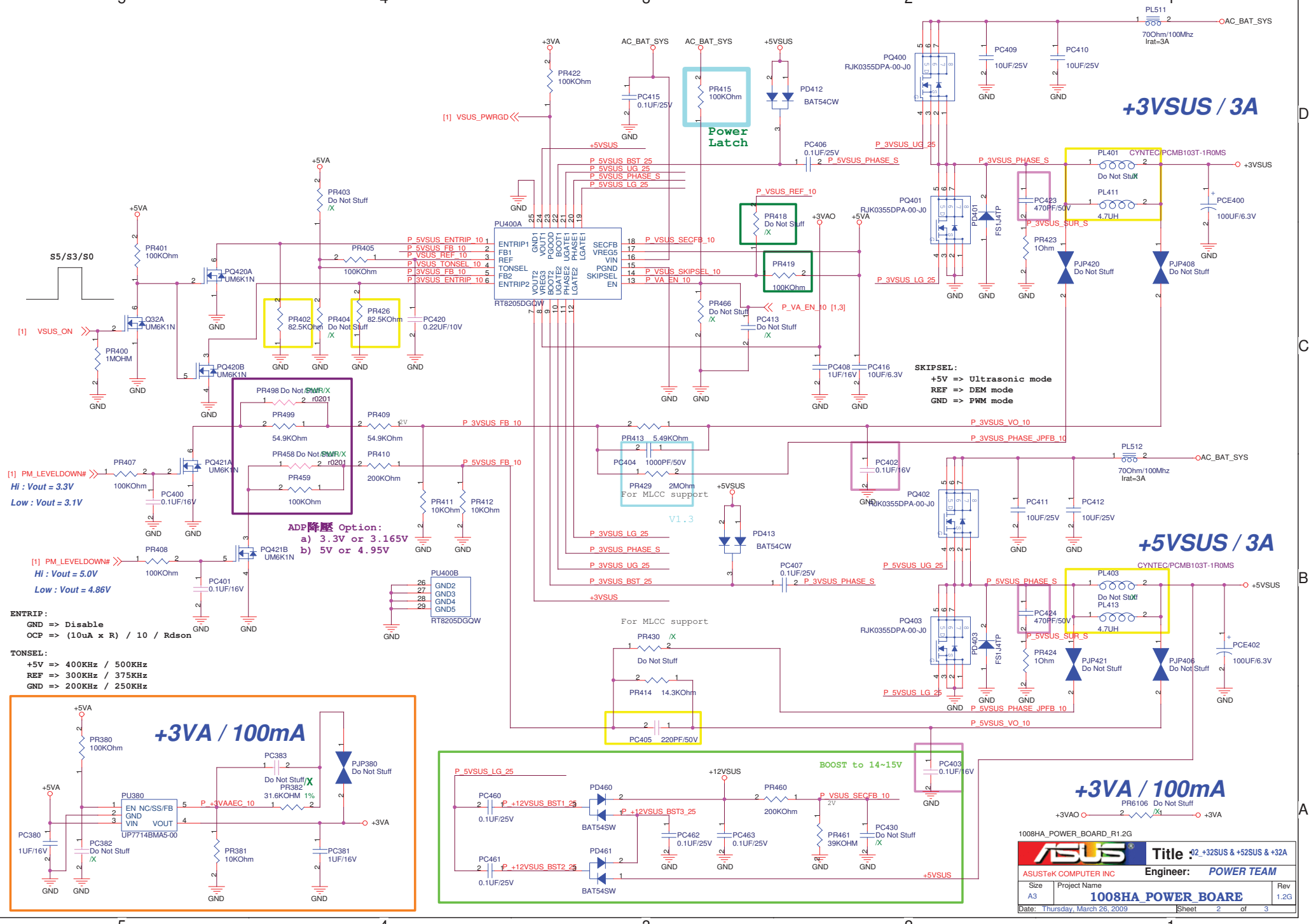
1008HA

ASUS		Title : History	
ASUSTek Computer INC.		Engineer: <i>Aaron Tsao</i>	
Size A3	Project Name 1008HA	Rev 1.3G	
Date: Friday, March 27, 2009		Sheet 50 of 51	



1008HA_POWER_BOARD_R1.2G

		Title : 01_PWR_CON	
ASUSTek Computer INC.		Engineer: Kenneth Hung	
Size A4	Project Name 1008HA_POWER_BOARE		Rev 1.2G
Date: Thursday, March 26, 2009		Sheet 1 of 3	



5
4
3
2
1

A
B
C
D

[1] VSUS_ON
[1] VSUS_PWRGD
[1] PM_LEVELDOWN#
[1] VSUS_PULSE

Hi : Vout = 3.3V
Low : Vout = 3.1V

Hi : Vout = 5.0V
Low : Vout = 4.86V

ENTRIP :
GND => Disable
OCP => (10uA x R) / 10 / Rds(on)

TONSEL :
+5V => 400KHz / 500KHz
REF => 300KHz / 375KHz
GND => 200KHz / 250KHz

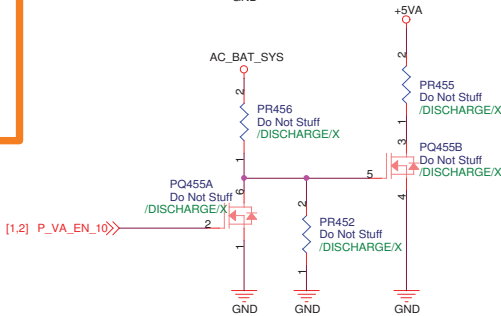
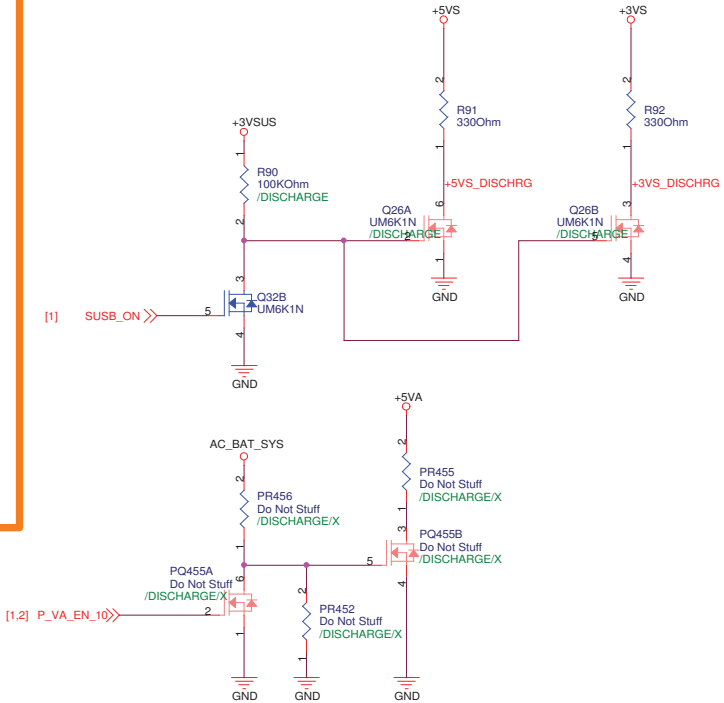
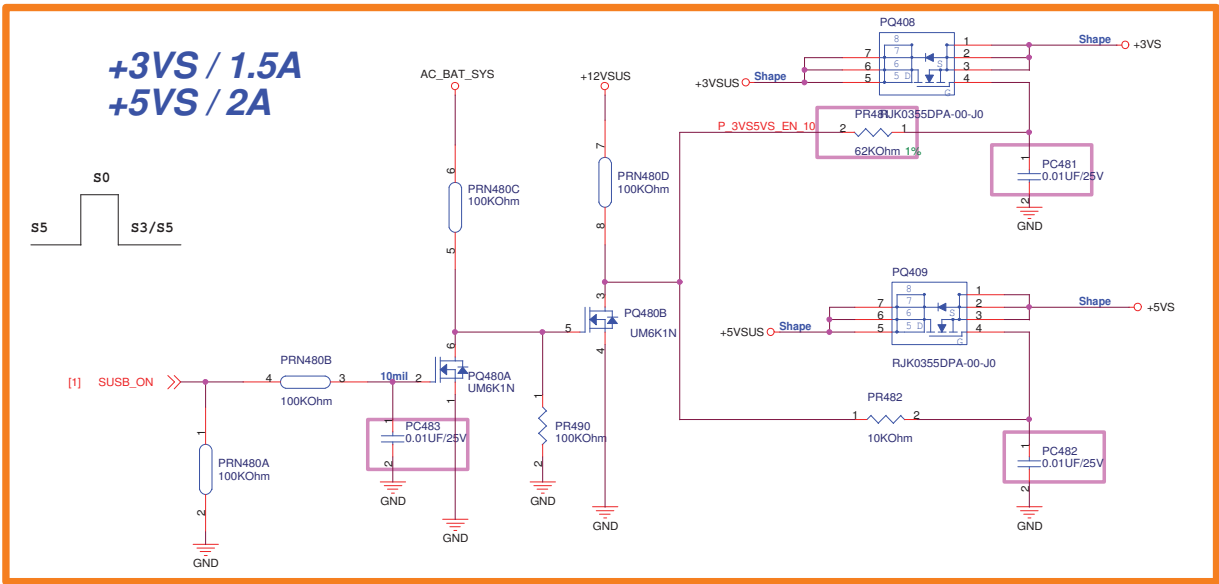
ADP降壓 Option:
a) 3.3V or 3.165V
b) 5V or 4.95V

For MLCC support
PR430 / X
Do Not Stuff
PR414 14.3KOhm
PC405 220PF/50V

SKIPSEL:
+5V => Ultrasonic mode
REF => DEM mode
GND => PWM mode

BOOST to 14-15V
PC403 0.1UF/16V

700H8A_POWER_BOARD_R1.2G
Title : 02_+32SUS & +52SUS & +32A
ASUSTeK COMPUTER INC
Project Name
Engineer: POWER TEAM
Size A3
1008HA_POWER BOARE
Rev 1.2G
Date: Thursday, March 26, 2009
Sheet 2 of 3




1008HA_POWER_BOARD_R1.2G

ASUS		Title : 03_+32S & +52S	
ASUSTek Computer INC.		Engineer: POWER TEAM	
Size A3	Project Name 1008HA_POWER_BOARE	Rev 1.2G	
Date: Thursday, March 26, 2009	Sheet	3	of 3

Rev0.7	Initial release	2008/10/22
Rev0.71	Remove hotkey switch(R2, C427, SW6, OR25) Remove White LED (LED4, R86, Q17, SB_GPIO7) Add 40 pin FPC connector Change netname from BT_LED to Wireless_LED Change NMOS from 2N7002 to UM6K1N(Q25, 23, Q18, Q15, Q58, Q19, Q16, Q19) Change 3G power name to +3VS_3GA Add dual-core support (not ready)	2008/10/22
Rev0.72	Change the part for LAN connector Change the SPEAKER connector (same as FAN)	2008/10/23
Rev0.73	Remove CC36-40, CC45, CC24-25, CC16-17 for layout space Remove HC32, 33 for Layout space Remove HC14-16, HC125 (stuff HC126) for Layout space Remove C422, 212, 232 (stuff C223) for Layout space Remove C436, 292, 305 for Layout space Change U47 to /X and stuff R265 Remove MC5, 6, 7, 9, 15, 17, 22, 24, 26, 27, 33 for Layout space Remove WIFI & 3G RSV component	2008/10/27

1008HA

		Title : History
ASUSTek Computer INC.		Engineer: Aaron Tsao
Size	Project Name	Rev
A3	1008HA	1.3G
Date: Friday, March 27, 2009		Sheet 51 of 49