

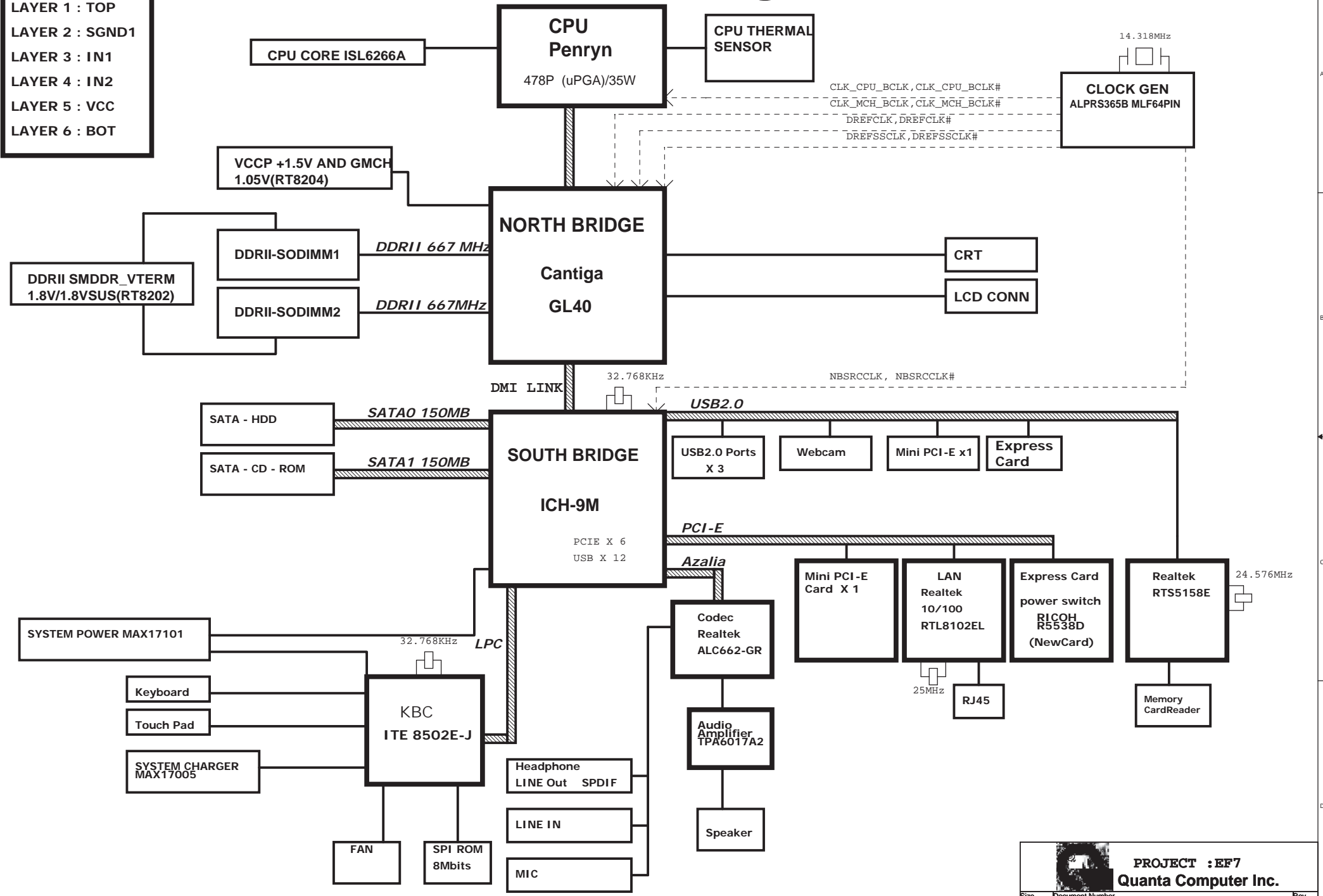
Li 37/39 Block Diagram


<http://hobi-elektronika.net>

01

PCB STACK UP

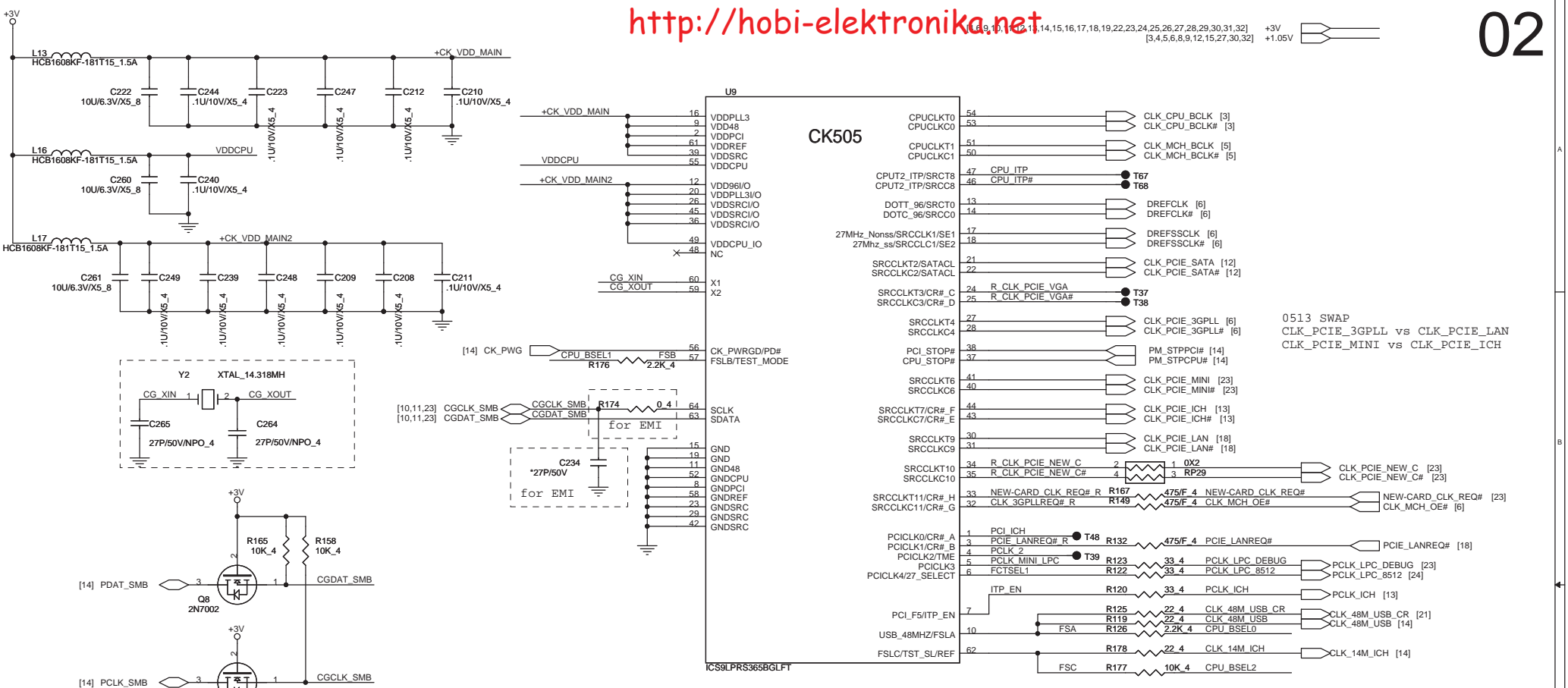
- LAYER 1 : TOP
- LAYER 2 : SGND1
- LAYER 3 : IN1
- LAYER 4 : IN2
- LAYER 5 : VCC
- LAYER 6 : BOT





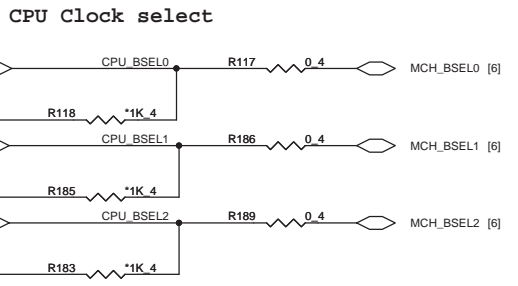
PROJECT : EF7
Quanta Computer Inc.

Size Custom	Document Number Block Diagram	Rev 1A
Date: Friday, June 13, 2008		Sheet 1 of 34

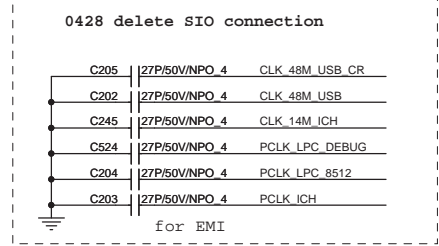
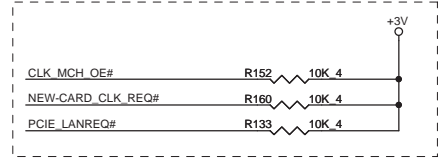


GCLK_SEL = FCTSEL1

FCTSEL1 (PIN13)	PIN13	PIN14	PIN17	PIN18
0=UMA	DOT96T	DOT96C	SRCT1/LCDT_100	SRCT1/LCDT_100
1 = External VGA	SRCT0	SRCC0	27Mout-NSS	27Mout-SS



FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33



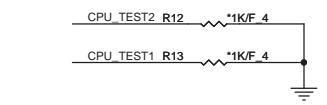
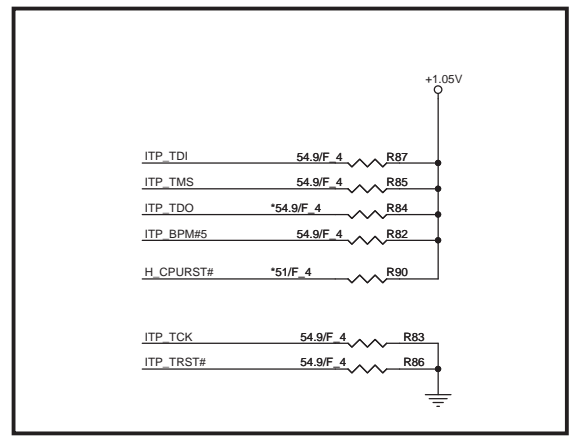
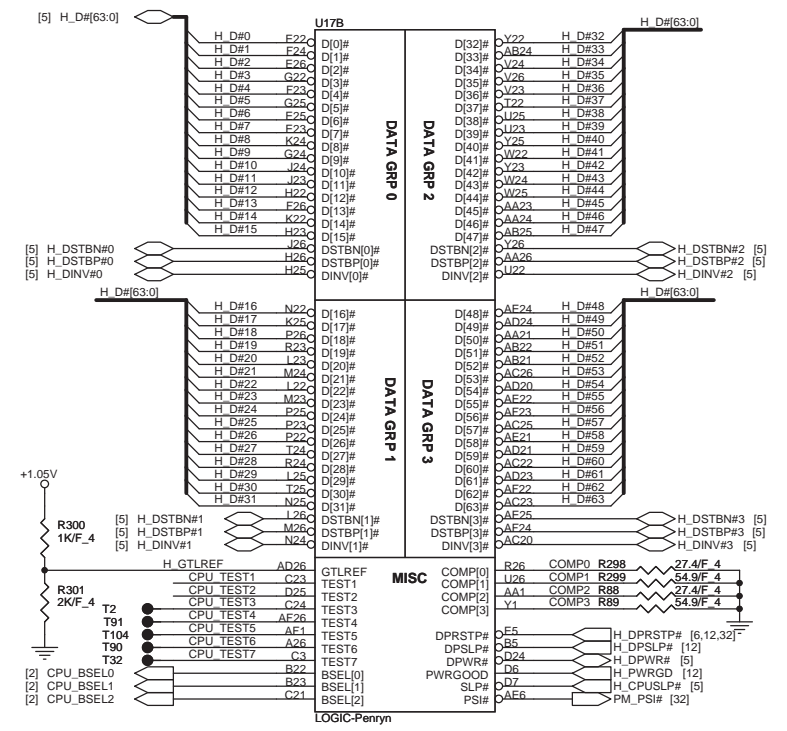
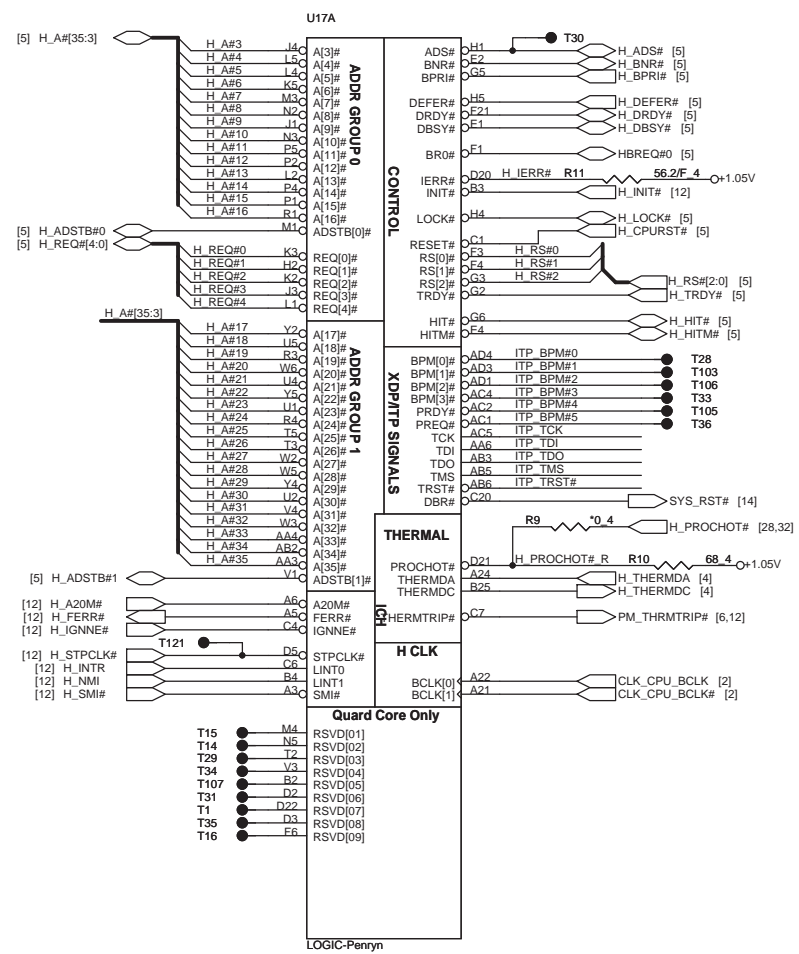
0=overclocking of CPU and SRC Allowed
1 = overclocking of CPU and SRC not Allowed

0428 correct Net name PCLK_2

0519 Update BSEL default followed CPU.

PROJECT : EF7
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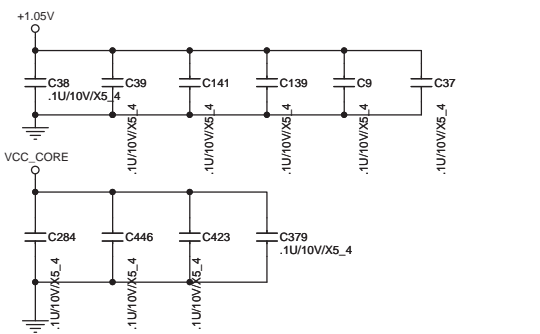
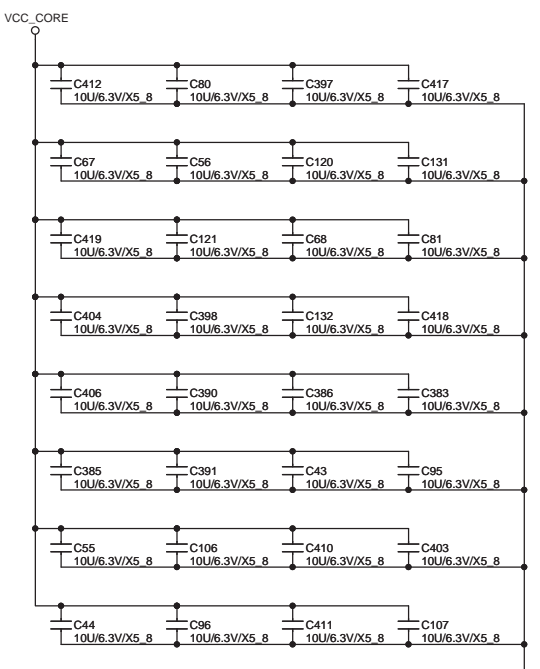
Size Custom Document Number **CLOCK GENERATOR** Rev 1A
Date: Wednesday, July 02, 2008 Sheet 2 of 34



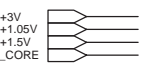
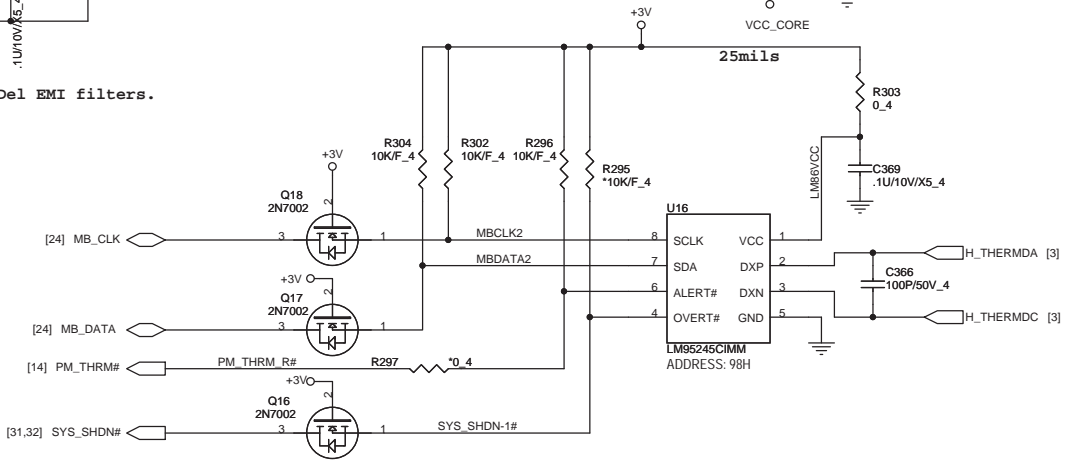
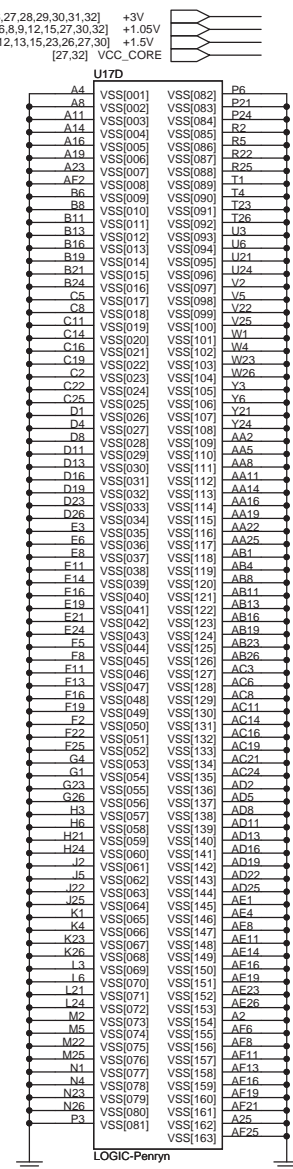
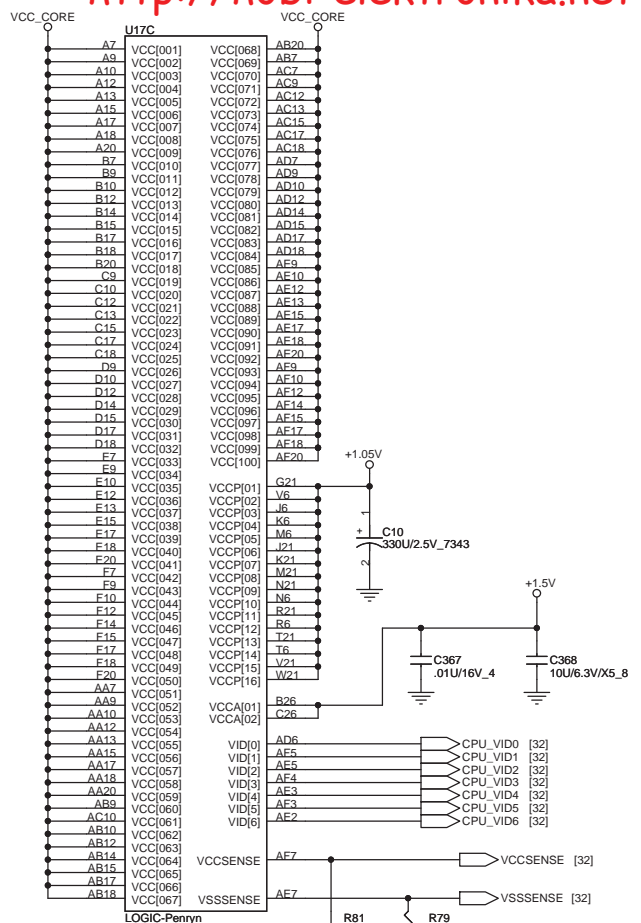
PROJECT : EF7
Quanta Computer Inc.

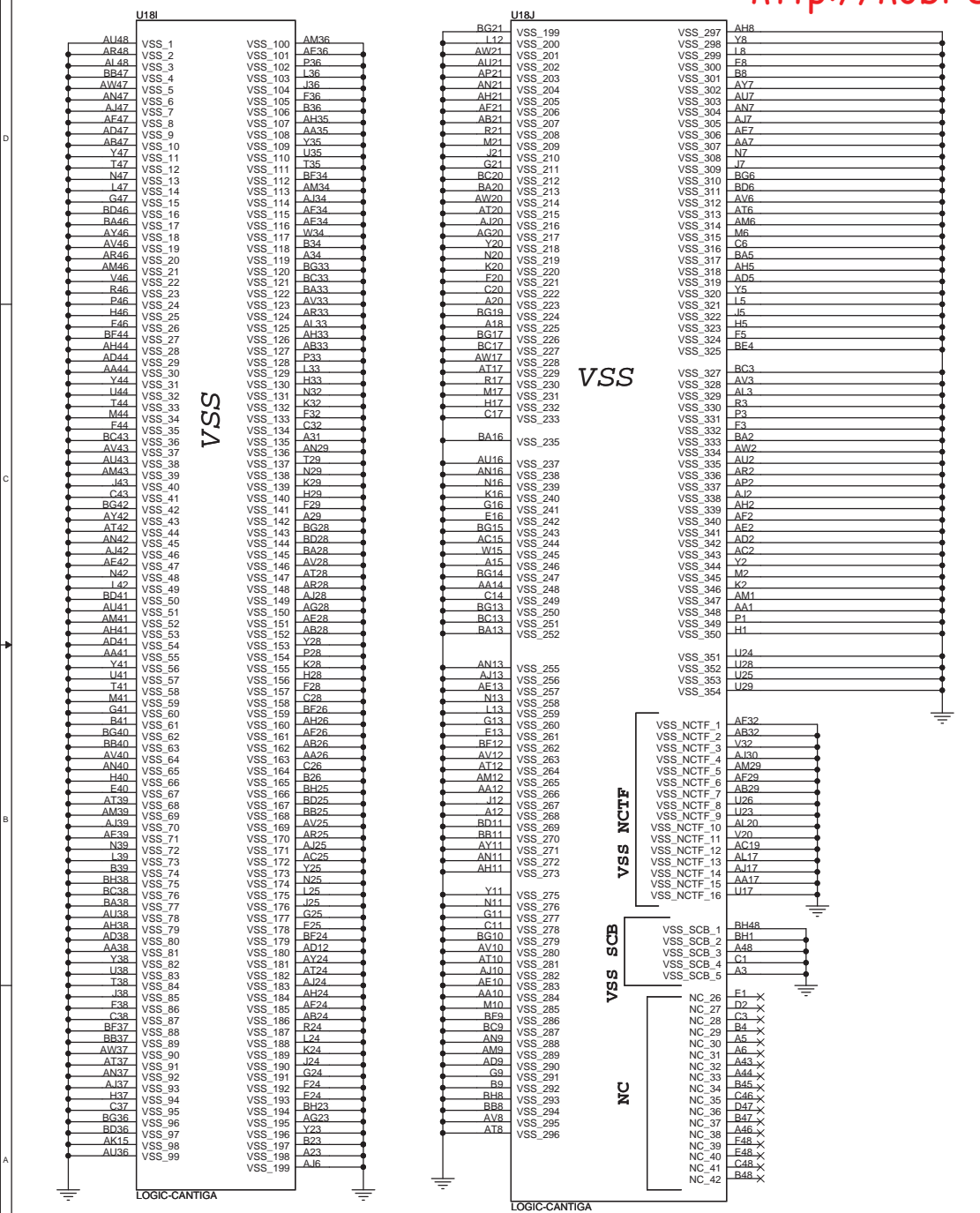
Size Custom Document Number Penryn 1/2 Host Rev 1A

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0519 Del EMI filters.

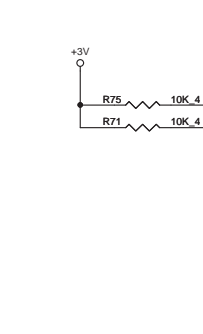
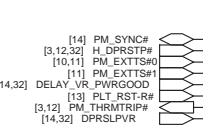
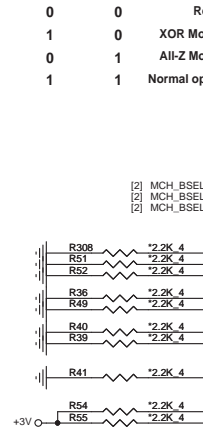




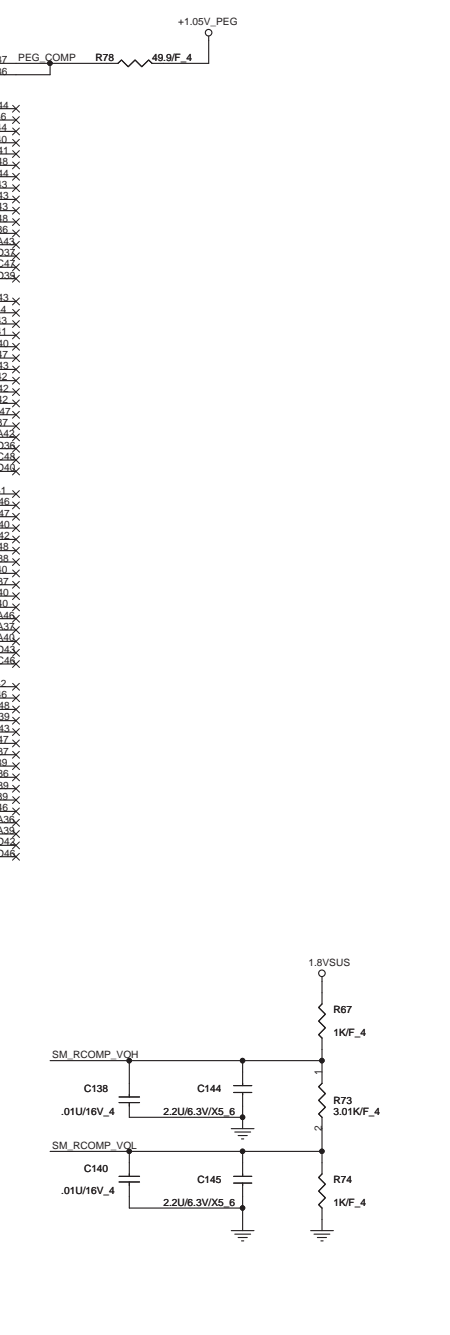
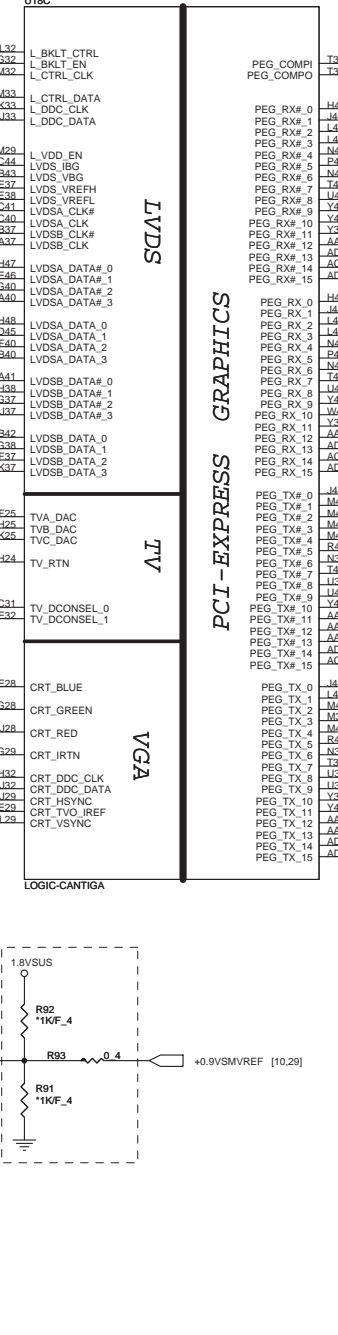
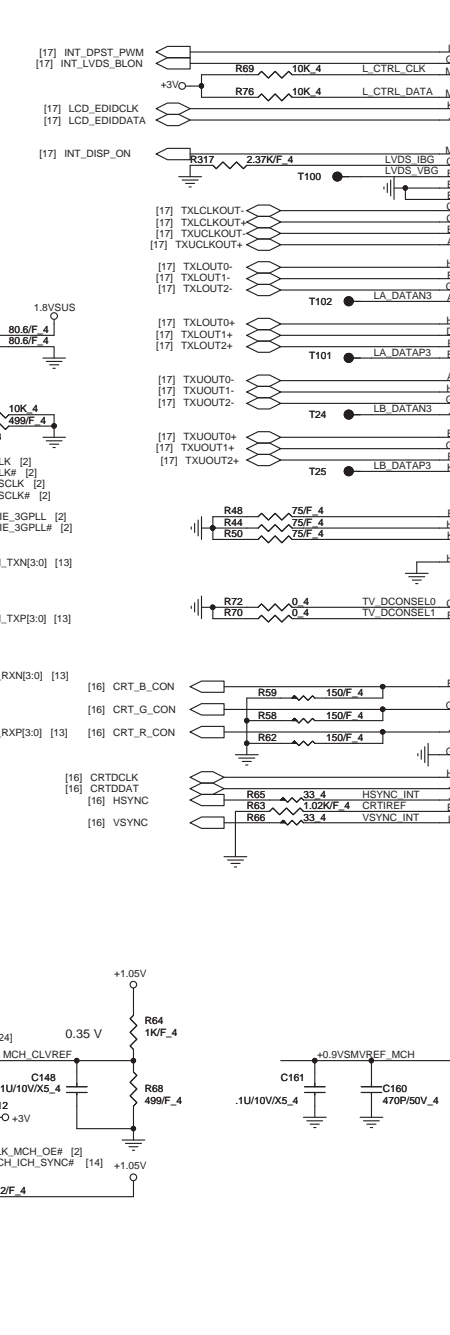
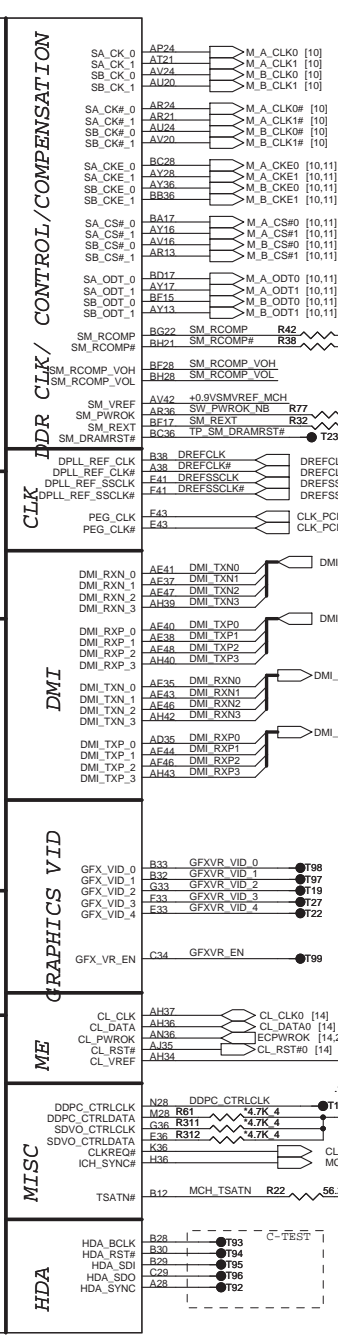
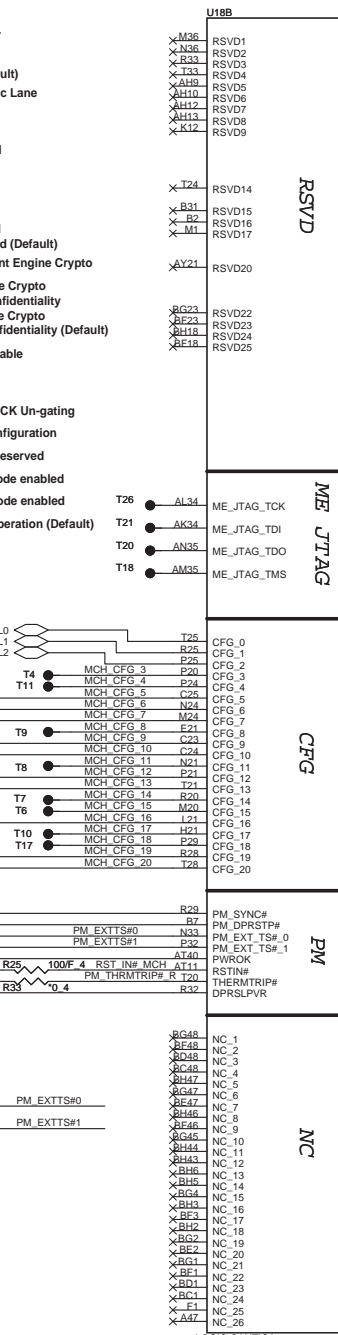
MCH_CFG_5 DMIx2 selection

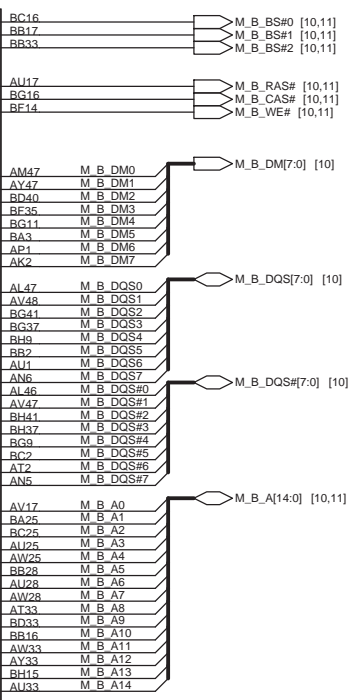
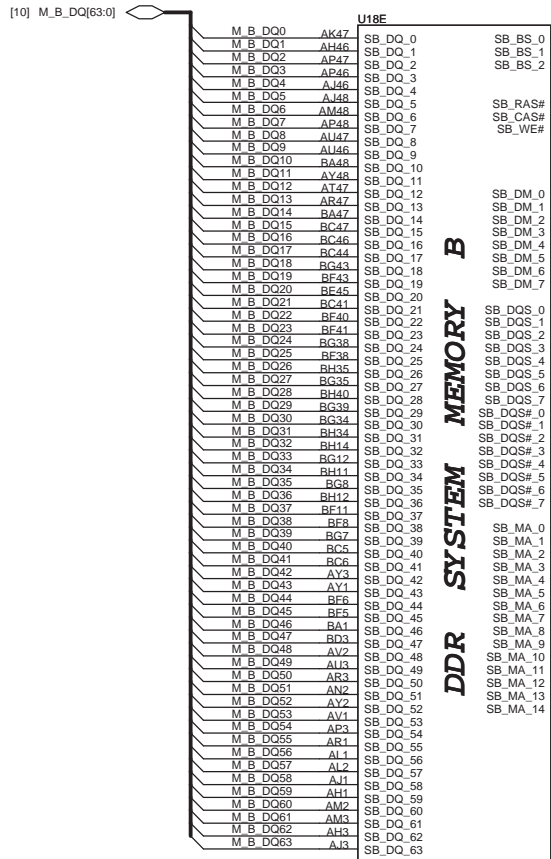
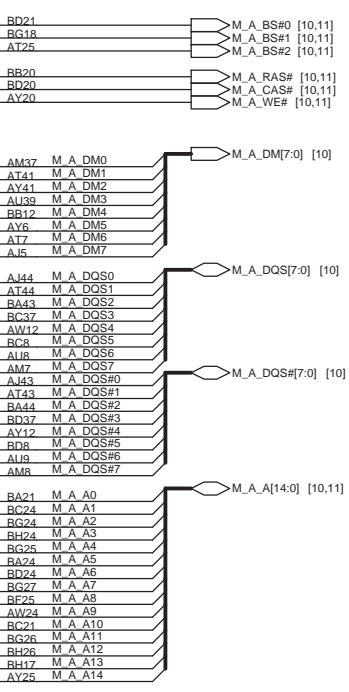
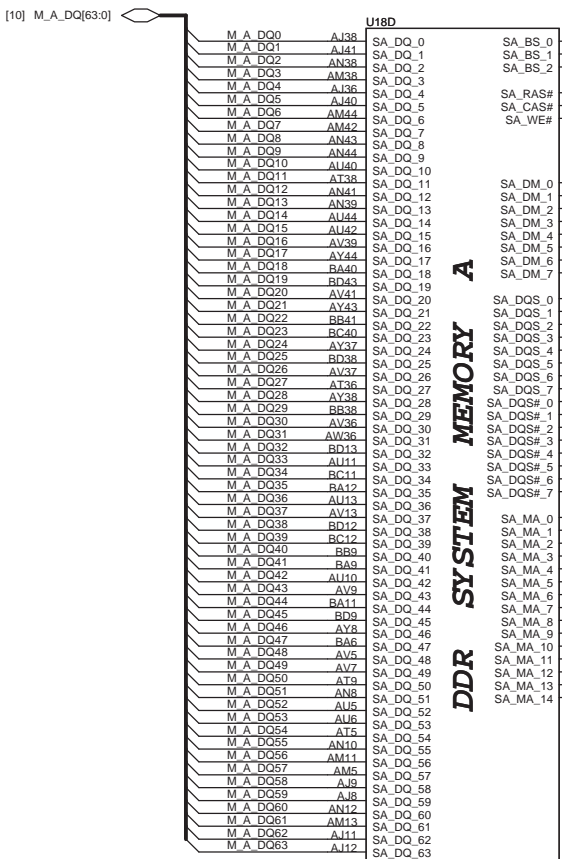
- Low: DMIx2
- High: DMIx4 (Default)
- MCH_CFG_16 FSB Dynamic ODT
- Low: Dynamic ODT disabled
- High: Dynamic ODT enabled (Default)
- MCH_CFG_9 PCI Express Graphic Lane
- Low: Reverse Lane
- High: Normal operation(Default)
- MCH_CFG_19 DMI Lane Reversal
- Low: Normal (Default)
- High: Lane Reserved
- MCH_CFG_6 iTPM Host Interface
- Low: iTPM Host Interface enabled
- High: iTPM Host Interface disabled (Default)
- MCH_CFG_7 Intel (R) Management Engine Crypto
- Low: Intel (R) Management Engine Crypto
- High: Intel (R) Management Engine Crypto
- Low: Enabled
- High: Disabled (Default)
- MCH_CFG_12/13 XOR/ALLZ/CLOCK Un-gating
- MCH_CFG_13 MCH_CFG_12 Configuration

- | | | |
|---|---|----------------------------|
| 0 | 0 | Reserved |
| 1 | 0 | XOR Mode enabled |
| 0 | 1 | All-Z Mode enabled |
| 1 | 1 | Normal operation (Default) |



LOGIC-CANTIGA



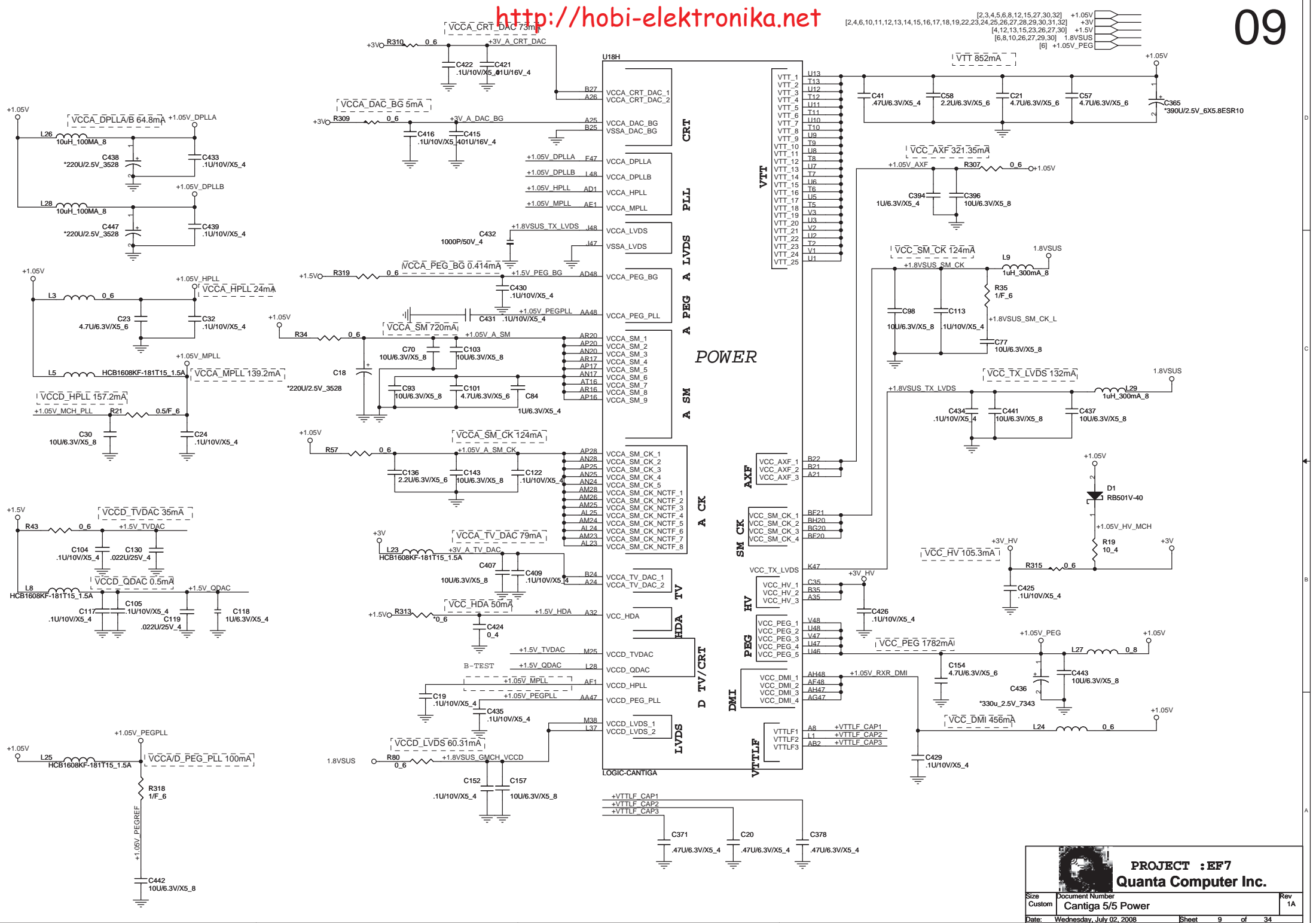


LOGIC-CANTIGA

LOGIC-CANTIGA

PROJECT : EF7
Quanta Computer Inc.

Size Custom Document Number Cantiga 3/5 DDR2 Rev 1A
 Date: Wednesday, July 02, 2008 Sheet 7 of 34

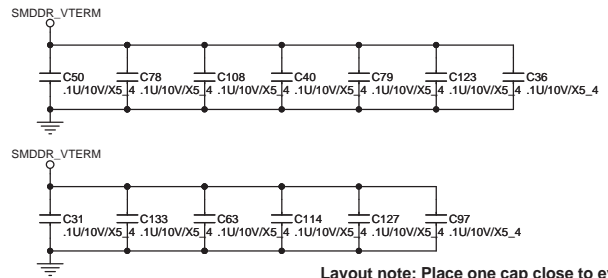


PROJECT : EF7
Quanta Computer Inc.

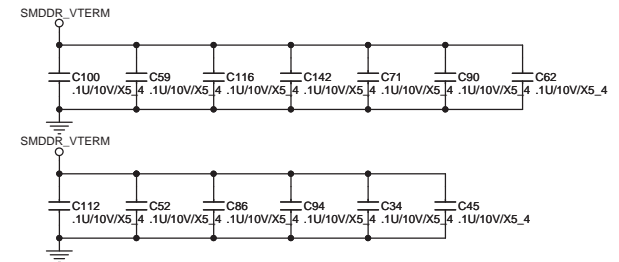
Size Custom Document Number Cantiga 5/5 Power Rev 1A
 Date: Wednesday, July 02, 2008 Sheet 9 of 34

DDR II DUAL CHANNEL A, B.

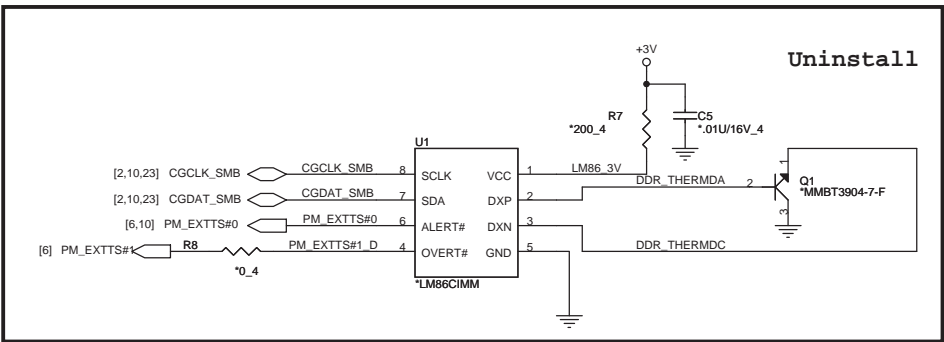
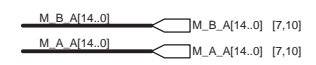
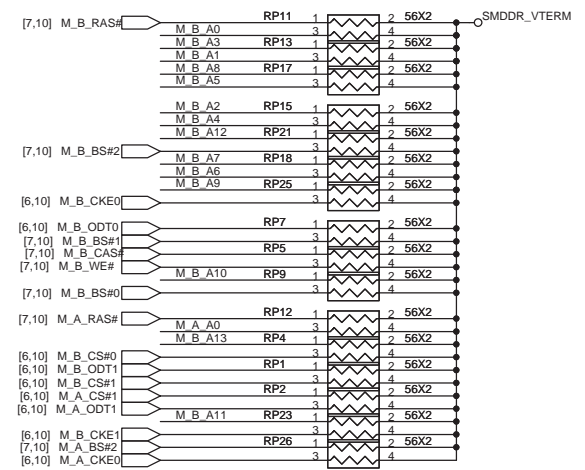
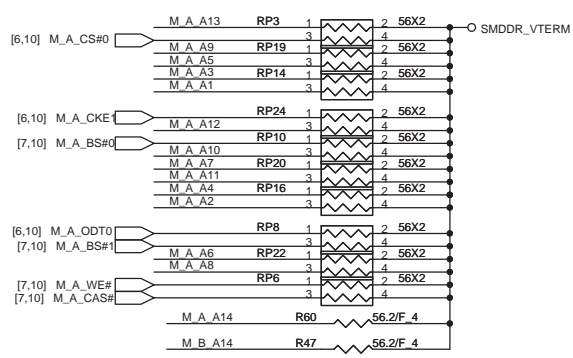
DDR II A CHANNEL

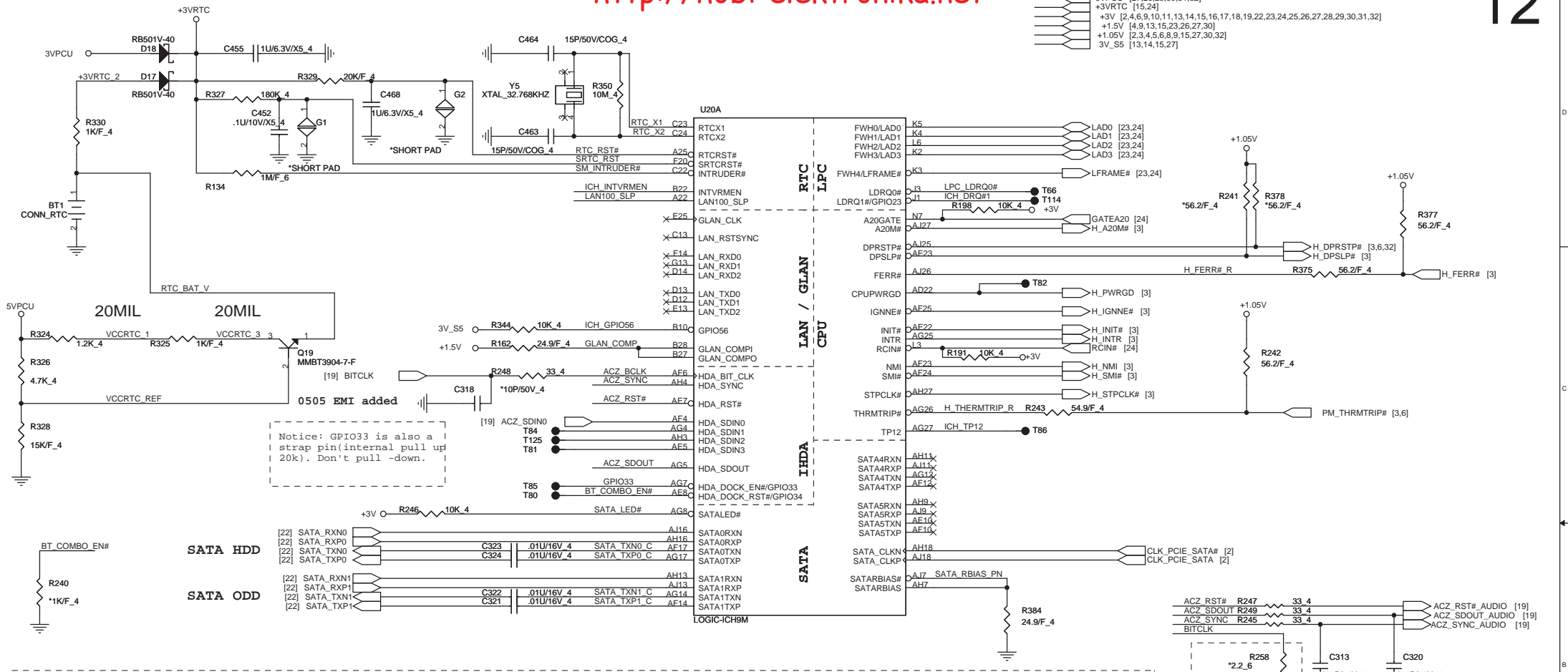


DDR II B CHANNEL



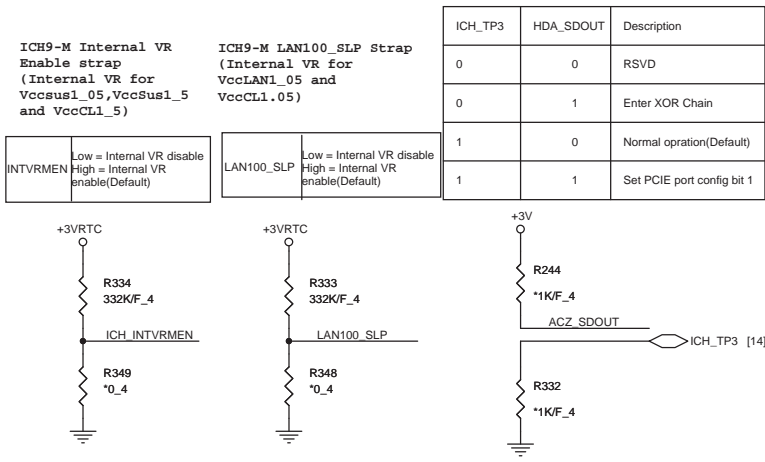
Layout note: Place one cap close to every 2 pullup resistors terminated to SMDDR_VTERM





Notice: GPIO33 is also a strap pin (internal pull up 20k). Don't pull -down.

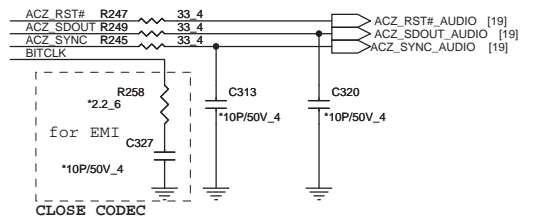
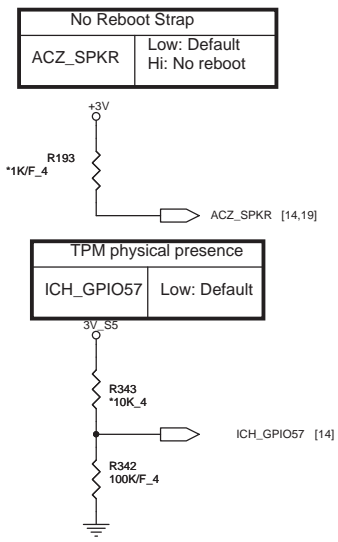
SB Strap

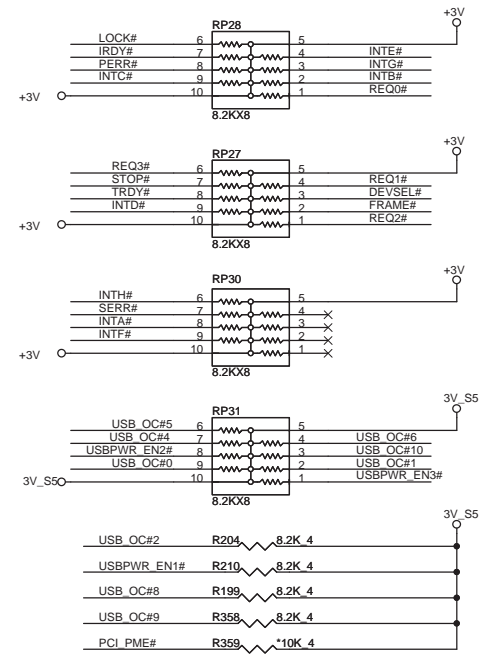
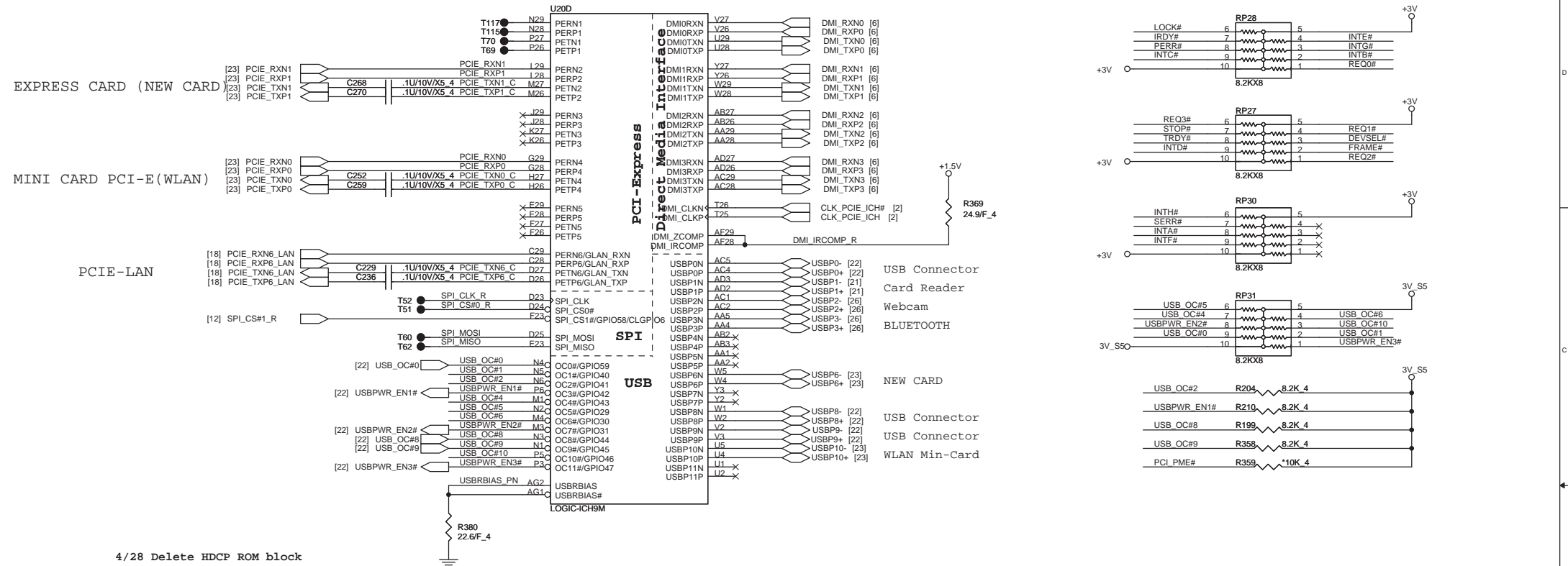


ICH9 Boot BIOS select

STRAP	PCI_GNT0#	SPL_CS#1
SPI	0	1
PCI	1	0
LPC	1	1

(default)

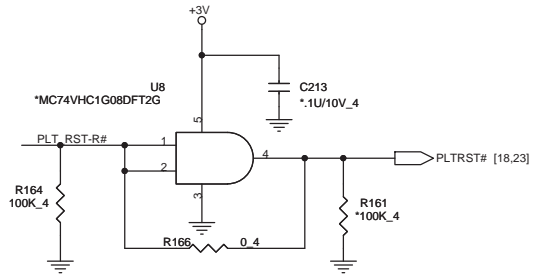




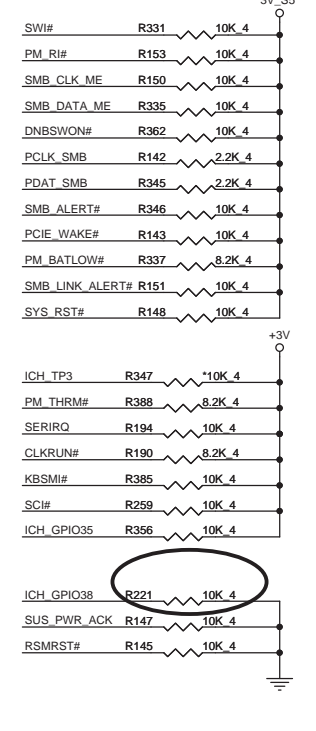
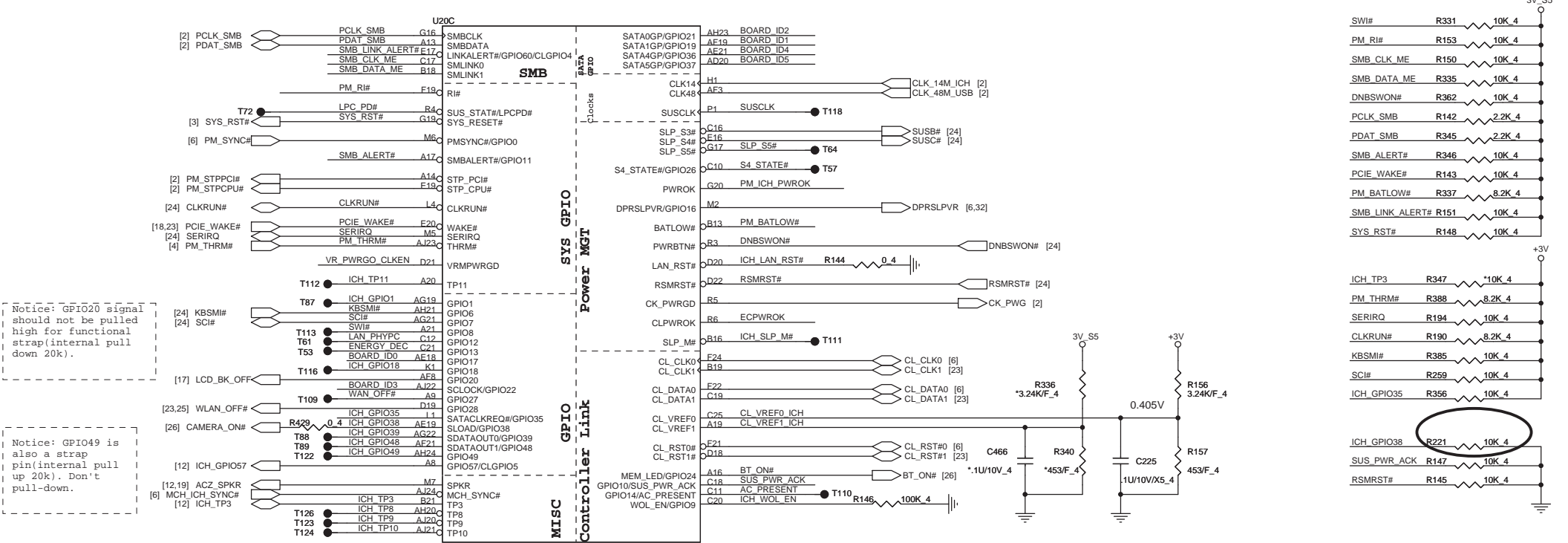
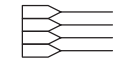
Notice: GPIO55,53,51 signal has a weak internal pull-up to 20k for functional strap. Don't pull-down.

PCI DEVICES IRQ ROUTING

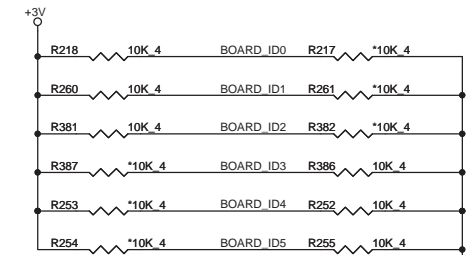
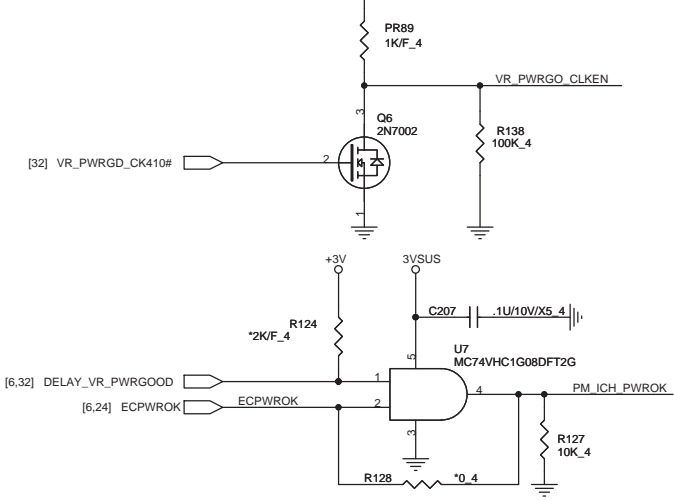
DEVICE	IDSEL #	REQ/GNT #	PCI_INT
CardBus/1394 /Card Reader	AD21	0	E,F



[4,9,12,13,15,23,26,27,30] +1.5V
 [2,4,6,9,10,11,12,13,15,16,17,18,19,22,23,24,25,26,27,28,29,30,31,32] +3V
 [12,13,15,27] 3V_S5
 [21,23,26,27,32] 3VSUS



0616 Connect GPIO38 R429 to CAMERA power ON in Ver.B



Board ID For Function	0	1
ID0, GPIO17	Camera attached	Camera NC<Default>
ID1, GPIO19	UXGA support	XGA support<Default>
ID2, GPIO21	EF7 Keyboard	EF9 Keyboard<Default>
ID3	Default	
ID4	Default	
ID5	Default	

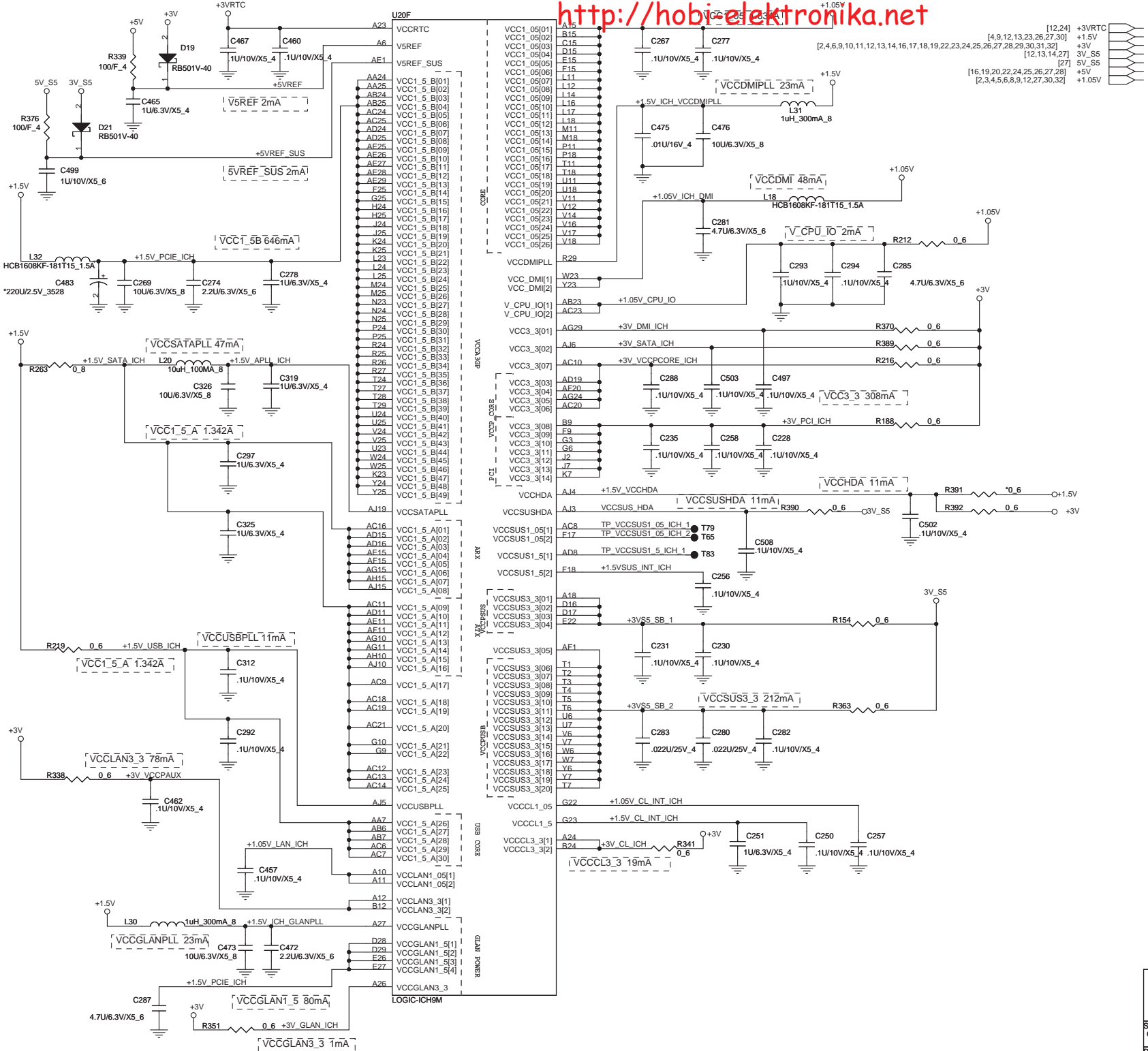
EC GPE2 Pin83 EF7 Keyboard EF9 Keyboard

- Camera HW detection
- XGA/UXGA selection
- EF7/EF9 keyboard selection

0609 Define ID for Ver.B
 0626 Modify ID2 for leakage concern in Ver.B

PROJECT : EF7
Quanta Computer Inc.

Size Custom Document Number ICH9-M 3/4 GPIO Rev 2A
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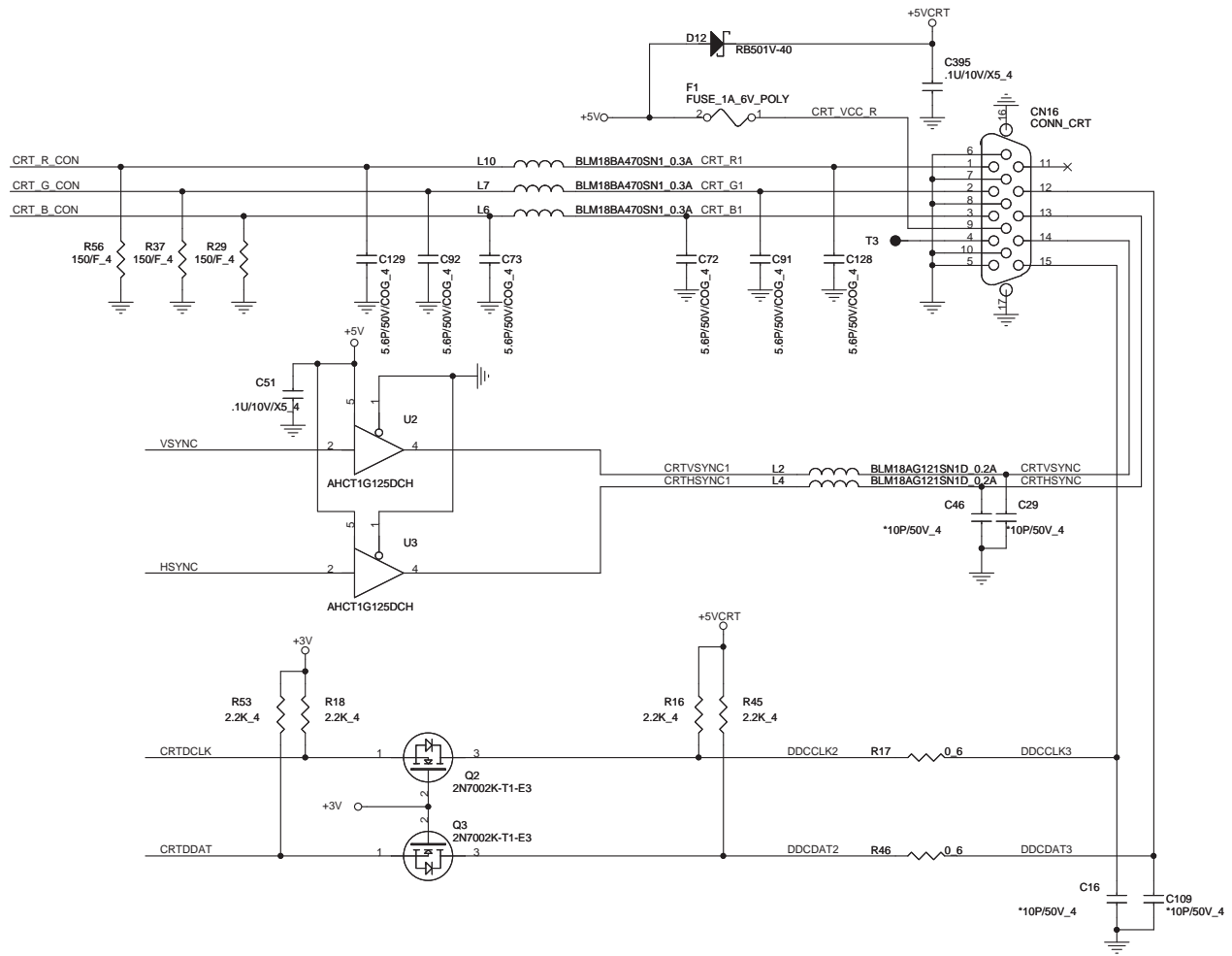
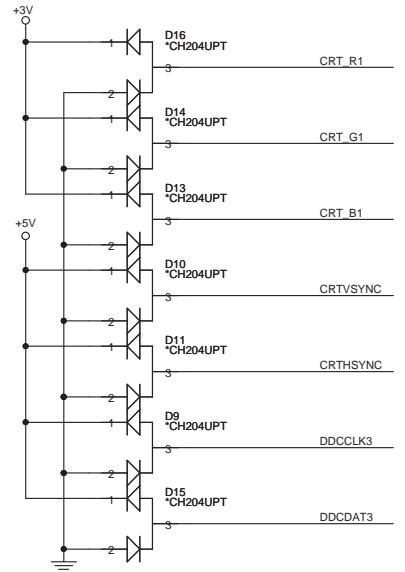


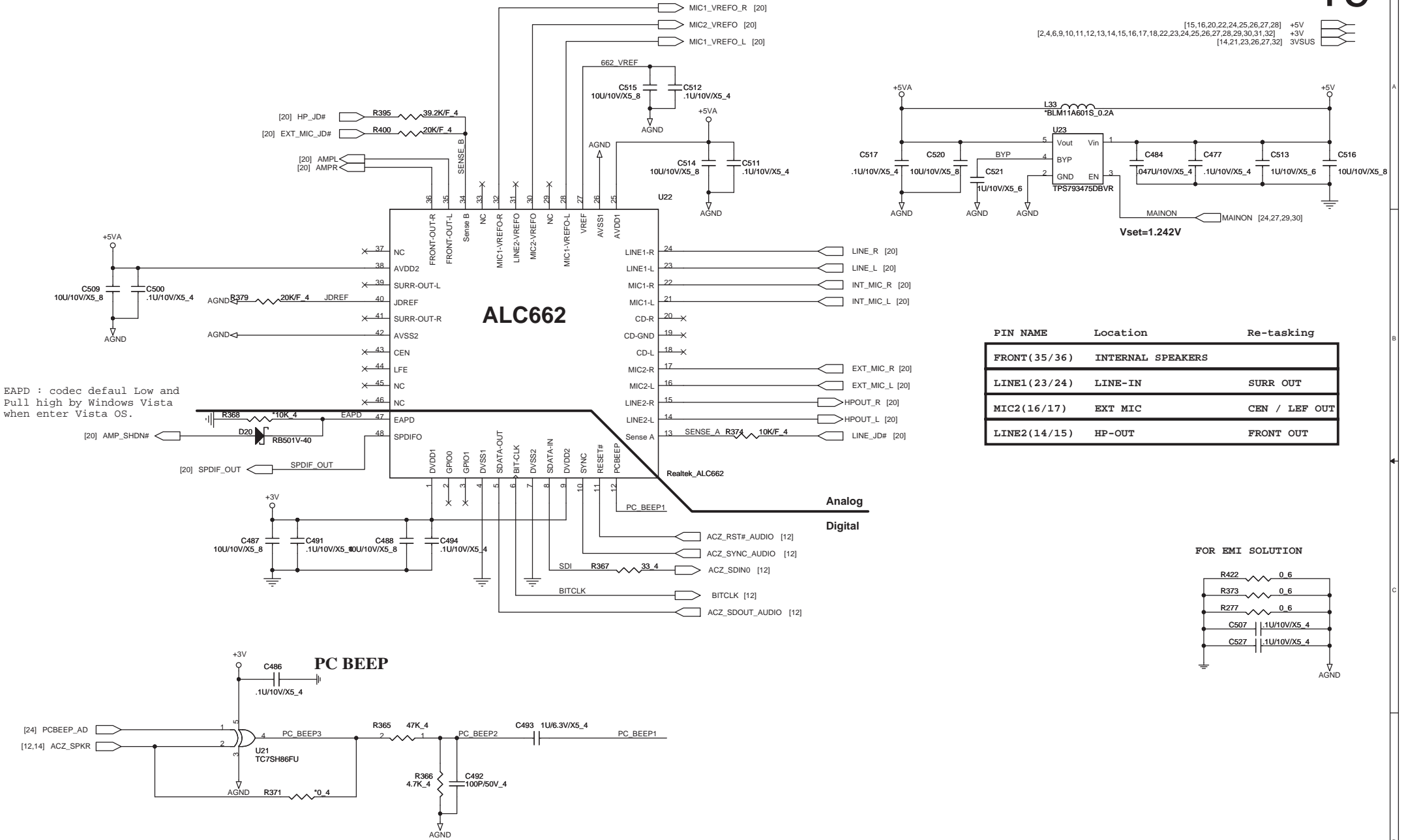
U20E	VSS	H5
AA26	VSS[001]	H5
AA27	VSS[002]	J23
AA3	VSS[003]	J26
AA6	VSS[004]	VSS[100]
AB1	VSS[005]	VSS[110]
AB2	VSS[006]	VSS[111]
AB3	VSS[007]	VSS[112]
AB4	VSS[008]	VSS[113]
AB5	VSS[009]	VSS[114]
AB6	VSS[010]	VSS[115]
AB7	VSS[011]	VSS[116]
AB8	VSS[012]	VSS[117]
AB9	VSS[013]	VSS[118]
AB10	VSS[014]	VSS[119]
AB11	VSS[015]	VSS[120]
AB12	VSS[016]	VSS[121]
AB13	VSS[017]	VSS[122]
AB14	VSS[018]	VSS[123]
AB15	VSS[019]	VSS[124]
AB16	VSS[020]	VSS[125]
AB17	VSS[021]	VSS[126]
AB18	VSS[022]	VSS[127]
AB19	VSS[023]	VSS[128]
AB20	VSS[024]	VSS[129]
AB21	VSS[025]	VSS[130]
AB22	VSS[026]	VSS[131]
AB23	VSS[027]	VSS[132]
AB24	VSS[028]	VSS[133]
AB25	VSS[029]	VSS[134]
AB26	VSS[030]	VSS[135]
AB27	VSS[031]	VSS[136]
AB28	VSS[032]	VSS[137]
AB29	VSS[033]	VSS[138]
AB30	VSS[034]	VSS[139]
AB31	VSS[035]	VSS[140]
AB32	VSS[036]	VSS[141]
AB33	VSS[037]	VSS[142]
AB34	VSS[038]	VSS[143]
AB35	VSS[039]	VSS[144]
AB36	VSS[040]	VSS[145]
AB37	VSS[041]	VSS[146]
AB38	VSS[042]	VSS[147]
AB39	VSS[043]	VSS[148]
AB40	VSS[044]	VSS[149]
AB41	VSS[045]	VSS[150]
AB42	VSS[046]	VSS[151]
AB43	VSS[047]	VSS[152]
AB44	VSS[048]	VSS[153]
AB45	VSS[049]	VSS[154]
AB46	VSS[050]	VSS[155]
AB47	VSS[051]	VSS[156]
AB48	VSS[052]	VSS[157]
AB49	VSS[053]	VSS[158]
AB50	VSS[054]	VSS[159]
AB51	VSS[055]	VSS[160]
AB52	VSS[056]	VSS[161]
AB53	VSS[057]	VSS[162]
AB54	VSS[058]	VSS[163]
AB55	VSS[059]	VSS[164]
AB56	VSS[060]	VSS[165]
AB57	VSS[061]	VSS[166]
AB58	VSS[062]	VSS[167]
AB59	VSS[063]	VSS[168]
AB60	VSS[064]	VSS[169]
AB61	VSS[065]	VSS[170]
AB62	VSS[066]	VSS[171]
AB63	VSS[067]	VSS[172]
AB64	VSS[068]	VSS[173]
AB65	VSS[069]	VSS[174]
AB66	VSS[070]	VSS[175]
AB67	VSS[071]	VSS[176]
AB68	VSS[072]	VSS[177]
AB69	VSS[073]	VSS[178]
AB70	VSS[074]	VSS[179]
AB71	VSS[075]	VSS[180]
AB72	VSS[076]	VSS[181]
AB73	VSS[077]	VSS[182]
AB74	VSS[078]	VSS[183]
AB75	VSS[079]	VSS[184]
AB76	VSS[080]	VSS[185]
AB77	VSS[081]	VSS[186]
AB78	VSS[082]	VSS[187]
AB79	VSS[083]	VSS[188]
AB80	VSS[084]	VSS[189]
AB81	VSS[085]	VSS[190]
AB82	VSS[086]	VSS[191]
AB83	VSS[087]	VSS[192]
AB84	VSS[088]	VSS[193]
AB85	VSS[089]	VSS[194]
AB86	VSS[090]	VSS[195]
AB87	VSS[091]	VSS[196]
AB88	VSS[092]	VSS[197]
AB89	VSS[093]	VSS[198]
AB90	VSS[094]	VSS[199]
AB91	VSS[095]	VSS[200]
AB92	VSS[096]	VSS[201]
AB93	VSS[097]	VSS[202]
AB94	VSS[098]	VSS[203]
AB95	VSS[099]	VSS[204]
AB96	VSS[100]	VSS[205]
AB97	VSS[101]	VSS[206]
AB98	VSS[102]	VSS[207]
AB99	VSS[103]	VSS[208]
AB100	VSS[104]	VSS[209]
AB101	VSS[105]	VSS[210]
AB102	VSS[106]	VSS[211]
AB103	VSS[107]	VSS[212]
AB104	VSS[108]	VSS[213]
AB105	VSS[109]	VSS[214]
AB106	VSS[110]	VSS[215]
AB107	VSS[111]	VSS[216]
AB108	VSS[112]	VSS[217]
AB109	VSS[113]	VSS[218]
AB110	VSS[114]	VSS[219]
AB111	VSS[115]	VSS[220]
AB112	VSS[116]	VSS[221]
AB113	VSS[117]	VSS[222]
AB114	VSS[118]	VSS[223]
AB115	VSS[119]	VSS[224]
AB116	VSS[120]	VSS[225]
AB117	VSS[121]	VSS[226]
AB118	VSS[122]	VSS[227]
AB119	VSS[123]	VSS[228]
AB120	VSS[124]	VSS[229]
AB121	VSS[125]	VSS[230]
AB122	VSS[126]	VSS[231]
AB123	VSS[127]	VSS[232]
AB124	VSS[128]	VSS[233]
AB125	VSS[129]	VSS[234]
AB126	VSS[130]	VSS[235]
AB127	VSS[131]	VSS[236]
AB128	VSS[132]	VSS[237]
AB129	VSS[133]	VSS[238]
AB130	VSS[134]	VSS[239]
AB131	VSS[135]	VSS[240]
AB132	VSS[136]	VSS[241]
AB133	VSS[137]	VSS[242]
AB134	VSS[138]	VSS[243]
AB135	VSS[139]	VSS[244]
AB136	VSS[140]	VSS[245]
AB137	VSS[141]	VSS[246]
AB138	VSS[142]	VSS[247]
AB139	VSS[143]	VSS[248]
AB140	VSS[144]	VSS[249]
AB141	VSS[145]	VSS[250]
AB142	VSS[146]	VSS[251]
AB143	VSS[147]	VSS[252]
AB144	VSS[148]	VSS[253]
AB145	VSS[149]	VSS[254]
AB146	VSS[150]	VSS[255]
AB147	VSS[151]	VSS[256]
AB148	VSS[152]	VSS[257]
AB149	VSS[153]	VSS[258]
AB150	VSS[154]	VSS[259]
AB151	VSS[155]	VSS[260]
AB152	VSS[156]	VSS[261]
AB153	VSS[157]	VSS[262]
AB154	VSS[158]	VSS[263]
AB155	VSS[159]	VSS[264]
AB156	VSS[160]	VSS[265]
AB157	VSS[161]	VSS[266]
AB158	VSS[162]	VSS[267]
AB159	VSS[163]	VSS[268]
AB160	VSS[164]	VSS[269]
AB161	VSS[165]	VSS[270]
AB162	VSS[166]	VSS[271]
AB163	VSS[167]	VSS[272]
AB164	VSS[168]	VSS[273]
AB165	VSS[169]	VSS[274]
AB166	VSS[170]	VSS[275]
AB167	VSS[171]	VSS[276]
AB168	VSS[172]	VSS[277]
AB169	VSS[173]	VSS[278]
AB170	VSS[174]	VSS[279]
AB171	VSS[175]	VSS[280]
AB172	VSS[176]	VSS[281]
AB173	VSS[177]	VSS[282]
AB174	VSS[178]	VSS[283]
AB175	VSS[179]	VSS[284]
AB176	VSS[180]	VSS[285]
AB177	VSS[181]	VSS[286]
AB178	VSS[182]	VSS[287]
AB179	VSS[183]	VSS[288]
AB180	VSS[184]	VSS[289]
AB181	VSS[185]	VSS[290]
AB182	VSS[186]	VSS[291]
AB183	VSS[187]	VSS[292]
AB184	VSS[188]	VSS[293]
AB185	VSS[189]	VSS[294]
AB186	VSS[190]	VSS[295]
AB187	VSS[191]	VSS[296]
AB188	VSS[192]	VSS[297]
AB189	VSS[193]	VSS[298]
AB190	VSS[194]	VSS[299]
AB191	VSS[195]	VSS[300]
AB192	VSS[196]	VSS[301]
AB193	VSS[197]	VSS[302]
AB194	VSS[198]	VSS[303]
AB195	VSS[199]	VSS[304]
AB196	VSS[200]	VSS[305]
AB197	VSS[201]	VSS[306]
AB198	VSS[202]	VSS[307]
AB199	VSS[203]	VSS[308]
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AB204	VSS[208]	VSS[313]
AB205	VSS[209]	VSS[314]
AB206	VSS[210]	VSS[315]
AB207	VSS[211]	VSS[316]
AB208	VSS[212]	VSS[317]
AB209	VSS[213]	VSS[318]
AB210	VSS[214]	VSS[319]
AB211	VSS[215]	VSS[320]
AB212	VSS[216]	VSS[321]
AB213	VSS[217]	VSS[322]
AB214	VSS[218]	VSS[323]
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AB216	VSS[220]	VSS[325]
AB217	VSS[221]	VSS[326]
AB218	VSS[222]	VSS[327]
AB219	VSS[223]	VSS[328]
AB220	VSS[224]	VSS[329]
AB221	VSS[225]	VSS[330]
AB222	VSS[226]	VSS[331]
AB223	VSS[227]	VSS[332]
AB224	VSS[228]	VSS[333]
AB225	VSS[229]	VSS[334]
AB226	VSS[230]	VSS[335]
AB227	VSS[231]	VSS[336]
AB228	VSS[232]	VSS[337]
AB229	VSS[233]	VSS[338]
AB230	VSS[234]	VSS[339]
AB231	VSS[235]	VSS[340]
AB232	VSS[236]	VSS[341]
AB233	VSS[237]	VSS[342]
AB234	VSS[238]	VSS[343]
AB235	VSS[239]	VSS[344]
AB236	VSS[240]	VSS[345]
AB237	VSS[241]	VSS[346]
AB238	VSS[242]	VSS[347]
AB239	VSS[243]	VSS[348]
AB240	VSS[244]	VSS[349]
AB241	VSS[245]	VSS[350]
AB242	VSS[24	

CRT PORT

- [6] CRT_R_CON CRT_R_CON
- [6] CRT_G_CON CRT_G_CON
- [6] CRT_B_CON CRT_B_CON
- [6] HSYNC HSYNC
- [6] VSYNC VSYNC
- [6] CRTDCLK CRTDCLK
- [6] CRTDDAT CRTDDAT

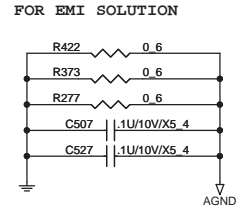
del VGA CRT
Mika 2008/04/10






EAPD : codec default Low and Pull high by Windows Vista when enter Vista OS.

PIN NAME	Location	Re-tasking
FRONT (35/36)	INTERNAL SPEAKERS	
LINE1 (23/24)	LINE-IN	SURR OUT
MIC2 (16/17)	EXT MIC	CEN / LEF OUT
LINE2 (14/15)	HP-OUT	FRONT OUT



0627 Change U21 GND to AGND in Ver.B



PROJECT : EF7
Quanta Computer Inc.

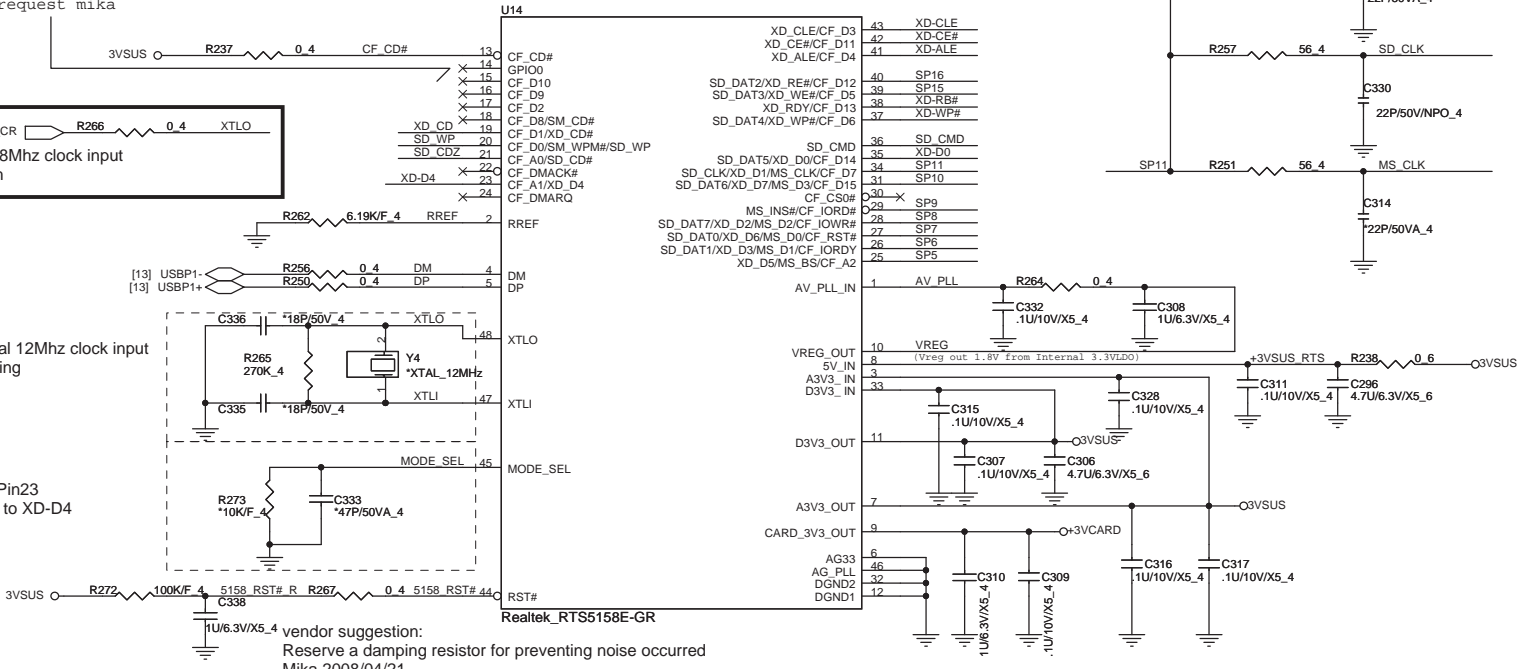
Size Custom	Document Number AUDIO-CODEC	Rev 2A
Date: Wednesday, July 02, 2008	Sheet 19 of 34	

Fix card reader led problem
check LED request mika

[2] CLK_48M_USB_CR
For external 48Mhz clock input
pin13 pull high

For external 12Mhz clock input
pin13 floating

Left NC if Pin23
connected to XD-D4

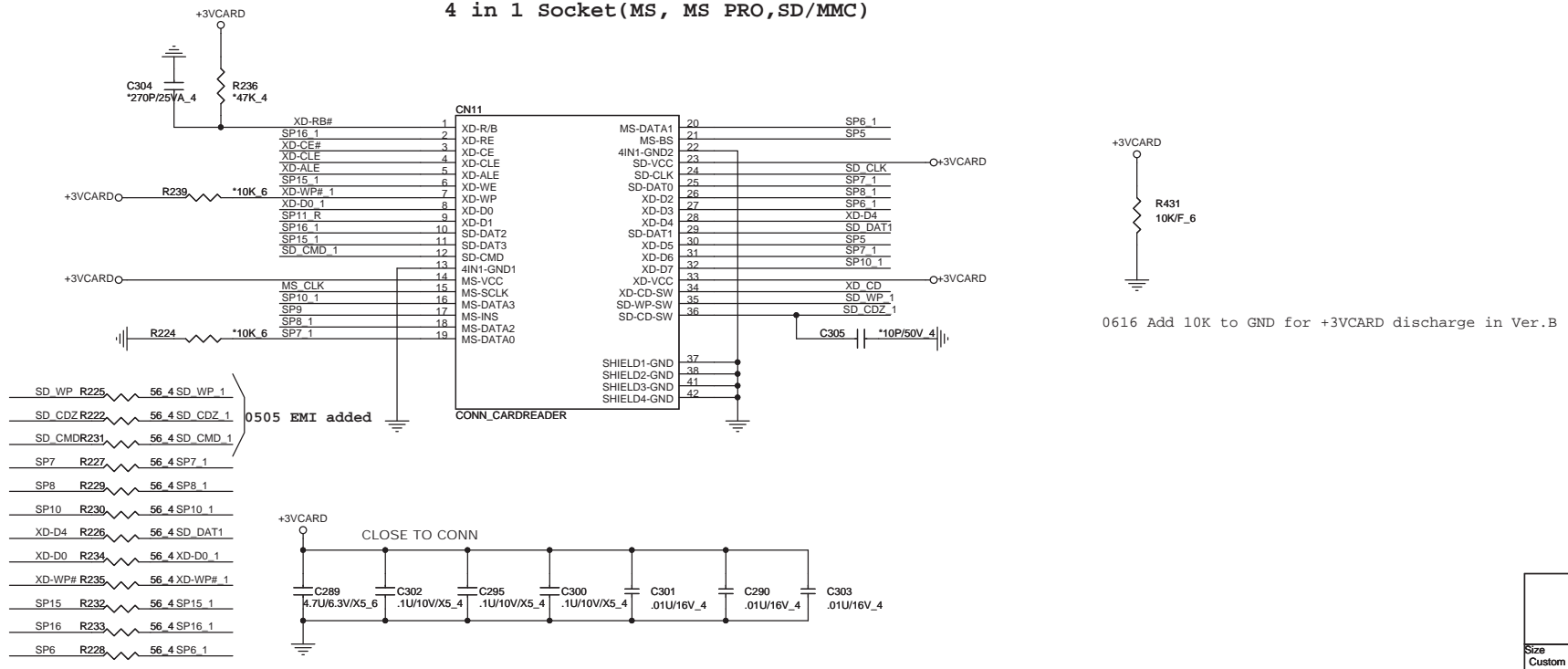


1u/6.3V/X5_4 vendor suggestion:
Reserve a damping resistor for preventing noise occurred
Mika 2008/04/21

Note:

SP0	SD/MMC	MS	XD
SP1			XD_CD#
SP2	SD_WP		
SP3	SD_CD#		
SP4		XD D4	
SP5		MS BS	XD D5
SP6		MS D1	XD D3
SP7	SD DAT0	MS D0	XD D6
SP8	SD DAT7	MS D2	XD D2
SP9		MS INS#	
SP10	SD DAT6	MS D3	XD D7
SP11	SD CLK	MS SCLK	XD D1
SP12	SD DAT5		XD D0
SP13	SD DAT4		XD WP#
SP14			XD R/B#
SP15	SD DAT3		XD WE#
SP16	SD DAT2		XD RE#
SP17			XD ALE
SP18			XD CE#
SP19			XD CLE

4 in 1 Socket (MS, MS PRO, SD/MMC)

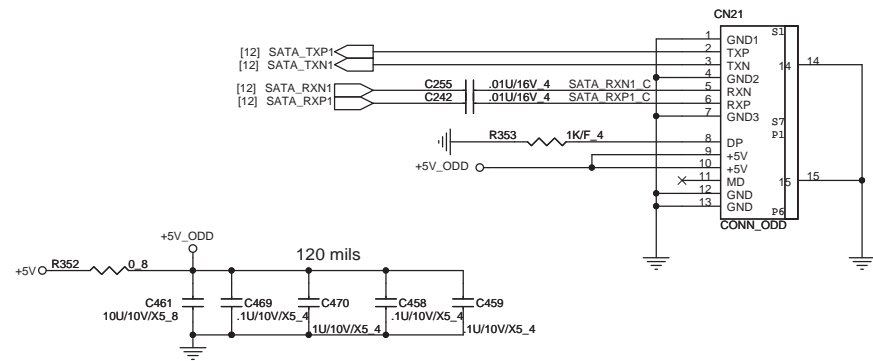


PROJECT : EF7
Quanta Computer Inc.

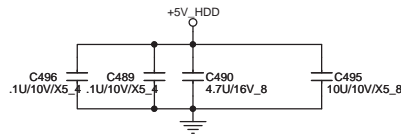
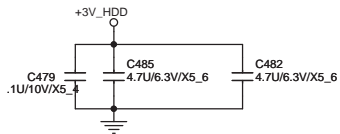
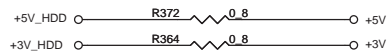
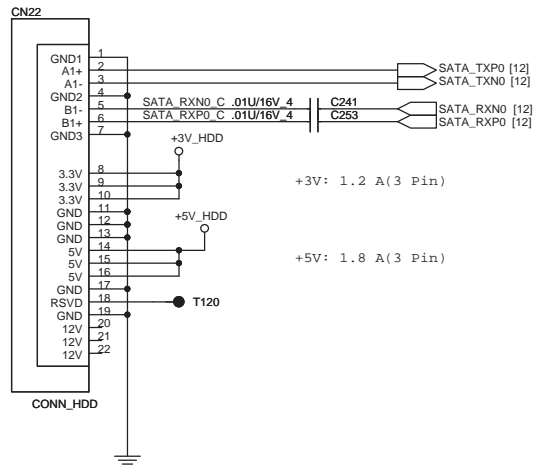
Size Custom Document Number Card Reader-RTS5158E Rev 2A

Date: Tuesday, July 01, 2008 Sheet 21 of 34

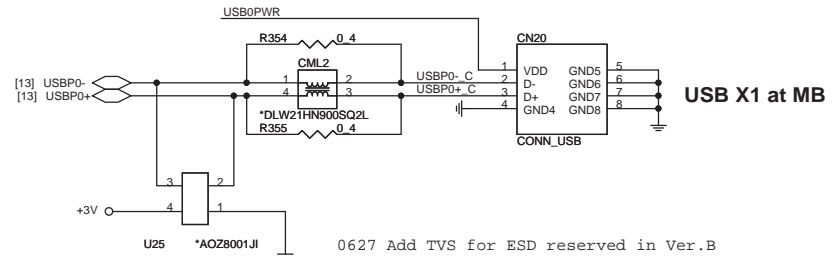
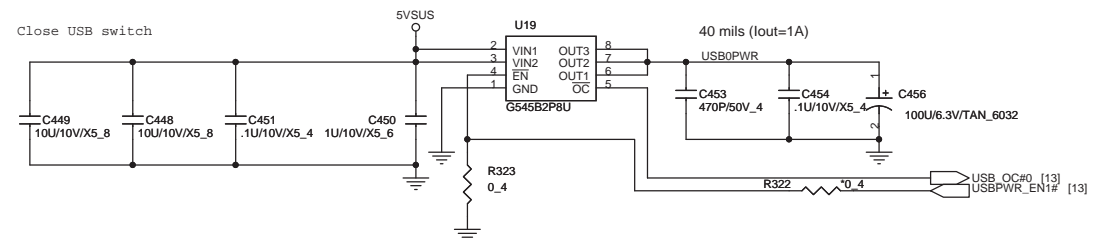
SATA CD-ROM



SATA-HDD CONNECTOR

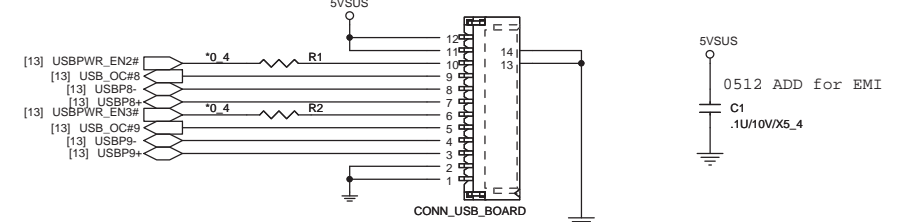


USB X1



0627 Add TVS for ESD reserved in Ver.B

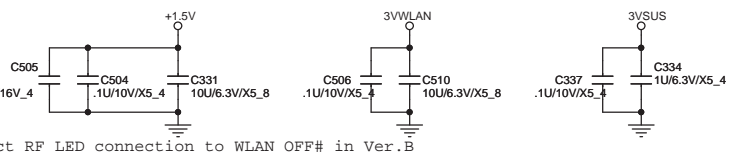
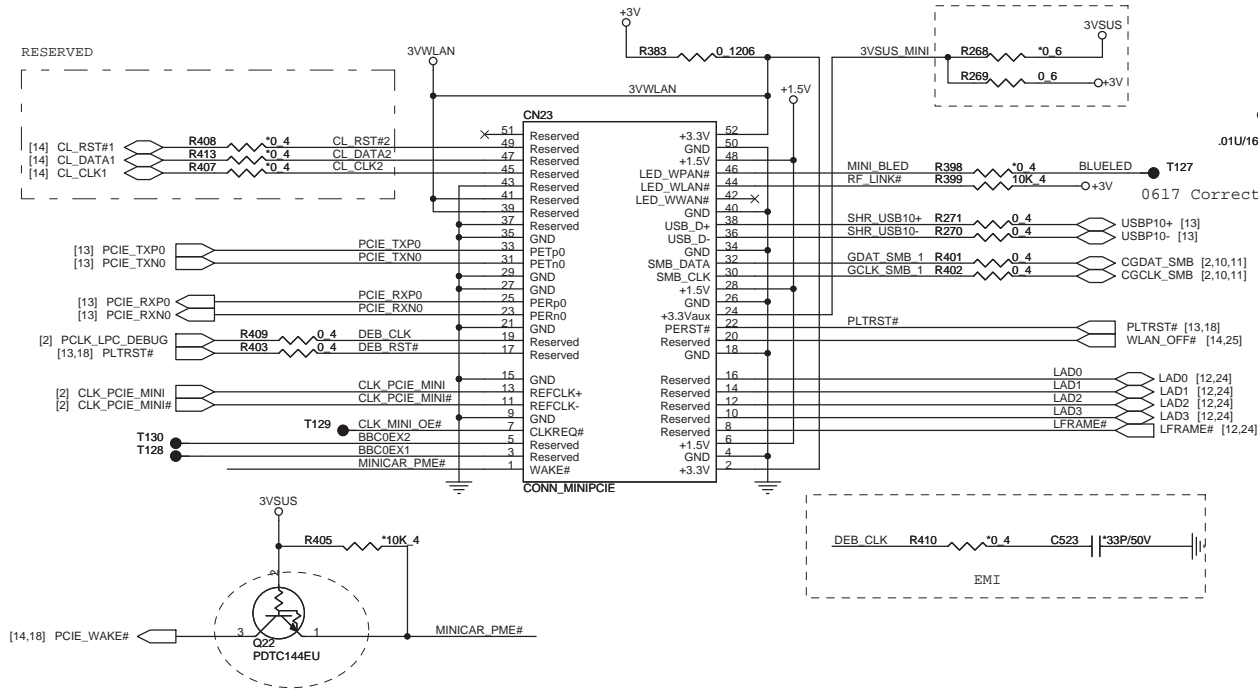
USB X2 at Daughter Board



0512 ADD for EMI

Mini PCI-E Card 1 WLAN

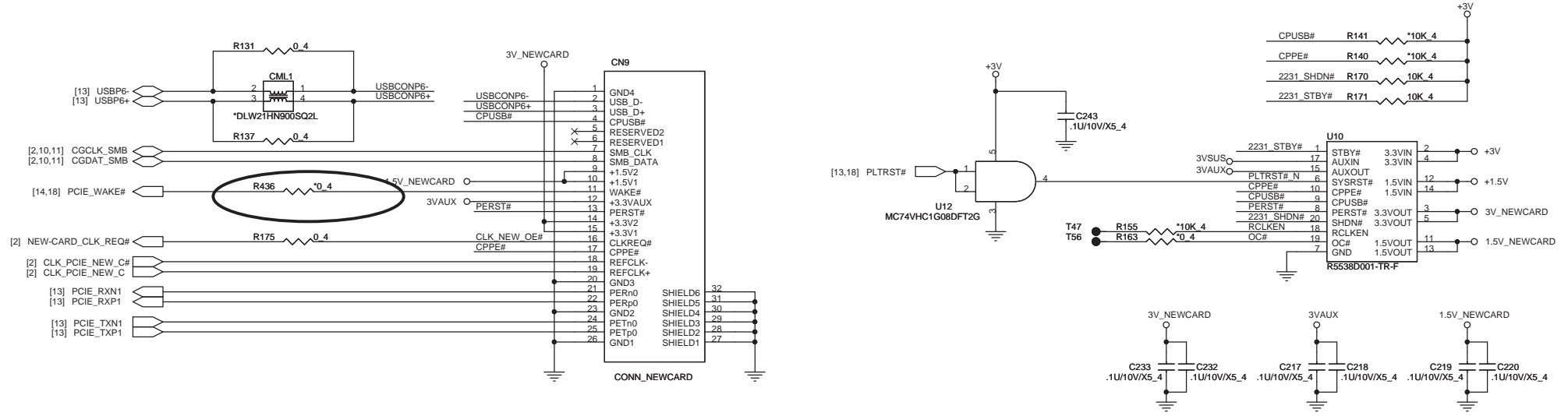
[2,4,6,9,10,11,12,13,14,15,16,17,18,19,22,24,25,26,27,28,29,30,31,32] [14,21,26,27,32] 3VSUS [4,9,12,13,15,26,27,30] +1.5V +3V



0617 Correct RF LED connection to WLAN_OFF# in Ver.B

INTEL WLAN CARD PIN 20 W_DISABLE# have internal pull-up 110k ohm

Express Card

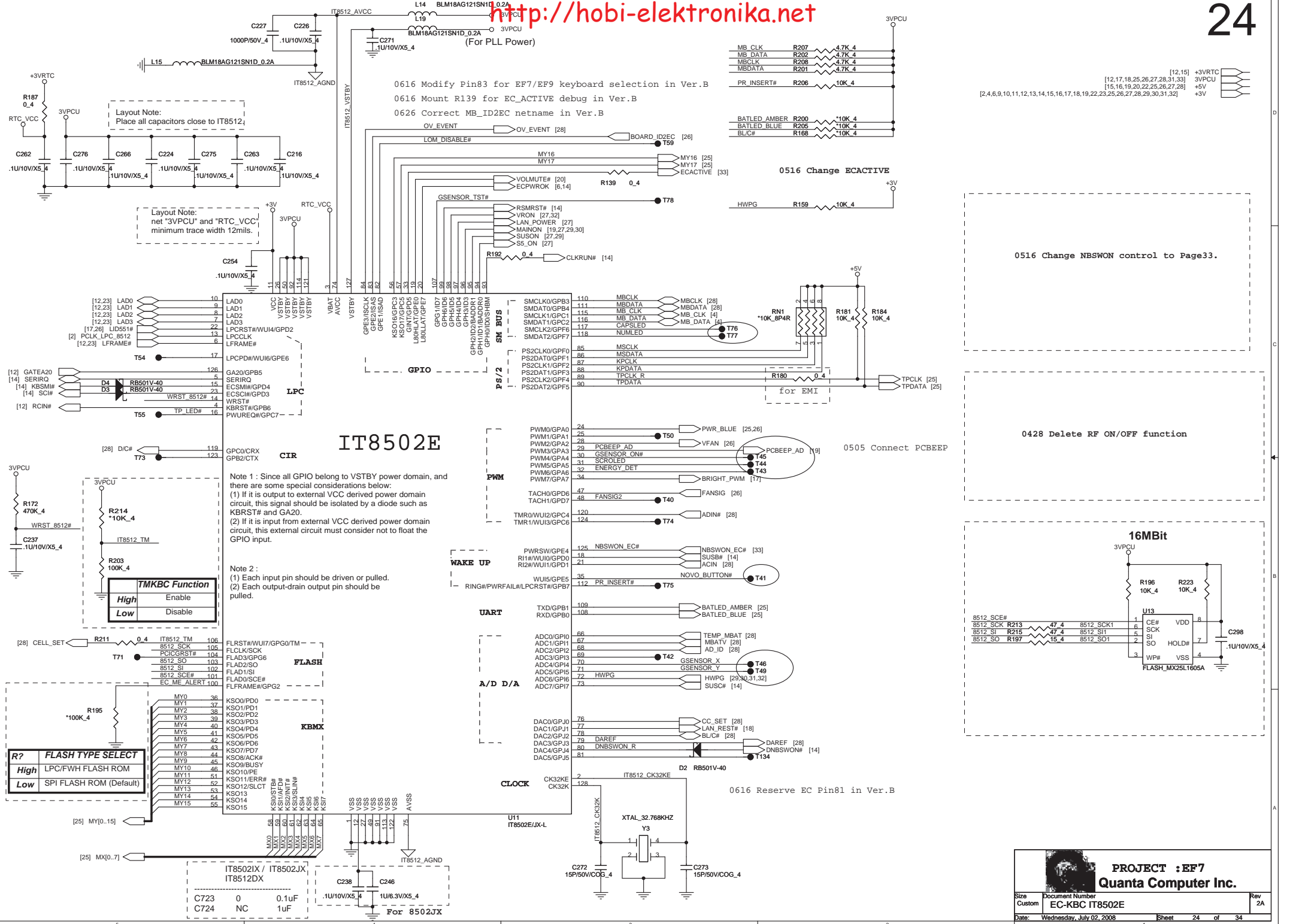


0627 Update Footprint in Ver.B
0702 Add R436 to Disconnect PCIE_WAKE# in Ver.B

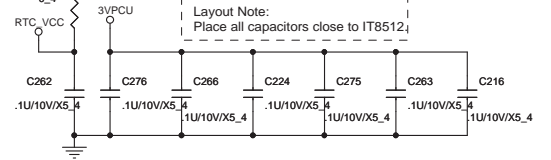
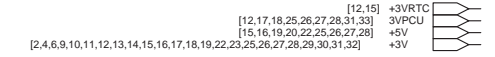
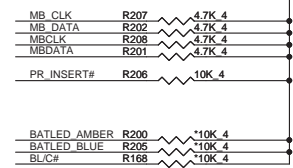
PROJECT : EF7
Quanta Computer Inc.

Size Custom Document Number Conn-Xpress Card Rev 2A

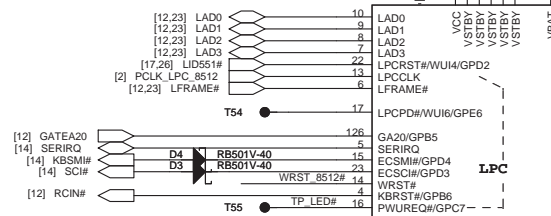
Date: Thursday, July 03, 2008 Sheet 23 of 34



0616 Modify Pin83 for EF7/EF9 keyboard selection in Ver.B
 0616 Mount R139 for EC_ACTIVE debug in Ver.B
 0626 Correct MB_ID2EC netname in Ver.B



Layout Note:
net "3VPCU" and "RTC_VCC"
minimum trace width 12mils.

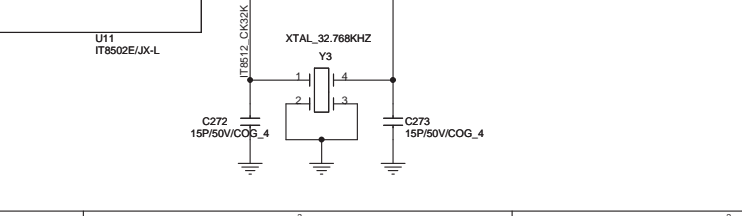
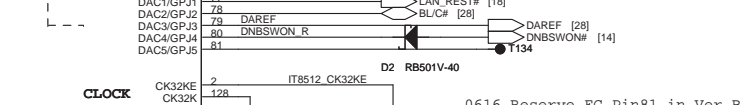
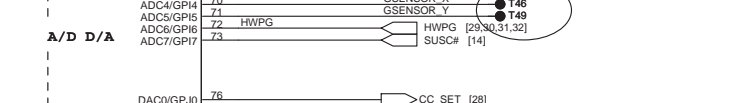
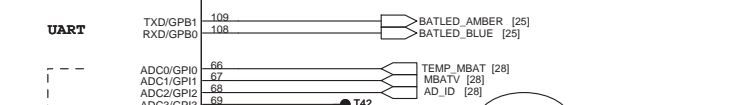
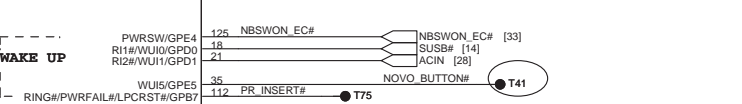
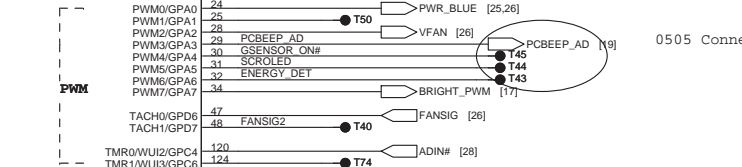
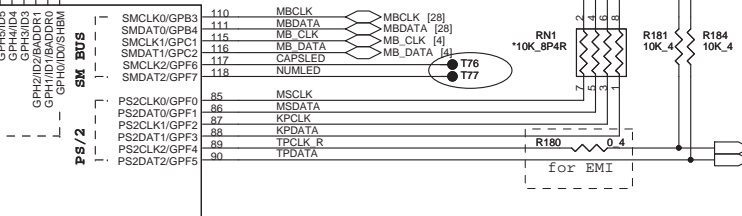
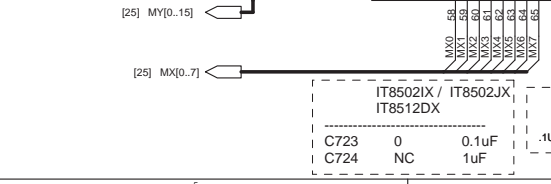
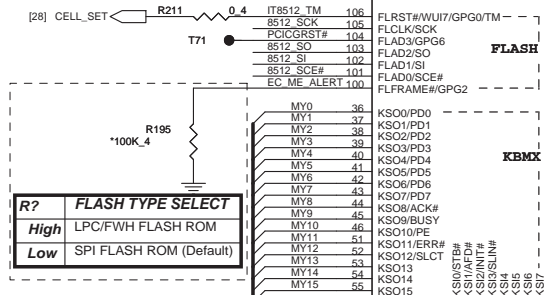


IT8502E

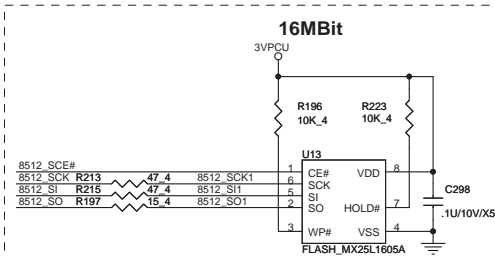
Note 1 : Since all GPIO belong to VSTBY power domain, and there are some special considerations below:
 (1) If it is output to external VCC derived power domain circuit, this signal should be isolated by a diode such as KBRST# and GA20.
 (2) If it is input from external VCC derived power domain circuit, this external circuit must consider not to float the GPIO input.

Note 2 :
 (1) Each input pin should be driven or pulled.
 (2) Each output-drain output pin should be pulled.

TMKBC Function	
High	Enable
Low	Disable



0516 Change ECACTIVE
 0516 Change NBSWON control to Page33.
 0428 Delete RF ON/OFF function



DUAL Layout for both 16" and 18.4"

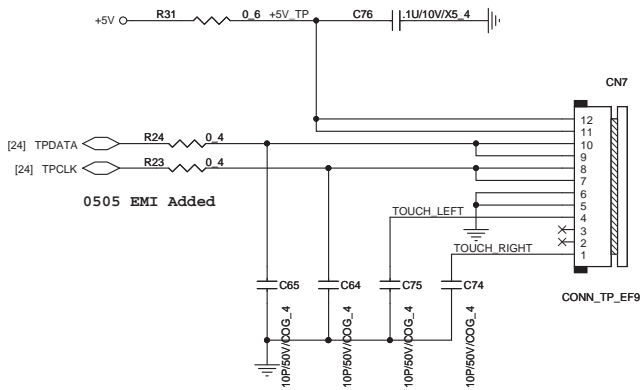
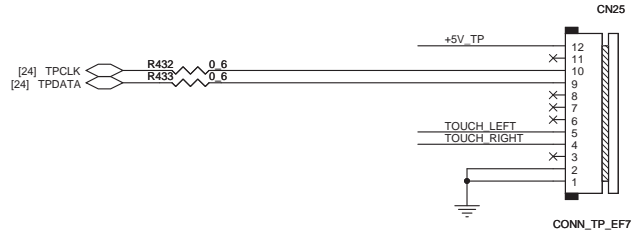
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[15,16,19,20,22,24,26,27,28]
[12,17,18,24,26,27,28,31,33]



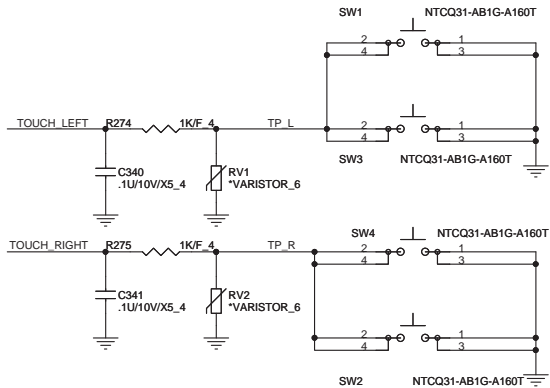
TOUCH PAD

0617 Add one more touch PAD connector for EF7 different pin definition.

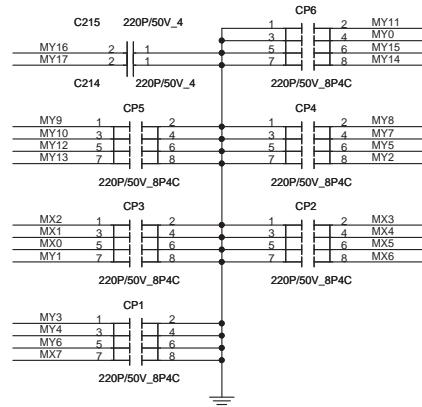
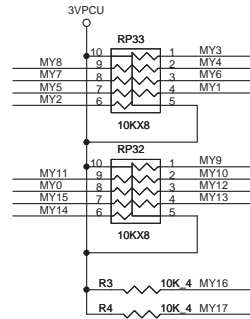
0625 Del C548/C549, Change R432/R433 to 0603 in Ver.B



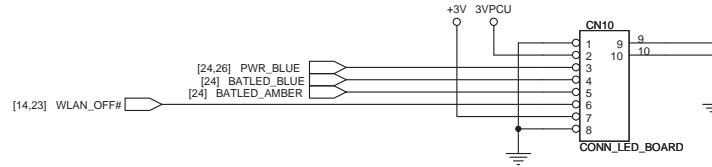
0505 EMI Added



DUAL Layout for both 16" and 18.4"
KEYBOARD

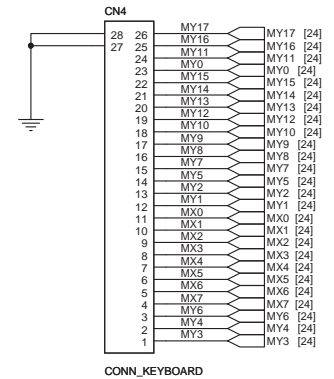


0514 Change CP6 to 2 0402 Capacitor
0514 Swap nets.



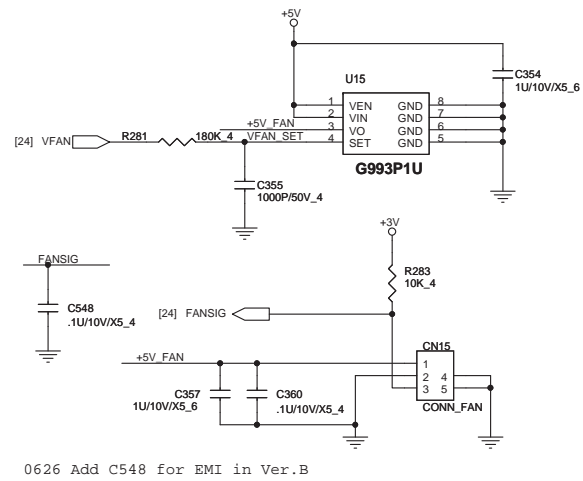
0617 Correct RF LED connection to WLAN_OFF# in Ver.B

0625 Delete CN1 in Ver.B

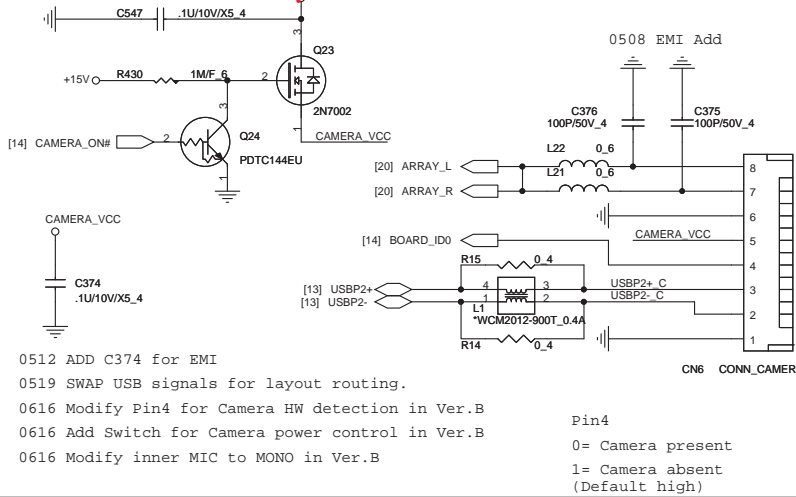


PROJECT : EF7
Quanta Computer Inc.
Size Custom Document Number Conn-KB/TB/LEDs Rev 2A
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FAN CONTROL

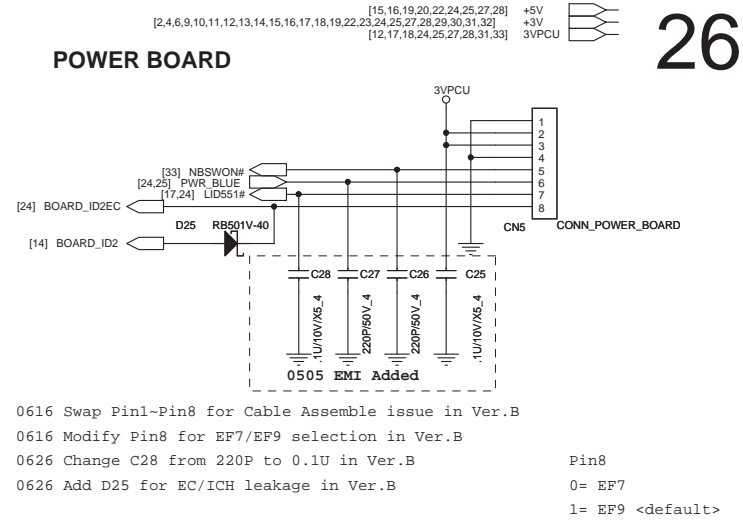


CAMERA Wire to Board

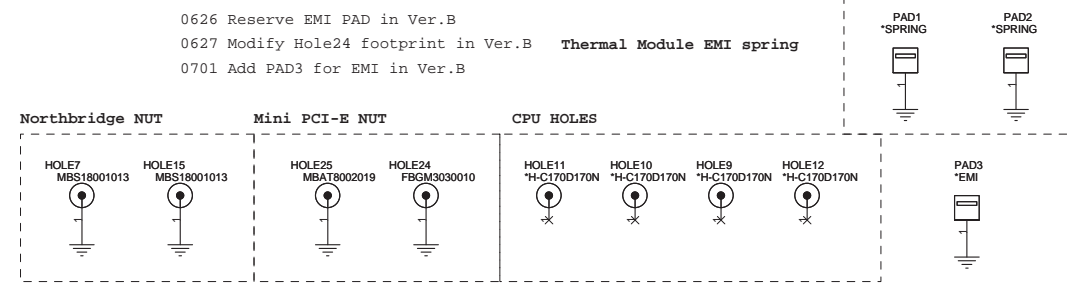
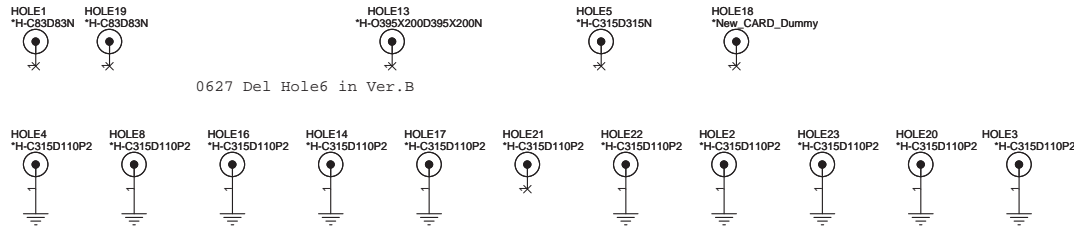


<http://hobi-elektronika.net>

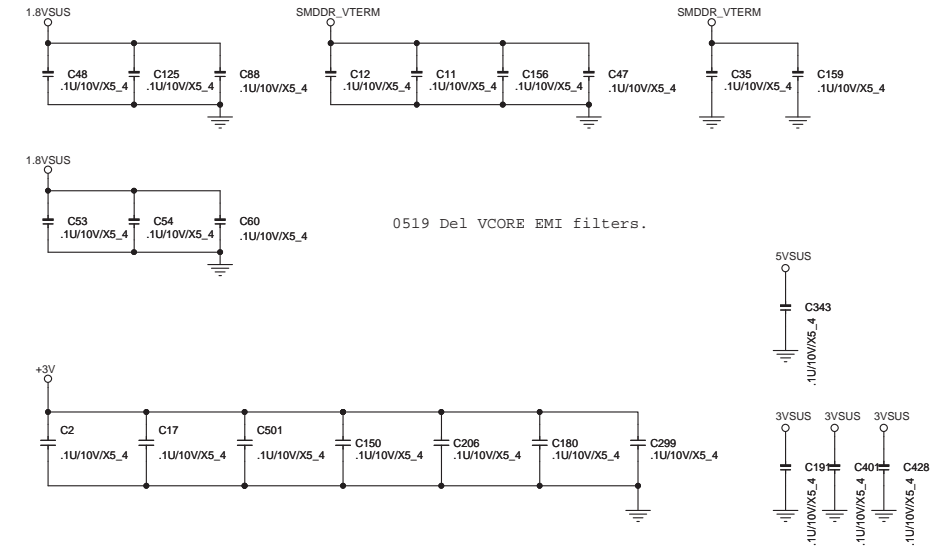
POWER BOARD



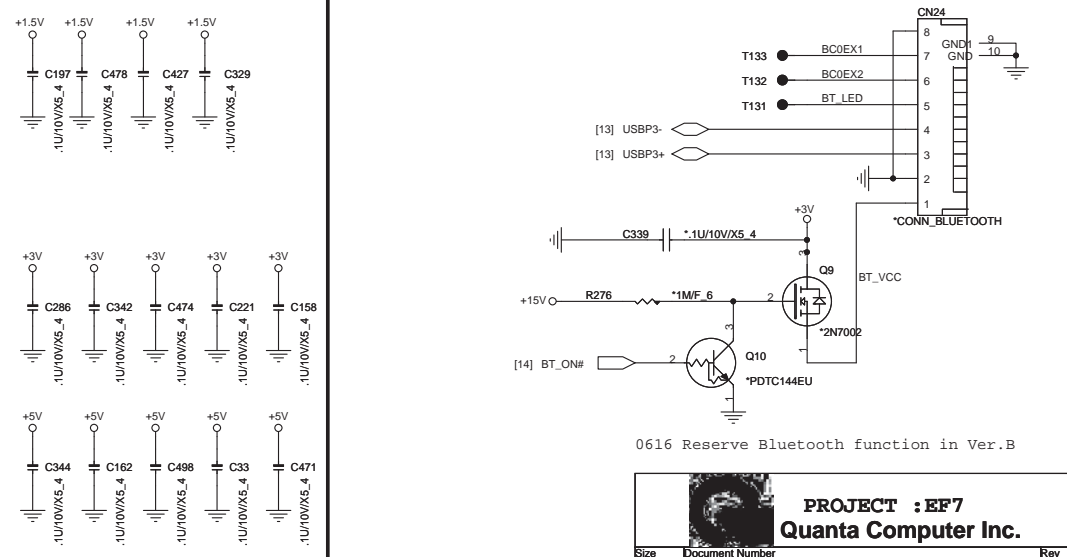
HOLES and EMI PADS



EMI reserved



BLUETOOTH



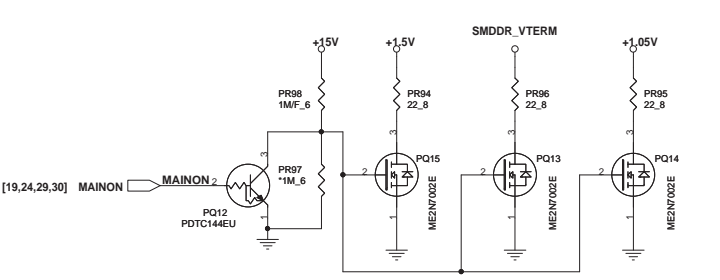
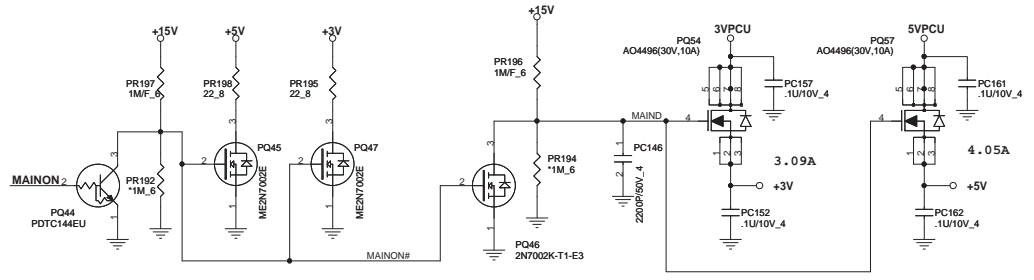
PROJECT : EF7
Quanta Computer Inc.

Size Custom Document Number Conn-BtoB/Fan/Holes/BT Rev 2A
 Date: Thursday, July 03, 2008 Sheet 26 of 34

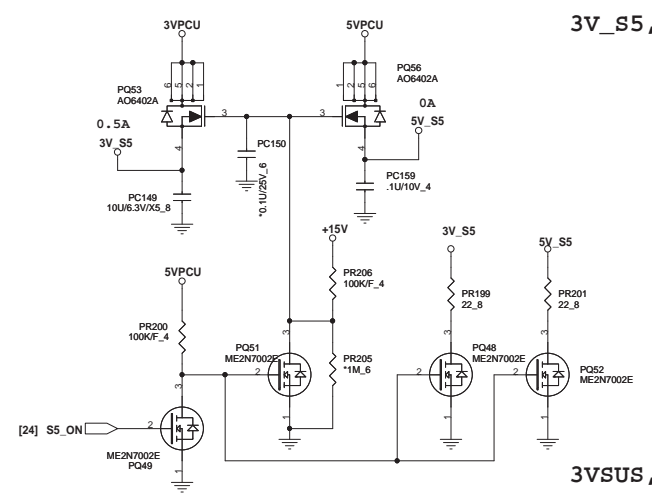
DISCHARGE

+3V, +5V

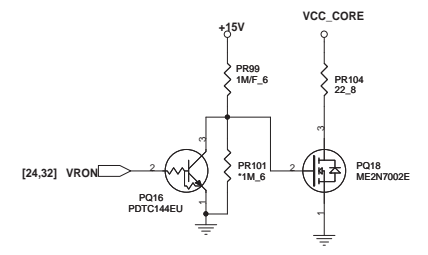
+1.5V, +1.05V



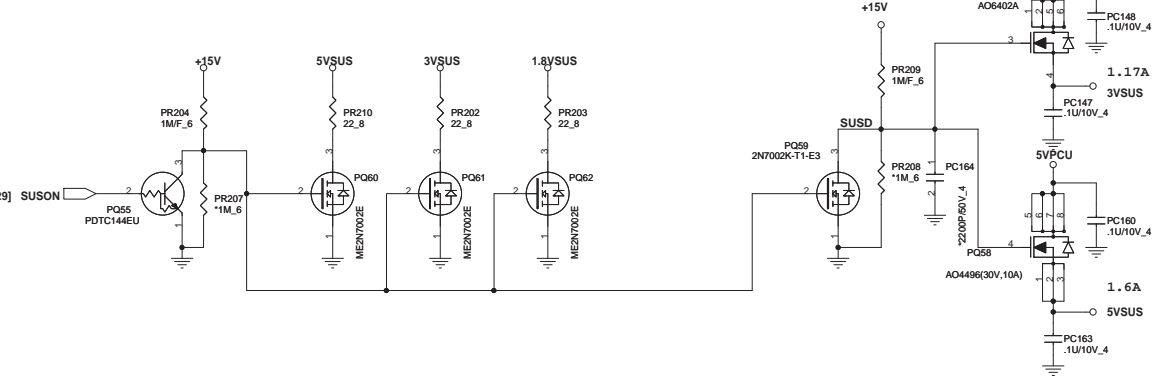
3V_S5, 5V_S5



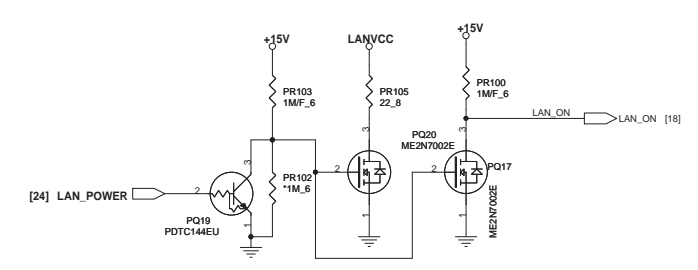
VCC_CORE

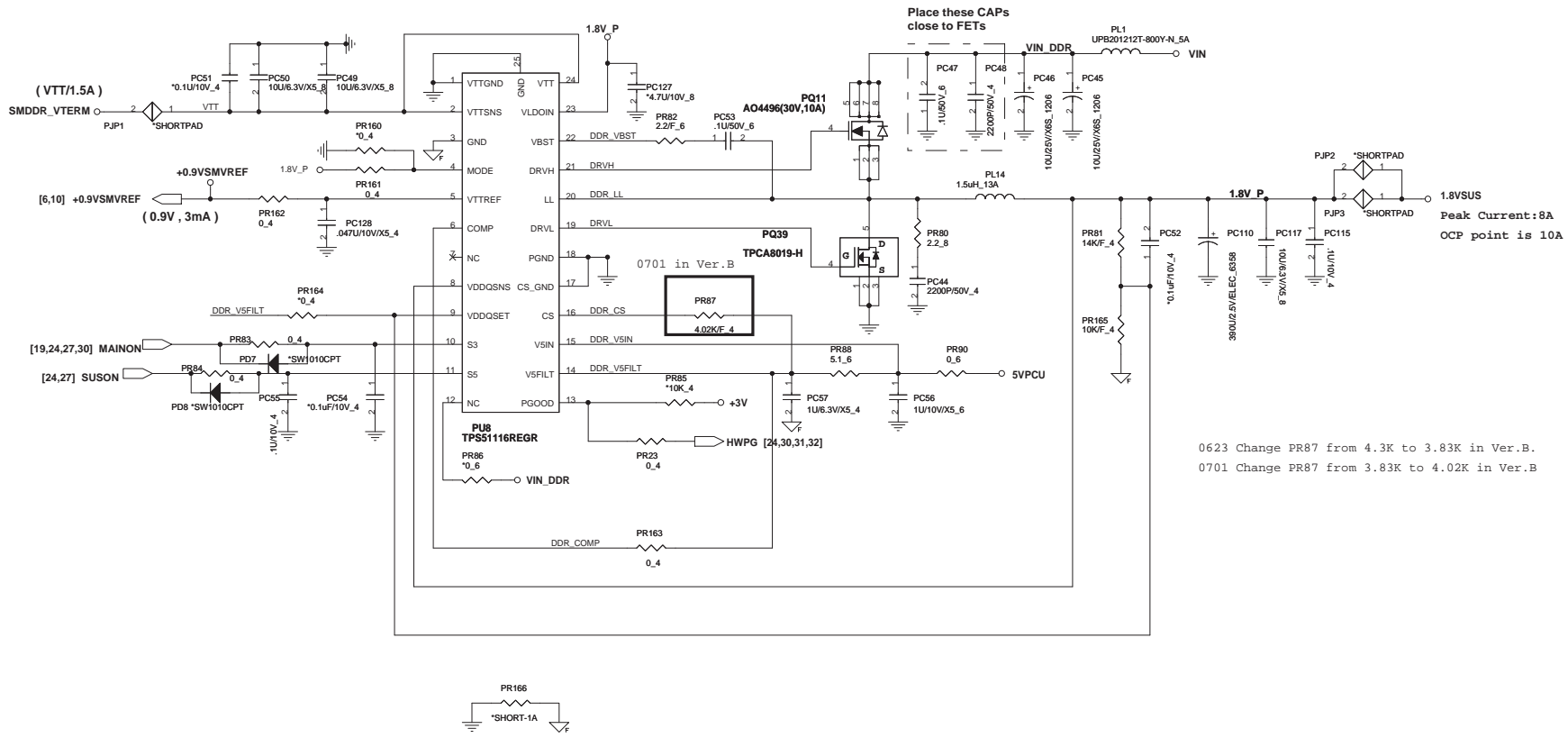


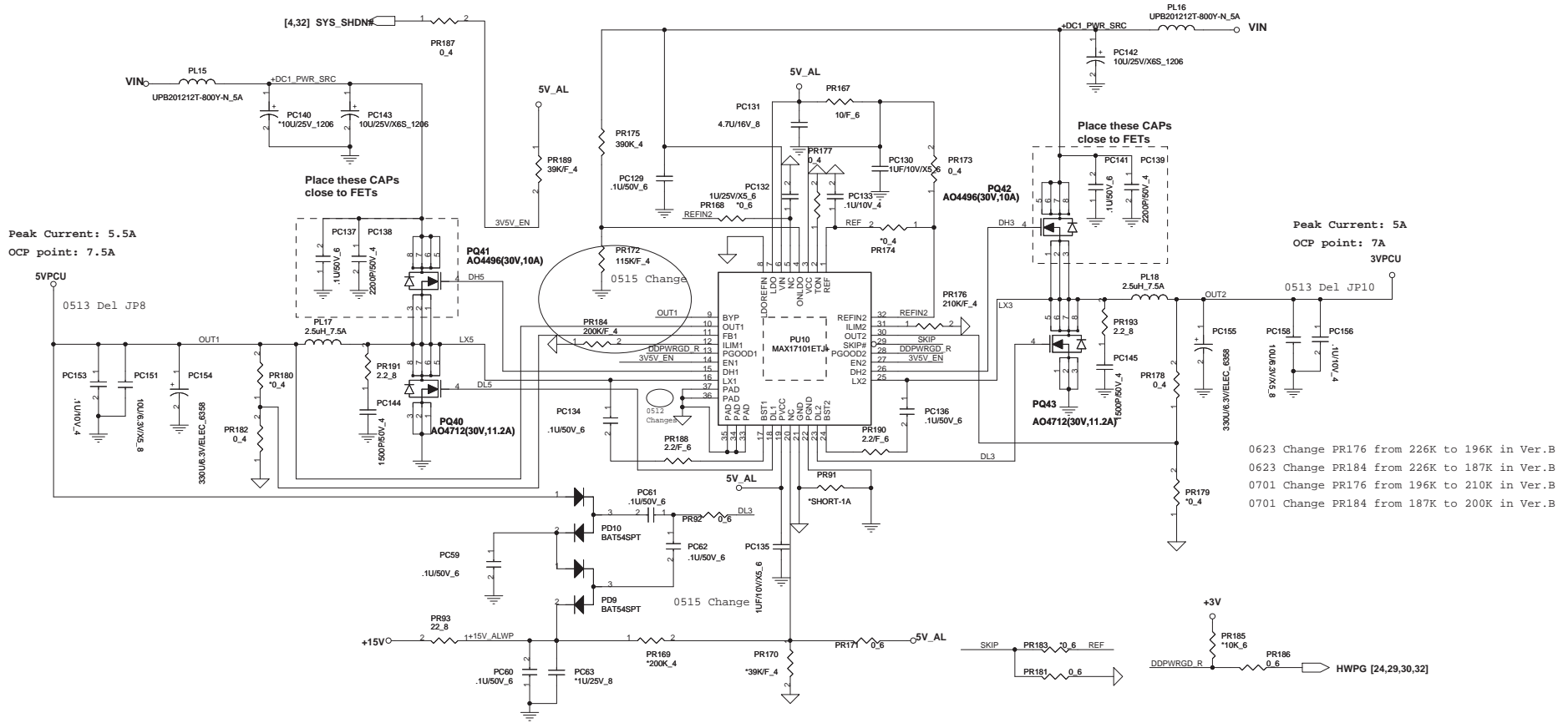
3VSUS, 5VSUS, 1.8VSUS



LANVCC







Peak Current: 5.5A
OCP point: 7.5A

Peak Current: 5A
OCP point: 7A
3VPCU

- 0623 Change PR176 from 226K to 196K in Ver.B
- 0623 Change PR184 from 226K to 187K in Ver.B
- 0701 Change PR176 from 196K to 210K in Ver.B
- 0701 Change PR184 from 187K to 200K in Ver.B

0616 Remove PC2, Mount PC1 for S5-off circuit fine tune in Ver.B
0625 Mount PR1 to disable S5_off function in Ver.B

