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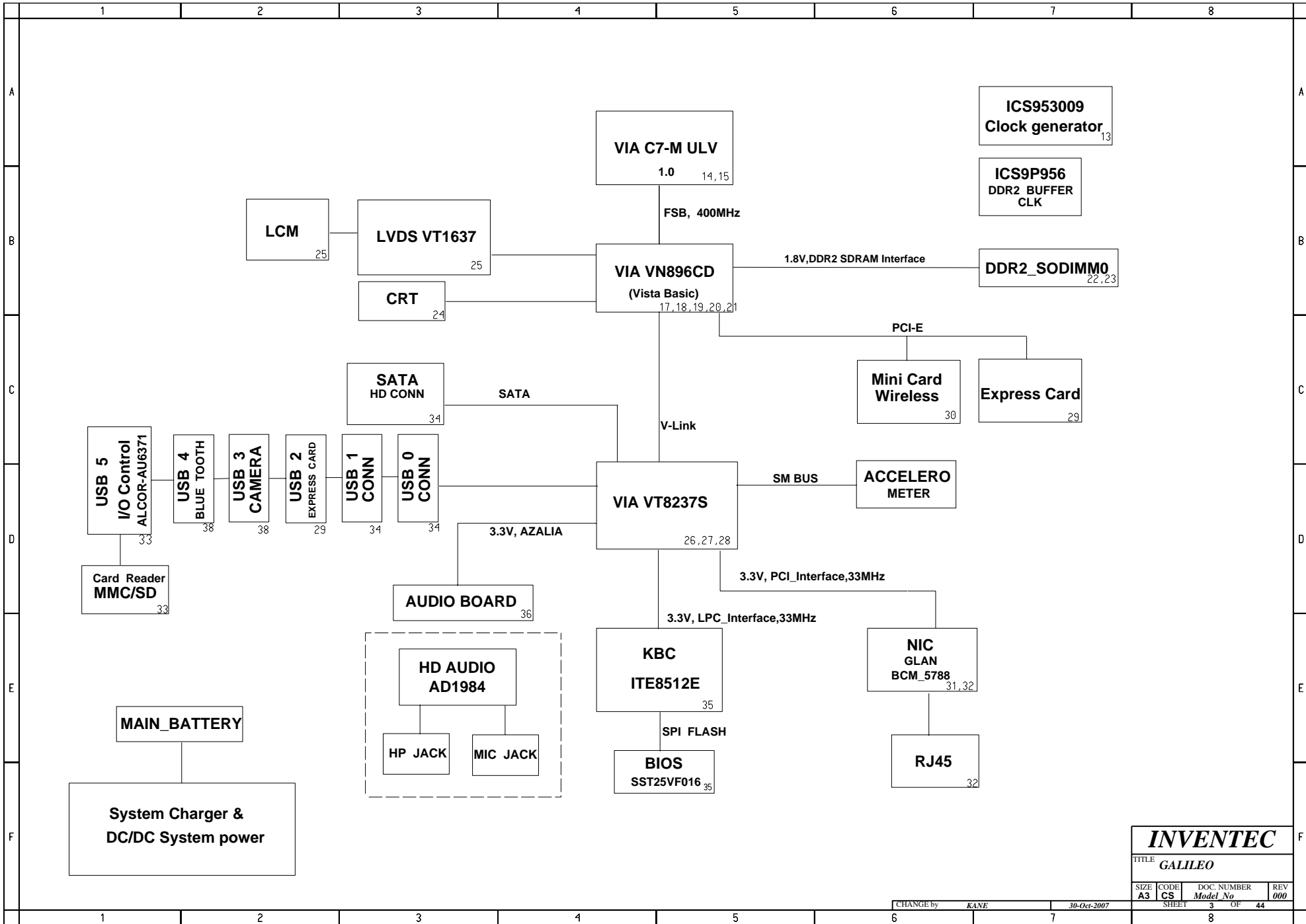
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15.CPU VIA C7-M ULV -2	36.KEYBOARD CONN
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INVENTEC

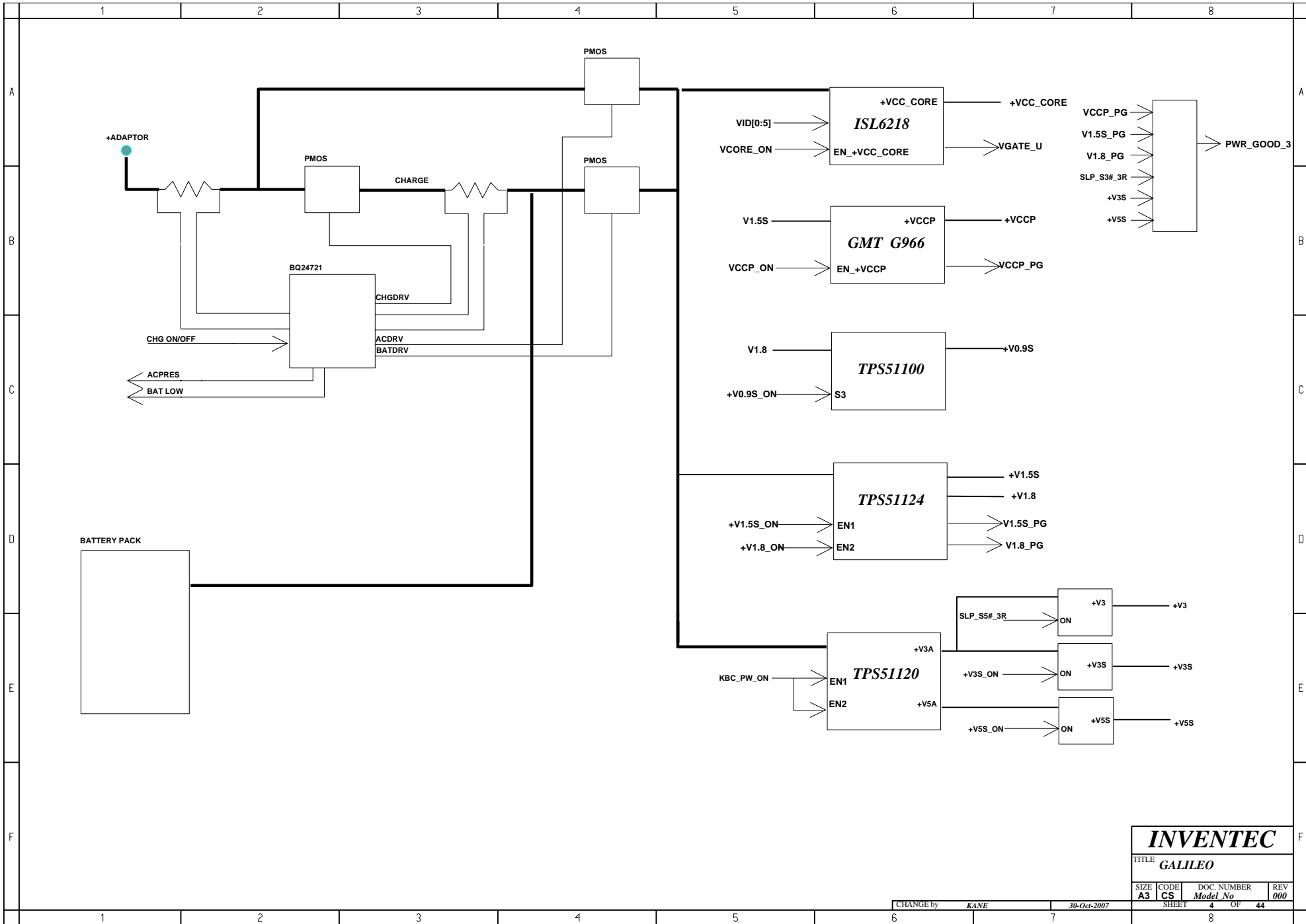
TITLE *GALILEO*

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
SHEET		2	OF 44

CHANGE by *KANE* 30-Oct-2007



INVENTEC			
TITLE GALILEO			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
CHANGE by KANE		30-Oct-2007	
SHEET 3		OF 44	



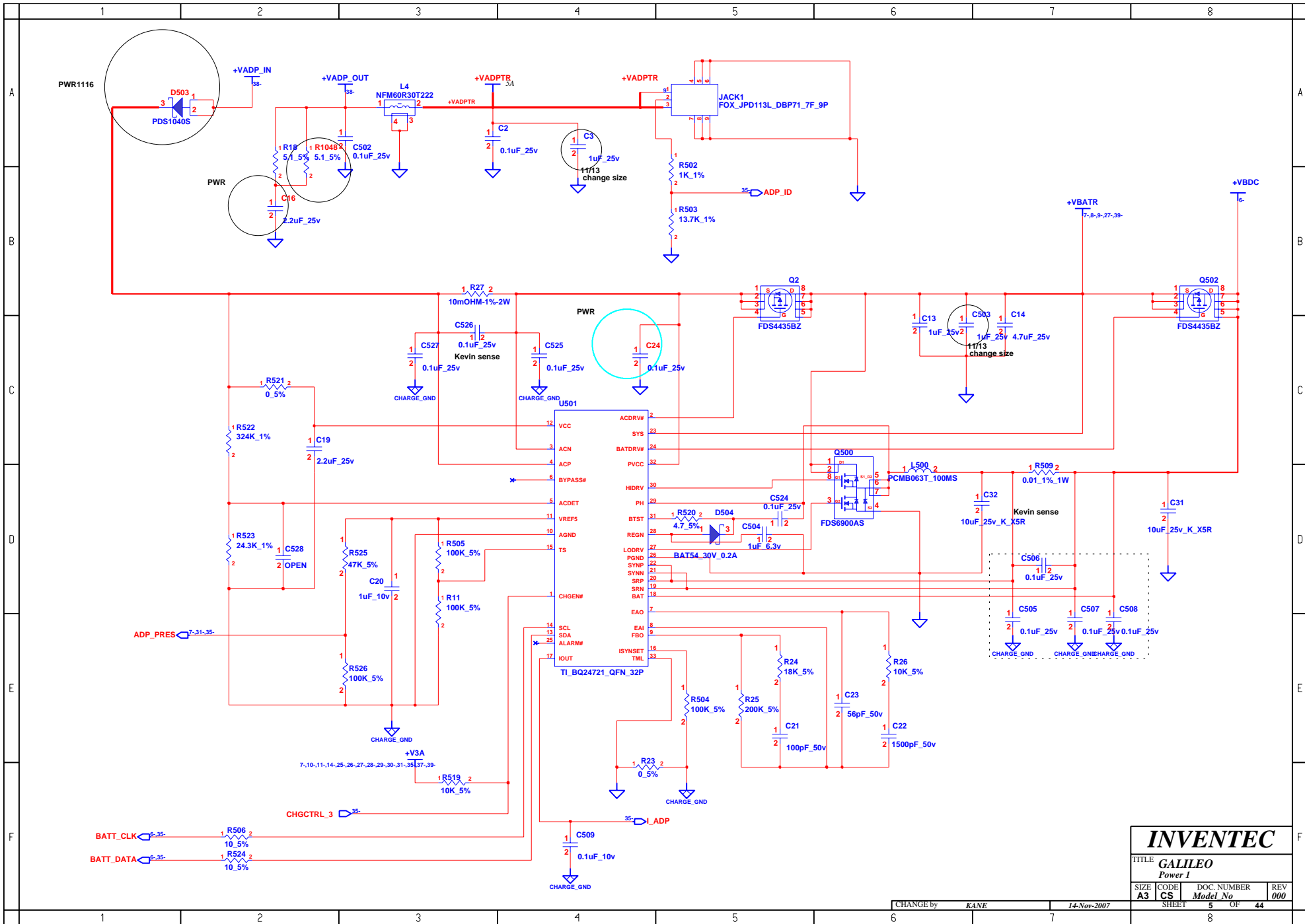
INVENTEC

TITLE **GALILEO**

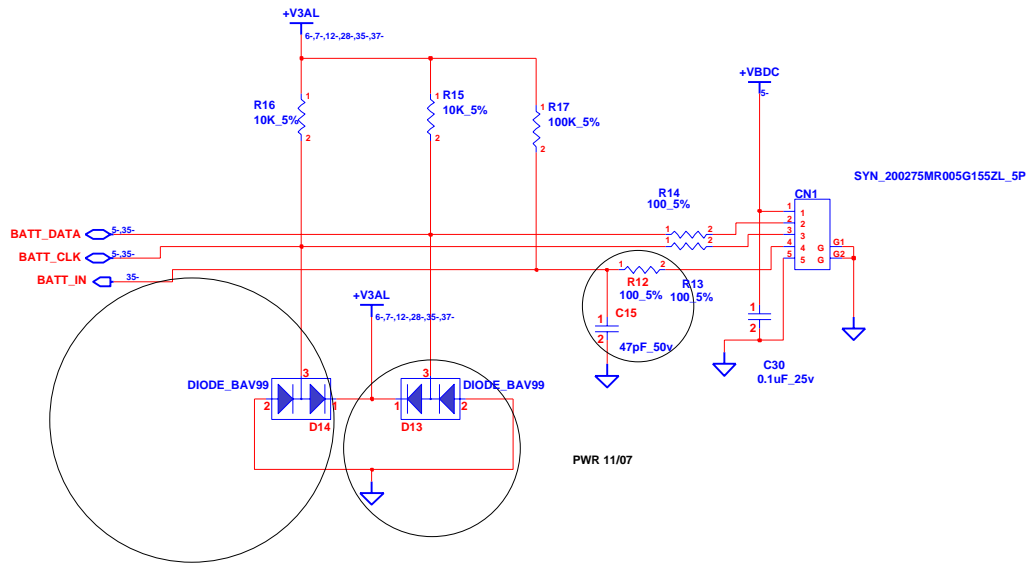
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000

CHANGE by **KANE** 30-Oct-2007

SHEET 4 OF 44



INVENTEC			
TITLE GALILEO			
Power 1			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
CHANGE by		14-Nov-2007	
KANE		SHEET 5 OF 44	

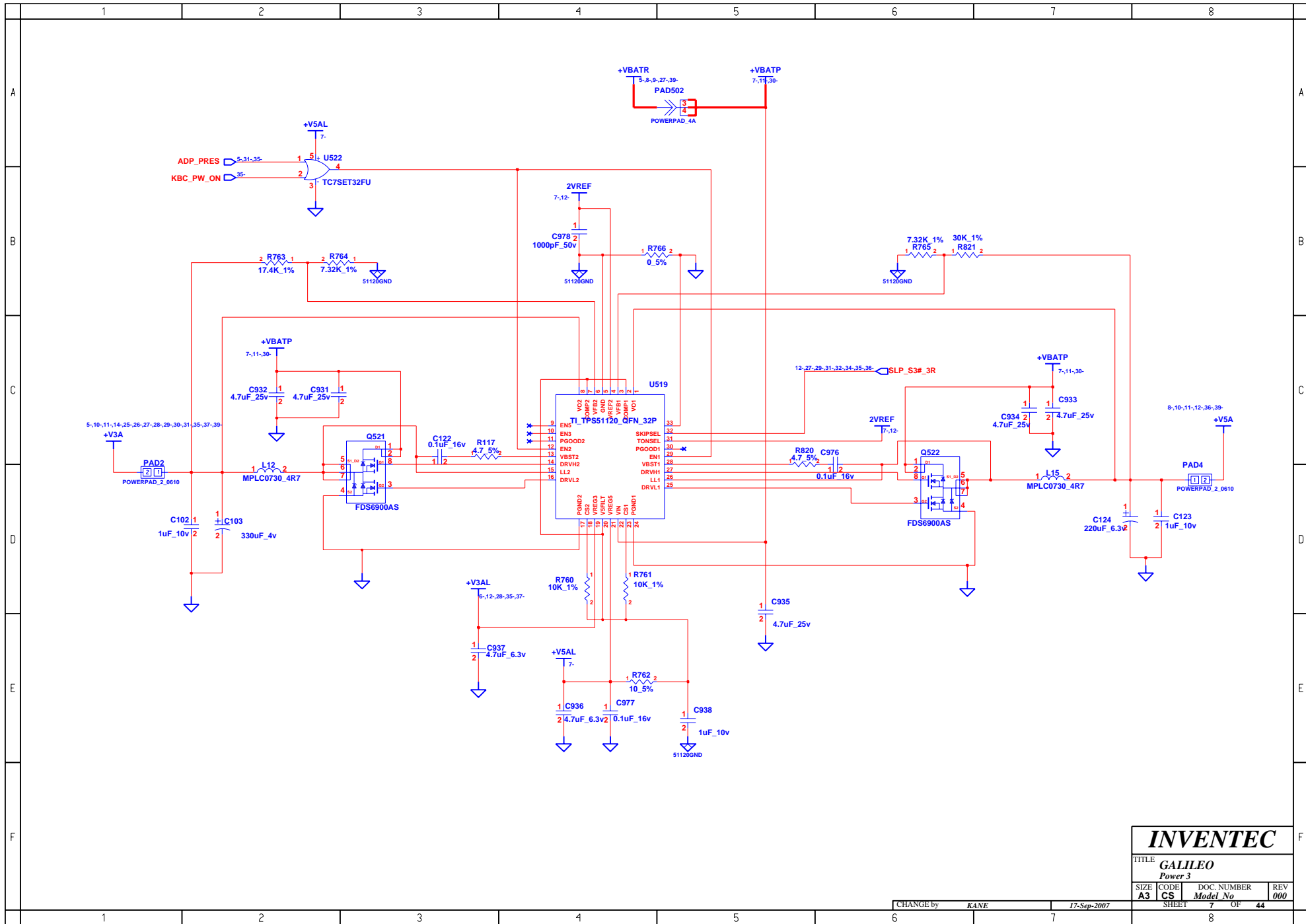


PWR 11/07

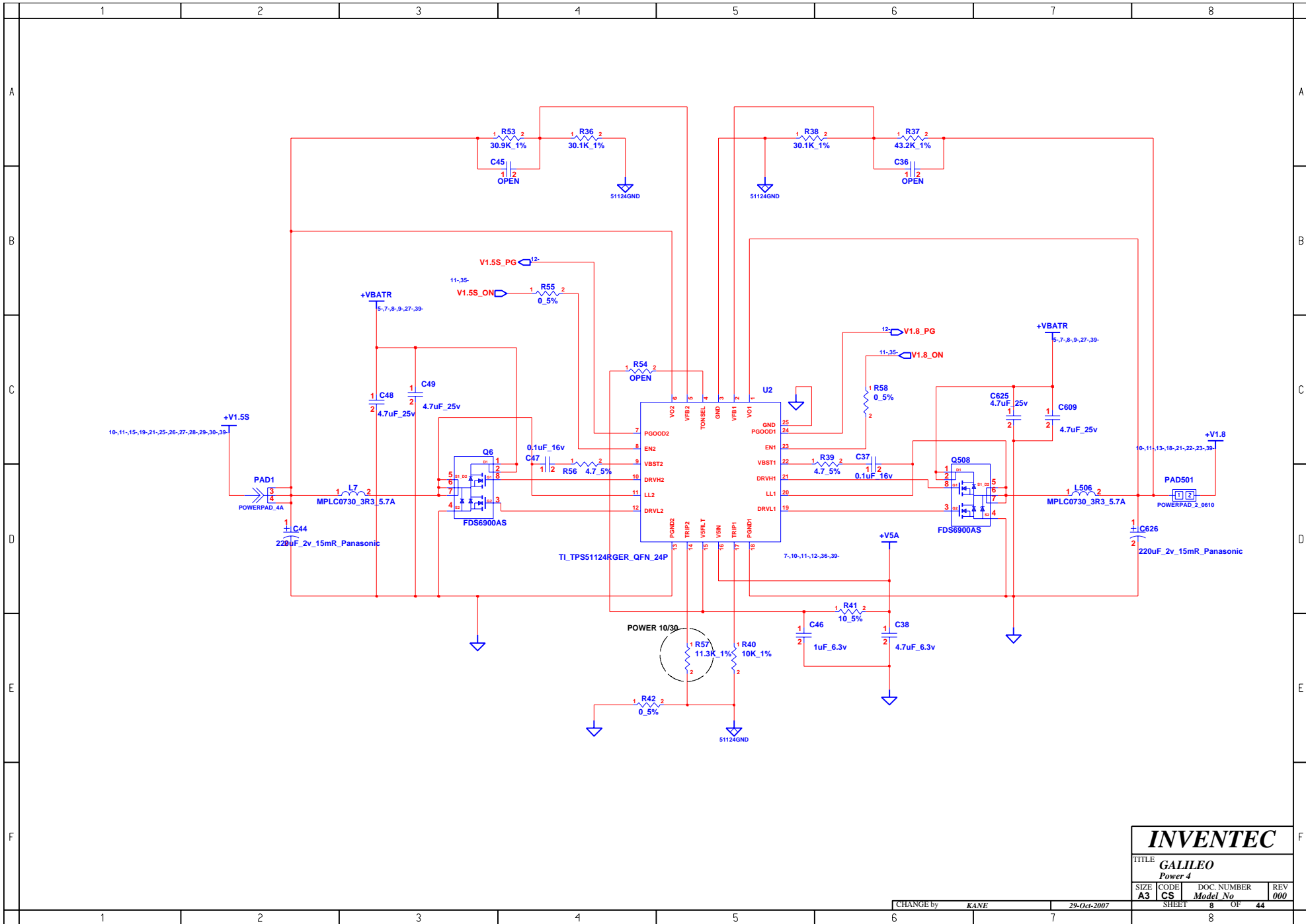
INVENTEC

TITLE			
GALILEO			
Power 2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
CHANGE by		SHEET	
KANE		6 OF 44	

8-Nov-2007

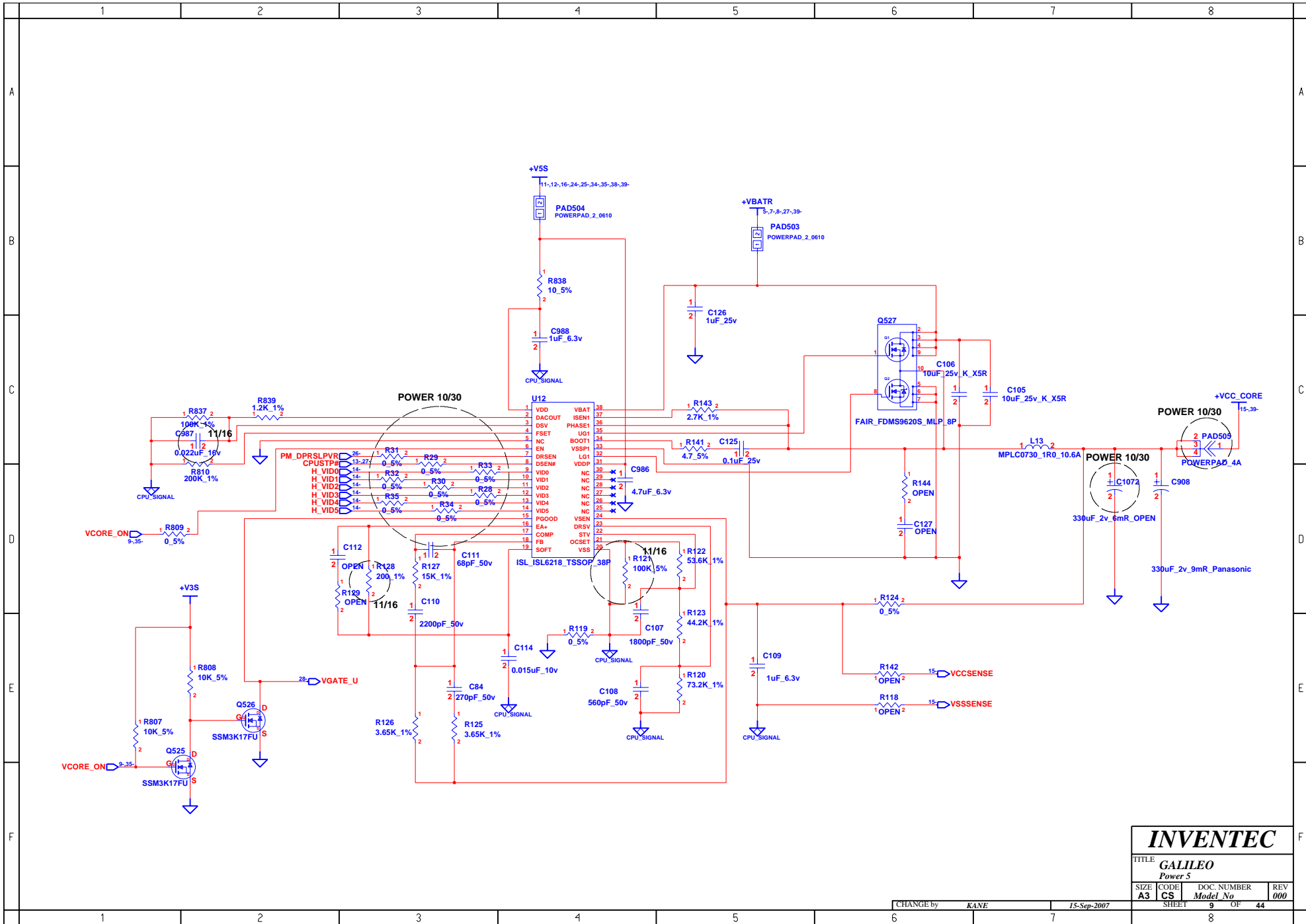


INVENTEC			
TITLE GALILEO			
Power 3			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV 000
CHANGE by KANE		17-Sep-2007	
SHEET 7		OF 44	

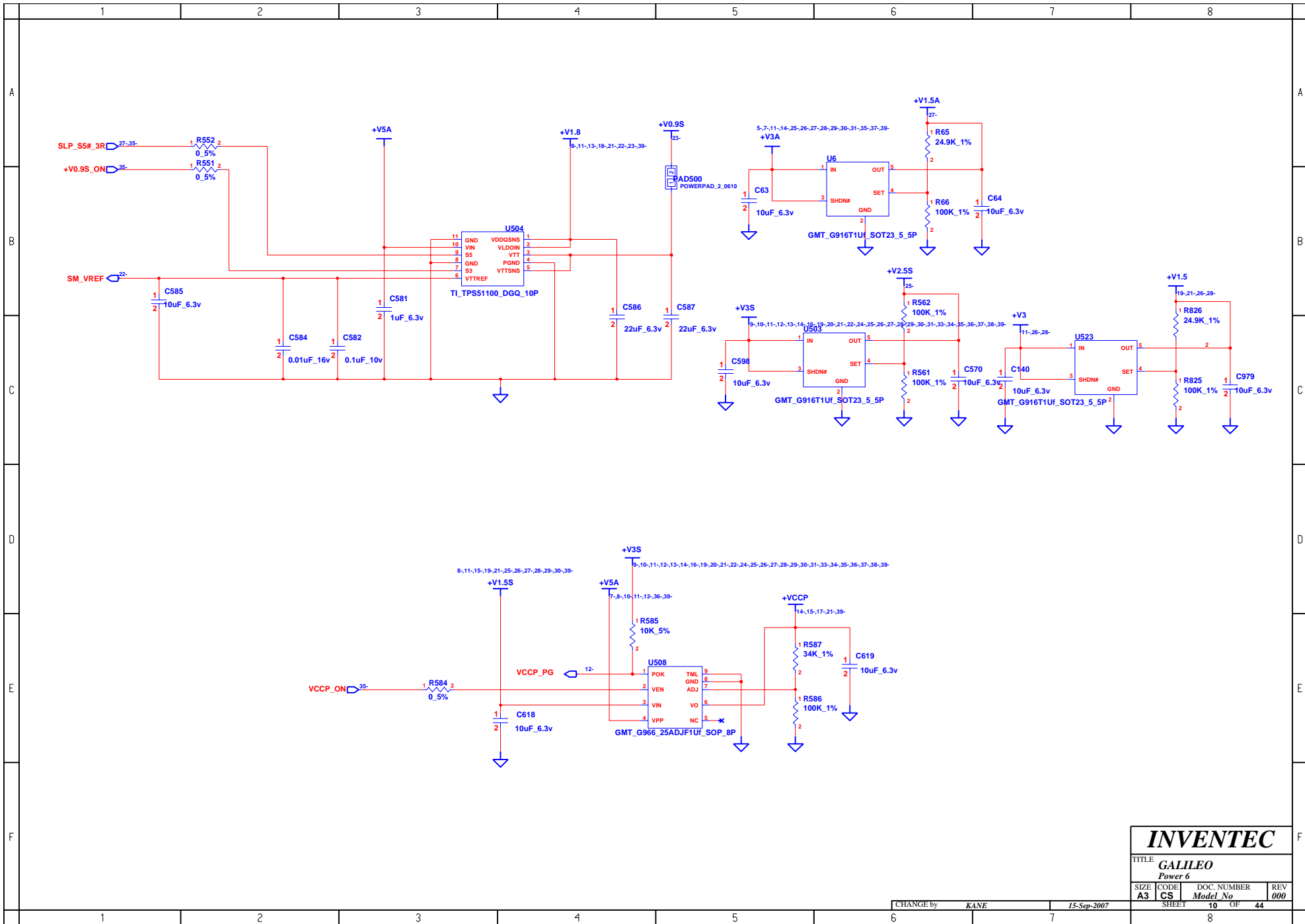


INVENTEC

TITLE			
GALILEO			
Power 4			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000

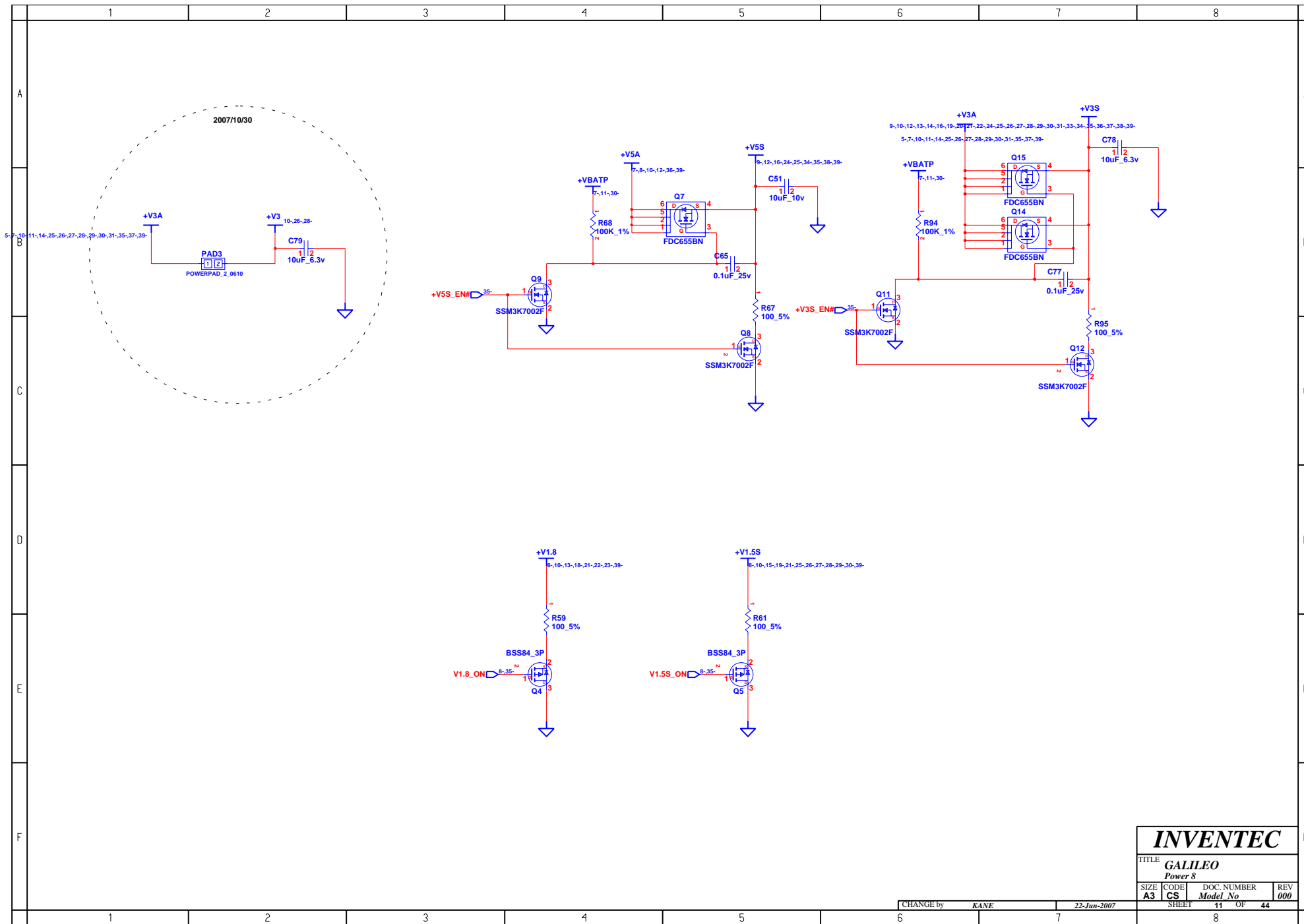


INVENTEC			
TITLE GALILEO			
Power 5			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
CHANGE by KANE		15-Sep-2007	SHEET 9 OF 44

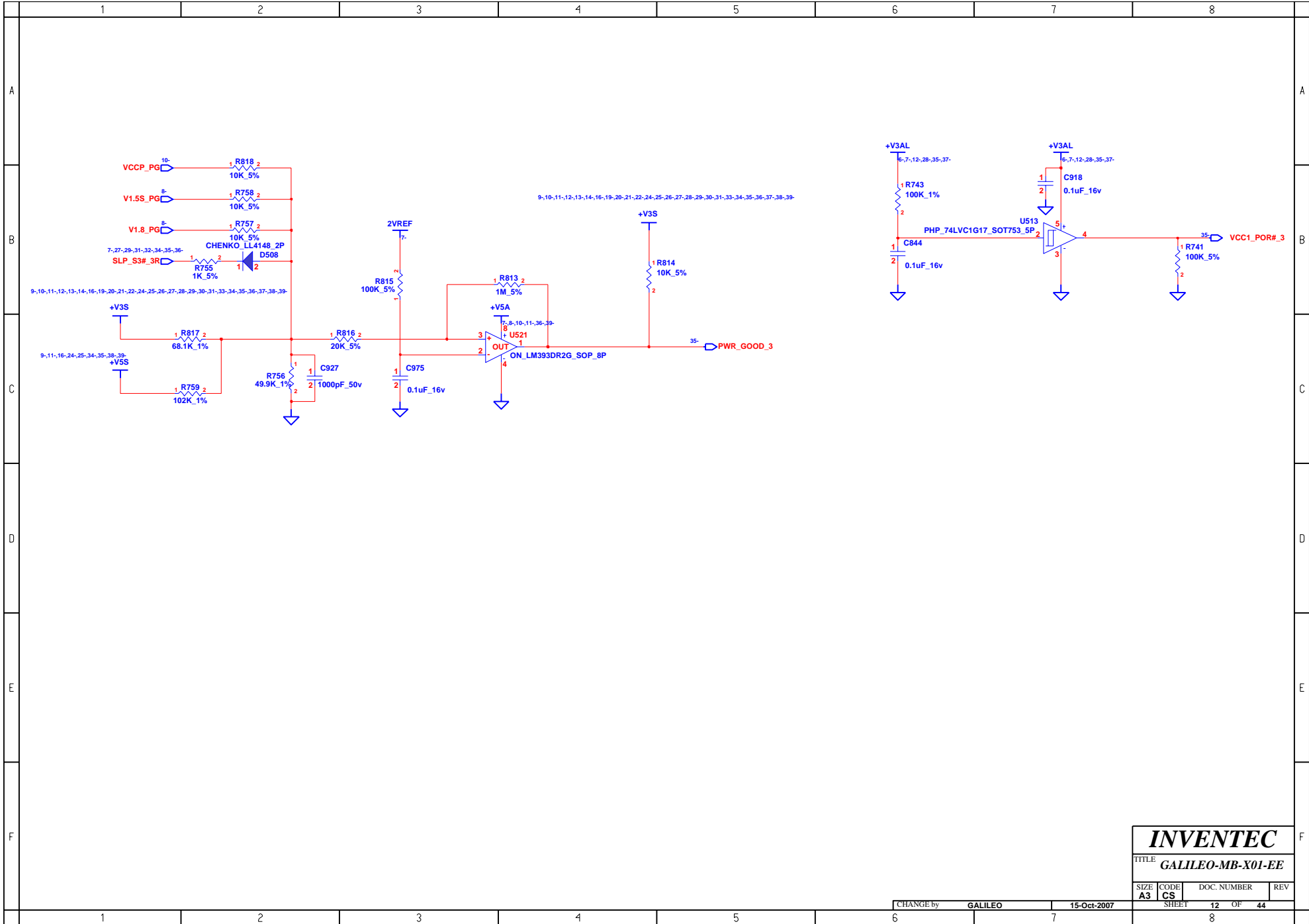


INVENTEC

TITLE			
GALILEO			
Power 6			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000



INVENTEC			
TITLE GALILEO			
Power 8			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
CHANGE by KANE		22-Jun-2007	SHEET 11 OF 44



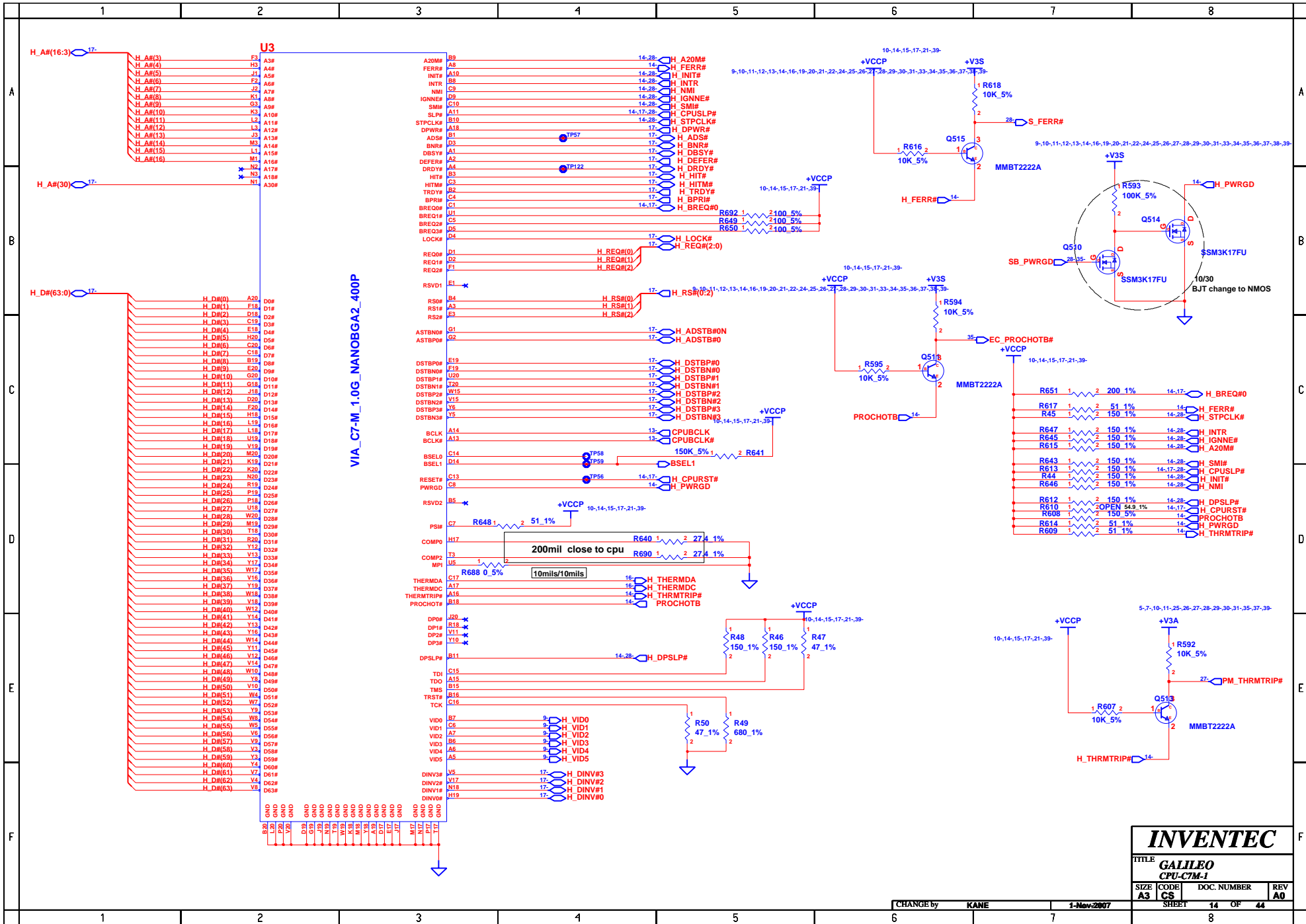
INVENTEC

TITLE GALILEO-MB-X01-EE

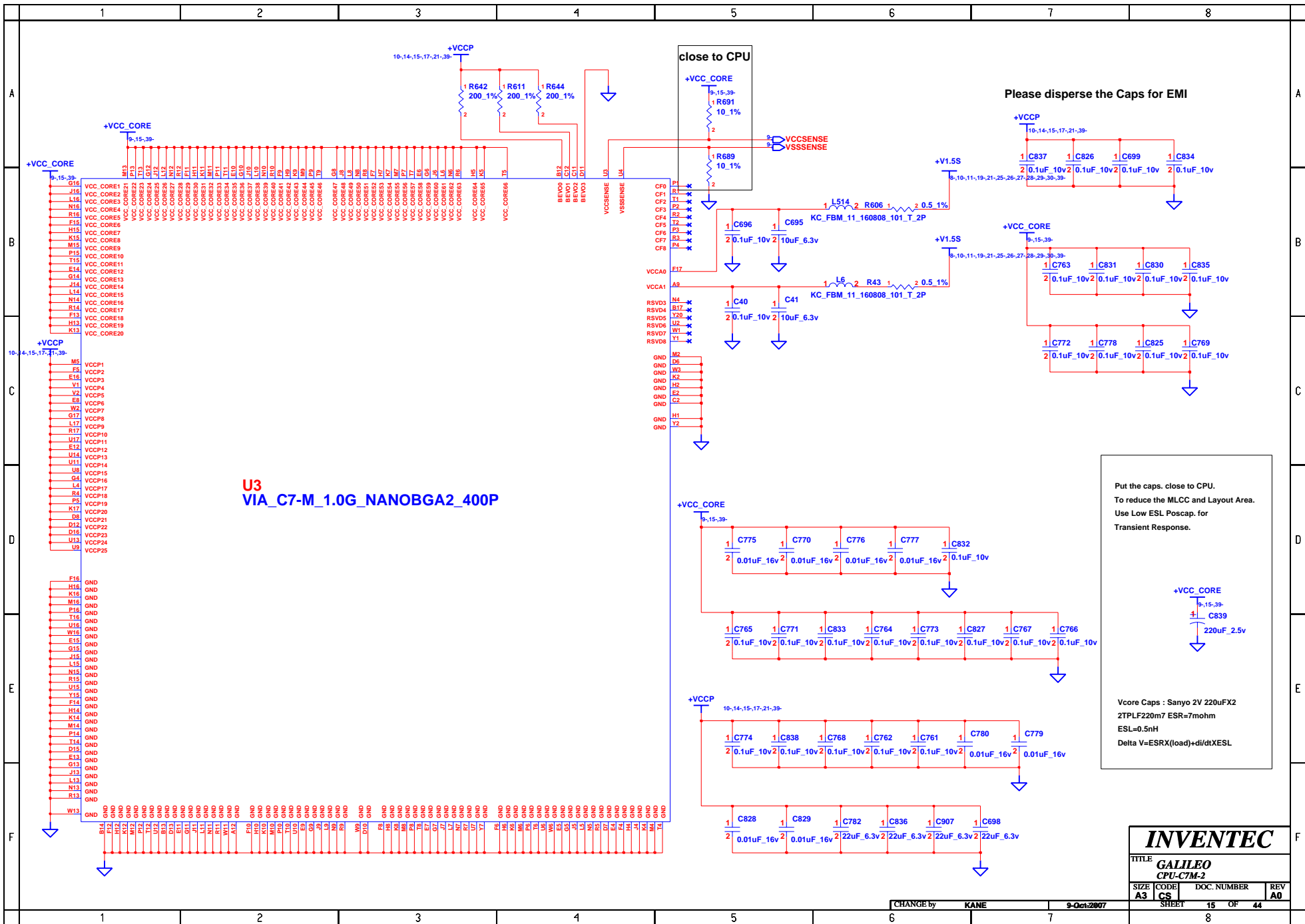
SIZE A3	CODE CS	DOC. NUMBER 12 OF 44	REV
------------	------------	-------------------------	-----

CHANGE by GALILEO 15-Oct-2007

SHEET 12 OF 44

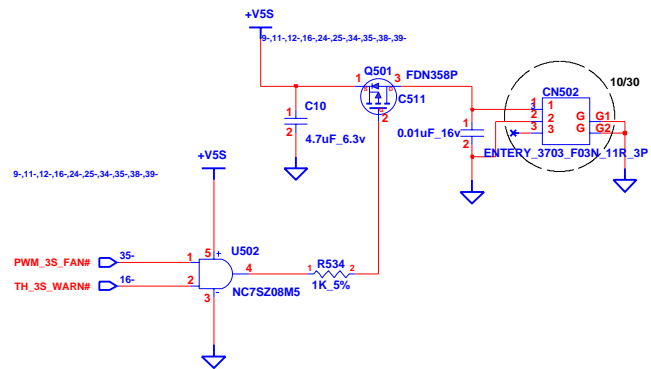
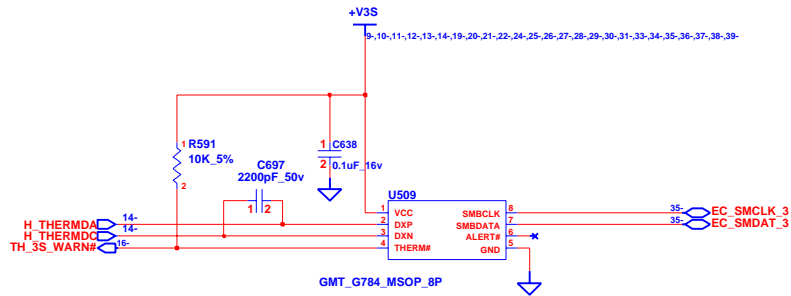


INVENTEC			
TITLE GALILEO CPU-C7M-1			
SIZE A3	CODE CS	DOC. NUMBER SHEET 14 OF 44	REV A0
CHANGE by KANE		1-Nov-2007	



INVENTEC			
TITLE GALILEO CPU-C7M-2			
SIZE A3	CODE CS	DOC NUMBER 15 OF 44	REV A0

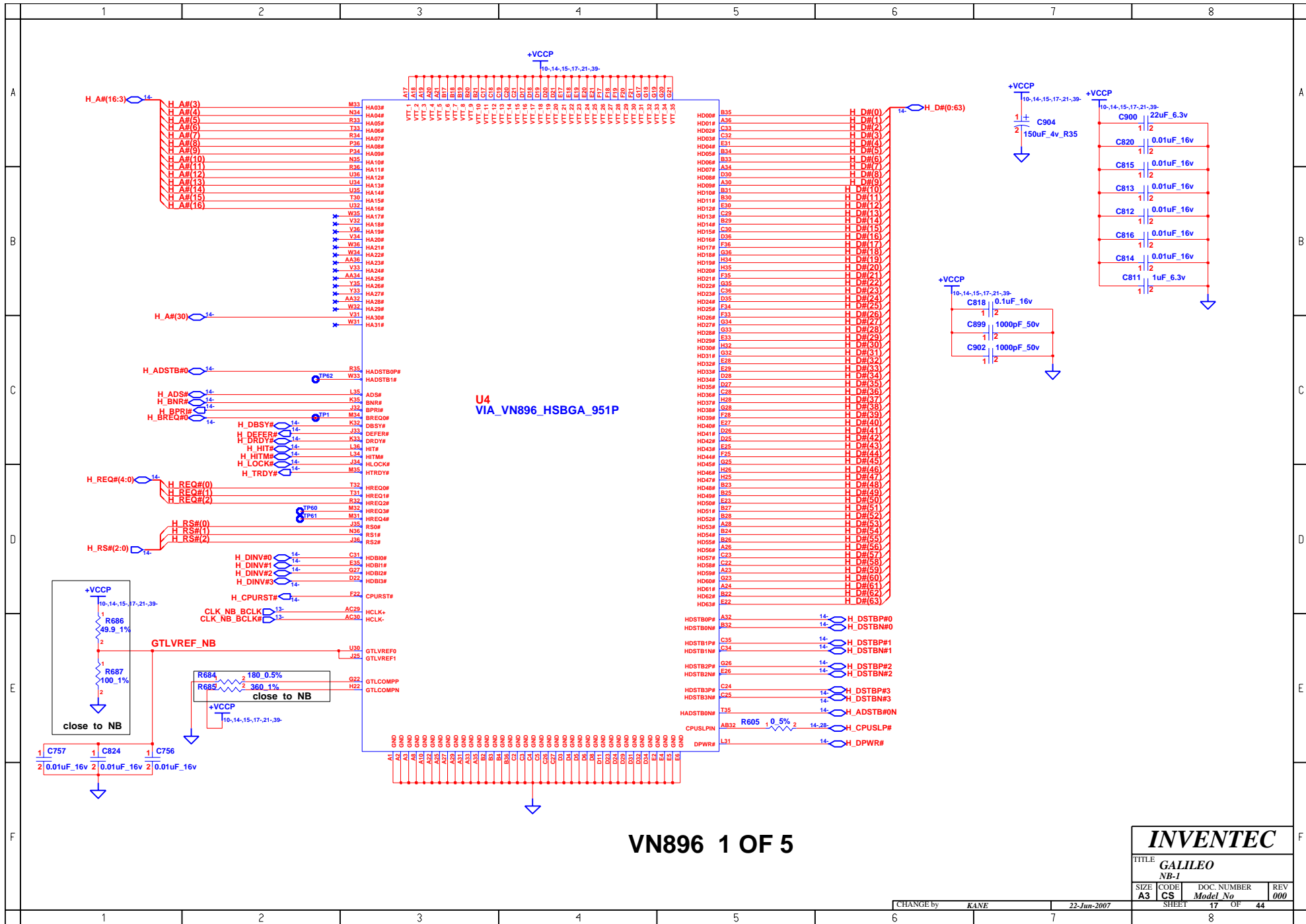
CHANGE by KANE 9-Oct-2007



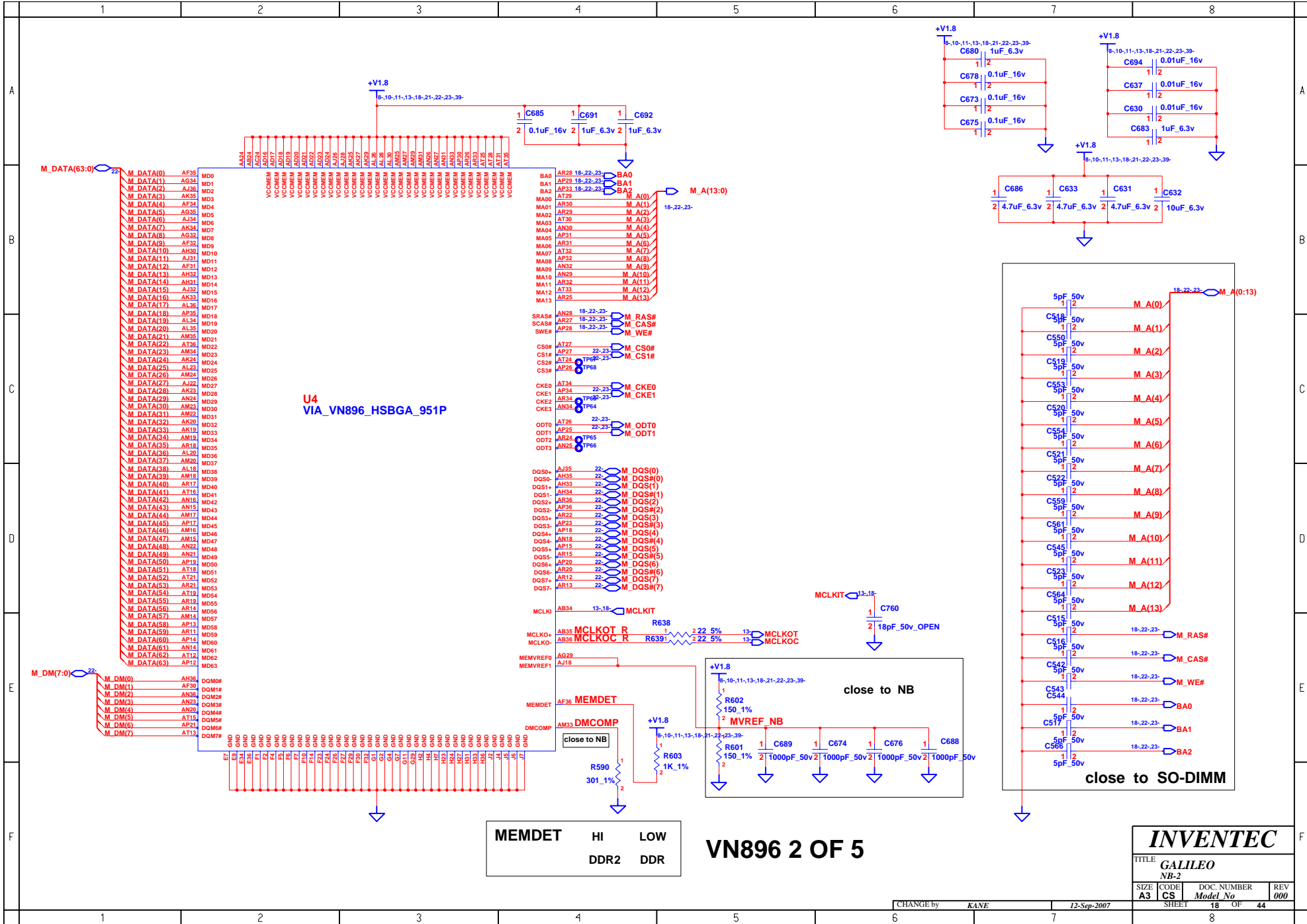
INVENTEC

TITLE			
GALILEO			
Fan And Thermal			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
SHEET		16	OF 44

CHANGE by KANE



INVENTEC			
TITLE GALILEO NB-1			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
CHANGE by KANE		22-Jun-2007	
SHEET 17		OF 44	

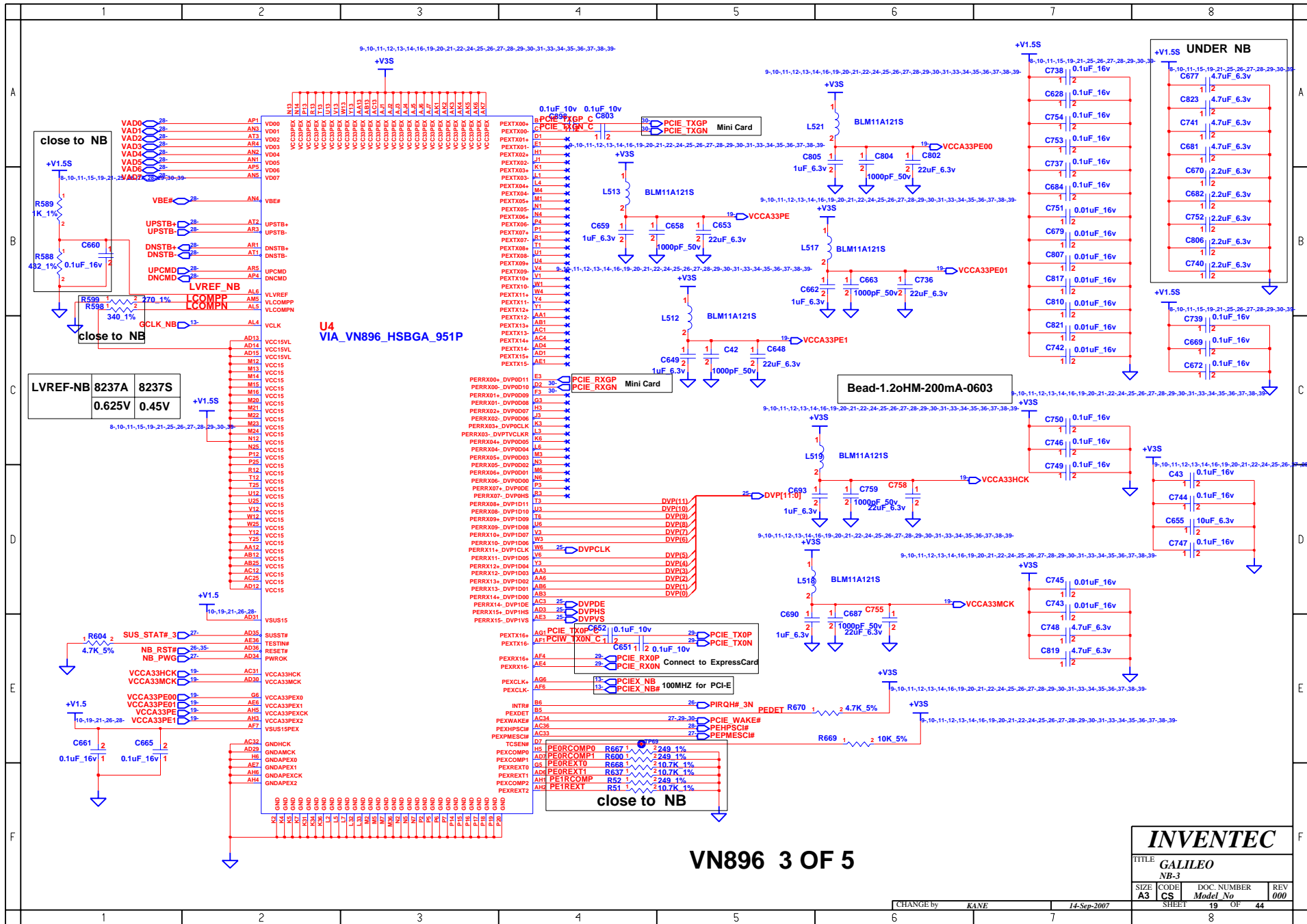


MEMDET HI LOW
DDR2 DDR

VN896 2 OF 5

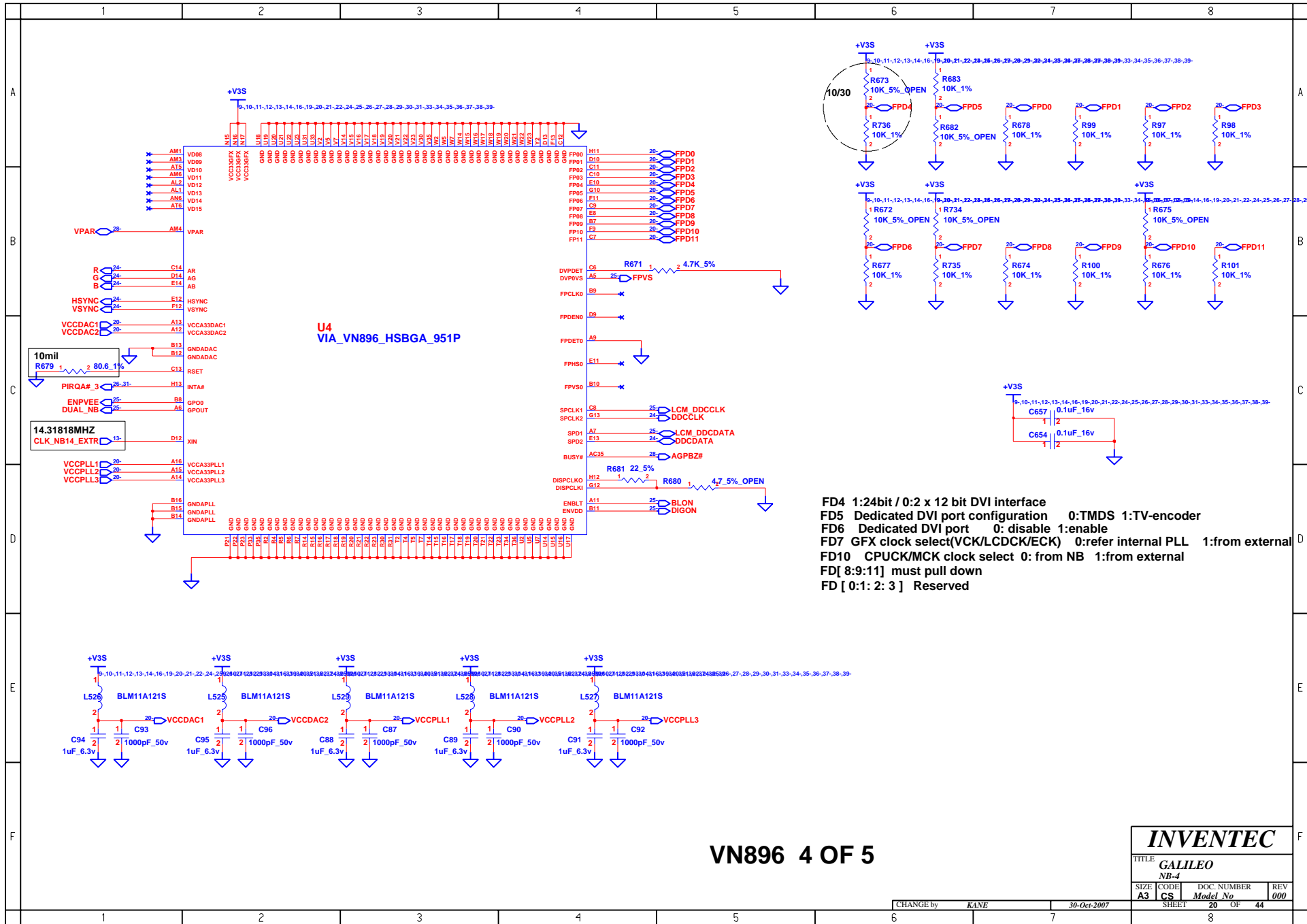
INVENTEC

TITLE GALILEO NB-2			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
CHANGE by KANE		12-Sep-2007	
SHEET 18		OF 44	



VN896 3 OF 5

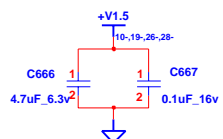
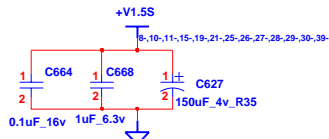
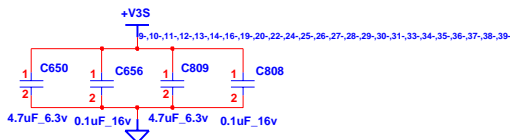
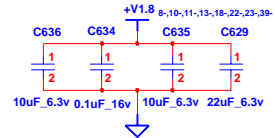
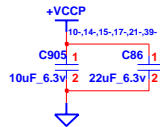
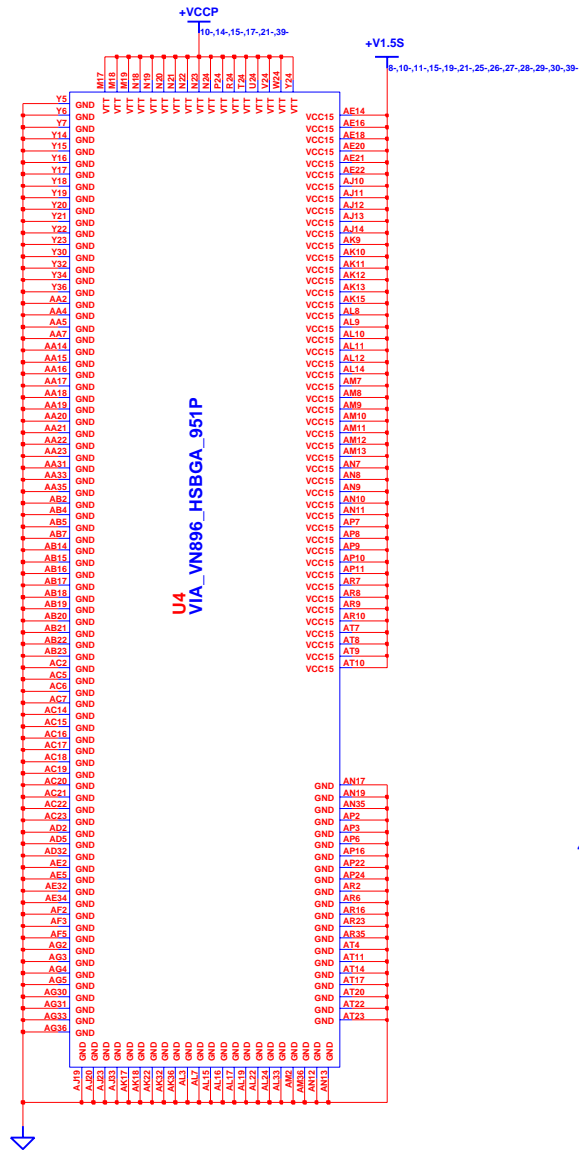
INVENTEC			
TITLE GALILEO			
NB-3			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
CHANGE by NAME		14-Sep-2007	
SHEET 19		OF 44	



FD4 1:24bit / 0:2 x 12 bit DVI interface
 FD5 Dedicated DVI port configuration 0:TMD5 1:TV-encoder
 FD6 Dedicated DVI port 0: disable 1:enable
 FD7 GFX clock select(VCK/LCDCK/ECK) 0:refer internal PLL 1:from external
 FD10 CPUCK/MCK clock select 0: from NB 1:from external
 FD[8:9:11] must pull down
 FD [0:1: 2: 3] Reserved

VN896 4 OF 5

INVENTEC			
TITLE: GALILEO			
NB-4			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
CHANGE by		KANE	30-Oct-2007
SHEET		20	OF 44



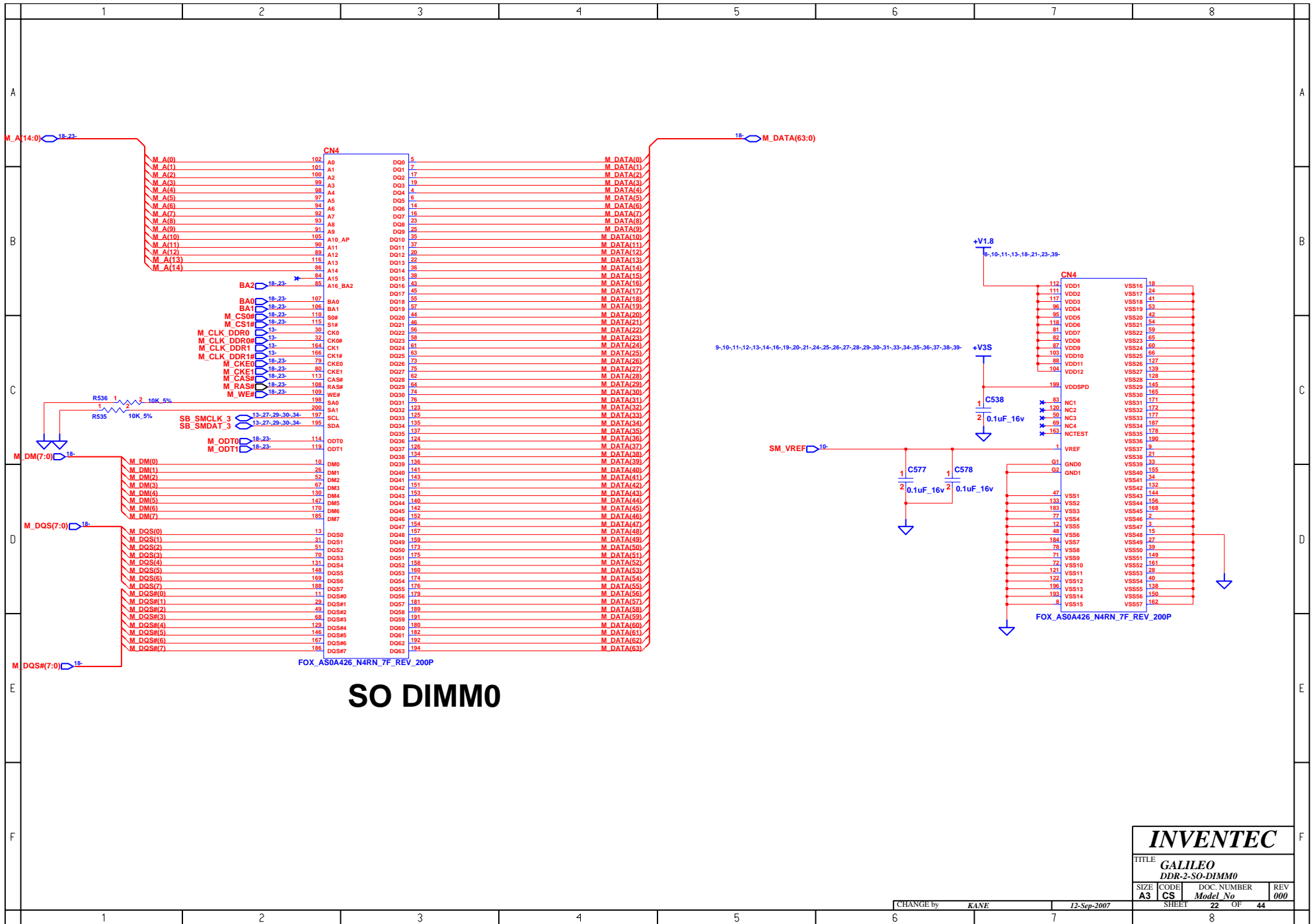
VN896 5 OF 5

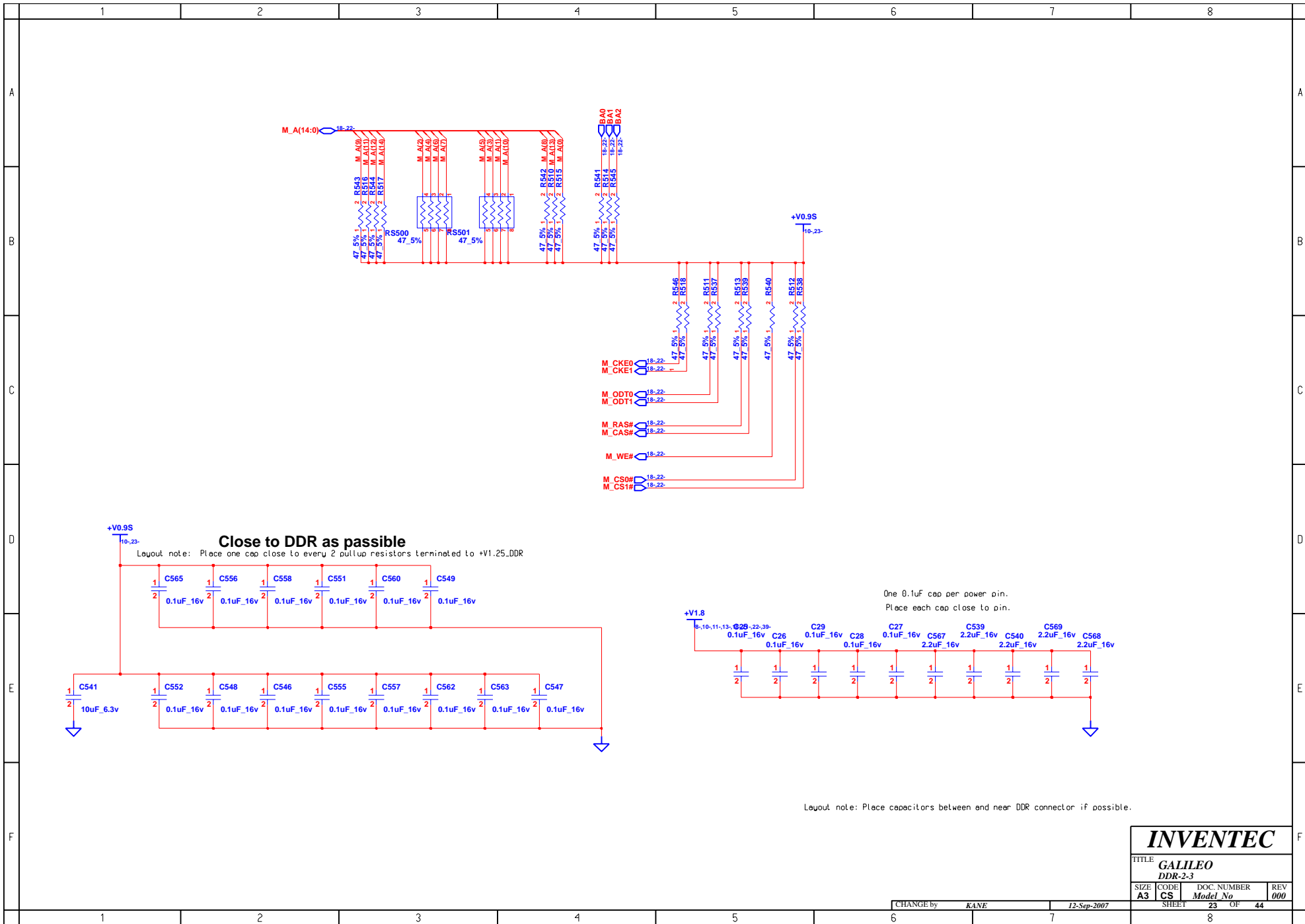
INVENTEC

TITLE			
GALILEO			
NB-5			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
CHANGE by		SHEET	
KANE		21 OF 44	

18-Jul-2007

8

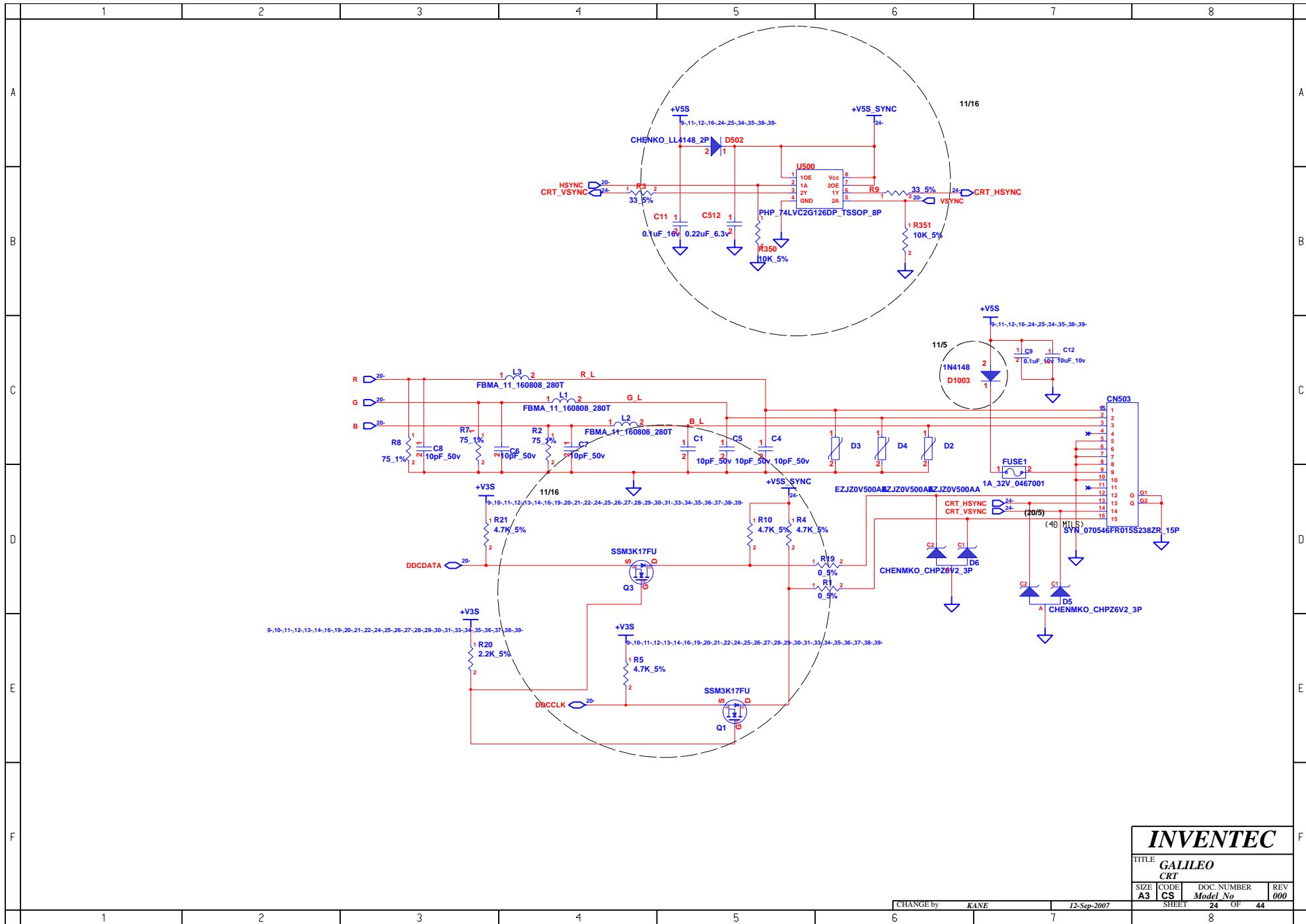




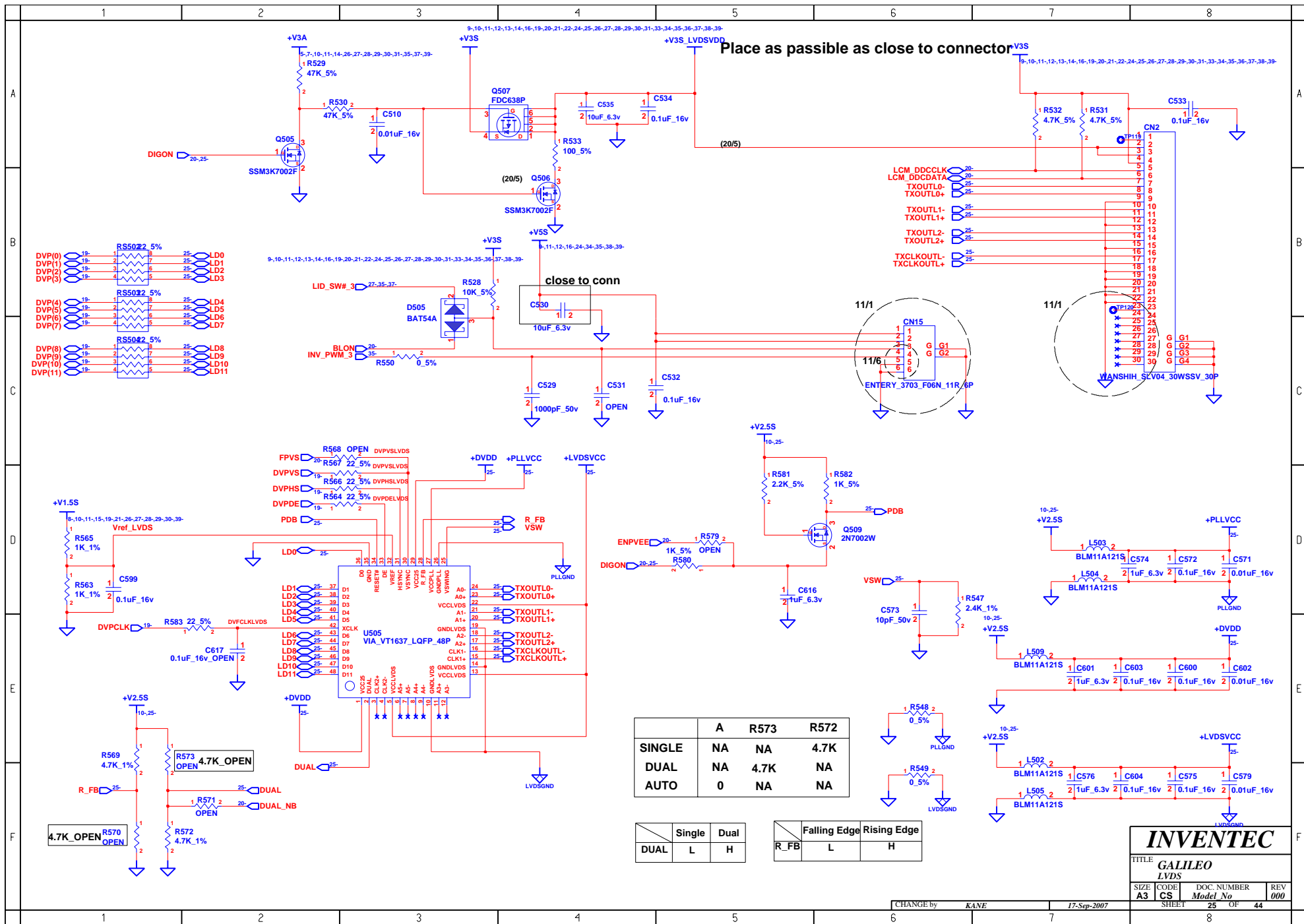
INVENTEC

TITLE			
GALILEO			
DDR-2-3			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
CHANGE by		SHEET	
KANE		23 OF 44	

CHANGE by KANE 12-Sep-2007



INVENTEC			
TITLE GALILEO CRT			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
CHANGE by KANE		12-Sep-2007	
SHEET 24		OF 44	



Place as possible as close to connector +V3S

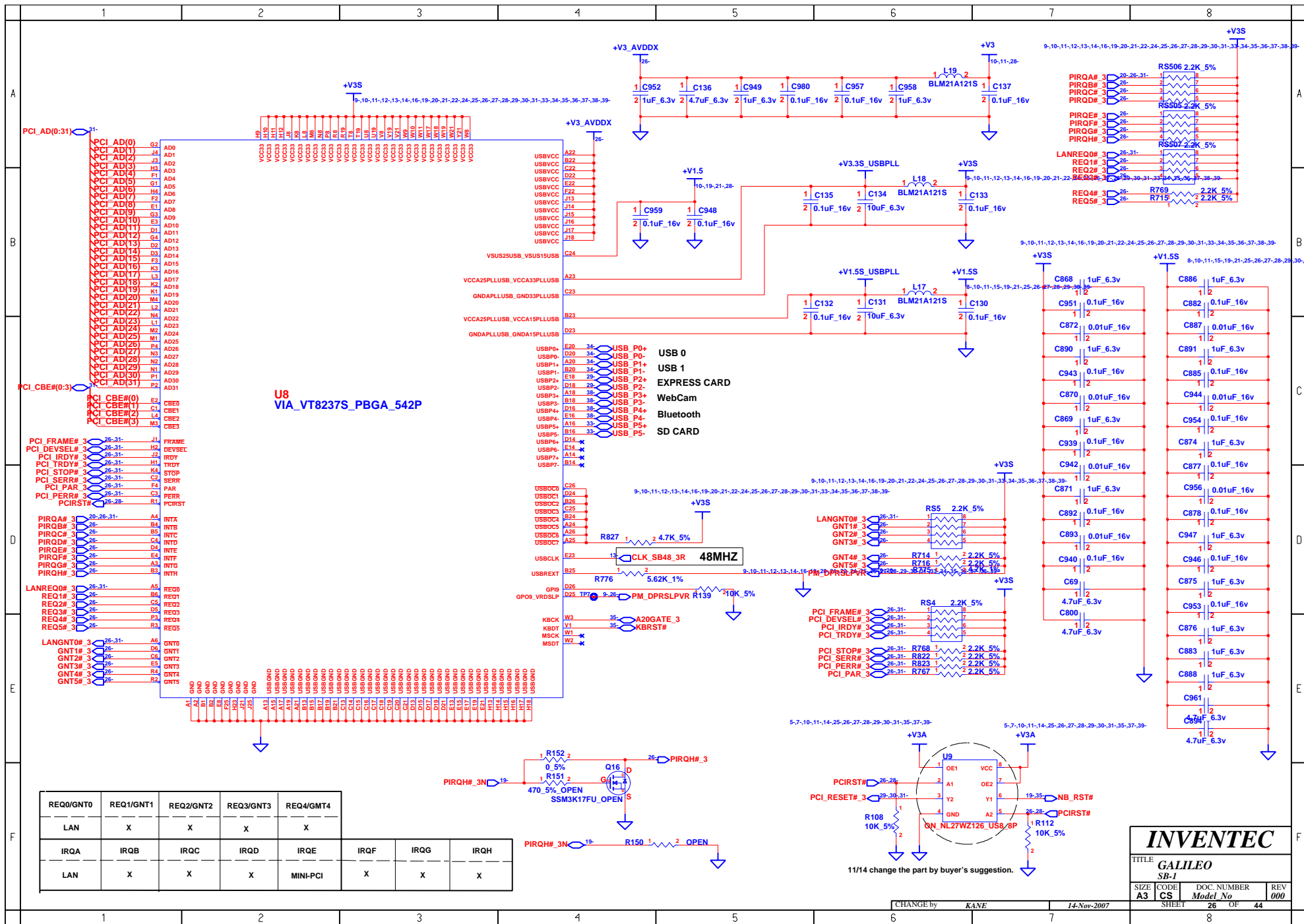
	A	R573	R572
SINGLE	NA	NA	4.7K
DUAL	NA	4.7K	NA
AUTO	0	NA	NA

	Single	Dual		Falling Edge	Rising Edge	
DUAL	L	H		R_FB	L	H

INVENTEC

TITLE: GALILEO LVDS

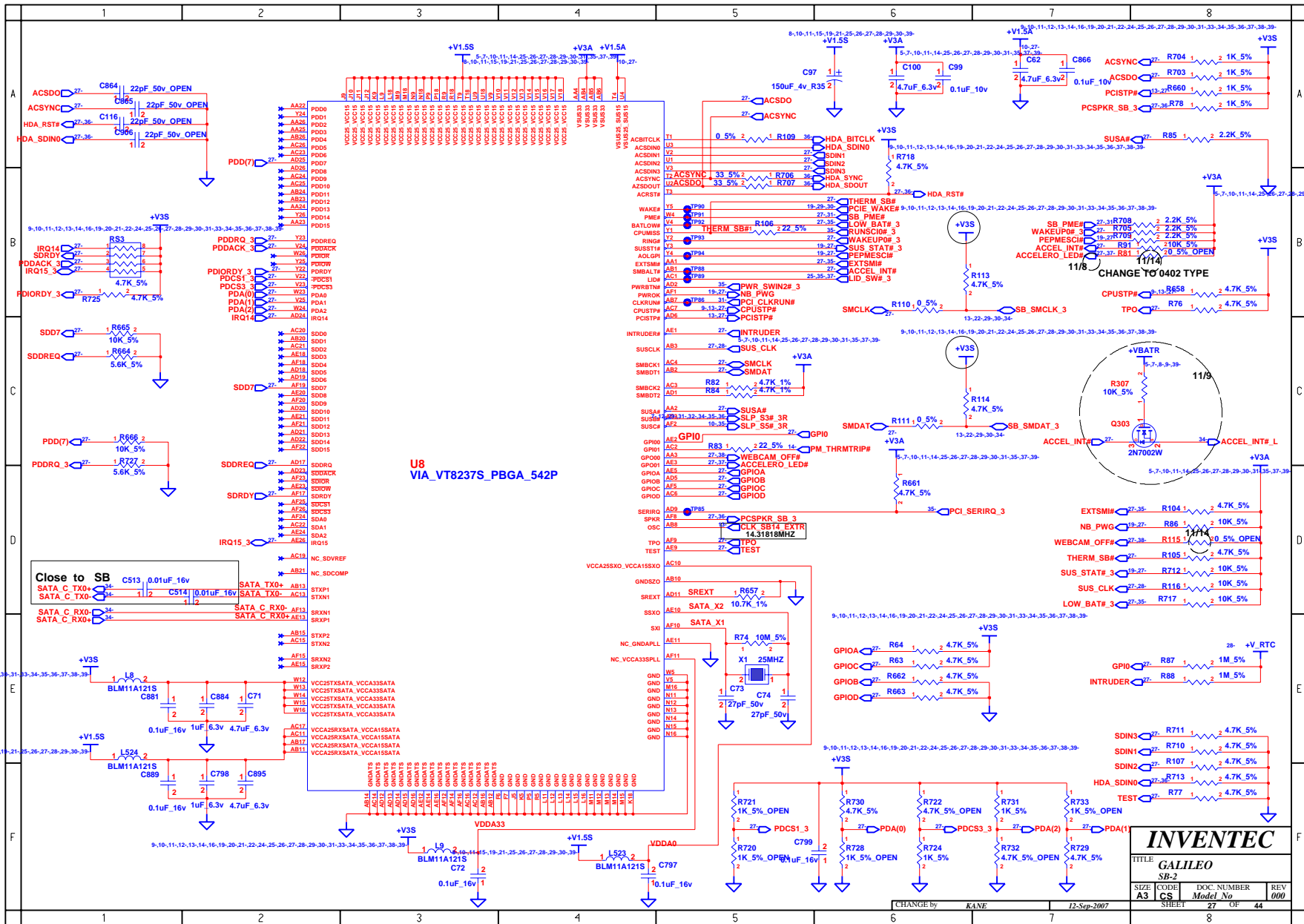
SIZE A3	CODE CS	DOC. NUMBER	REV 000
SHEET 25		OF 44	



INVENTEC

TITLE: GALILEO SB-1
 SIZE: A3 CODE: CS DOC. NUMBER: Model No. REV: 000
 SHEET: 26 OF 44

11/14 change the part by buyer's suggestion.



U8
VIA_VT8237S_PBG4_542P

Close to SB
SATA_C_TX0+ C513 0.01uF 16v
SATA_C_TX0- C514 0.01uF 16v
SATA_C_RX0+ C515 0.01uF 16v
SATA_C_RX0- C516 0.01uF 16v

INVENTEC
 TITLE GALILEO
 SB-2
 SIZE CODE DOC NUMBER REV
 A3 CS Model No 000
 SHEET 27 OF 44

CHANGE by RAJVE 12-Sep-2007

GPIO GPIOA
 LL: 100MHz
 LH: 133MHz
 HL: 200MHz
 HH: Auto (166MHz can be obtained) *

GPIOB = IOQ Depth
 L: 8 Level *
 H: 1 Level

GPIOD = AGTL + internal Pullups
 L: Enable *
 H: Disable

PDA(0) = Vlink compensation
 L: Auto mode
 H: Manual mode *

PDA(1) = V4-Lite Capability
 L: Disable *
 H: Enable

PDA(2) = V4 Capability
 L: Disable *
 H: Enable

PDCS3_3 = V-Link reference
 L: 0.75 (8251) *
 H: 0.9V (8237)

PDDACK_3
 V-Link 4X mode
 L: Auto mode
 H: Manual mode *

PDCS1_3
 SATA Stagger Spin Up
 L: Disable
 H: Enable *

ACSYNC
 LPC FWH command
 L: Enable
 H: Disable *

ACSDO
 Auto Reboot
 L: Enable
 H: Disable *

PCISTP#
 100MHz Vlink clock
 L: Enable
 H: Disable *

SEEDI
 LAN shadow EEPROM
 L: Disable *
 H: Enable

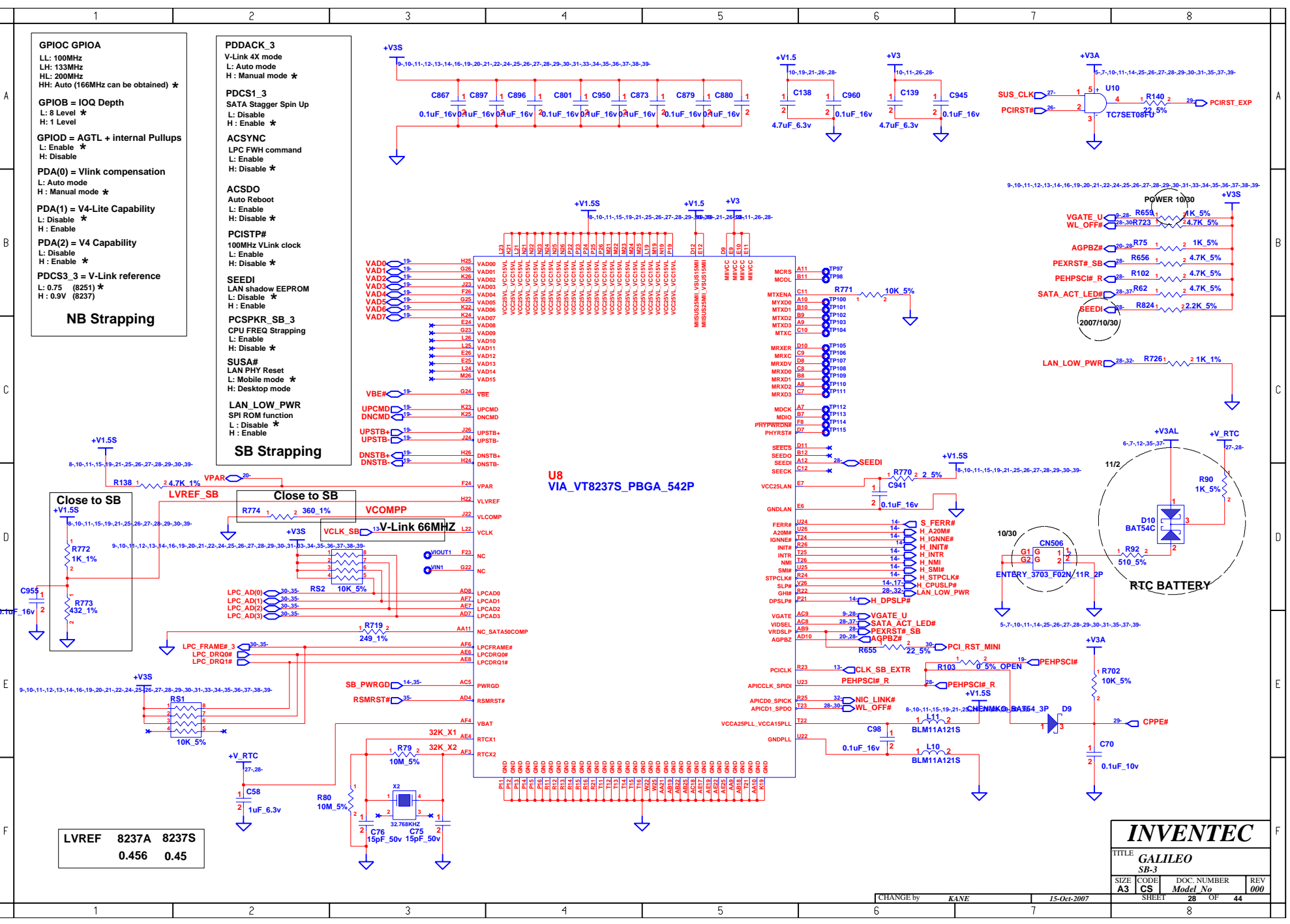
PCSPKR_SB_3
 CPU FREQ Strapping
 L: Enable
 H: Disable *

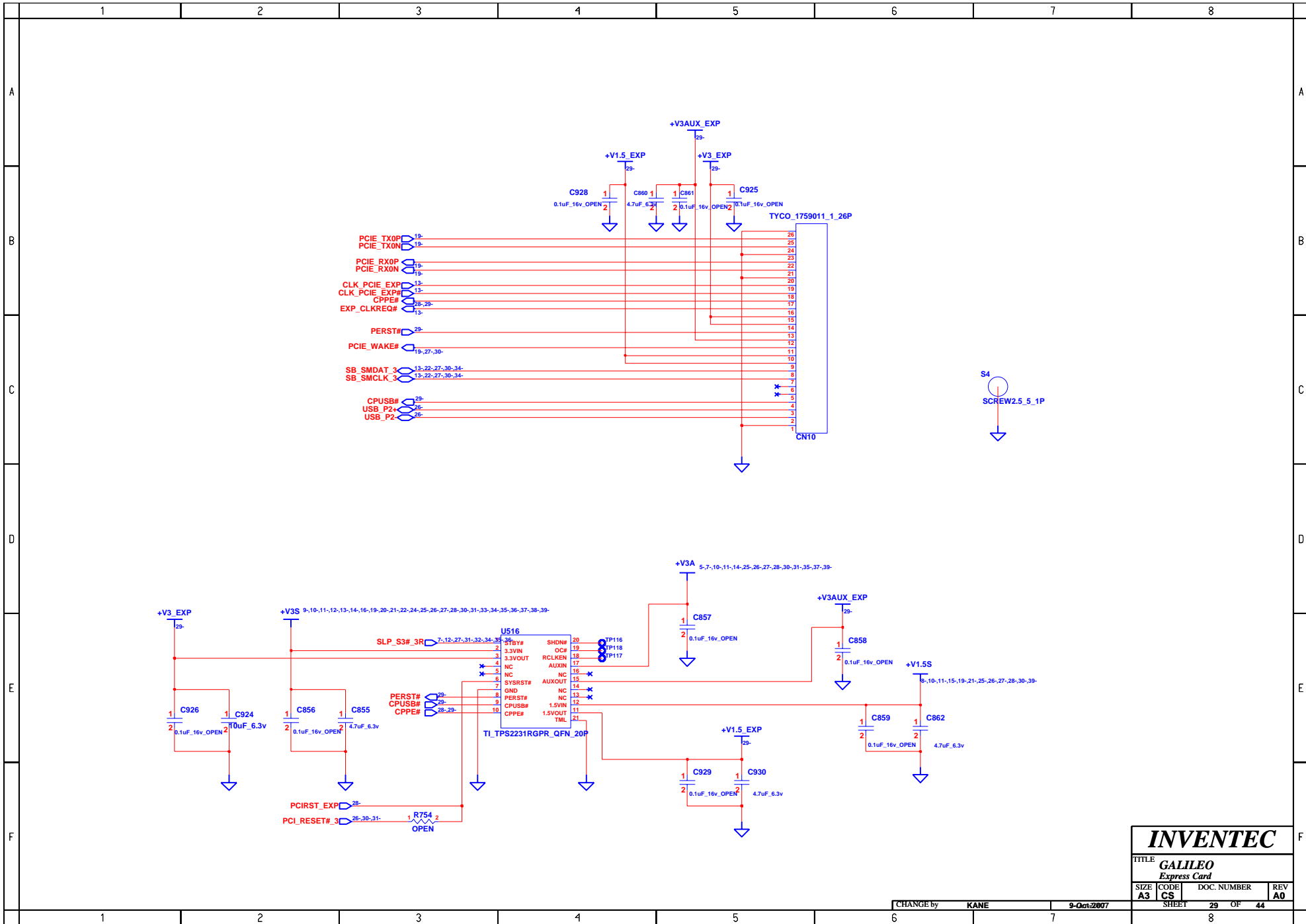
SUSA#
 LAN PHY Reset
 L: Mobile mode *
 H: Desktop mode

LAN_LOW_PWR
 SPI ROM function
 L: Disable *
 H: Enable

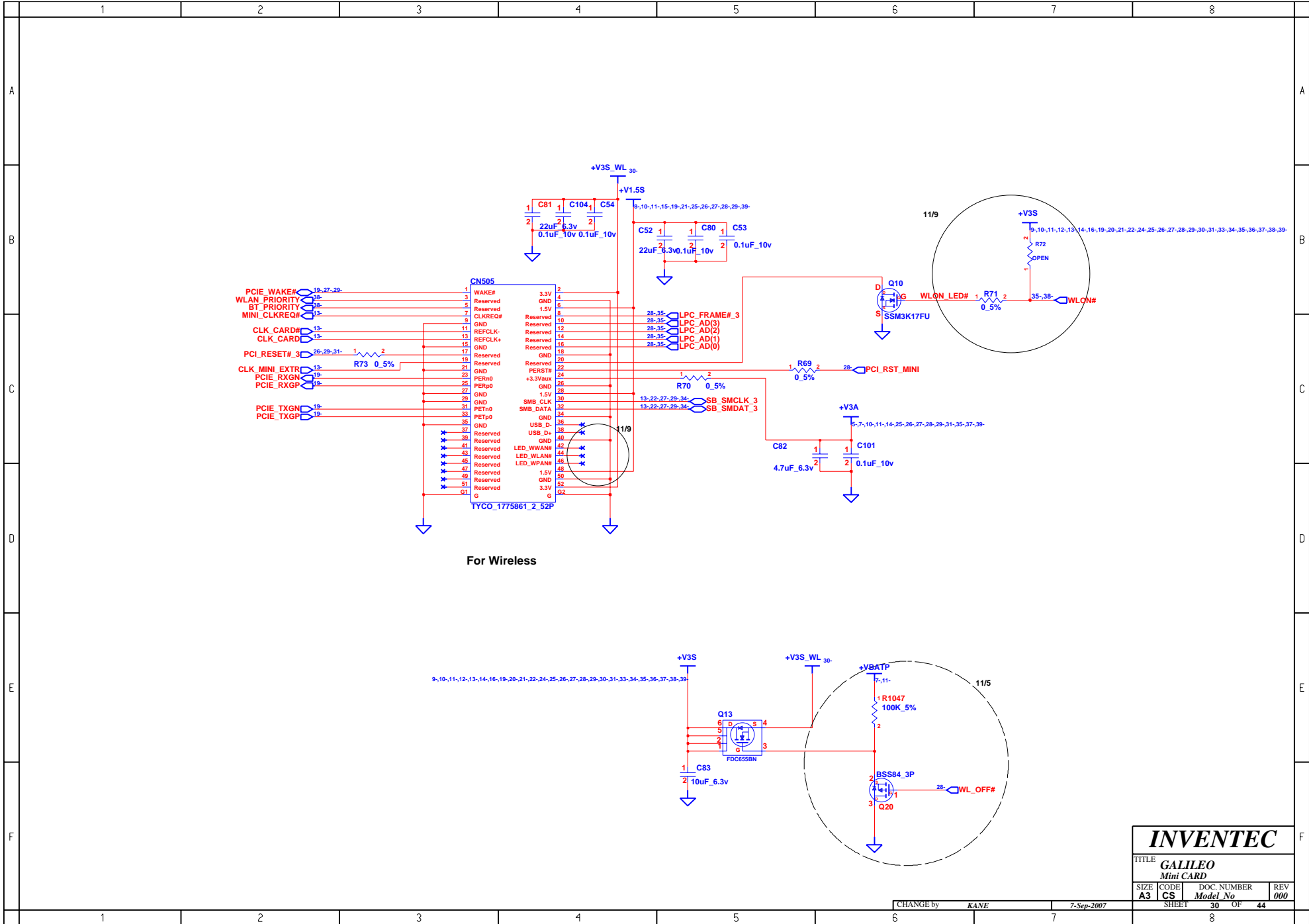
NB Strapping

SB Strapping





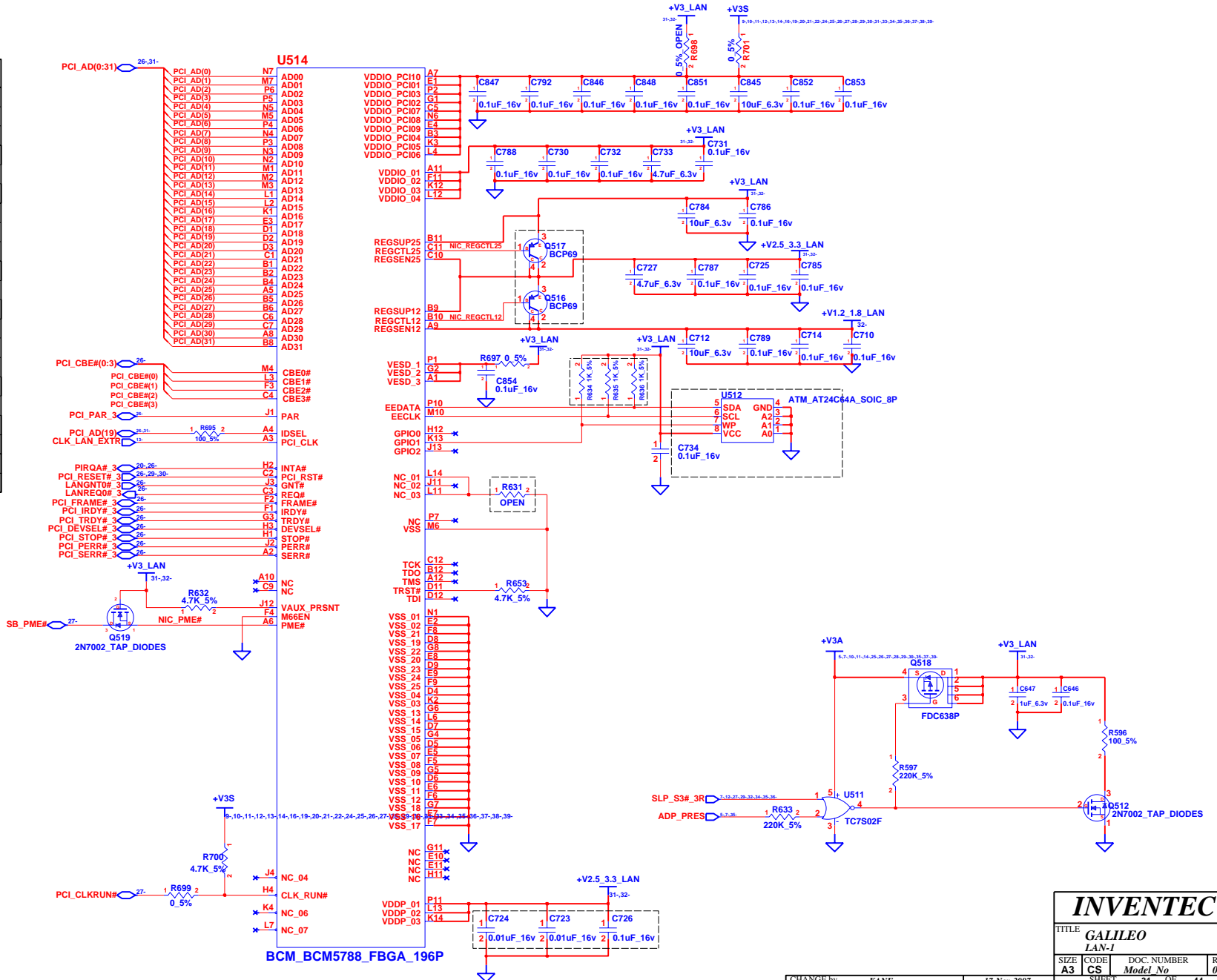
INVENTEC			
TITLE GALILEO Express Card			
SIZE A3	CODE CS	DOC. NUMBER 29 OF 44	REV A0
CHANGE by KANE		9-Oct-2007	
SHEET		29 OF 44	



INVENTEC			
TITLE GALILEO			
Mini CARD			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
CHANGE by KANE		7-Sep-2007	SHEET 30 OF 44

REF (U23)	(FF) BCM5788	(DF) BCM4401
R698	NI	0.5%
R701	0.5%	NI
Q517	BCP69	NI
Q516	BCP69	NI
R634	1K_5%	NI
R635	1K_5%	NI
R636	1K_5%	NI
U512	AT24C64A	NI
U30	NI	AT93C46A
R631	NI	0.5%
C724	0.01uF_16V	NI
C723	0.01uF_16V	NI
C726	0.01uF_16V	NI

Lead Free Parts
 FF:BCM5788MKFBG
 DF:BCM4401KFBG

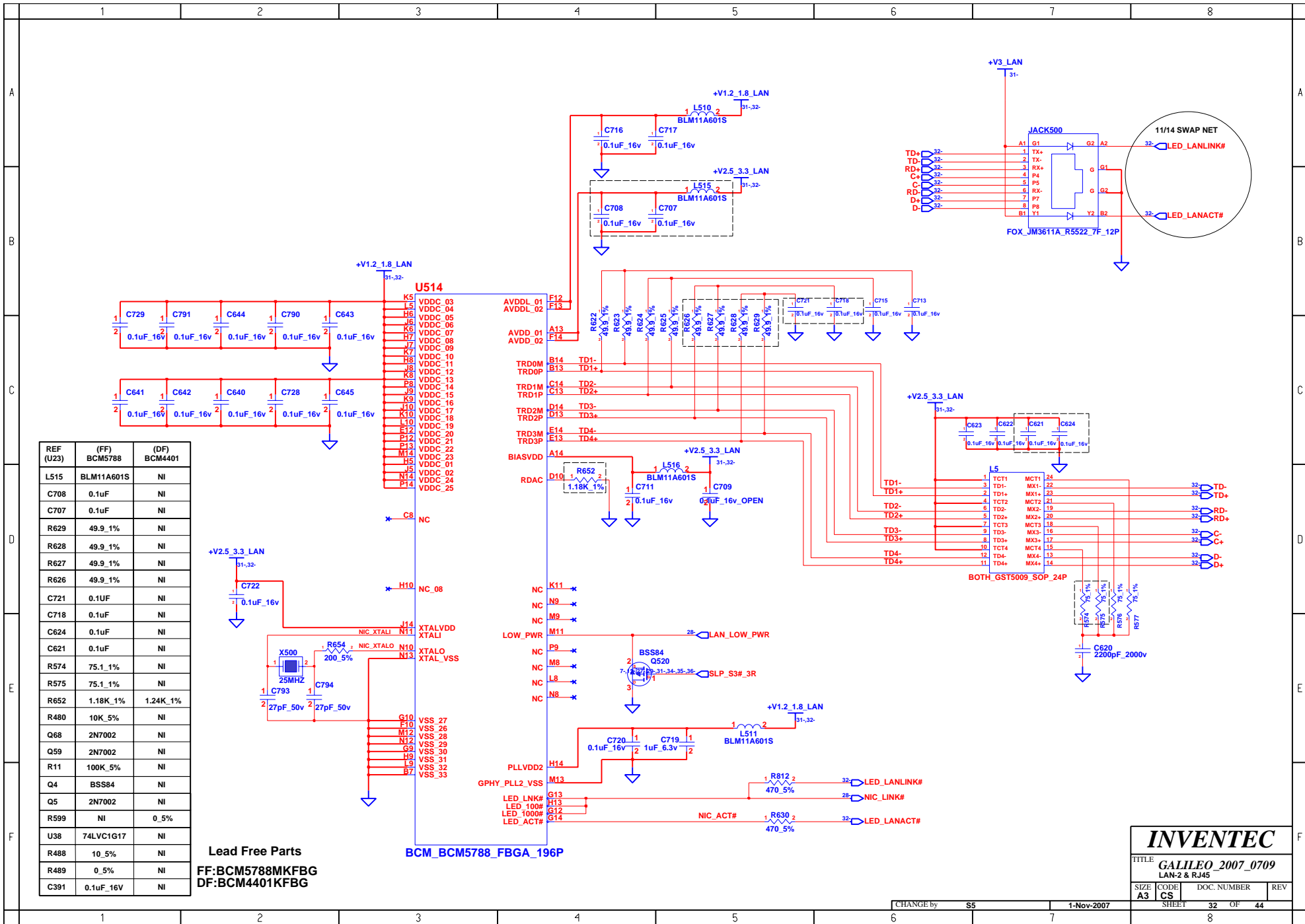


INVENTEC

TITLE
GALILEO LAN-1

SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
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CHANGE by **KANE** 17-Nov-2007 SHEET **31** OF **44**

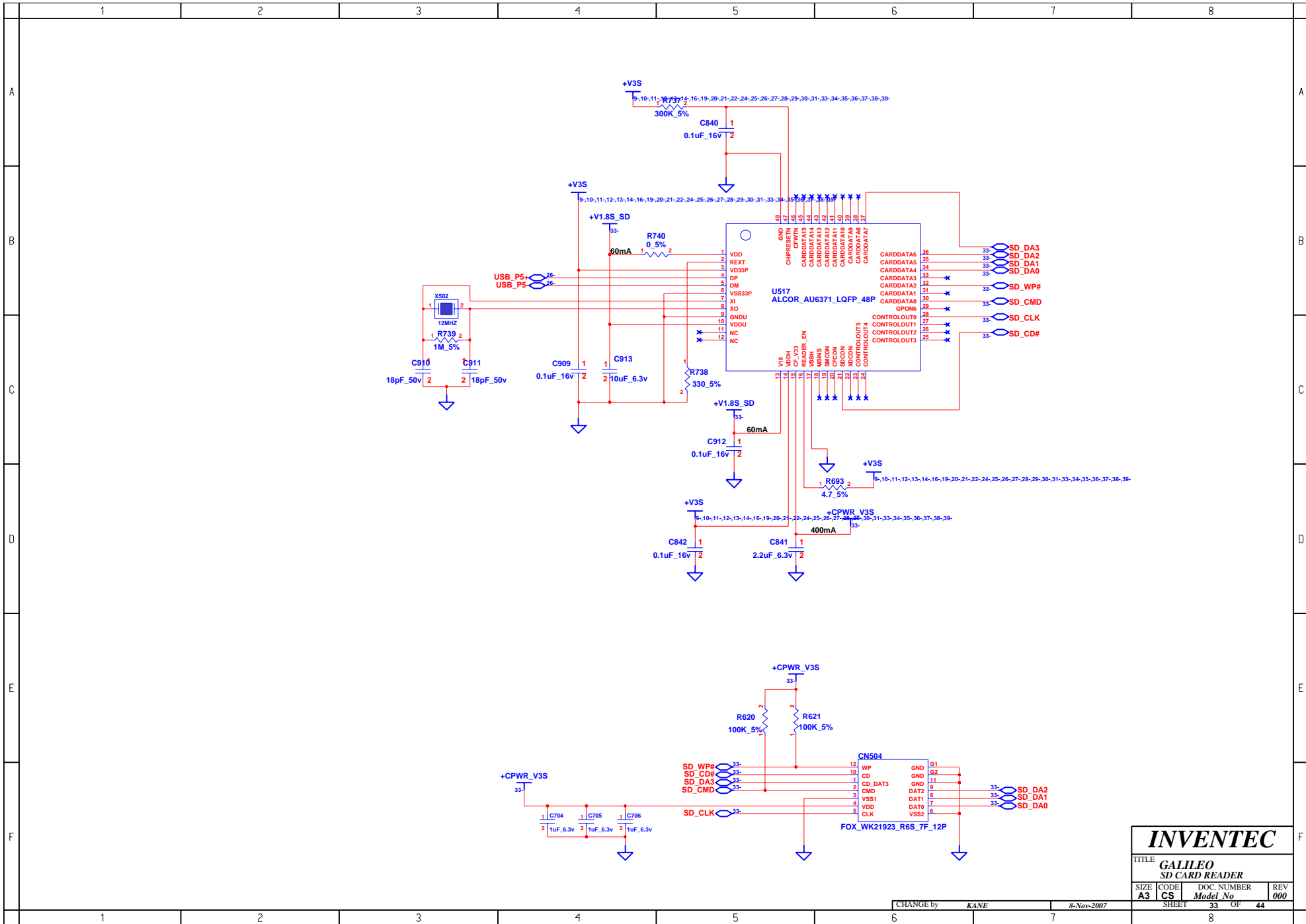


REF (U23)	(FF) BCM5788	(DF) BCM4401
L515	BLM11A601S	NI
C708	0.1uF	NI
C707	0.1uF	NI
R629	49.9_1%	NI
R628	49.9_1%	NI
R627	49.9_1%	NI
R626	49.9_1%	NI
C721	0.1uF	NI
C718	0.1uF	NI
C624	0.1uF	NI
C621	0.1uF	NI
R574	75.1_1%	NI
R575	75.1_1%	NI
R652	1.18K_1%	1.24K_1%
R480	10K_5%	NI
Q68	2N7002	NI
Q59	2N7002	NI
R11	100K_5%	NI
Q4	BSS84	NI
Q5	2N7002	NI
R599	NI	0_5%
U38	74LVC1G17	NI
R488	10_5%	NI
R489	0.5%	NI
C391	0.1uF_16V	NI

Lead Free Parts
 FF:BCM5788MKFBG
 DF:BCM4401KFBG

BCM_BCM5788_FBGA_196P

INVENTEC
 TITLE GALILEO_2007_0709
 LAN-2 & RJ45
 SIZE A3 CODE CS DOC NUMBER SHEET 32 OF 44 REV



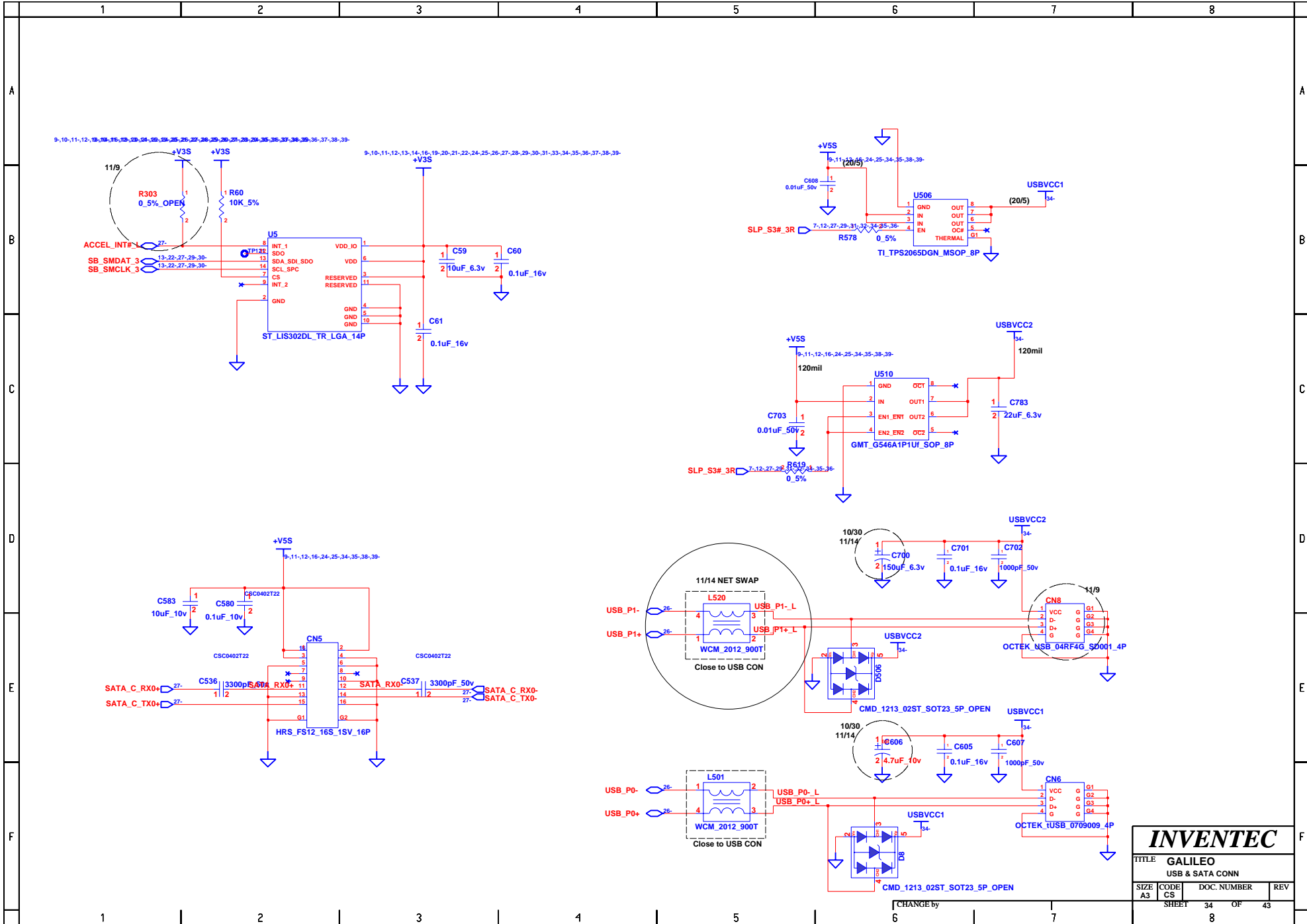
INVENTEC

TITLE
**GALILEO
 SD CARD READER**

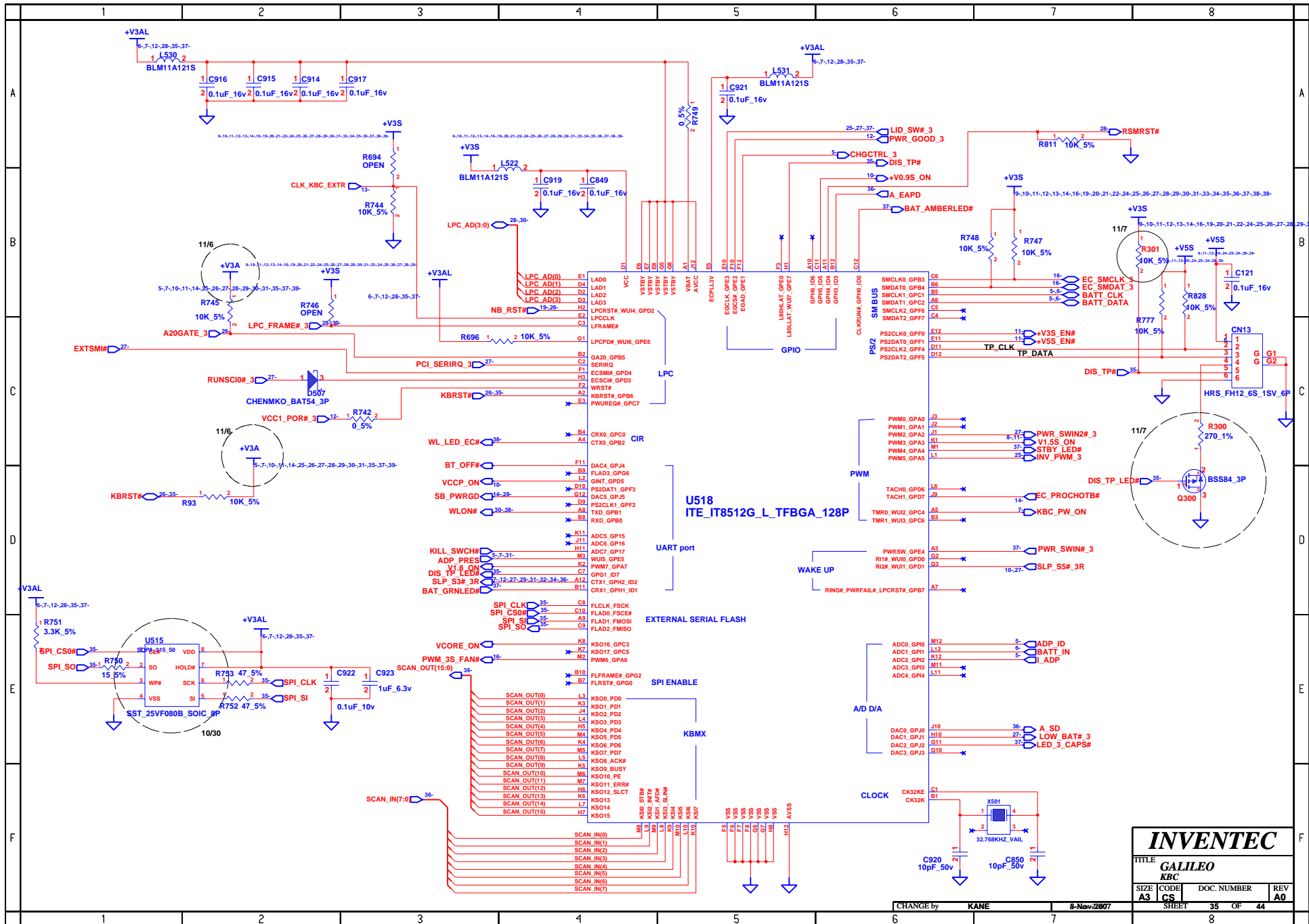
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000

CHANGE by **KANE** 8-Nov-2007

SHEET **33** OF **44**

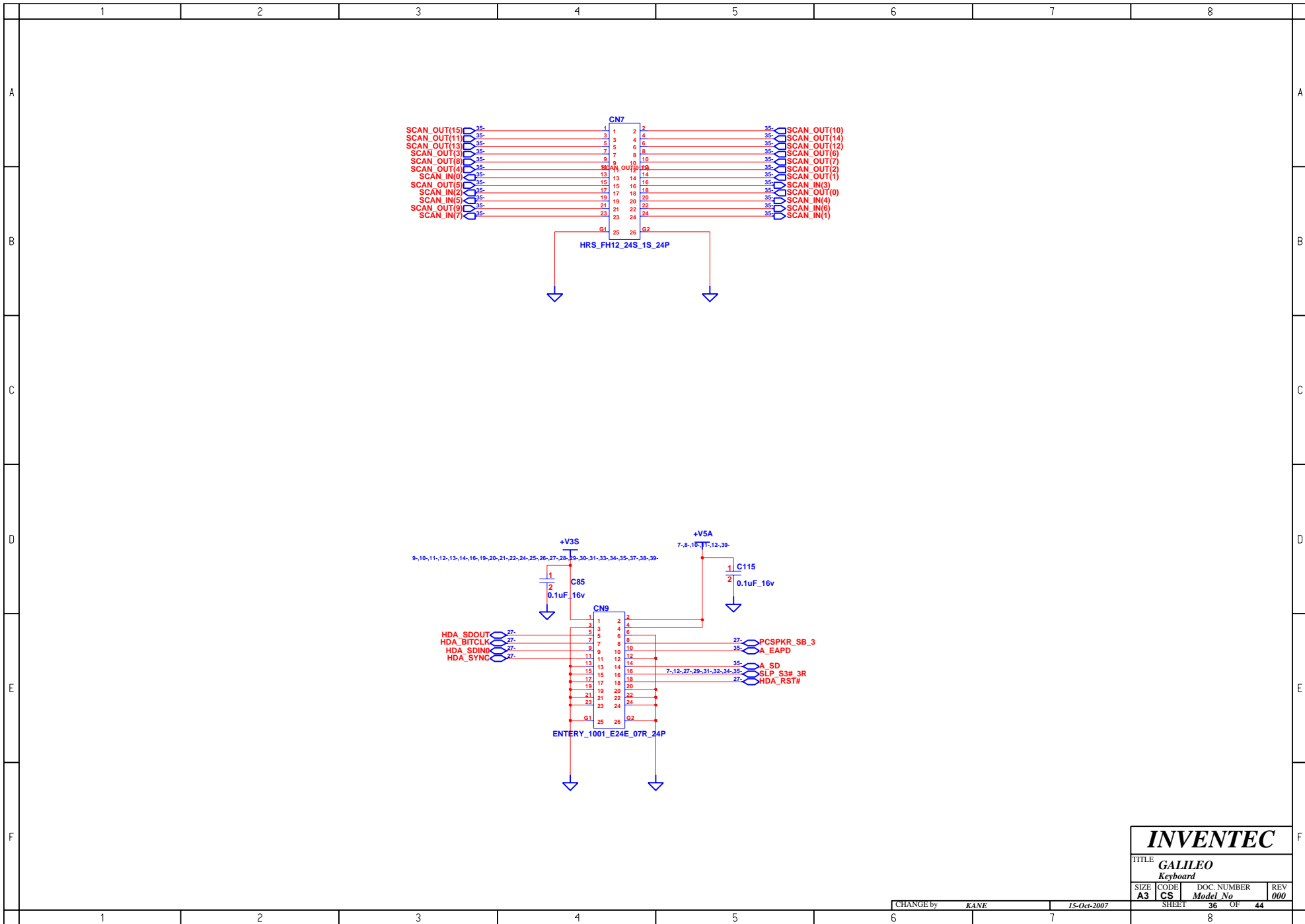


INVENTEC			
TITLE GALILEO USB & SATA CONN			
SIZE A3	CODE CS	DOC. NUMBER 34 OF 43	REV 8
CHANGE by		SHEET	

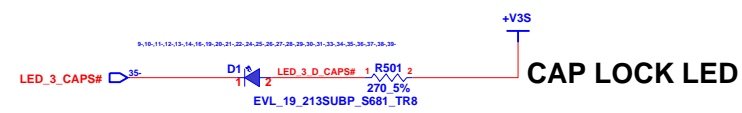
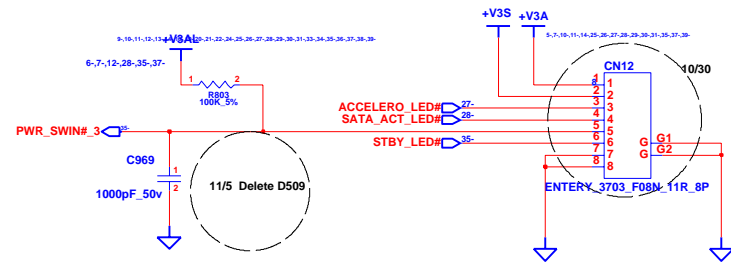
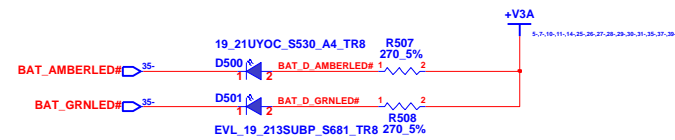
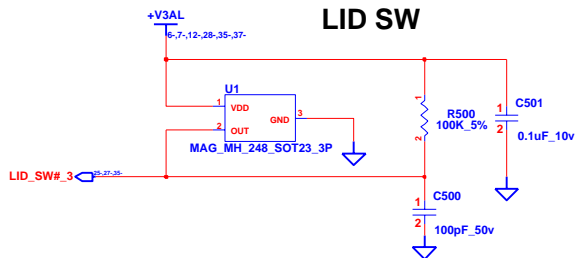


INVENTEC			
TITLE GALILEO KBC			
SIZE A3	CODE CS	DOC. NUMBER	REV A0
SHEET 1		35 OF 44	

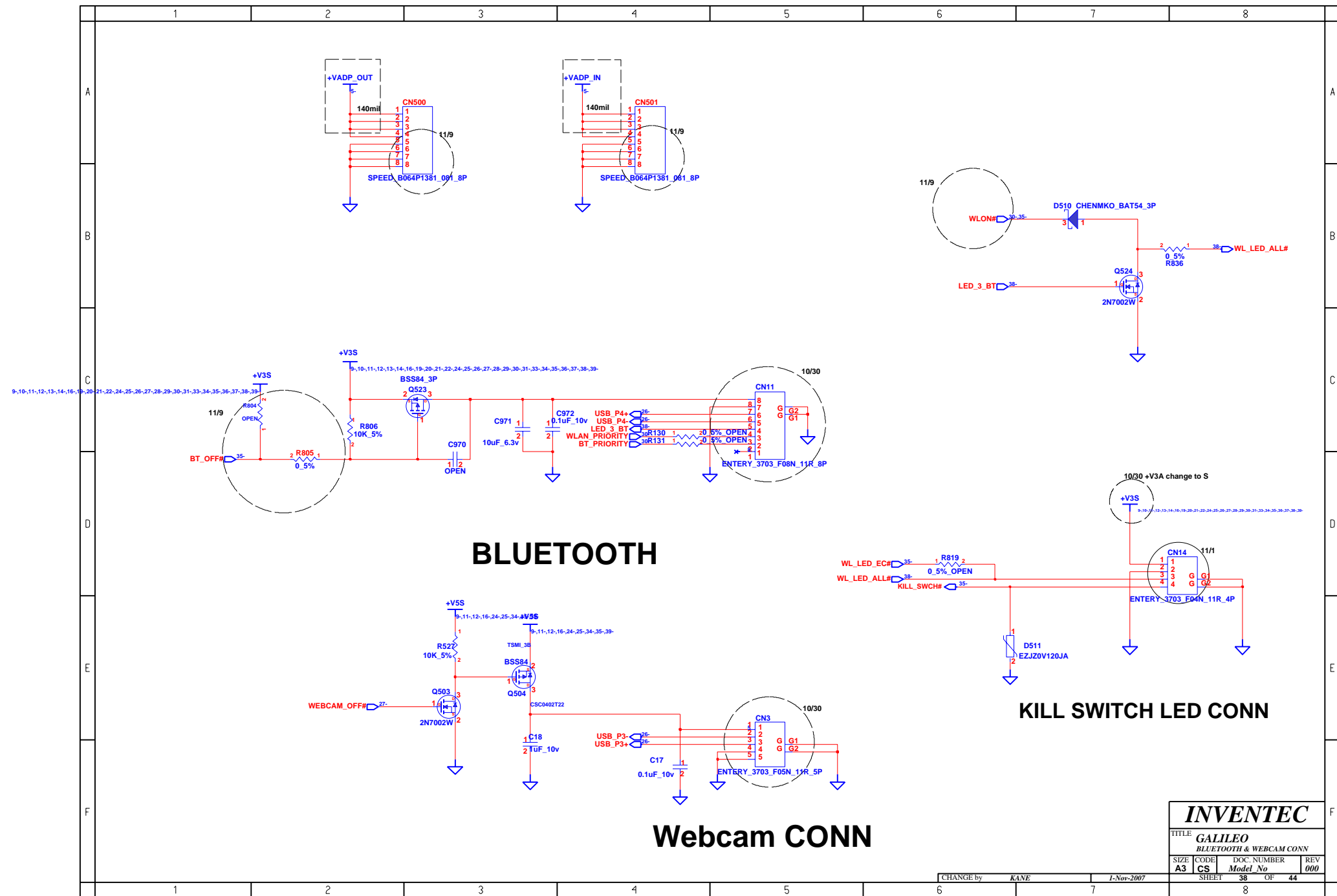
CHANGE by KANE 8-Nov-2007



INVENTEC			
TITLE GALILEO Keyboard			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
CHANGE by KANE		15-Oct-2007	SHEET 36 OF 44

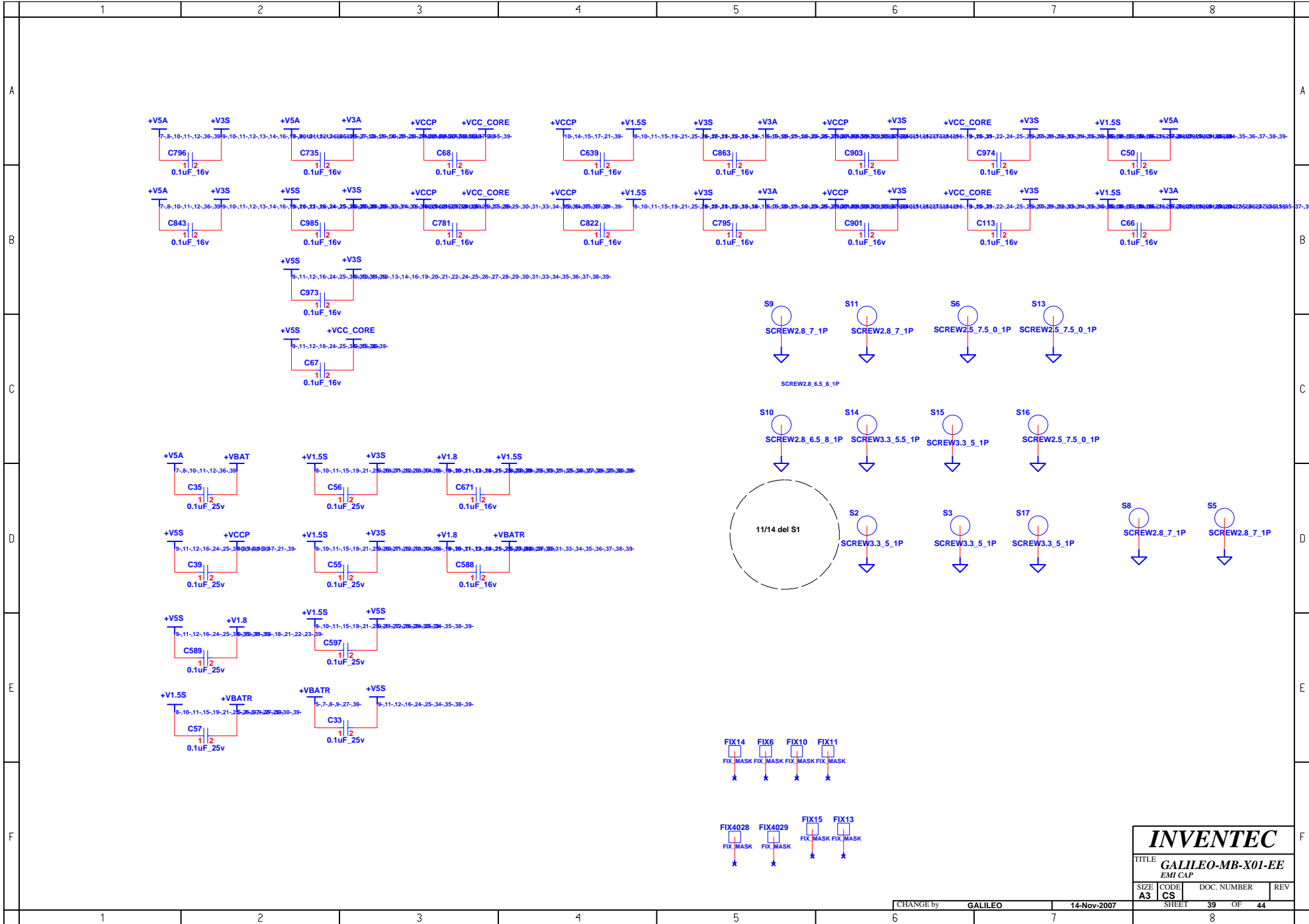


INVENTEC			
TITLE GALILEO-MB-X01-EE			
Button Board			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	37 OF 44	
CHANGE by GALILEO		5-Nov-2007	
SHEET		8	



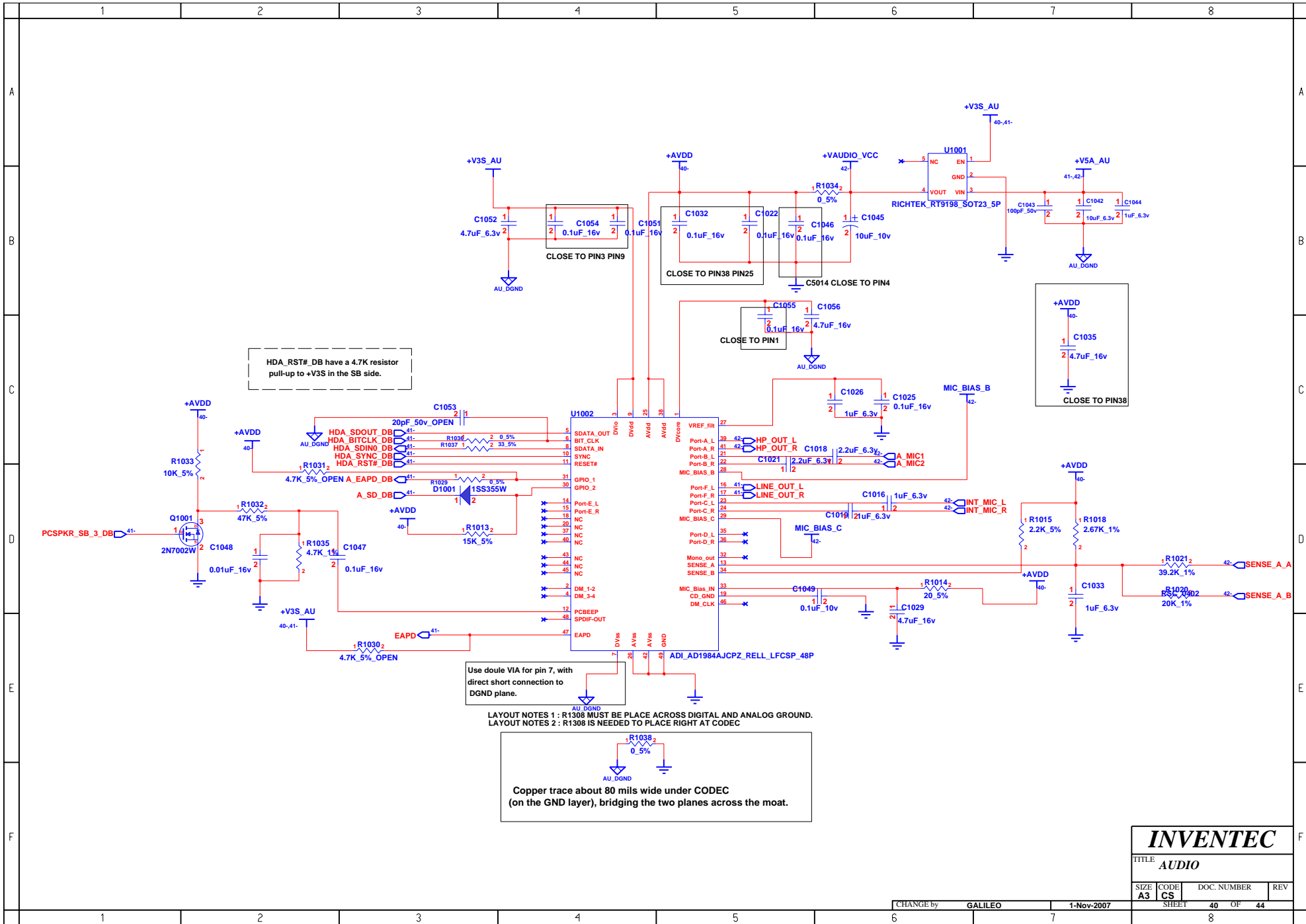
INVENTEC			
TITLE GALILEO			
BLUETOOTH & WEBCAM CONN			
SIZE A3	CODE CS	DOC. NUMBER	REV 000
Model No		SHEET 38 OF 44	

CHANGE by KANE L-Nov-2007



INVENTEC			
TITLE GALILEO-MB-X01-EE			
EMI CAP			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	SHEET 39 OF 44	

CHANGE by GALILEO 14-Nov-2007

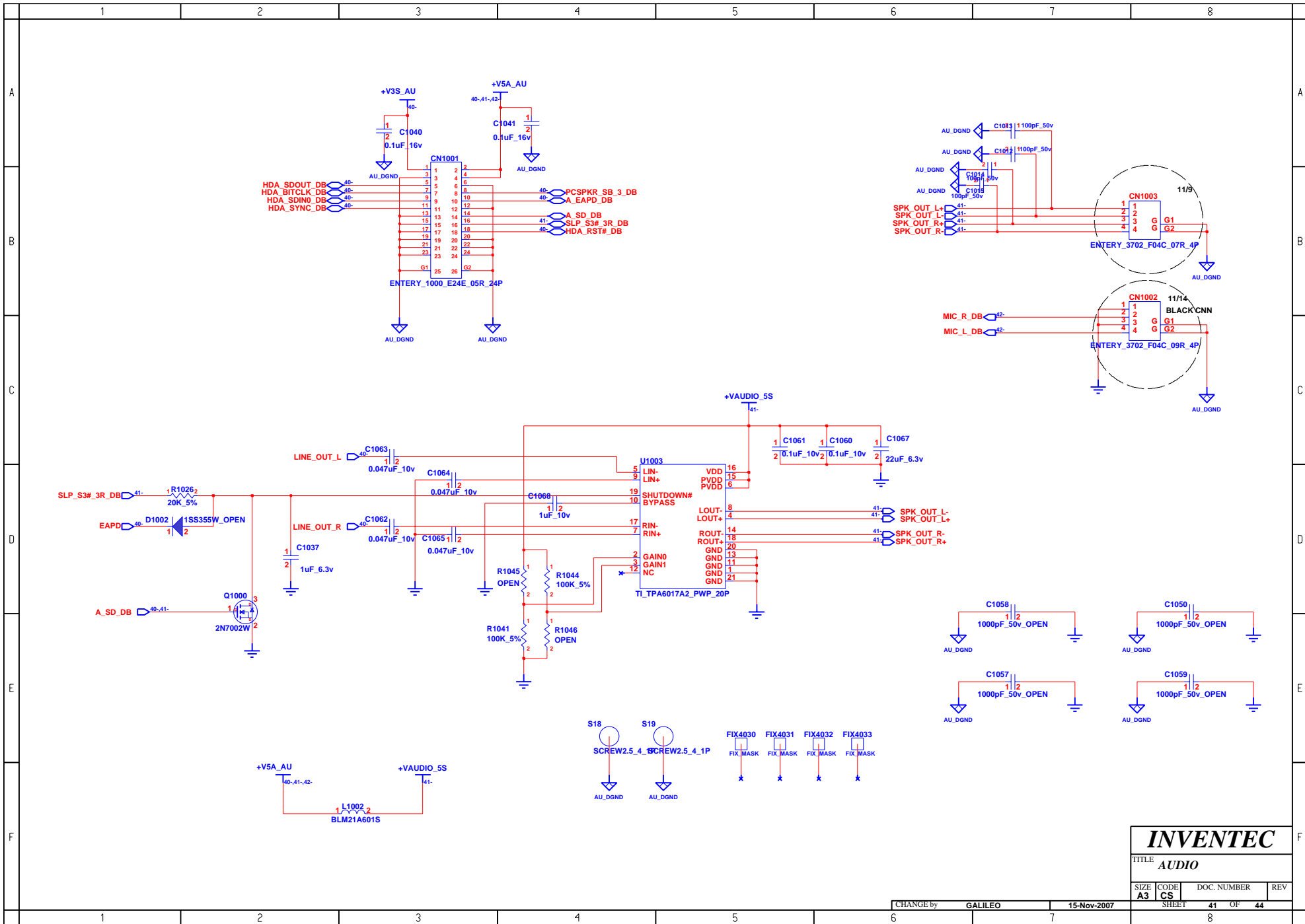


INVENTEC

TITLE
AUDIO

SIZE A3	CODE CS	DOC. NUMBER SHEET	REV 40 OF 44
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CHANGE by GALILEO 1-Nov-2007

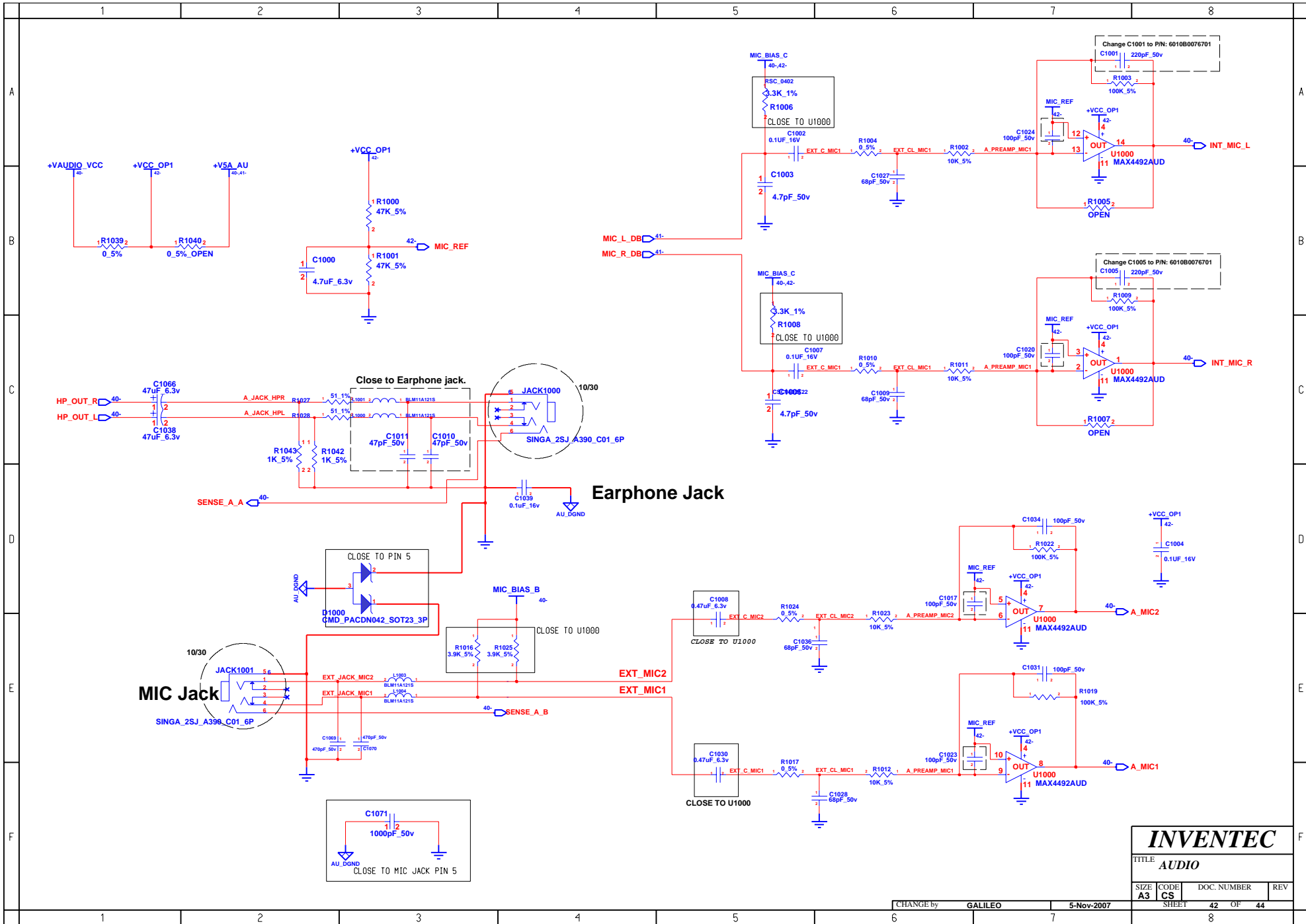


INVENTEC

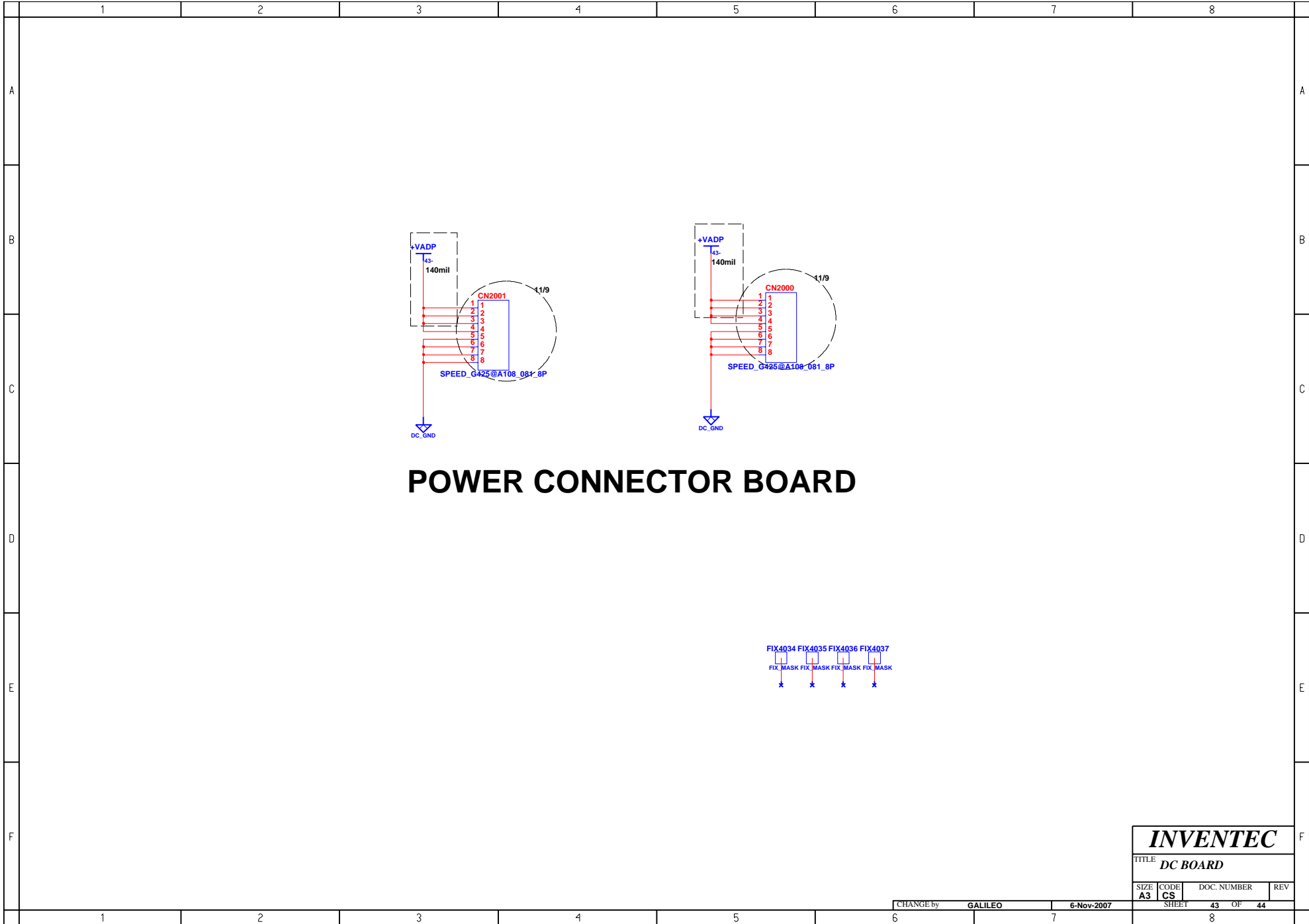
TITLE
AUDIO

SIZE	CODE	DOC. NUMBER	REV
A3	CS	SHEET 41 OF 44	

CHANGE by GALILEO 15-Nov-2007



INVENTEC
 TITLE: AUDIO
 SIZE: A3 CODE: CS DOC. NUMBER: SHEET 42 OF 44 REV: _____



POWER CONNECTOR BOARD

INVENTEC

TITLE **DC BOARD**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	43 OF 44	

CHANGE by GALILEO 6-Nov-2007

SHEET 8

	1	2	3	4	5	6	7	8		
A	<p>VERSION DB TO SI</p> <p>10/30</p> <p>1. PAGE 8 --- change R57 to 11.3K 2. PAGE 9 --- Add R28-R35 (0 ohm 5%) 3. PAGE 9 --- Add C1072 (330uF_OPEN) . Add a POWERPAD 4. PAGE 11 --- Delete R17(470hm) , R15 (100K), C17 (0.1uF_25V) 5. PAGE 11 --- Delete Q753, Q748(NMOS), Q4 (PMOS) 6. PAGE 11 --- Add a PAD and connects +V3A and +V3 7. PAGE 14 --- Q510, Q514 change to NMOS (SSM3K17FU) 8. PAGE 28 --- change R659 to 1K 9. PAGE 28 --- change SEEDI from GND to +V3S 10. PAGE 34 --- change C700,C606 to 150uF_6.3V 11. PAGE 20 --- change R673 to OPEN, change R736 to 10K_1% 12. PAGE 38 --- change pin1 from +V3A to +V3S 13. PAGE 35 --- change U515 to SST25V080B (1M FLASH)</p> <p>11/1</p> <p>1. PAGE 25 --- Add CN15 (6 pin connector) for LVDS 2. PAGE 38 --- change CN14 to 4pin connector</p> <p>11/2</p> <p>1. PAGE 41 --- change 8 pin connector to 4pin connector*2 for SPK and MIC 2. PAGE 28 --- Delete R89 ; change D10 to BAT54C ; change R90 to 1K ohm ; change R92 to 510 ohm.</p> <p>11/5</p> <p>1. PAGE 38 --- change CN500, CN501's pin 4,5 to connect +V5S 2. PAGE 43 --- change CN2000, CN2001's pin 4,5 to connect +V5S_DC 3. PAGE 30 --- Add Q20(PMOS), change R23 to R1047(100K) 4. PAGE 37 --- Delete D509 5. PAGE 24 --- Add D1003</p> <p>11/6</p> <p>1. PAGE 25 --- change pin 3,4 and pin 5,6 of CN15 2. PAGE 5 --- add R1048 3. PAGE 5 --- change c24 to 0.1UF 4. PAGE 5--- change C16 to 2.2uF 5. PAGE 35--- change KBRST#, A20GATE pull high to +V3A</p> <p>11/7</p> <p>1. PAGE 6 --- R12 change to 100ohms 2. PAGE 6 --- add D1006 3. PAGE 6 --- add D1007 4. PAGE 6--- change C15 47PF</p> <p>11/8</p> <p>1. PAGE 27 --- change ACCELERO_LED# pull high to +V3A</p> <p>11/9</p> <p>1. PAGE 38 --- LED_3_WLAN change to WLON# 2. PAGE 38 --- Delete U520, KILL_SWCH#. R805 change to 0 ohm 3. PAGE 34 --- ACCEL_INT# change to ACCEL_INT#_L 4. PAGE 30 --- CN505 pin 44 NC. Delete U7, R71 change to 0 ohm 5. PAGE 27 --- Delete R115, R81 . Add Q303, R307. 6. PAGE 27 --- SMCLK_SMDAT change to pull high to +V3S 7. PAGE 13 --- C981, C982 change to 15pF</p> <p>11/14</p> <p>1. PAGE 34 --- C606 change to 100uF_10V (Low ESR) 2. PAGE 34 --- C700 change to 150uF_6.3V (Low ESR) 3. PAGE 39 --- Del S1 4. PAGE 34 --- SWAP L520 D+ D- 5. PAGE 41 --- CHANGE CN1002 TO 6012B0239001 (BLACK) 6. PAGE 32 --- SWAP LED_LANACT# & LED_LANLINK# 7. PAGE 27 --- CHANGE R81 ,R115 TO 0402 TYPE</p> <p>11/16</p> <p>1. PAGE 24 --- C11 change to 0.1uF_6.3V 2. PAGE 24 --- C512 change to 0.22uF_6.3V 3. PAGE 24 --- R3,R9 change to 33ohm 4. PAGE 24 --- U500 change to 74LVC2G126DP 5. PAGE 24 --- Q1,Q3 change to SSM3K17FU 6. PAGE 24 --- Add R350,R351 (10Kohm)</p> <p>7. PAGE 24 --- D502 change to LL4148 8. PAGE 24 --- R5,R21 change to 4.7K 9. PAGE 24 --- Delete R6,R22 (10ohm) 10. PAGE 24 --- Change R1,R19 to 0 ohm 11. PAGE 24 --- R4,R10 change to 4.7Kohm 12. PAGE 9 --- C987 change to 22nF 13. PAGE 9 --- R128 change to 200ohm 14. PAGE 9 --- Add R121(100Kohm) 15 PAGE5--- D503change</p>									A
B										B
C										C
D										D
E										E
F										F
	1	2	3	4	5	6	7	8		

INVENTEC			
TITLE <i>History</i>			
SIZE A3	CODE CS	DOC. NUMBER SHEET 44 OF 44	REV
CHANGE by GALILEO		16-Nov-2007	
		8	