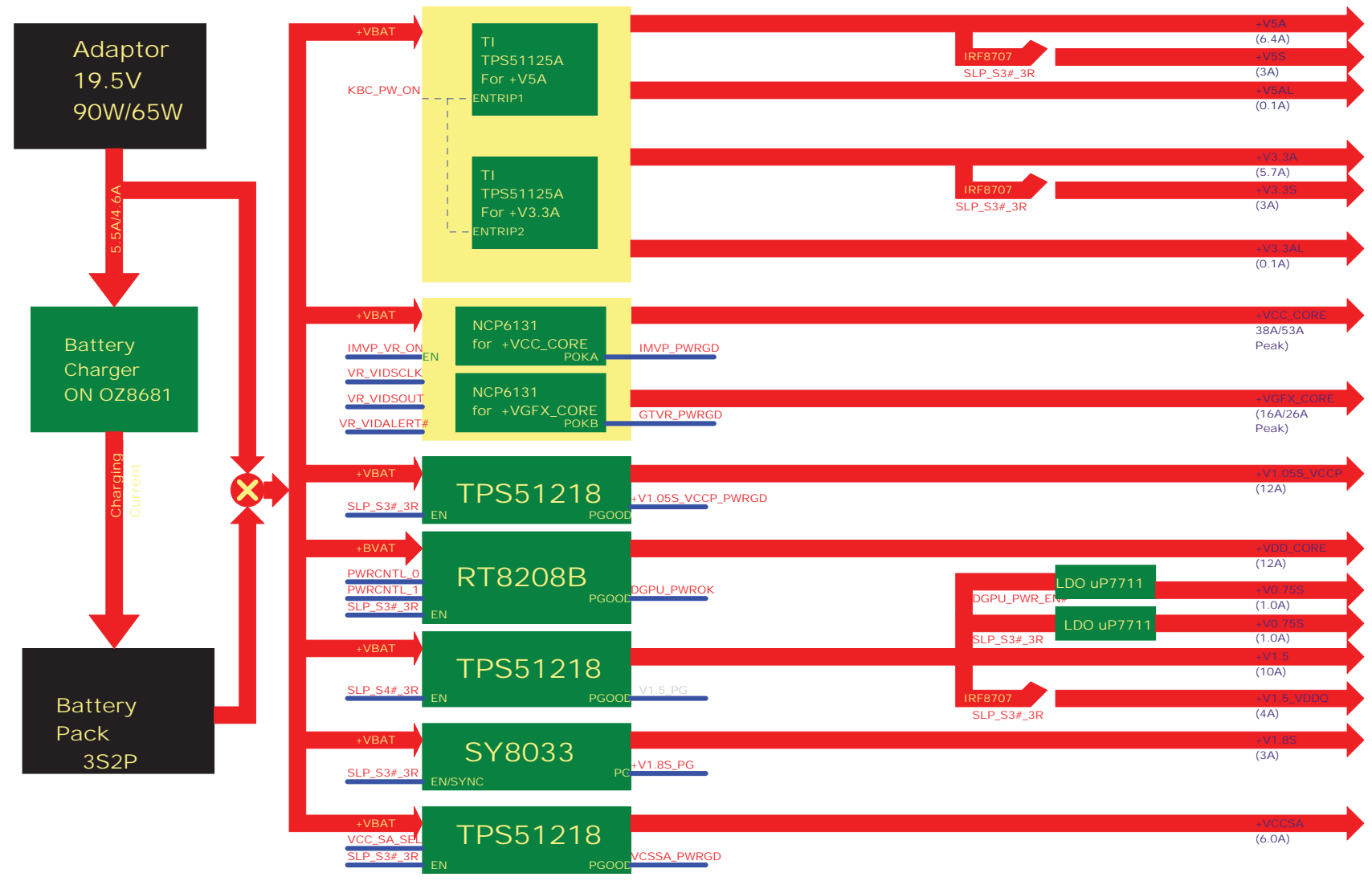
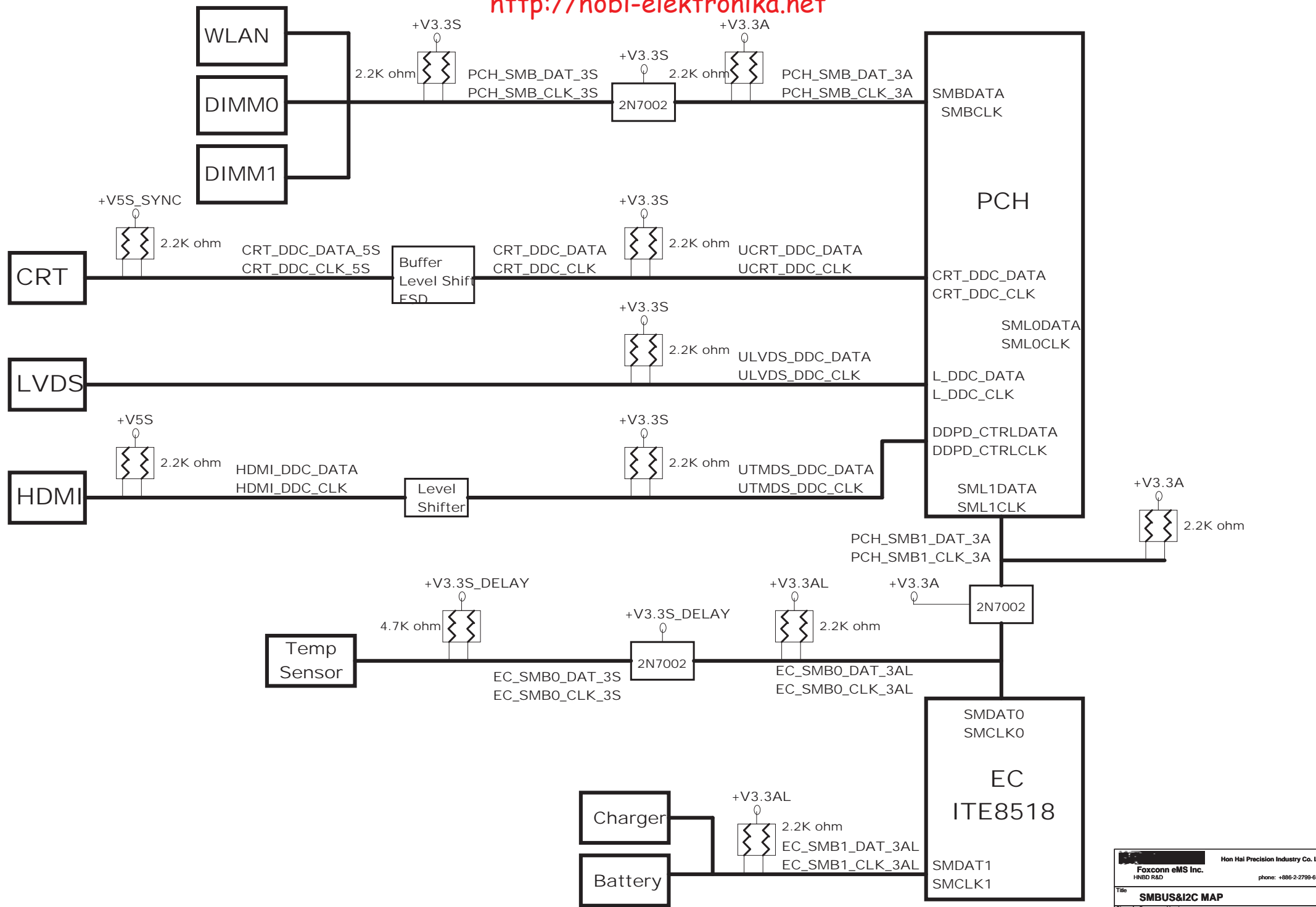


POWER MAP

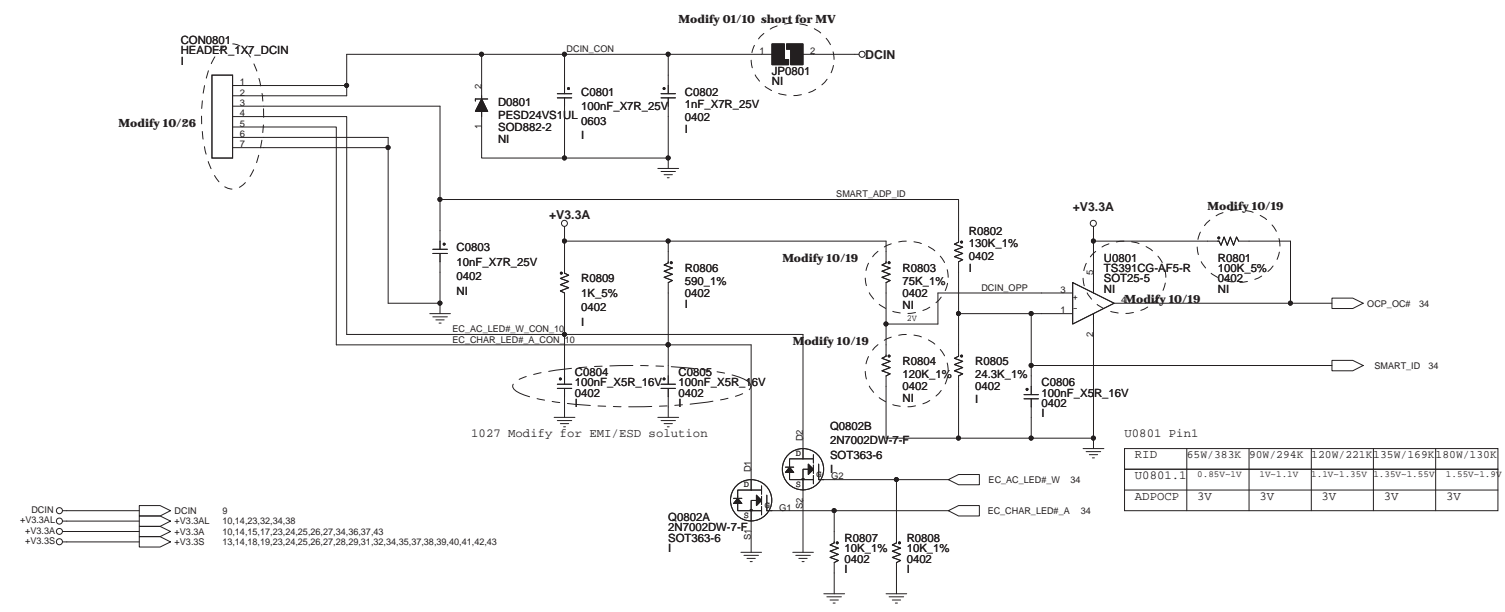




		Hon Hai Precision Industry Co. Ltd.	
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HNBD R&D			
Title			
BLANK			
Size	Document Number		Rev
A	CHICAGO		MV
Page Modified: Tuesday, March 08, 2011		08:28:58 (UTC/GMT)	Sheet 7 of 43

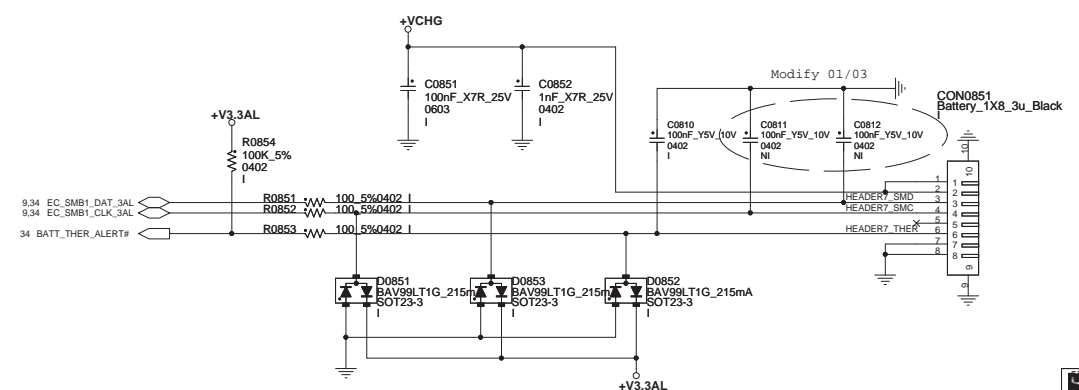
DC_JACK WIRE TO BOARD CONNECTOR

2010.1203.0

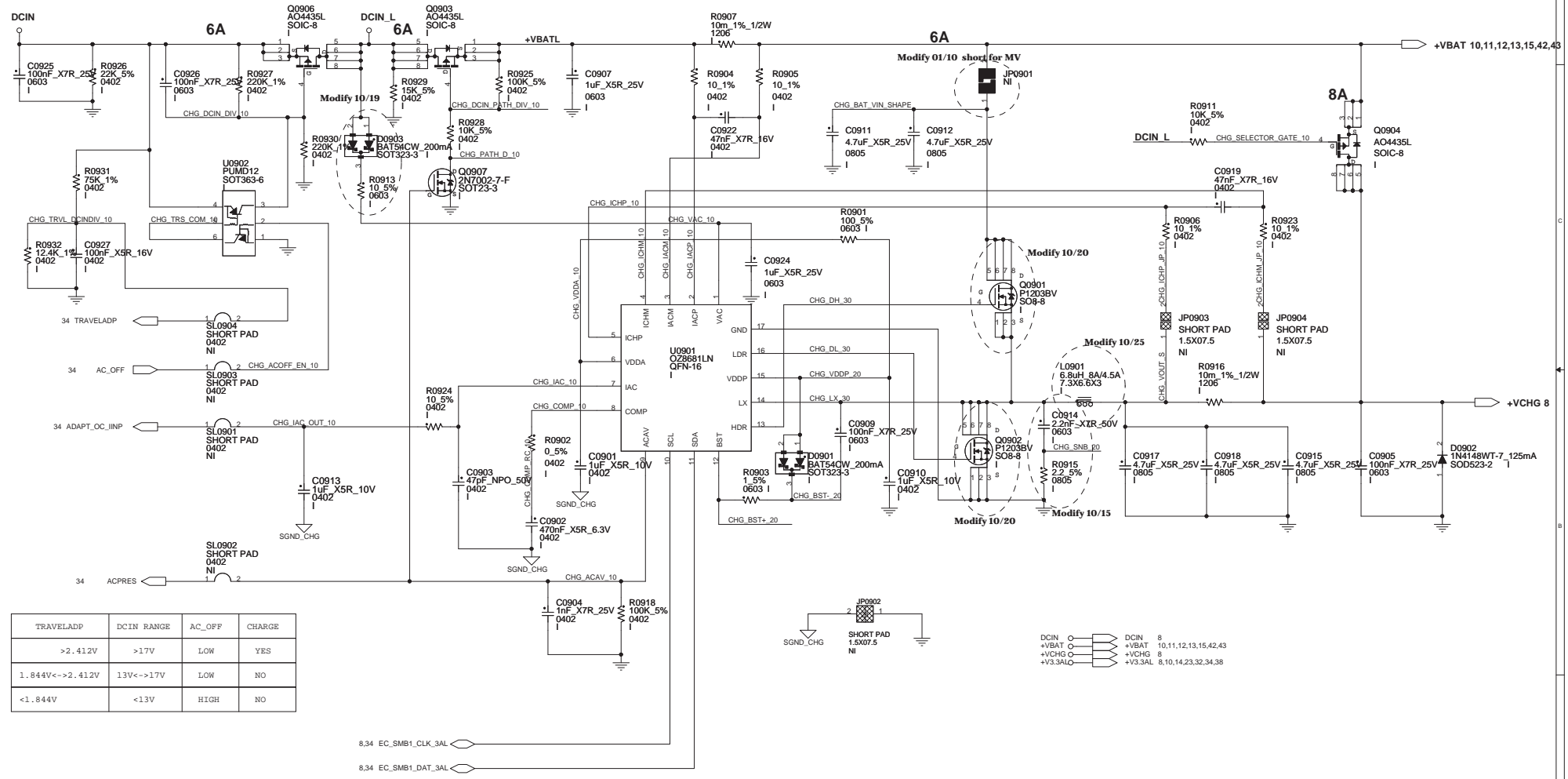


BATTERY CONNECTOR

2010.0914.0



BATTERY CHARGER

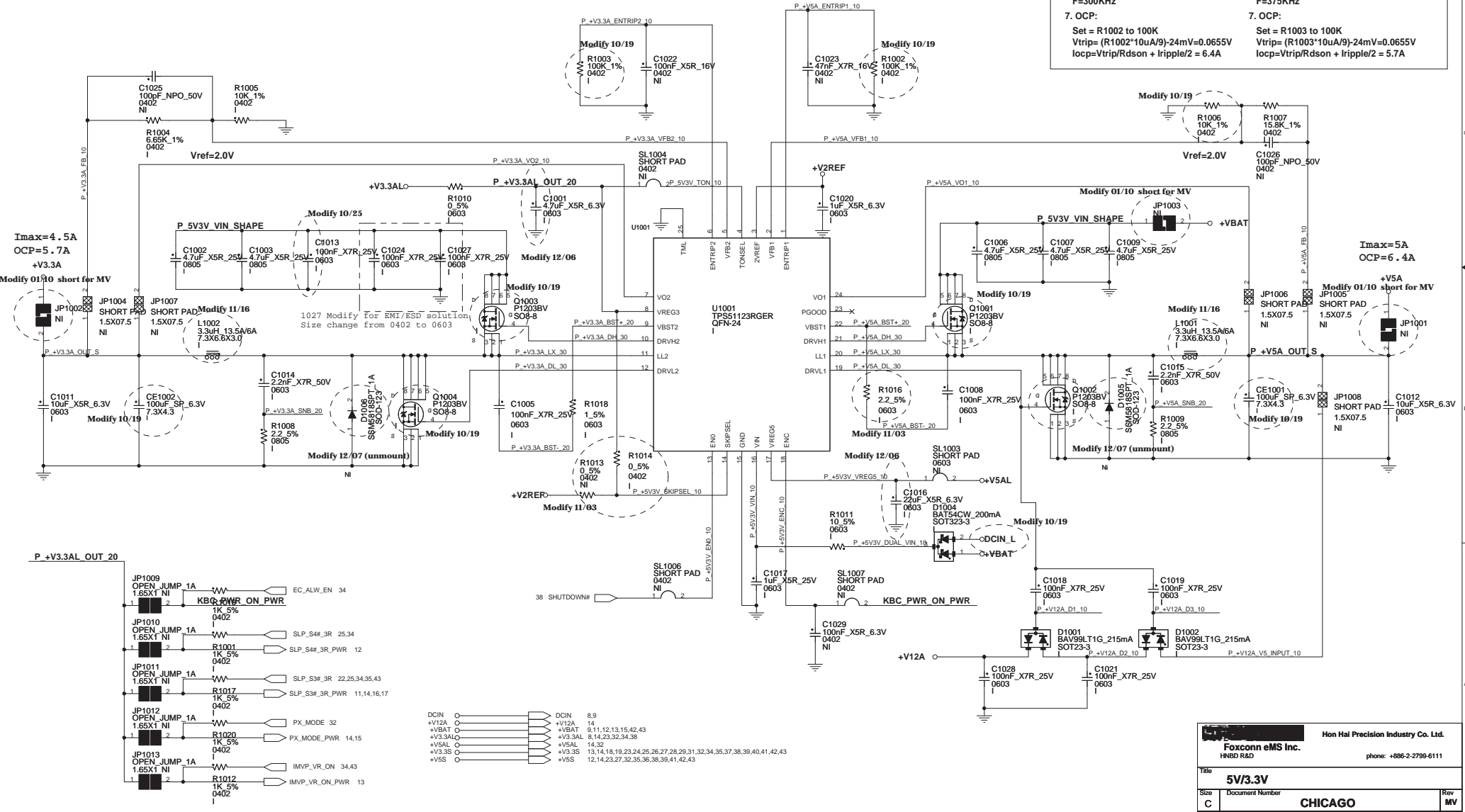


TRAVELADP	DCIN RANGE	AC_OFF	CHARGE
>2.412V	>17V	LOW	YES
1.844V<->2.412V	13V<->17V	LOW	NO
<1.844V	<13V	HIGH	NO

+V5A / +V3.3A POWER SUPPLY

2010.1103.0

- | | |
|---|--|
| <p>+V5A:</p> <ol style="list-style-type: none"> 1. I/P Current:
lin=Vo*Io/(0.75*Vin)=3.7A 2. Ripple Current:
Irip=3.72A 3. Ripple Voltage:
ESR/1=15mohm
Vrip=55.8mV 4. Inductor Spec:
Isat=13.5A
Idc=6A
DCR=30mohm 5. MOSFET Spec:
H-side MOSFET: IRF8707PBF
Rds(ON)=17.5mohm (Vgs=4.5 V)
I cont = 11A (T=25 °C)
I peak = 88A (Pause=10 us) 6. Frequency:
F=300KHz 7. OCP:
Set = R1002 to 100K
Vtrip= (R1002*10uA/9)-24mV=0.0655V
Iocp=Vtrip/Rdson + Iripple/2 = 6.4A | <p>+V3.3A:</p> <ol style="list-style-type: none"> 1. I/P Current:
lin=Vo*Io/(0.75*Vin)=2.2A 2. Ripple Current:
Irip=2.21A 3. Ripple Voltage:
ESR/1=15mohm
Vrip=33.15mV 4. Inductor Spec:
Isat=13.5A
Idc=6A
DCR=30mohm 5. MOSFET Spec:
L-side MOSFET: IRF8707PBF
Rds(ON)=17.5mohm (Vgs=4.5 V)
I cont = 11A (T=25 °C)
I peak = 88A (Pause=10 us) 6. Frequency:
F=375KHz 7. OCP:
Set = R1003 to 100K
Vtrip= (R1003*10uA/9)-24mV=0.0655V
Iocp=Vtrip/Rdson + Iripple/2 = 5.7A |
|---|--|



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Title: **5V/3.3V**

Size	Document Number	Rev
C	CHICAGO	MV

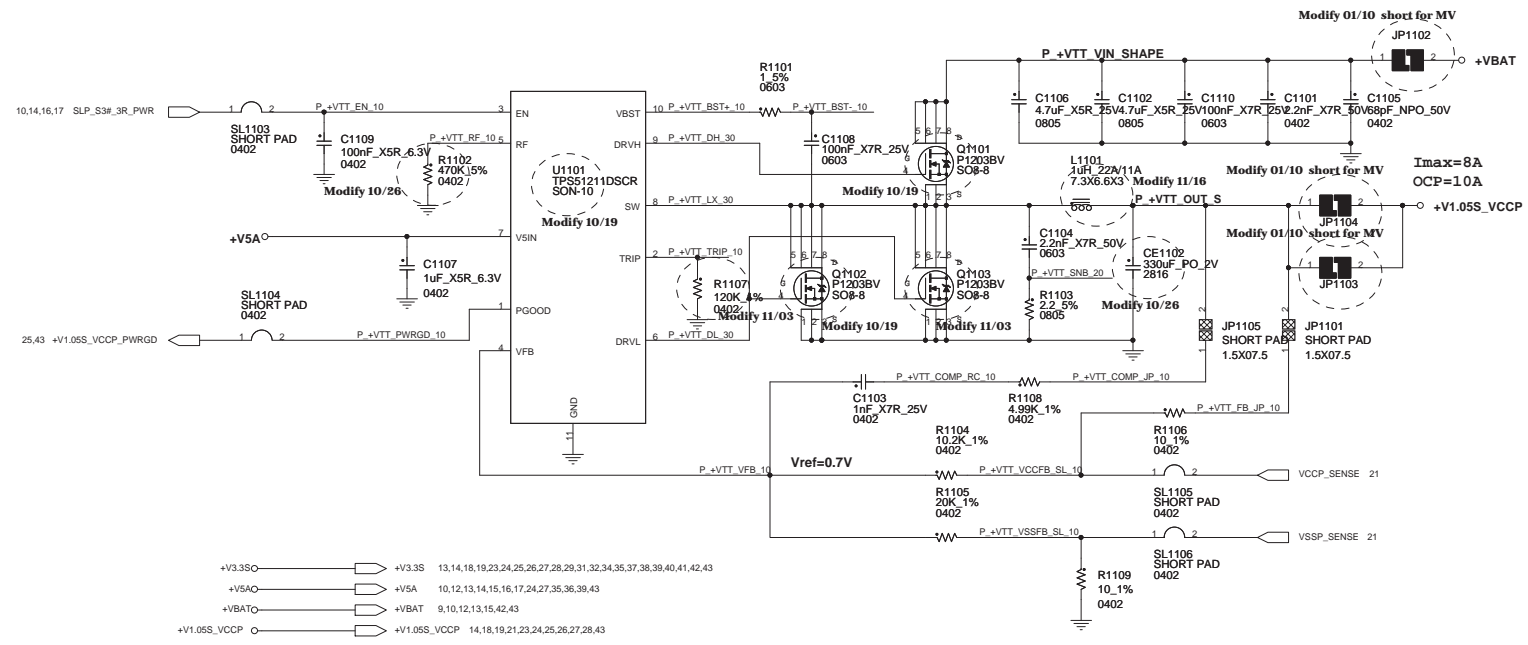
Page Modified: Tuesday, March 09, 2011 08:28:59 (UTC+08:00) Sheet 10 of 43

+VTT POWER SUPPLY

2010.1103.0

- +V1.05S_VCCP:**
- I/P Current:**
 $I_{in} = V_o \cdot I_o / (0.75 \cdot V_{in}) = 1.24A$
 - Ripple Current:**
 $I_{rip} = 3.42A$
 - Ripple Voltage:**
 $ESR/1 = 9mohm$
 $V_{rip} = 30.78mV$
 - Inductor Spec:**
 $I_{sat} = 36A$
 $I_{dc} = 18A$
 $DCR = 3.3mohm$
 - MOSFET Spec:**

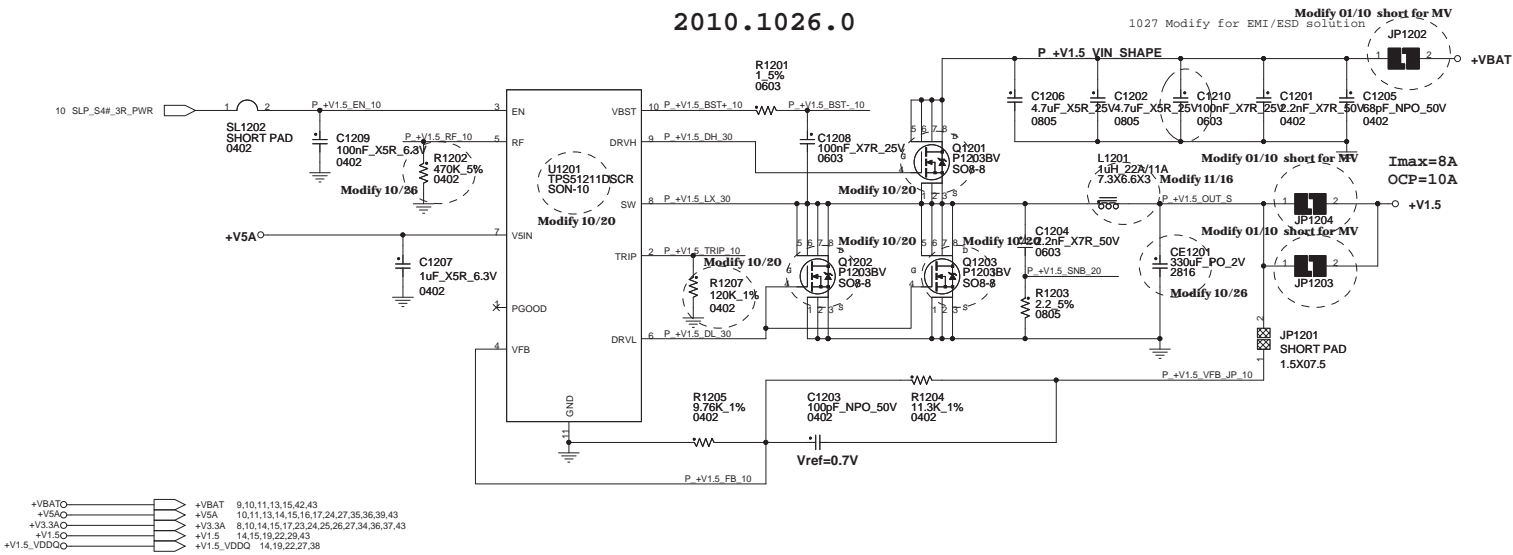
H-side MOSFET: IRF8707PBF	L-side MOSFET: IRF8707PBF
$R_{ds(ON)} = 17.5mohm$ ($V_{gs} = 4.5V$)	$R_{ds(ON)} = 17.5mohm$ ($V_{gs} = 4.5V$)
$I_{cont} = 11A$ ($T = 25^\circ C$)	$I_{cont} = 11A$ ($T = 25^\circ C$)
$I_{peak} = 88A$ ($Pause = 10us$)	$I_{peak} = 88A$ ($Pause = 10us$)
 - Frequency:**
 $F = 290KHz$ ($R1102 = 0ohm$)
 - OCp:**
 Set = R1107 to 120K
 $V_{trip} = R1107 \cdot 10uA = 1.2V$
 $I_{ocp} = (V_{trip} / 8 \cdot R_{ds(on)}) + I_{ripple} / 2 = 10A$



- +V3.3S \rightarrow 13,14,18,19,23,24,25,26,27,28,29,31,32,34,35,37,38,39,40,41,42,43
- +V5A \rightarrow 10,12,13,14,15,16,17,24,27,35,36,39,43
- +VBAT \rightarrow 9,10,12,13,15,42,43
- +V1.05S_VCCP \rightarrow +V1.05S_VCCP 14,18,19,21,23,24,25,26,27,28,43

+V1.5 POWER SUPPLY

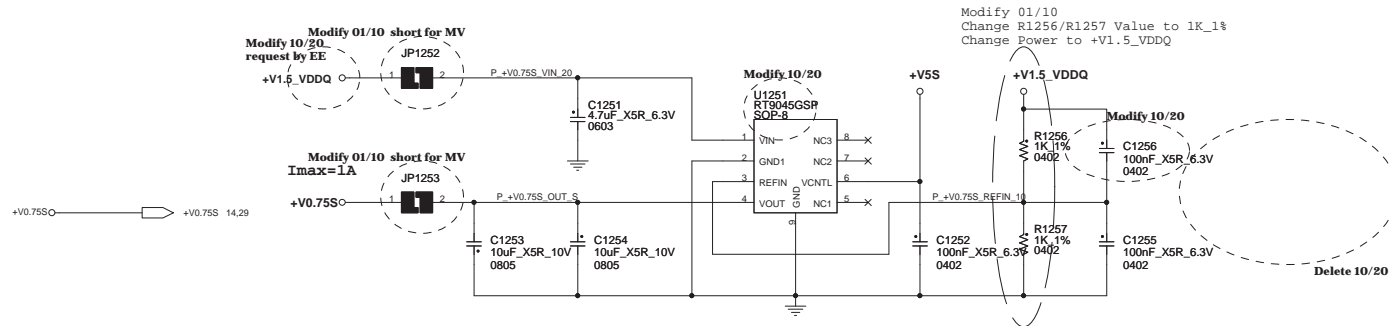
2010.1026.0



- +V1.5:**
- I/P Current:**
 $I_{in} = V_o \cdot I_o / (0.75 \cdot V_{in}) = 1.78A$
 - Ripple Current:**
 $I_{rip} = 3.34A$
 - Ripple Voltage:**
 $ESR/1 = 9m\Omega$
 $V_{rip} = 30.6mV$
 - Inductor Spec:**
 $I_{sat} = 36A$
 $I_{dc} = 18A$
 $DCR = 3.3m\Omega$
 $OCF = 1.0A$
 - MOSFET Spec:**
 H-side MOSFET: IRF8707PBF
 $R_{ds(ON)} = 17.5m\Omega$ ($V_{gs} = 4.5V$)
 $I_{cont} = 11A$ ($T = 25^\circ C$)
 $I_{peak} = 88A$ (Pause = 10 us)
 L-side MOSFET: IRF8707PBF
 $R_{ds(ON)} = 17.5m\Omega$ ($V_{gs} = 4.5V$)
 $I_{cont} = 11A$ ($T = 25^\circ C$)
 $I_{peak} = 88A$ (Pause = 10 us)
 - Frequency:**
 $F = 290KHz$ ($R_{0902} = 0\Omega$)
 - OCP:**
 Set = R1207 to 120K
 $V_{trip} = R1207 \cdot 10uA = 1.2V$
 $I_{ocp} = (V_{trip} / 8 \cdot R_{ds(on)}) + I_{ripple} / 2 = 10A$

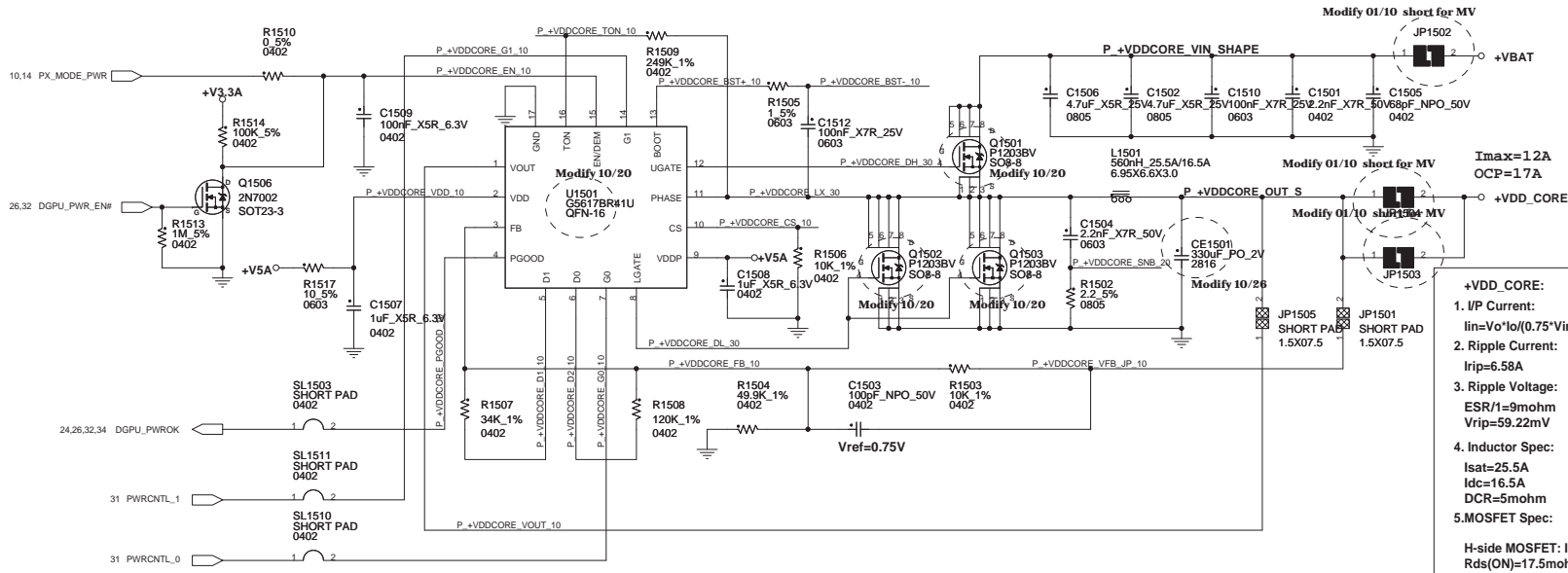
+V0.75S POWER SUPPLY

2010.1026.0



http://hobi-elektronika.net +VDD_CORE POWER SUPPLY

2010.1026.0



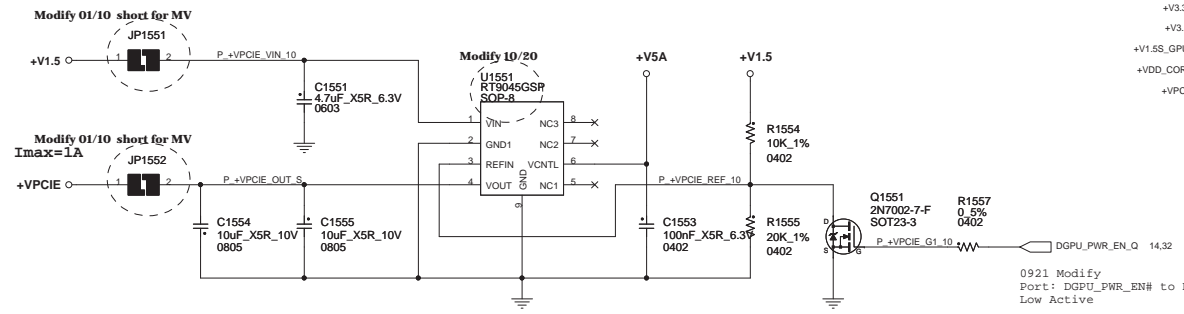
PWRCNTL_1	PWRCNTL_0	VDD_CORE
0	---	1.121V
---	---	---
1	---	0.9V
---	---	---

+VDD_CORE:

- IP Current:**
 $I_{in} = V_o \cdot I_o / (0.75 \cdot V_{in}) = 1.48A$
- Ripple Current:**
 $I_{rip} = 6.58A$
- Ripple Voltage:**
 $ESR/1 = 9m\Omega$
 $V_{rip} = 59.22mV$
- Inductor Spec:**
 $I_{sat} = 25.5A$
 $I_{dc} = 16.5A$
 $DCR = 5m\Omega$
- MOSFET Spec:**

H-side MOSFET: IRF8707PBF	L-side MOSFET: IRF8707PBF
$R_{ds(ON)} = 17.5m\Omega$ ($V_{gs} = 4.5V$)	$R_{ds(ON)} = 17.5m\Omega$ ($V_{gs} = 4.5V$)
$I_{cont} = 11A$ ($T = 25^\circ C$)	$I_{cont} = 11A$ ($T = 25^\circ C$)
$I_{peak} = 88A$ (Pause = 10 us)	$I_{peak} = 88A$ (Pause = 10 us)
- Frequency:**
 $TON = 9.6 \cdot P \cdot R1509 \cdot (VOUT + 0.1) / (VIN - 0.3) + 50ns = 206ns$
 $F = VOUT / (VIN \cdot TON) = 286KHz$
- OCP:**
Set = R1506 to 10K
 $V_{trip} = R1206 \cdot 10uA = 0.1V$
 $I_{ocp} = (V_{trip} / R_{dson}) + I_{ripple} / 2 = 17A$

2010.1020.0 +VPCIE POWER SUPPLY



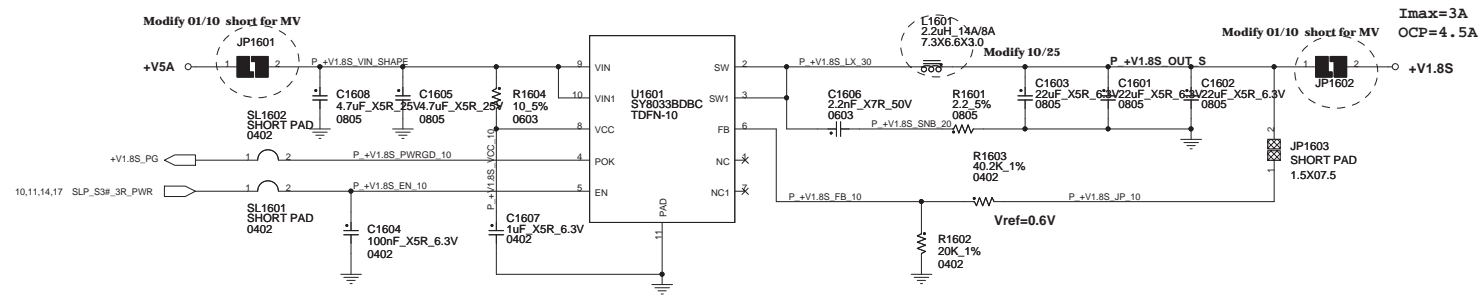
+VBAT	9,10,11,12,13,42,43
+V5A	10,11,12,13,14,16,17,24,27,35,36,39,43
+V3.3A	8,10,14,17,23,24,25,26,27,34,36,37,43
+V3.3S	13,14,18,19,23,24,25,26,27,28,29,31,32,34,35,37,38,39,40,41,42,43
+V1.5S_GPU	14,30,32,33,43
+VDD_CORE	32,43
+VPCIE	30,31,32,43

0921 Modify
Port: DGPU_PWR_EN# to DGPU_PWR_EN_Q
Low Active

+V1.8S POWER SUPPLY

2010.1025.0

- +V1.8S:**
- I/P Current:**
 $I_{in} = V_o \cdot I_o / (0.75 \cdot V_{in}) = 1.44A$
 - Ripple Current:**
 $I_{rip} = 0.53A$
 - Ripple Voltage:**
 $ESR/3 = 3.3m\Omega$
 $V_{rip} = 1.75mV$
 - Inductor Spec:**
 $I_{sat} = 14A$
 $I_{dc} = 8A$
 $DCR = 20m\Omega$
 - MOSFET Spec:**
H-side P-MOSFET: $R_{ds(ON)} = 110m\Omega$ ($V_{gs} = 4.5V$)
L-side N-MOSFET: $R_{ds(ON)} = 75m\Omega$ ($V_{gs} = 4.5V$)
 - Frequency:**
 $F = 1MHz$ (min=800KHz, max=1.2MHz)
 - OCP:**
 $I_{ocp} = 4A(\min) / 4.5A(\text{typ}) / 5A(\text{max})$

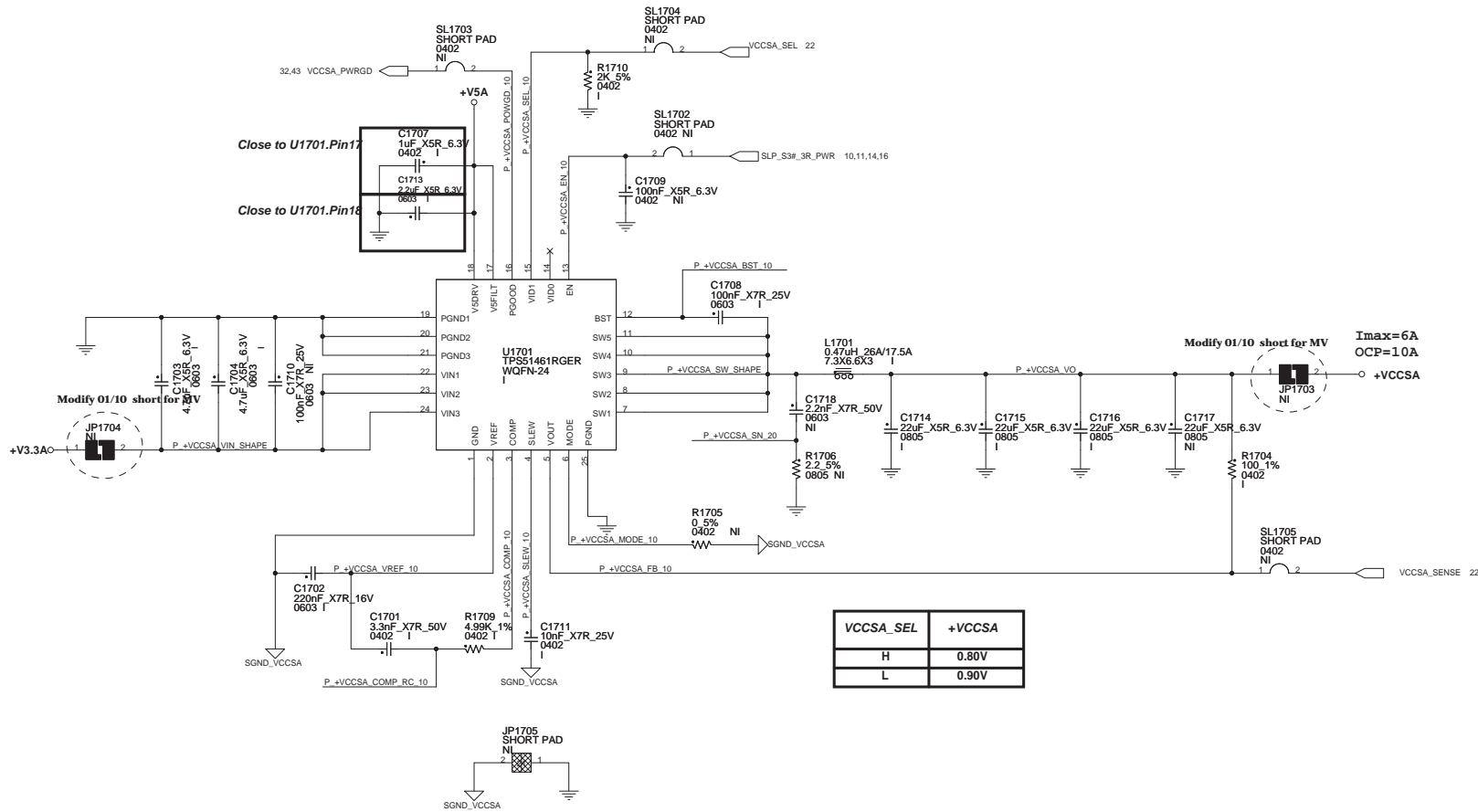


+VCCSA POWER SUPPLY

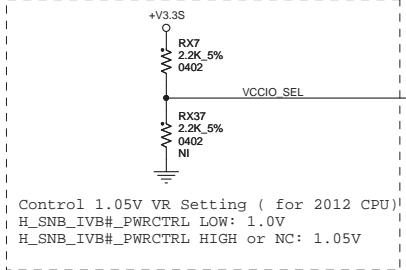
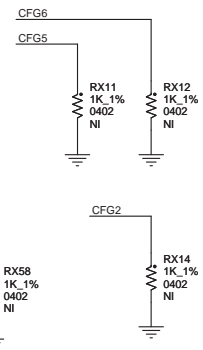
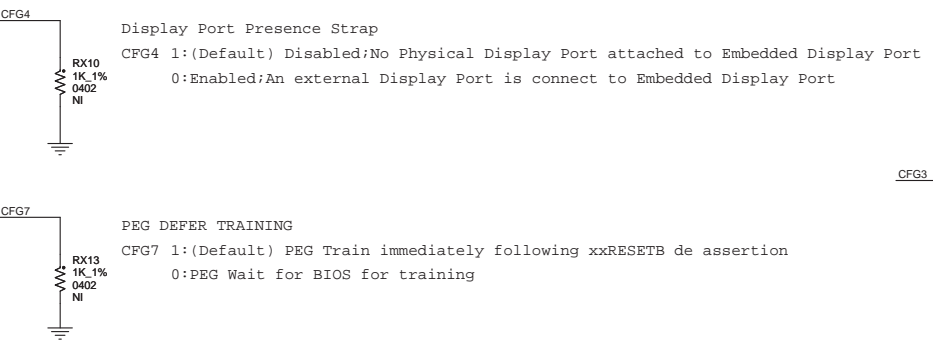
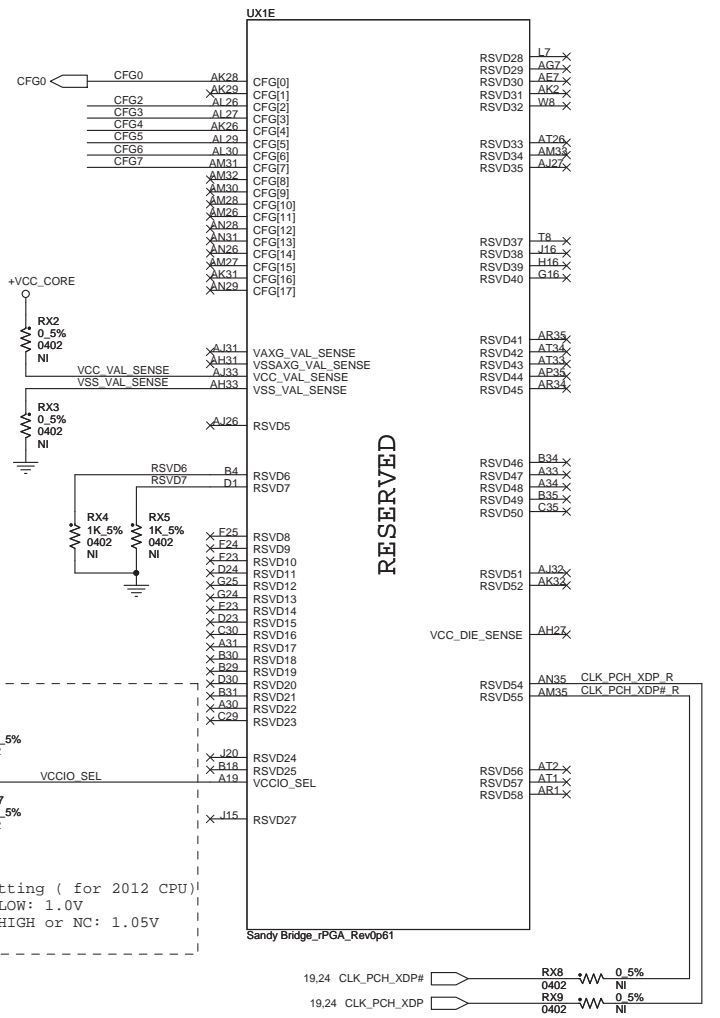
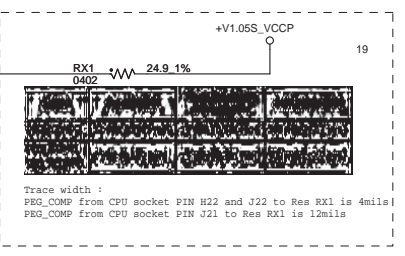
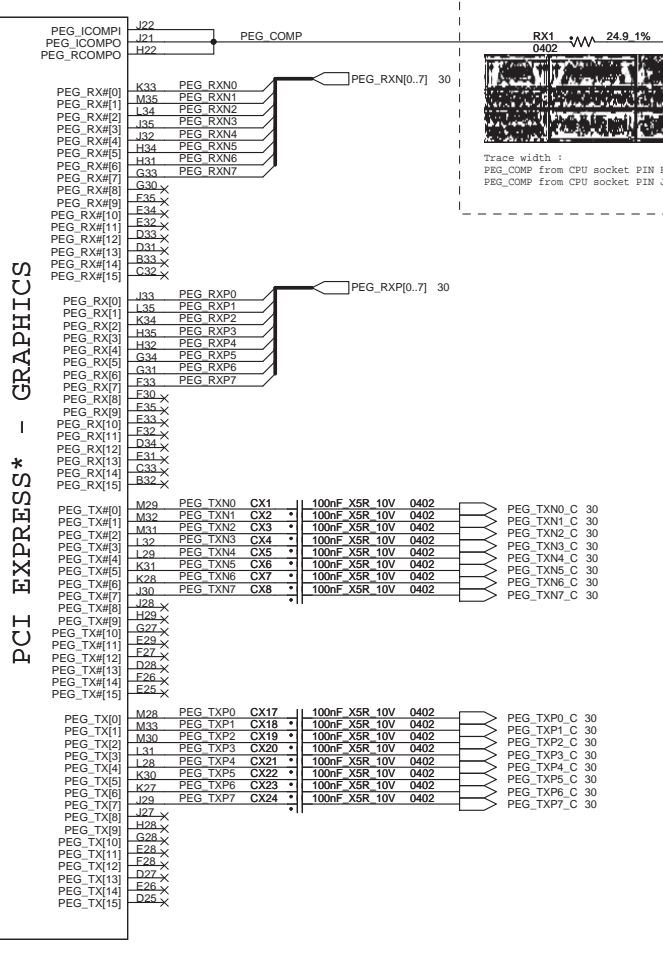
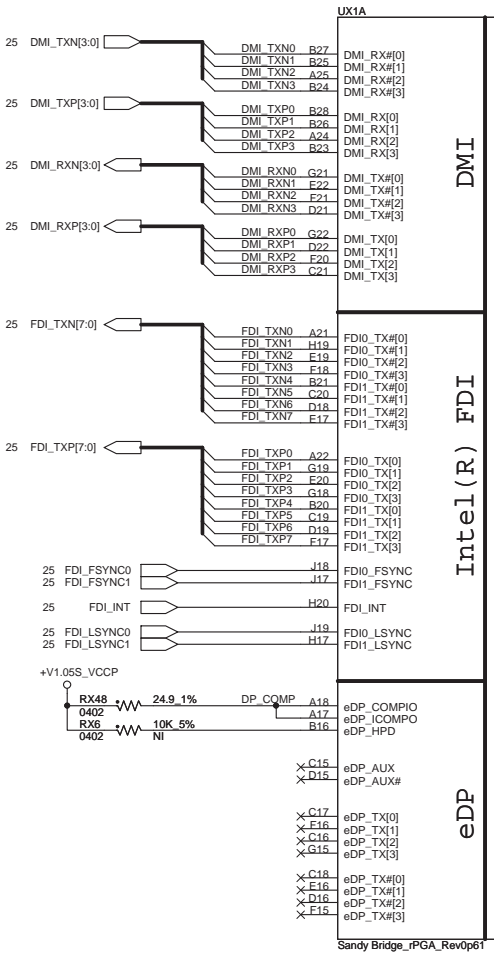
2010.1026.0

- +VCCSA:
- 1. I/P Current:
 $I_{in} = V_o \cdot I_o / (0.75 \cdot V_{in}) = 2.18A$
- 2. Ripple Current:
 $I_{rip} = 1.39A$
- 3. Ripple Voltage:
 $ESR/4 = 1mohm$
 $V_{rip} = 1.39mV$
- 4. Inductor Spec:
 $I_{sat} = 26A$
 $I_{dc} = 17.5A$
 $DCR = 4.2mohm$
- 5. MOSFET Spec:

- 6. Frequency:
 $F = 1MHz$ (R1705=Open)
- 7. OCP:
Min : 6A / Typ : 7.5A



+V3.3S 13,14,19,23,24,25,26,27,28,29,31,32,34,35,37,38,39,40,41,42,43
 +V1.05S_VCCP 11,14,19,21,23,24,25,26,27,28,43

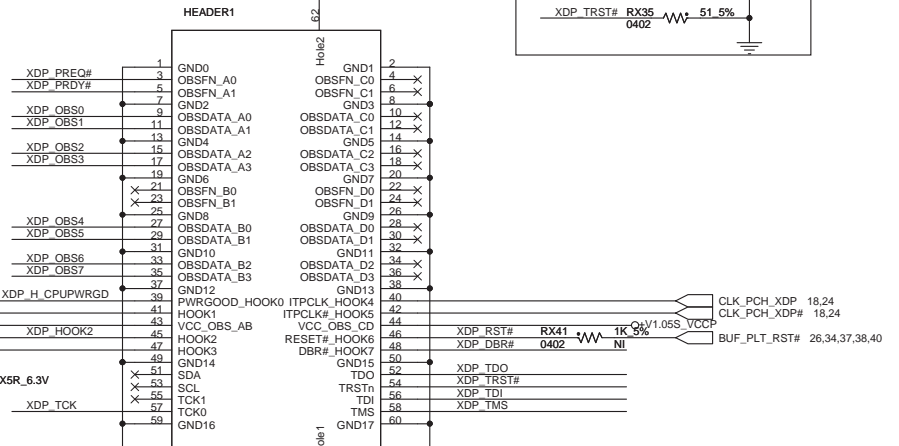
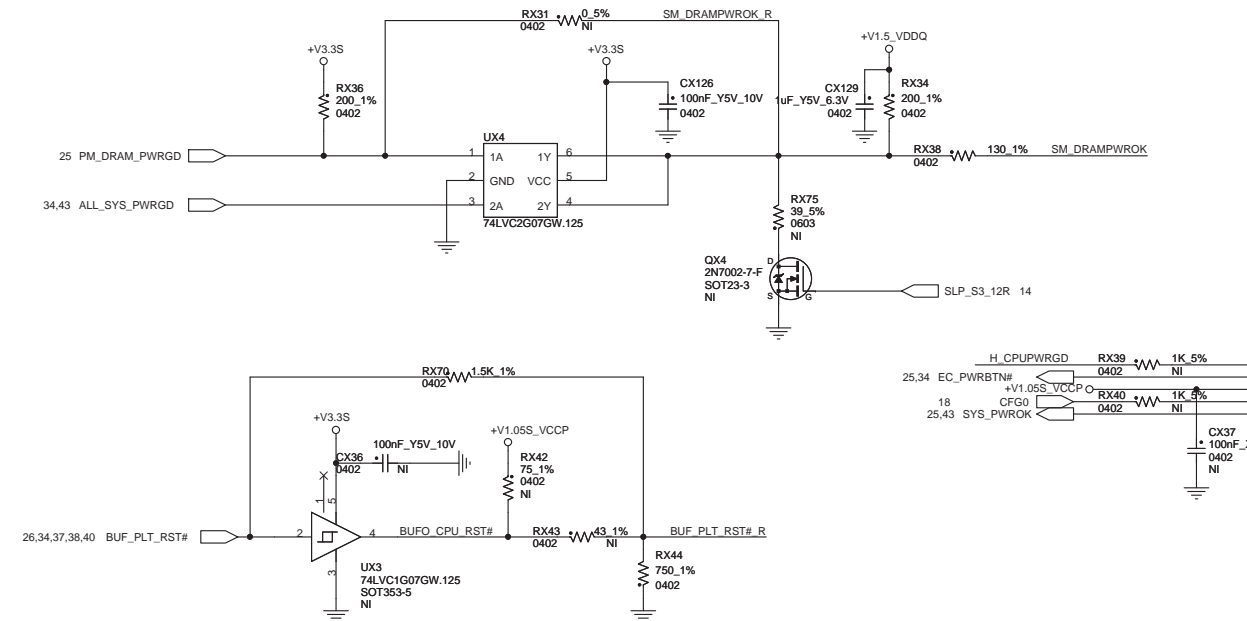
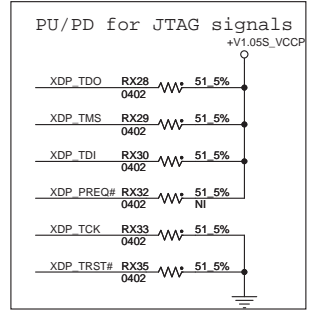
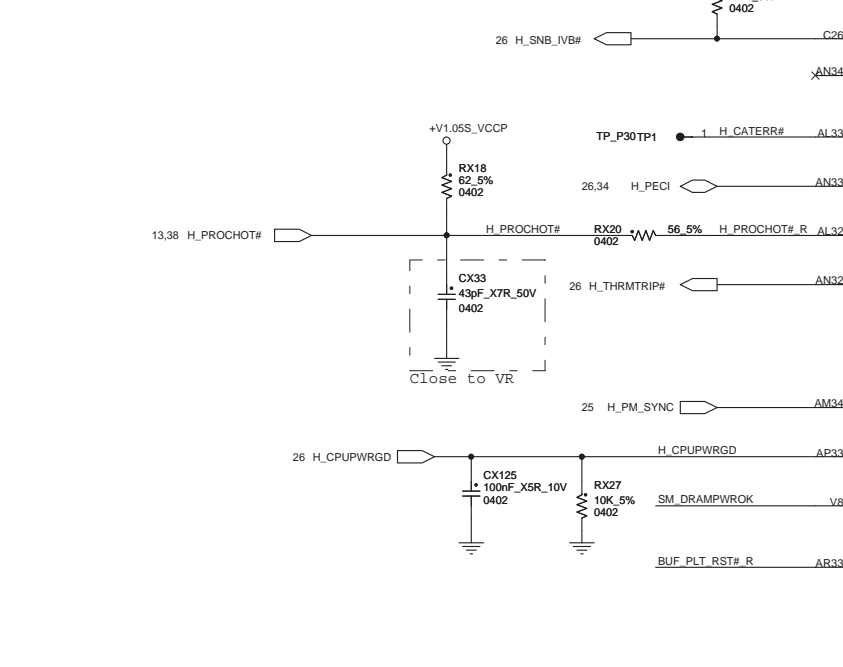
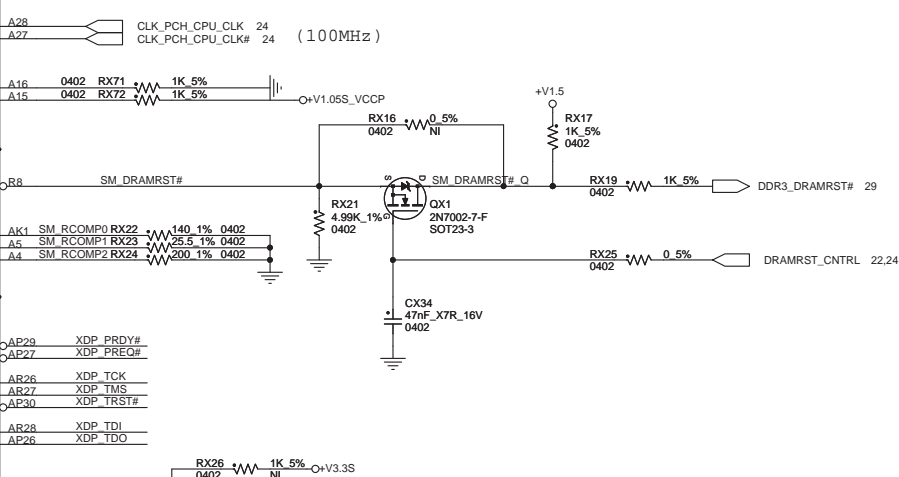
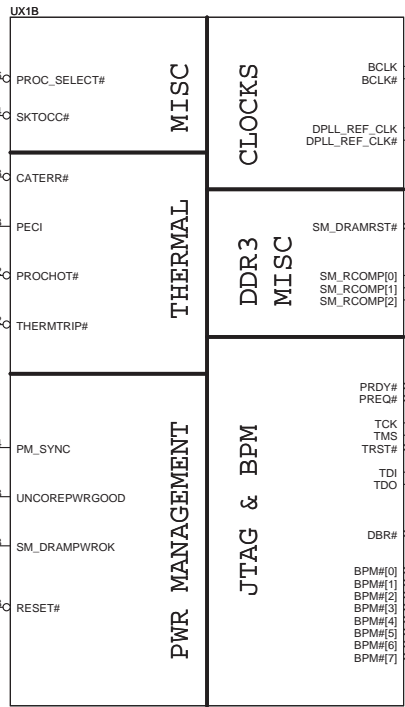


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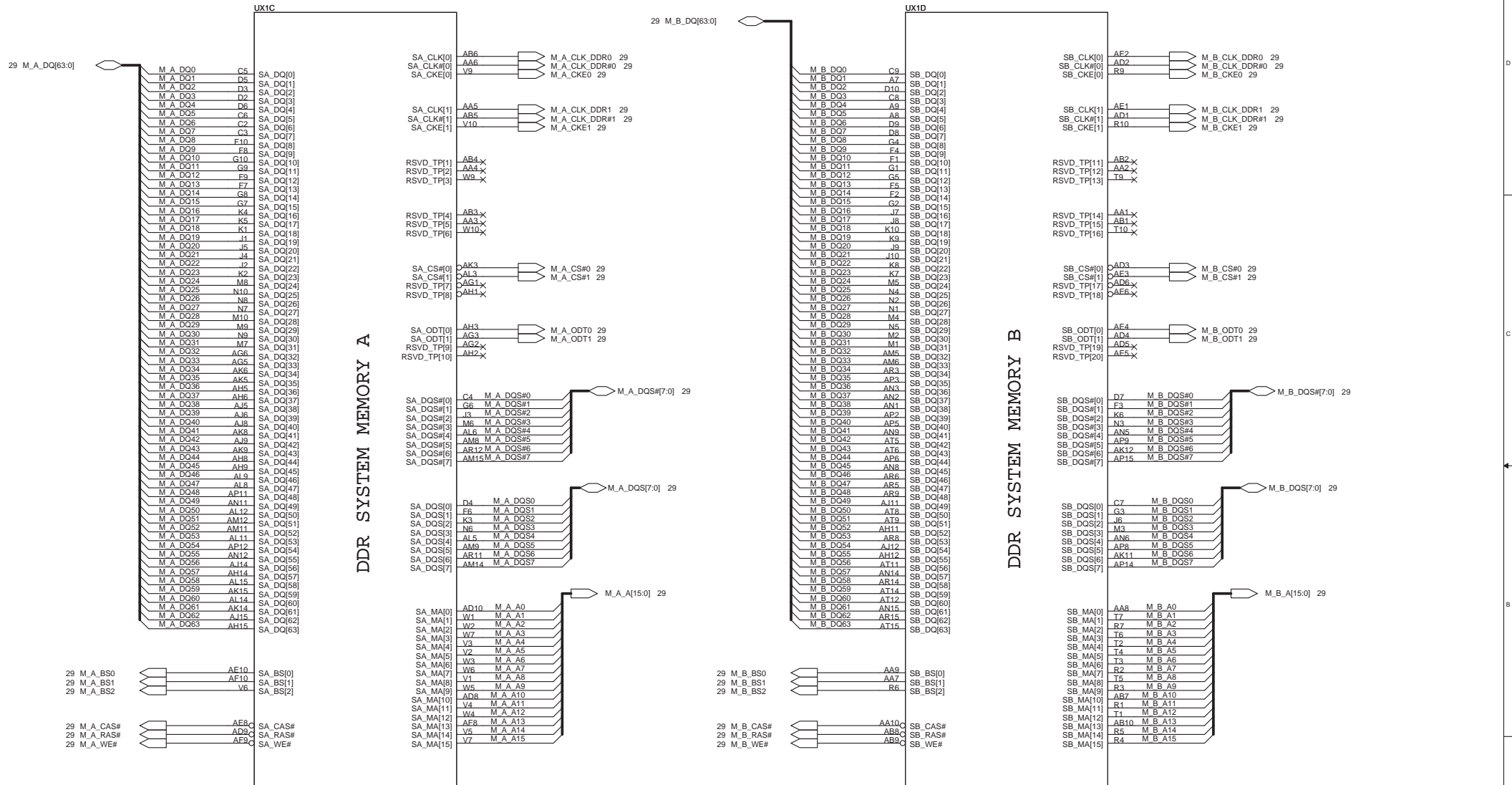
Title: **PROCESSOR(1 of 5)**

Size: Document Number
 Custom **CHICAGO** Rev **MV**

Page Modified: Tuesday, March 08, 2011 08:28:01 (UTC/GMT) Sheet 18 of 43

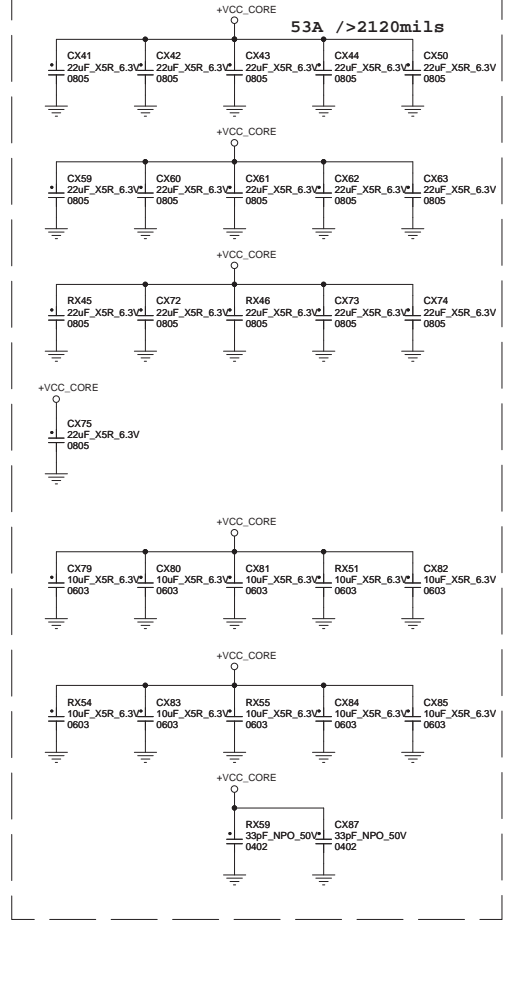


Foxconn eMS Inc. Hon Hai Precision Industry Co. Ltd. HNB D R&D. Title: PROCESSOR(2 of 5). Size: Custom CHICAGO. Rev: MV. Page Modified: Tuesday, March 08, 2011 08:28:56 (UTC/GMT) Sheet 19 of 43



+V1.05S_VCCP O +V1.05S_VCCP 11,14,18,19,23,24,25,26,27,28,43
+VCC_CORE O +VCC_CORE 13,18,43

FOR VCC:
4x 330 µF Bottom Edge,
10x 0603 10 µF Bottom Cavity,
8x 0805 22 µF Top Cavity,
8x 0805 22 µF Top Edge,



- VCC1
- VCC2
- VCC3
- VCC4
- VCC5
- VCC6
- VCC7
- VCC8
- VCC9
- VCC10
- VCC11
- VCC12
- VCC13
- VCC14
- VCC15
- VCC16
- VCC17
- VCC18
- VCC19
- VCC20
- VCC21
- VCC22
- VCC23
- VCC24
- VCC25
- VCC26
- VCC27
- VCC28
- VCC29
- VCC30
- VCC31
- VCC32
- VCC33
- VCC34
- VCC35
- VCC36
- VCC37
- VCC38
- VCC39
- VCC40
- VCC41
- VCC42
- VCC43
- VCC44
- VCC45
- VCC46
- VCC47
- VCC48
- VCC49
- VCC50
- VCC51
- VCC52
- VCC53
- VCC54
- VCC55
- VCC56
- VCC57
- VCC58
- VCC59
- VCC60
- VCC61
- VCC62
- VCC63
- VCC64
- VCC65
- VCC66
- VCC67
- VCC68
- VCC69
- VCC70
- VCC71
- VCC72
- VCC73
- VCC74
- VCC75
- VCC76
- VCC77
- VCC78
- VCC79
- VCC80
- VCC81
- VCC82
- VCC83
- VCC84
- VCC85
- VCC86
- VCC87
- VCC88
- VCC89
- VCC90
- VCC91
- VCC92
- VCC93
- VCC94
- VCC95
- VCC96
- VCC97
- VCC98
- VCC99
- VCC100

POWER

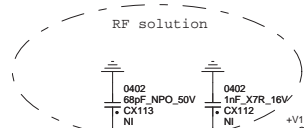
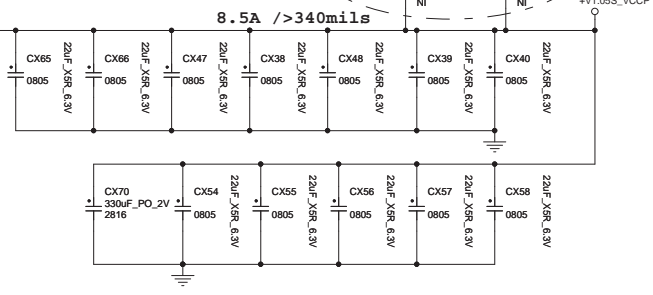
PEG AND DDR

CORE SUPPLY

SVID

SENSE LINES

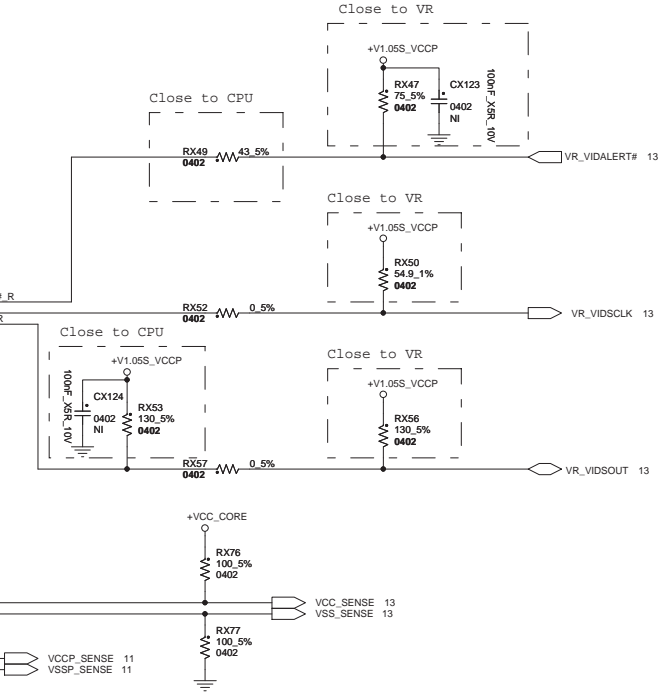
FOR VCCIO:
2x 330 µF,
5x 0805 22 µF Bottom Cavity,
7x 0805 22 µF Top Cavity,



- AH13
- AH10
- AC10
- Y10
- L110
- L110
- L11
- L12
- L11
- H14
- H12
- H11
- G14
- G13
- G12
- E14
- F12
- F11
- E14
- E12
- E11
- D14
- D13
- D12
- D11
- C14
- C13
- C12
- C11
- B14
- B12
- A14
- A13
- A12
- A11
- J23

- AJ29 VR_SVID_ALERT# R
- AJ30 VR_SVID_CLK R
- AJ28 VR_SVID_DATA R

- AJ35 VCC_SENSE
- AJ34 VSS_SENSE
- B10 VCCIO_SENSE
- A10 VSSIO_SENSE

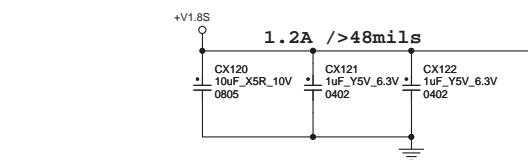
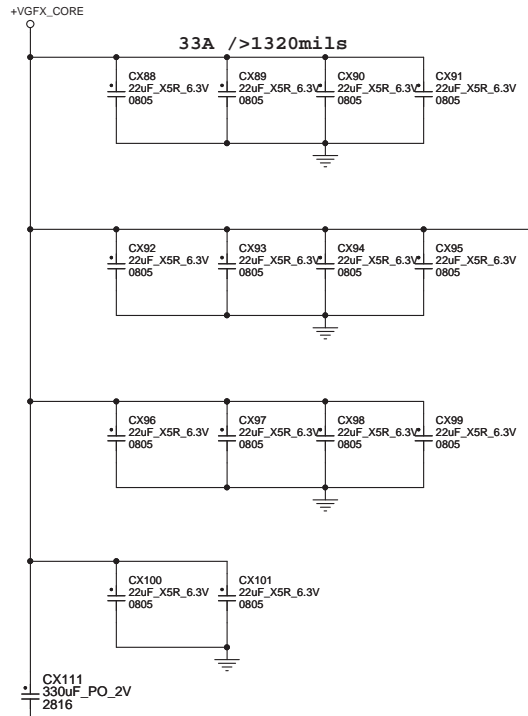


- AT36 VSS1
- AT32 VSS2
- AT29 VSS3
- AT27 VSS4
- AT26 VSS5
- AT25 VSS6
- AT19 VSS7
- AT16 VSS8
- AT15 VSS9
- AT14 VSS10
- AT7 VSS11
- AT4 VSS12
- AT3 VSS13
- AT2 VSS14
- AT1 VSS15
- AT VSS16
- AT VSS17
- AT VSS18
- AT VSS19
- AT VSS20
- AR4 VSS21
- AR2 VSS22
- AT3 VSS23
- AR25 VSS24
- AR22 VSS25
- AR19 VSS26
- AR16 VSS27
- AR13 VSS28
- AR10 VSS29
- AR7 VSS30
- AR4 VSS31
- AR2 VSS32
- AT3 VSS33
- AP31 VSS34
- AP28 VSS35
- AP25 VSS36
- AP22 VSS37
- AP19 VSS38
- AP16 VSS39
- AP13 VSS40
- AP10 VSS41
- AP7 VSS42
- AR4 VSS43
- AP1 VSS44
- AN30 VSS45
- AN27 VSS46
- AN25 VSS47
- AN22 VSS48
- AN19 VSS49
- AN16 VSS50
- AN13 VSS51
- AN10 VSS52
- AN7 VSS53
- AN4 VSS54
- AM29 VSS55
- AM22 VSS56
- AM19 VSS57
- AM16 VSS58
- AM13 VSS59
- AM10 VSS60
- AM7 VSS61
- AM4 VSS62
- AM2 VSS63
- AM VSS64
- AL25 VSS65
- AL22 VSS66
- AL19 VSS67
- AL16 VSS68
- AL13 VSS69
- AL10 VSS70
- AL7 VSS71
- AL4 VSS72
- AL2 VSS73
- AK33 VSS74
- AK30 VSS75
- AK27 VSS76
- AK22 VSS77
- AK19 VSS78
- AK16 VSS79
- AK13 VSS80
- AK10 VSS81
- AK7 VSS82
- AK4 VSS83
- AL25 VSS84
- VSS81
- AJ22
- AJ19
- AJ16
- AJ13
- AJ10
- AJ7
- AJ4
- AJ3
- AJ2
- AJ1
- AH35
- AH34
- AH33
- AH32
- AH31
- AH30
- AH29
- AH28
- AH27
- AH26
- AH25
- AH24
- AH23
- AH22
- AH21
- AH20
- AH19
- AH18
- AH17
- AH16
- AH15
- AH14
- AH13
- AH12
- AH11
- AH10
- AH9
- AH8
- AH7
- AH6
- AH5
- AH4
- AH3
- AH2
- AH1
- AG9
- AG8
- AG7
- AG6
- AG5
- AG4
- AG3
- AG2
- AG1
- AE35
- AE34
- AE33
- AE32
- AE31
- AE30
- AE29
- AE28
- AE27
- AE26
- AE25
- AE24
- AE23
- AE22
- AE21
- AE20
- AE19
- AE18
- AE17
- AE16
- AE15
- AE14
- AE13
- AE12
- AE11
- AE10
- AE9
- AE8
- AE7
- AE6
- AE5
- AE4
- AE3
- AE2
- AE1
- AB35
- AB34
- AB33
- AB32
- AB31
- AB30
- AB29
- AB28
- AB27
- AB26
- AB25
- AB24
- AB23
- AB22
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- AB20
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- AB17
- AB16
- AB15
- AB14
- AB13
- AB12
- AB11
- AB10
- AB9
- AB8
- AB7
- AB6
- AB5
- AB4
- AB3
- AB2
- AB1
- AY9
- AY8
- AY7
- AY6
- AY5
- AY4
- AY3
- AY2
- AY1
- AW35
- AW34
- AW33
- AW32
- AW31
- AW30
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- AW15
- AW14
- AW13
- AW12
- AW11
- AW10
- AW9
- AW8
- AW7
- AW6
- AW5
- AW4
- AW3
- AW2
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- AV7
- AV6
- AV5
- AV4
- AV3
- AV2
- AV1
- AV VSS160

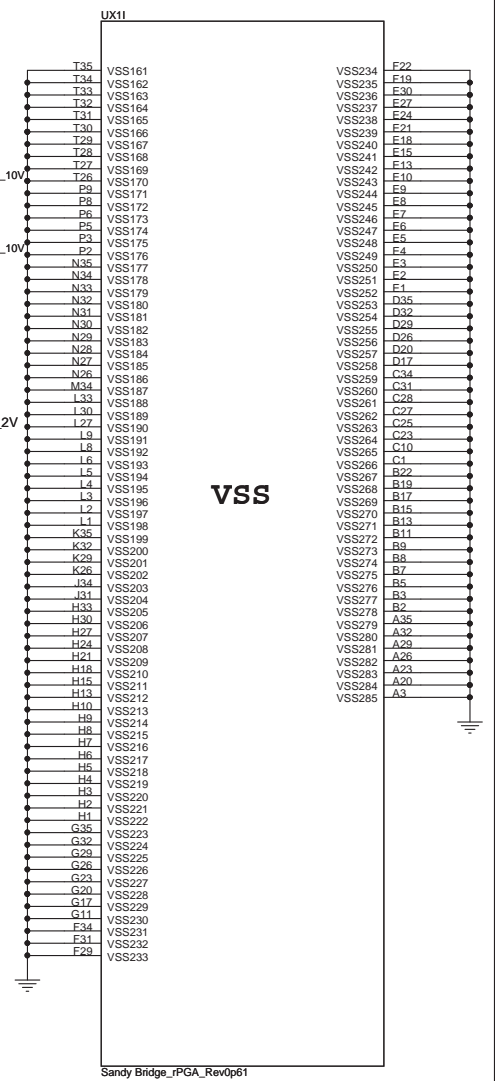
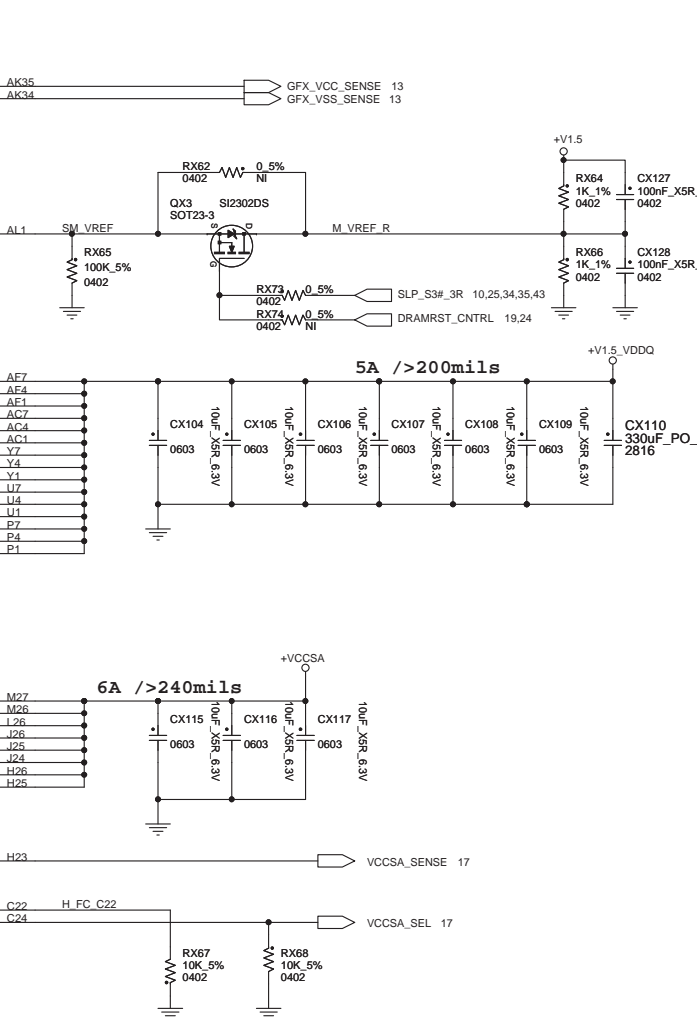
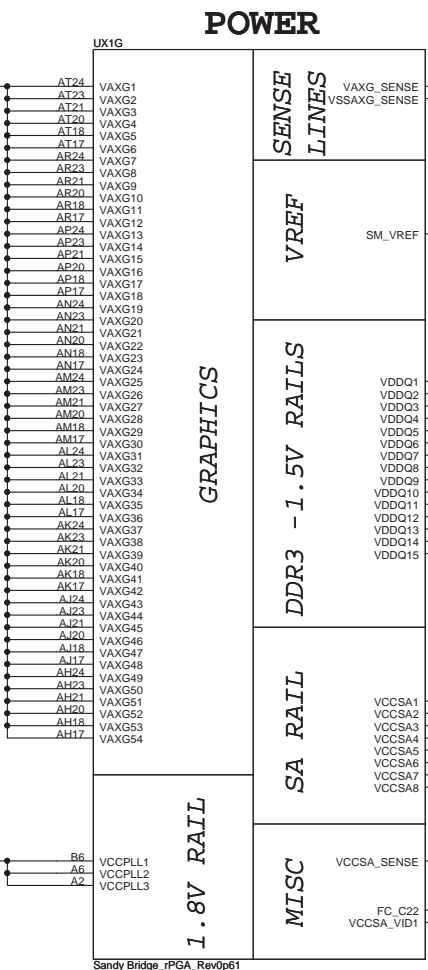
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HNB0 R&D
phone: +886-2-2799-6111

FOR VAXG:
2x 330 μ F Bottom Edge,
4x 0805 22 μ F Top & Bottom Cavity,
8x 0805 22 μ F Top & Bottom Edge,

- +VCCSA 17
- +V1.8S 14,16,19,28,43
- +V1.5O 12,14,15,19,29,43
- +V1.5_VDDQ 12,14,19,27,38
- +VGF_X_CORE 13,43



FOR VCCPLL:
1x 330 μ F Bottom Edge,
2x 0402 1 μ F Bottom Edge,
1x 0805 10 μ F Bottom Edge,

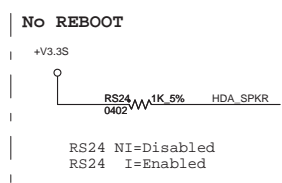
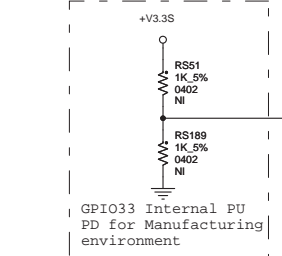
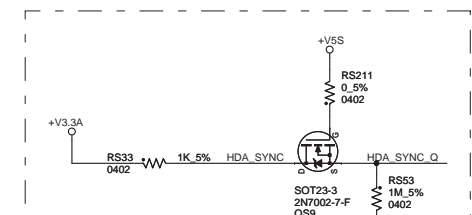
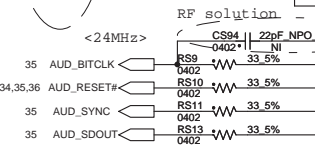
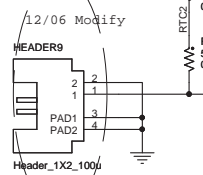
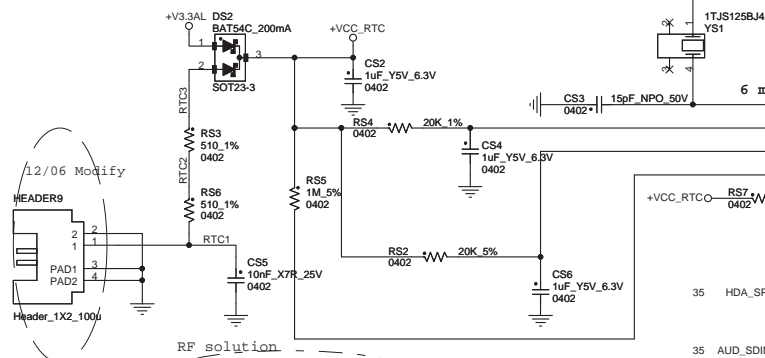
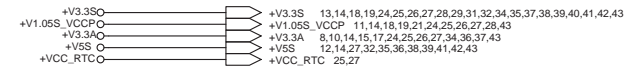
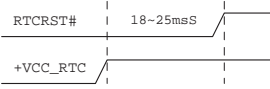


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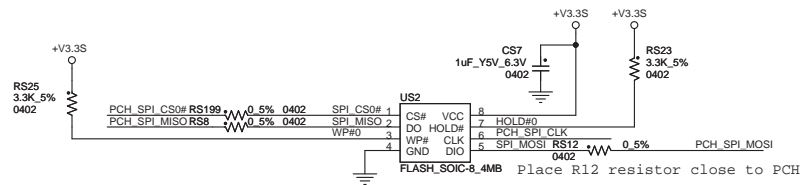
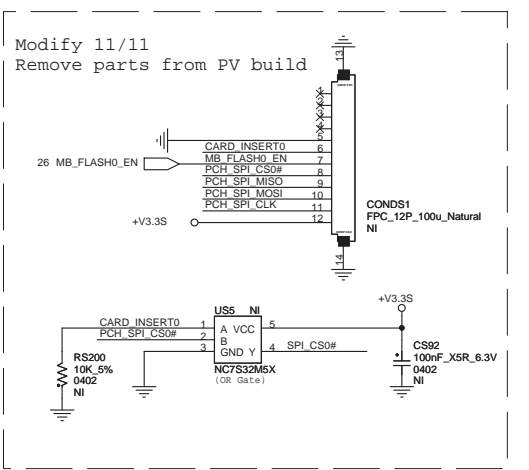
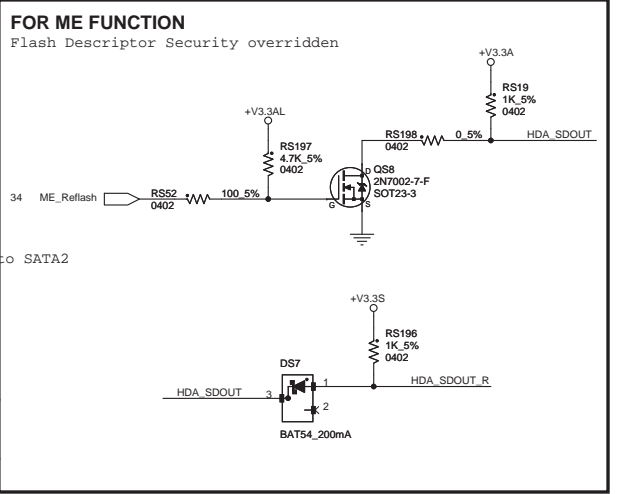
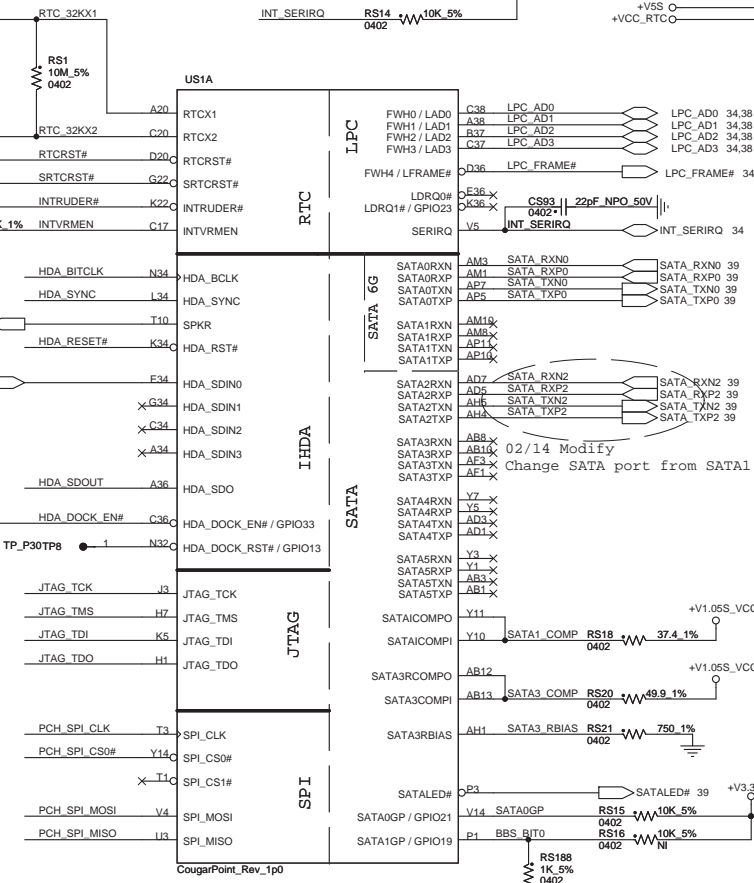
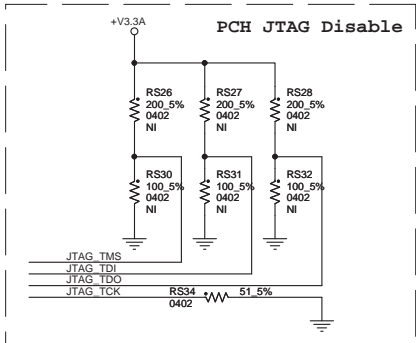
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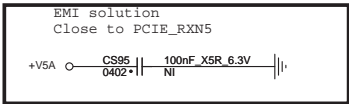
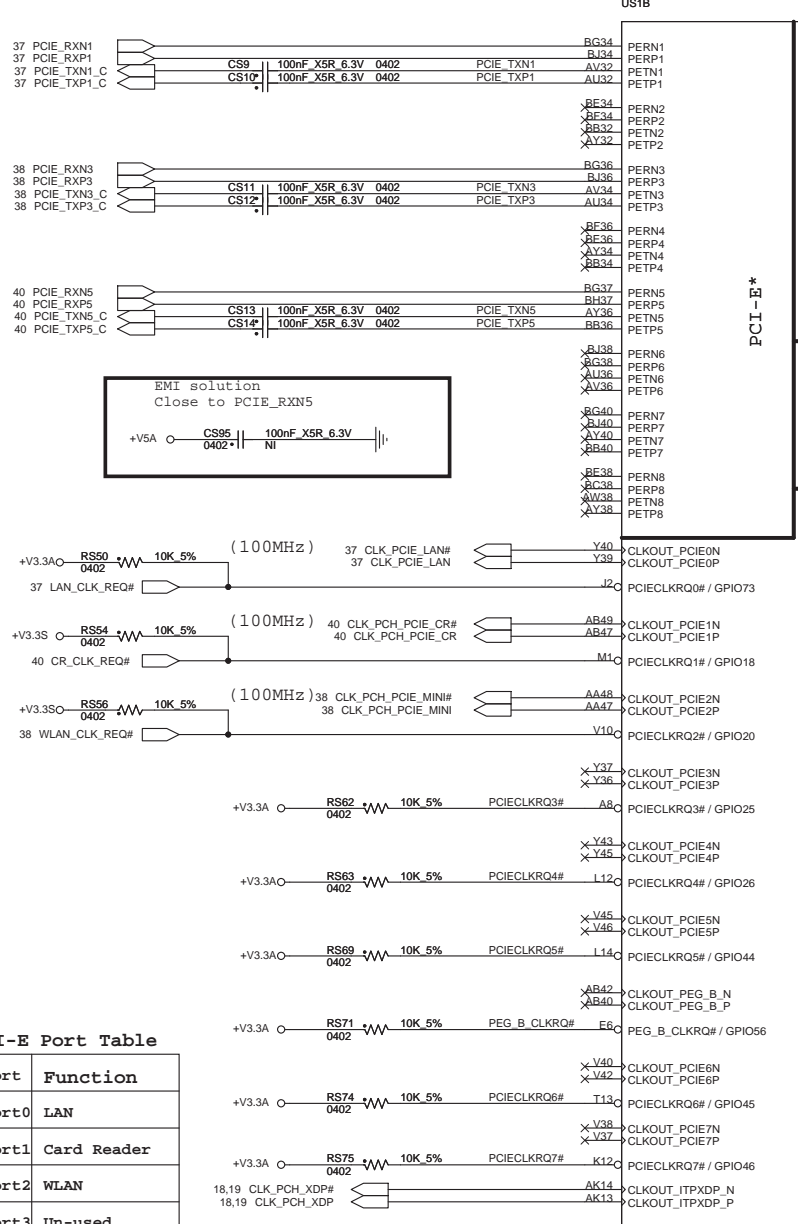
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 Custom **CHICAGO** Rev **MV**

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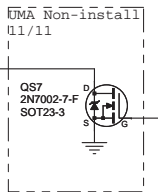
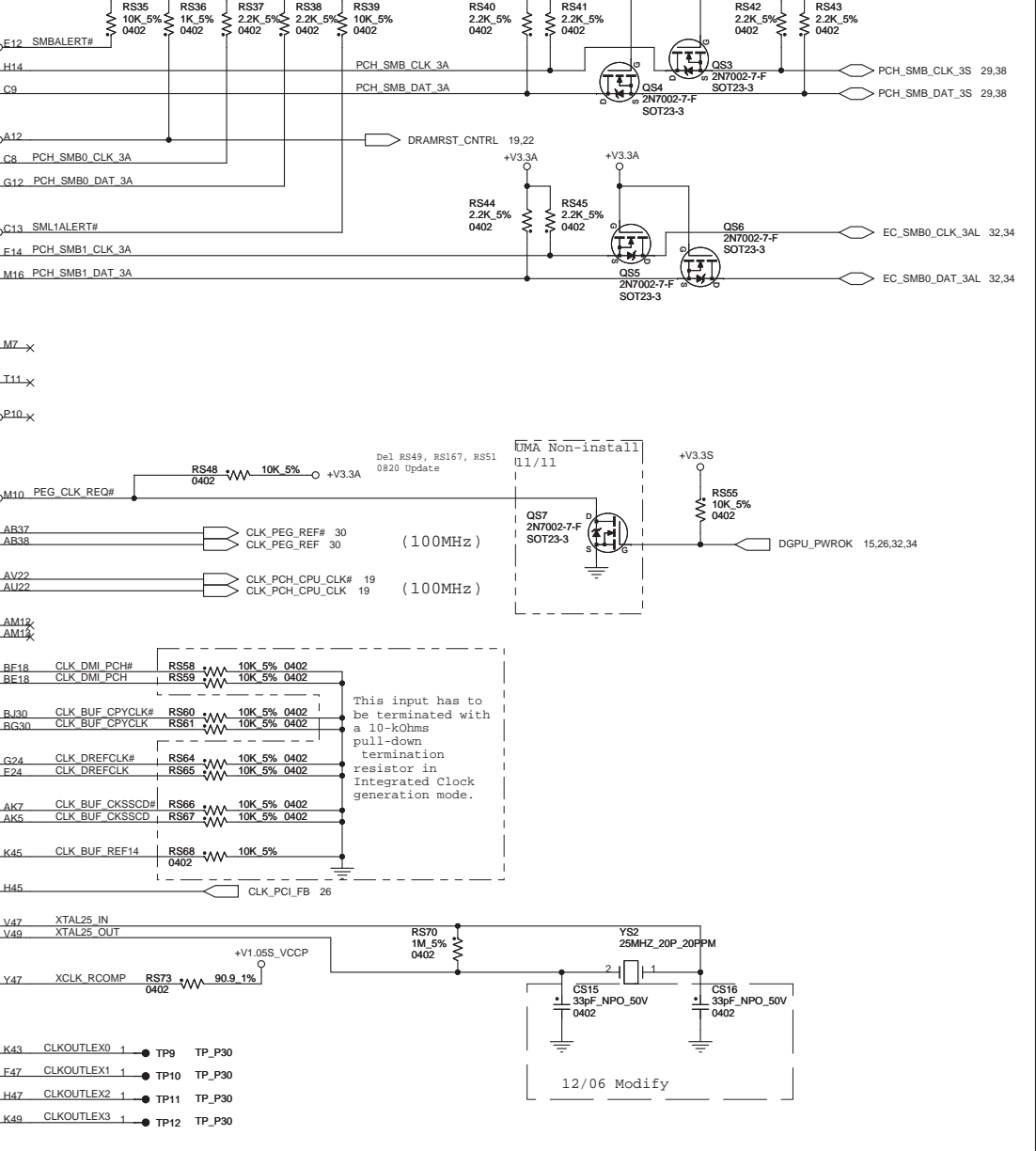
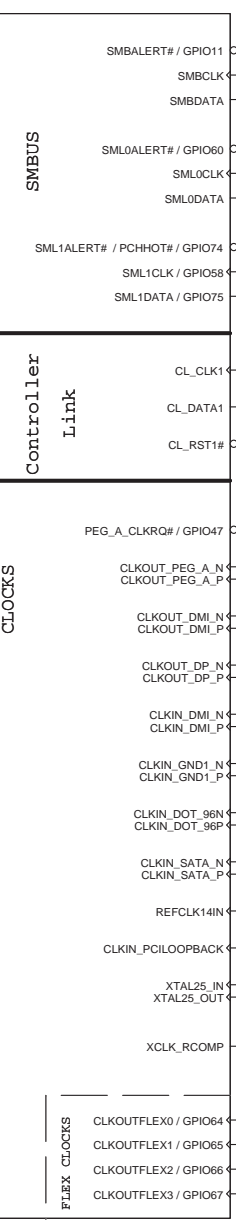
On Die PLL VR is supplied by 1.5 V when sampled high, 1.8 V when sampled low. (Default:1.5V for Mobile)





PCI-E Port Table

Port	Function
Port0	LAN
Port1	Card Reader
Port2	WLAN
Port3	Un-used
Port4	Un-used
Port5	Un-used
Port6	Un-used
Port7	Un-used



This input has to be terminated with a 10-kOhms pull-down termination resistor in Integrated Clock generation mode.

12/06 Modify

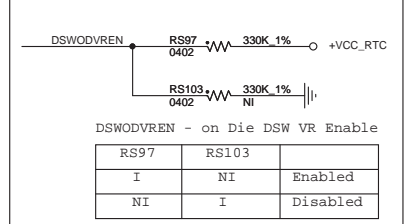
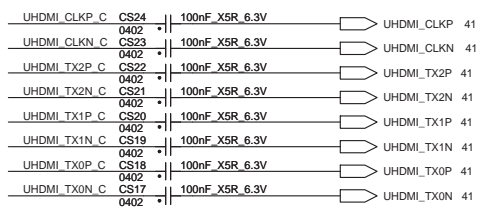
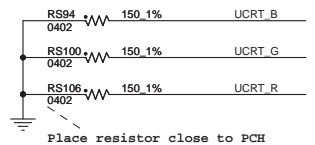
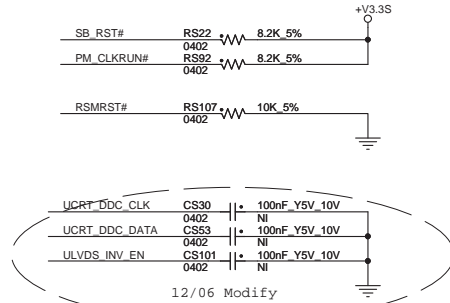
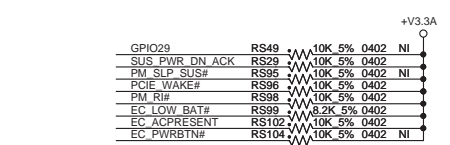
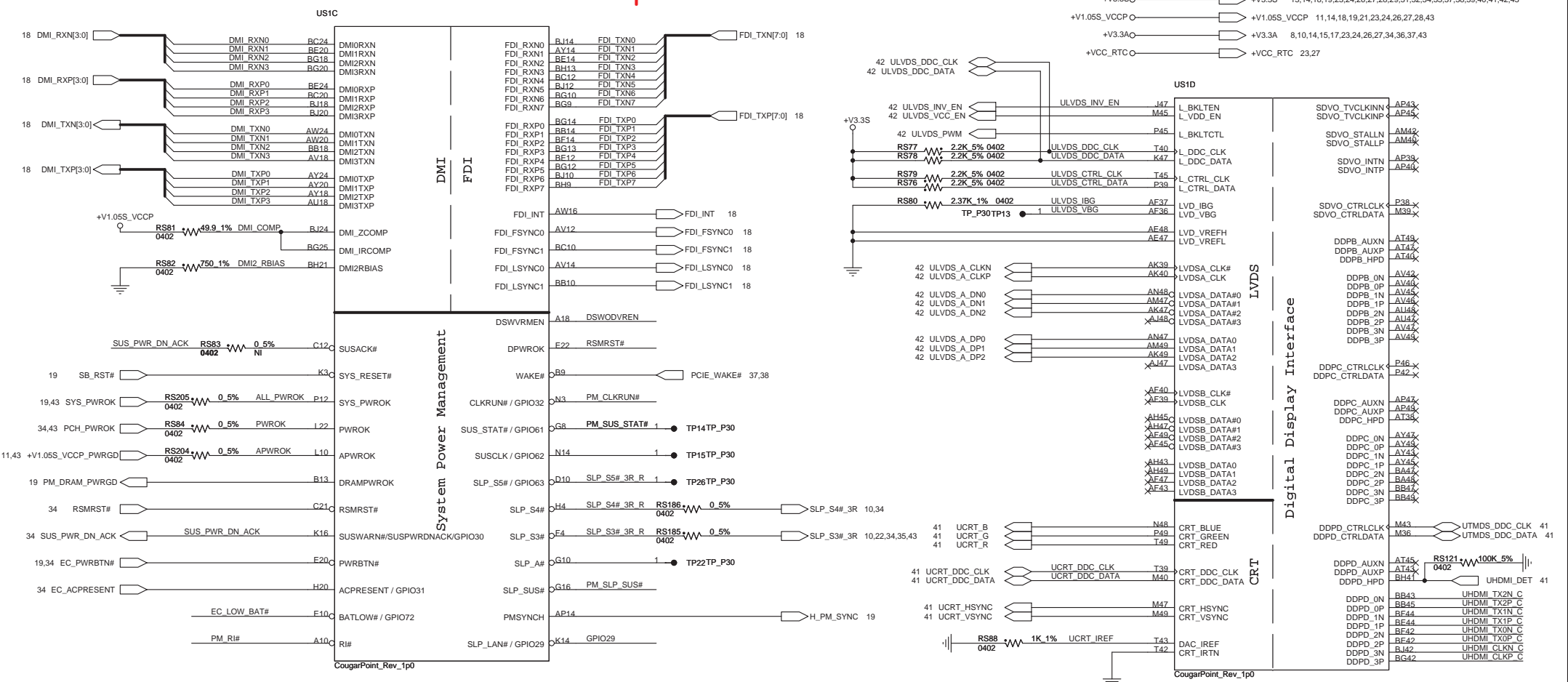
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Title: **CougarPoint(2 of 6)**

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RS97	RS103	Enabled
I	NI	Enabled
NI	I	Disabled

Modify CS30/CS53/CS101 to non-stuff on 01/11

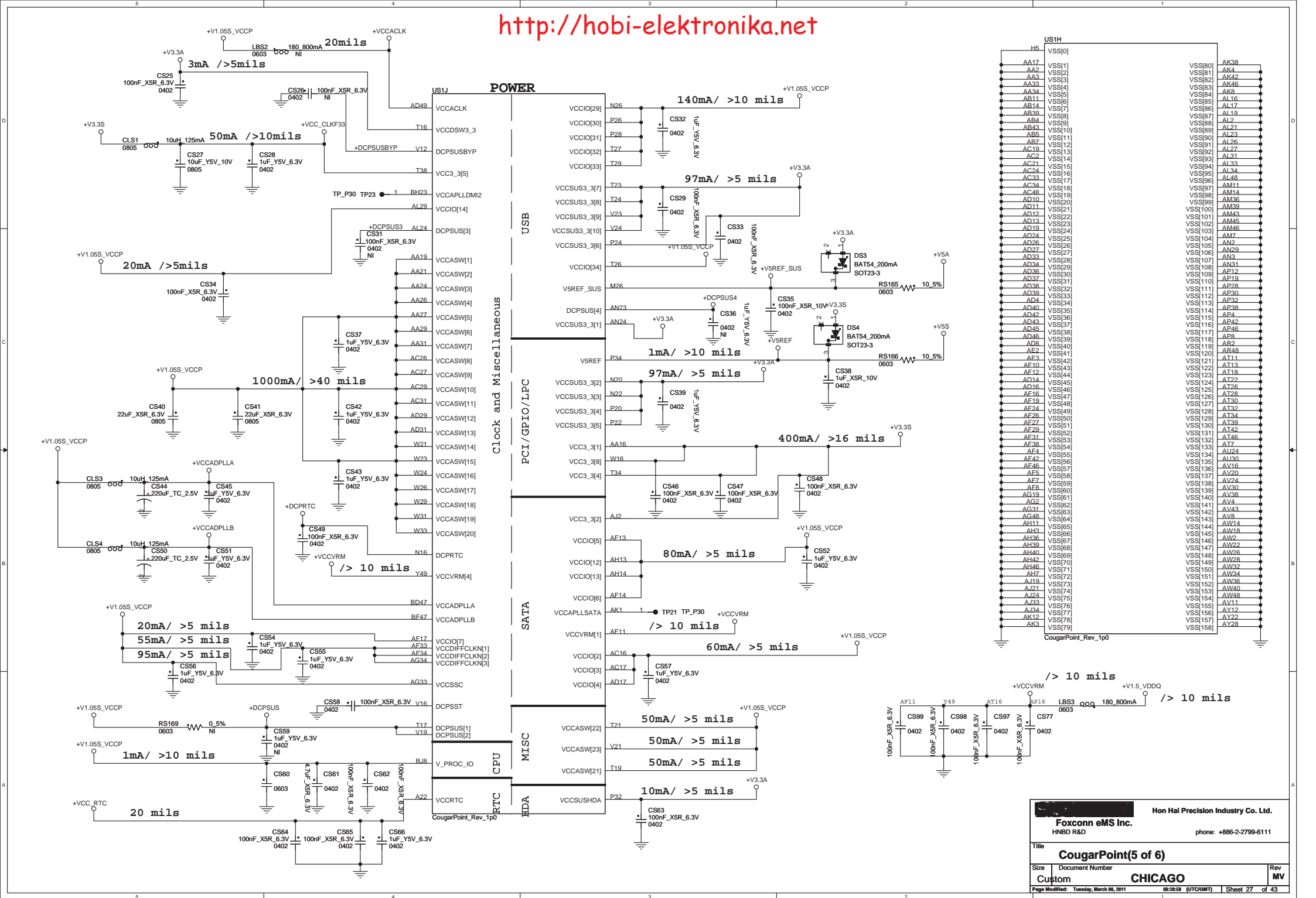
Place resistor close to PCH

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Size: Document Number
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US1H

H5	VSS1[0]	VSS1[80]	AK38
AA17	VSS1[1]	VSS1[81]	AK4
AA2	VSS1[2]	VSS1[82]	AK42
AA3	VSS1[3]	VSS1[83]	AK46
AA33	VSS1[4]	VSS1[84]	AK6
AA34	VSS1[5]	VSS1[85]	AL16
AB11	VSS1[6]	VSS1[86]	AL17
AB14	VSS1[7]	VSS1[87]	AL19
AB39	VSS1[8]	VSS1[88]	AL2
AB4	VSS1[9]	VSS1[89]	AL21
AB43	VSS1[10]	VSS1[90]	AL23
AB5	VSS1[11]	VSS1[91]	AL26
AB7	VSS1[12]	VSS1[92]	AL27
AC19	VSS1[13]	VSS1[93]	AL31
AC2	VSS1[14]	VSS1[94]	AL33
AC21	VSS1[15]	VSS1[95]	AL34
AC24	VSS1[16]	VSS1[96]	AL48
AC33	VSS1[17]	VSS1[97]	AM11
AC34	VSS1[18]	VSS1[98]	AM14
AC48	VSS1[19]	VSS1[99]	AM36
AD10	VSS1[20]	VSS1[100]	AM39
AD11	VSS1[21]	VSS1[101]	AM43
AD12	VSS1[22]	VSS1[102]	AM45
AD13	VSS1[23]	VSS1[103]	AM46
AD19	VSS1[24]	VSS1[104]	AM7
AD24	VSS1[25]	VSS1[105]	AM29
AD26	VSS1[26]	VSS1[106]	AN3
AD33	VSS1[27]	VSS1[107]	AN31
AD34	VSS1[28]	VSS1[108]	AP19
AD38	VSS1[29]	VSS1[109]	AP28
AD39	VSS1[30]	VSS1[110]	AP30
AD4	VSS1[31]	VSS1[111]	AP32
AD40	VSS1[32]	VSS1[112]	AP38
AD42	VSS1[33]	VSS1[113]	AP4
AD43	VSS1[34]	VSS1[114]	AP42
AD45	VSS1[35]	VSS1[115]	AP46
AD59	VSS1[36]	VSS1[116]	AP8
AD66	VSS1[37]	VSS1[117]	AR2
AD8	VSS1[38]	VSS1[118]	AR48
AE2	VSS1[39]	VSS1[119]	AT11
AE3	VSS1[40]	VSS1[120]	AT13
AE10	VSS1[41]	VSS1[121]	AT18
AE12	VSS1[42]	VSS1[122]	AT22
AE14	VSS1[43]	VSS1[123]	AT26
AE16	VSS1[44]	VSS1[124]	AT28
AE19	VSS1[45]	VSS1[125]	AT30
AE24	VSS1[46]	VSS1[126]	AT32
AE26	VSS1[47]	VSS1[127]	AT34
AE27	VSS1[48]	VSS1[128]	AT39
AE29	VSS1[49]	VSS1[129]	AT42
AE31	VSS1[50]	VSS1[130]	AT46
AE38	VSS1[51]	VSS1[131]	AT7
AE4	VSS1[52]	VSS1[132]	AU24
AE42	VSS1[53]	VSS1[133]	AU30
AE46	VSS1[54]	VSS1[134]	AV16
AE5	VSS1[55]	VSS1[135]	AV20
AE57	VSS1[56]	VSS1[136]	AV24
AE7	VSS1[57]	VSS1[137]	AV30
AE8	VSS1[58]	VSS1[138]	AV4
AG19	VSS1[59]	VSS1[139]	AV38
AG2	VSS1[60]	VSS1[140]	AV44
AG31	VSS1[61]	VSS1[141]	AV43
AG48	VSS1[62]	VSS1[142]	AV8
AH11	VSS1[63]	VSS1[143]	AW14
AH3	VSS1[64]	VSS1[144]	AW2
AH36	VSS1[65]	VSS1[145]	AW22
AH39	VSS1[66]	VSS1[146]	AW26
AH40	VSS1[67]	VSS1[147]	AW28
AH42	VSS1[68]	VSS1[148]	AW32
AH46	VSS1[69]	VSS1[149]	AW34
AH7	VSS1[70]	VSS1[150]	AW36
AJ19	VSS1[71]	VSS1[151]	AW40
AJ21	VSS1[72]	VSS1[152]	AW48
AJ24	VSS1[73]	VSS1[153]	AV11
AJ33	VSS1[74]	VSS1[154]	AY12
AJ34	VSS1[75]	VSS1[155]	AY22
AK12	VSS1[76]	VSS1[156]	AY28
AK3	VSS1[77]	VSS1[157]	
	VSS1[78]	VSS1[158]	
	VSS1[79]	VSS1[159]	

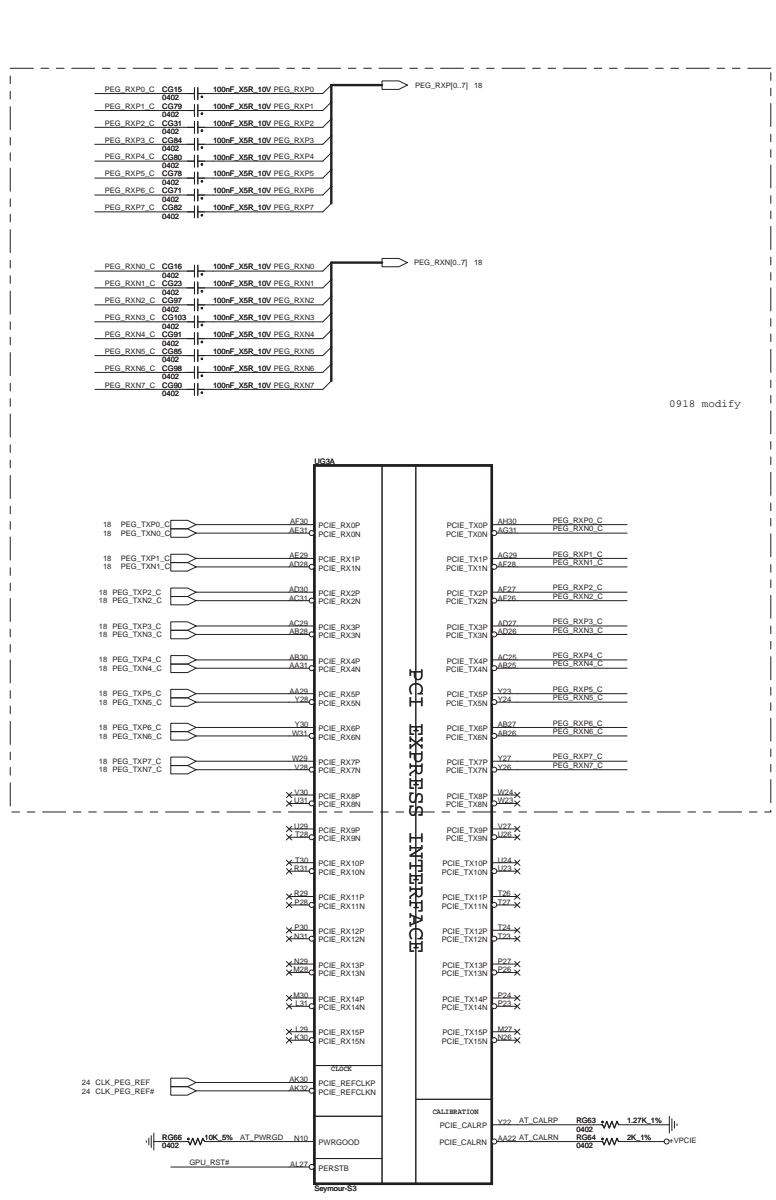
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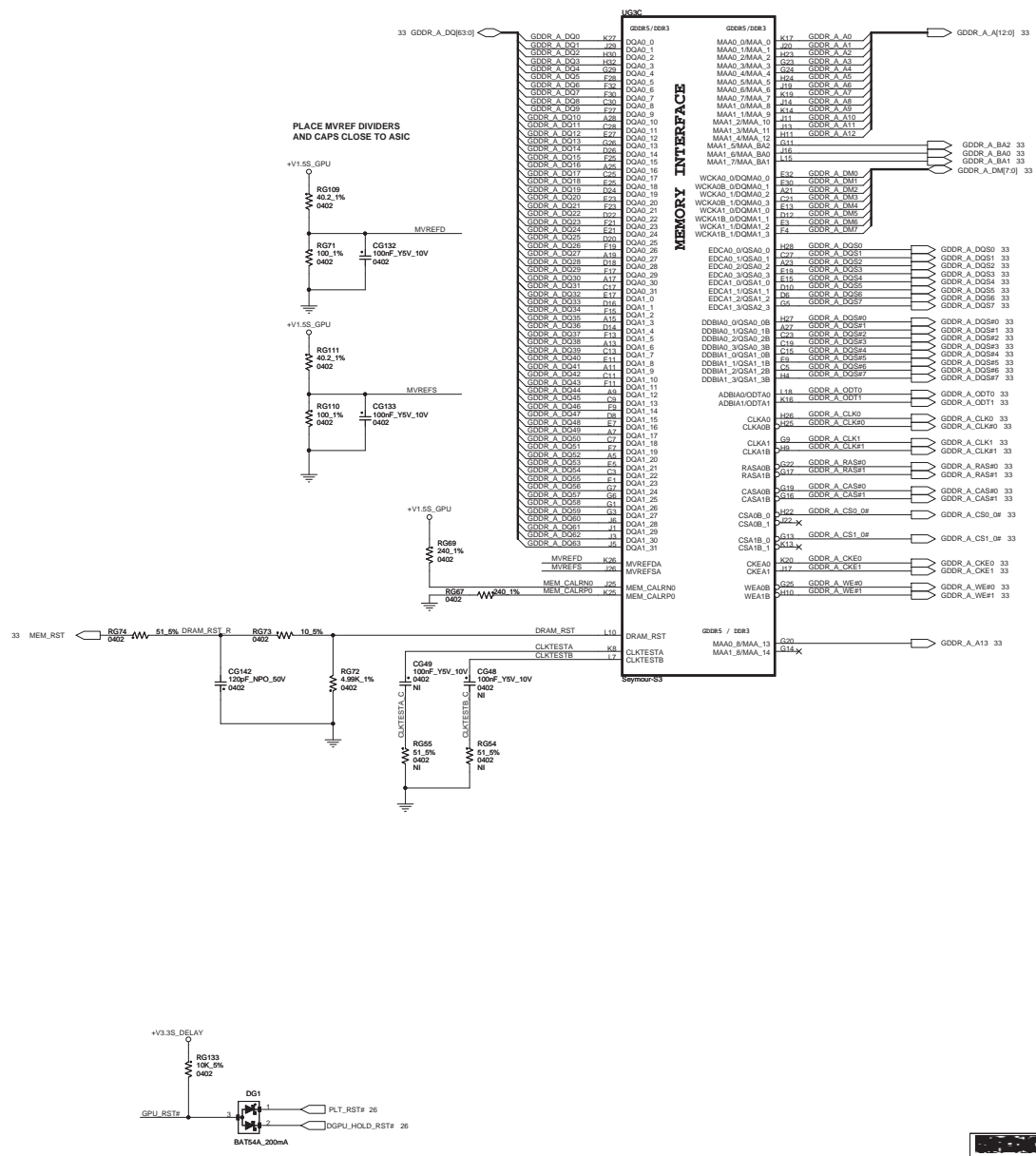
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0918 modify



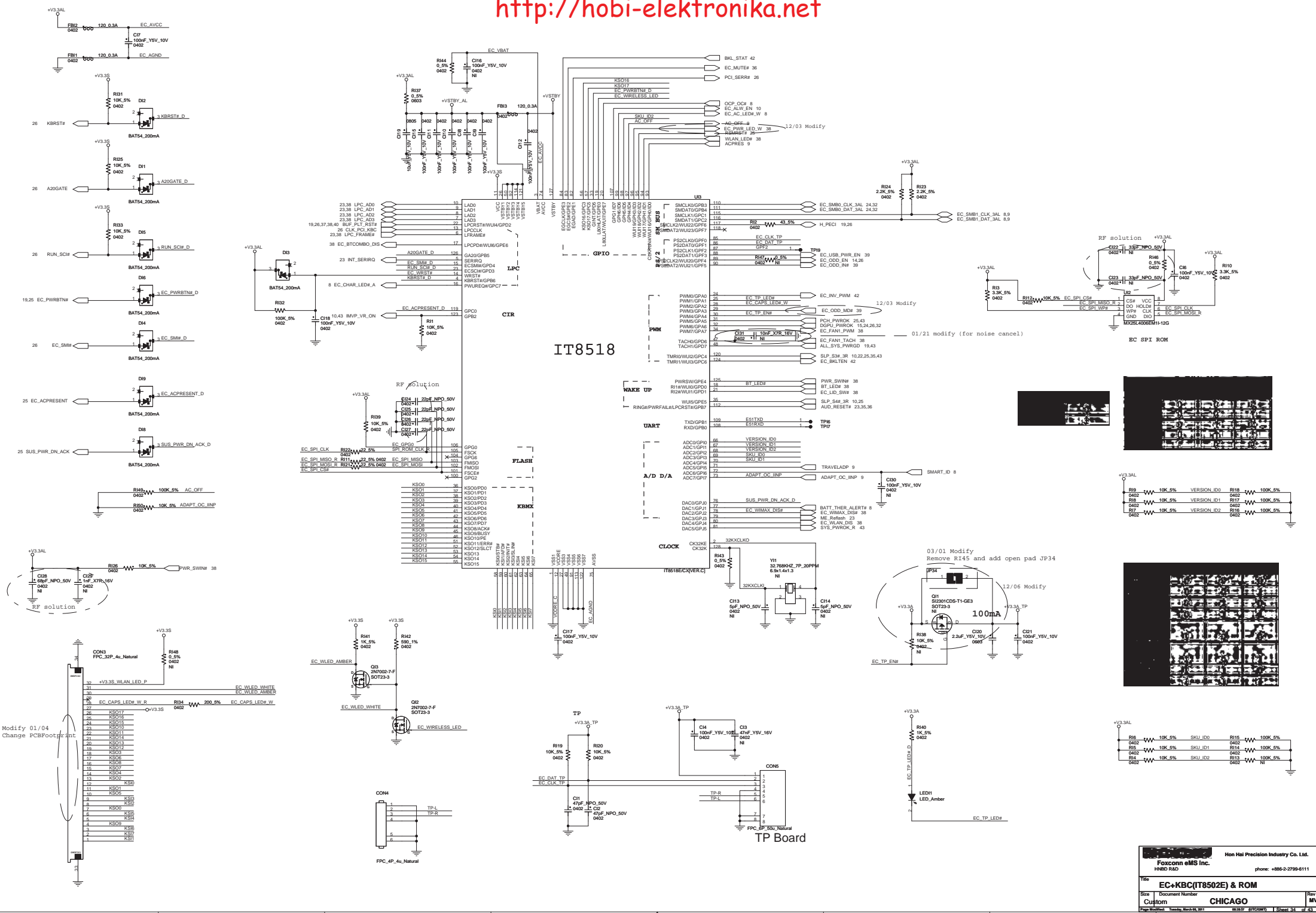
PLACE MVREF DIVIDERS AND CAPS CLOSE TO ASIC

MEMORY INTERFACE

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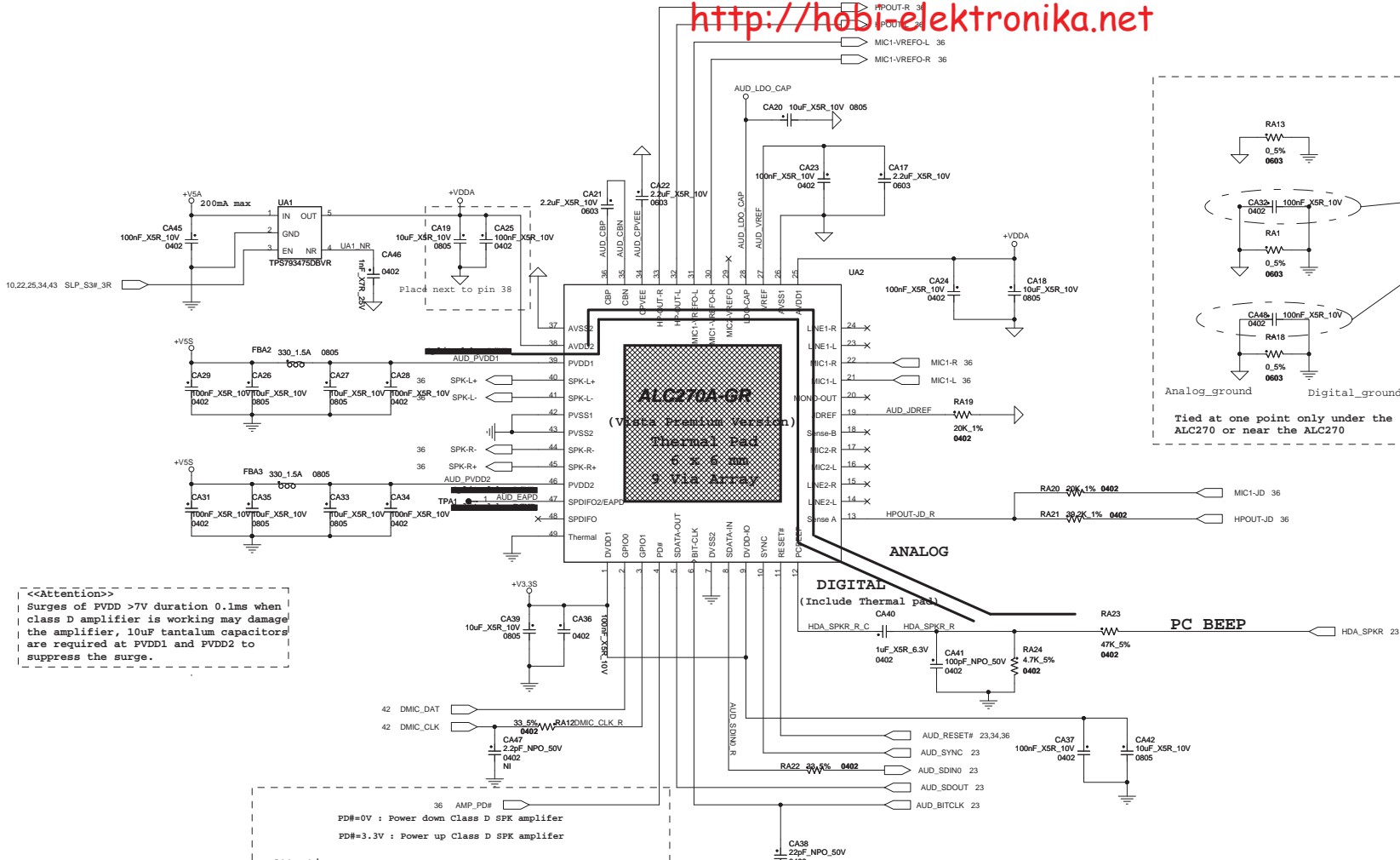
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 Custom: **CHICAGO**

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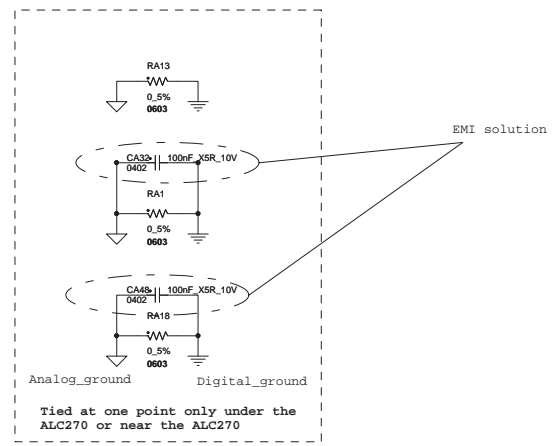
IT518

Modify 01/04
Change PCB footprint

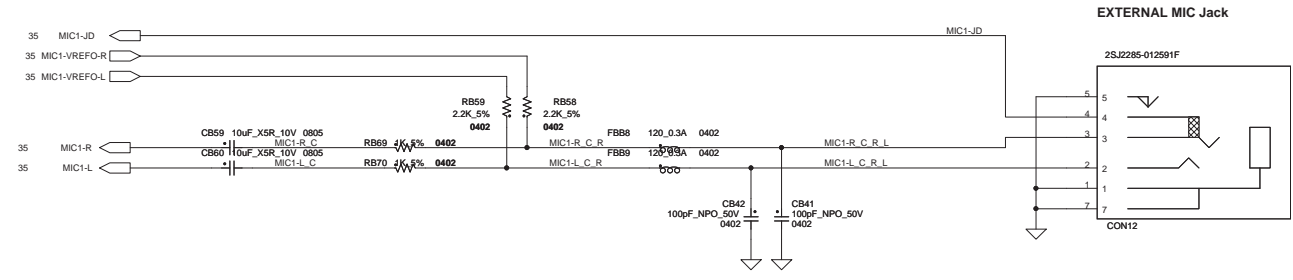
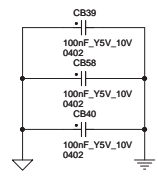
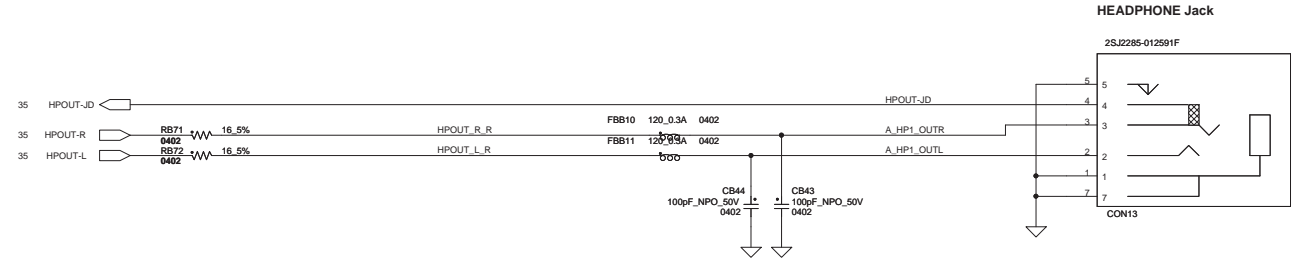
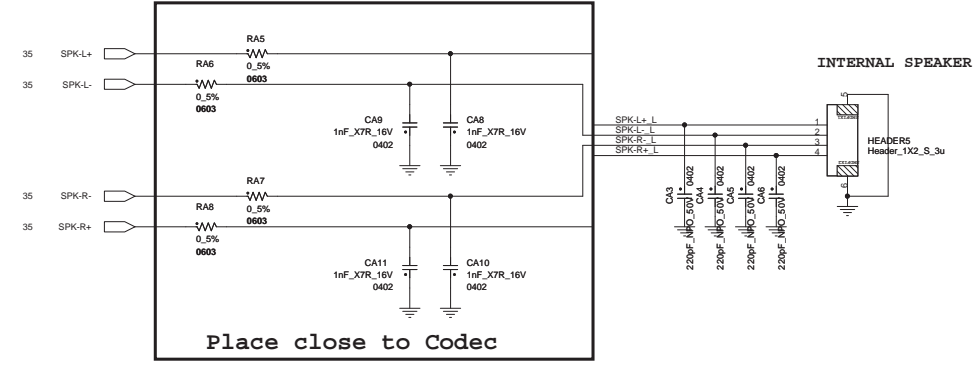
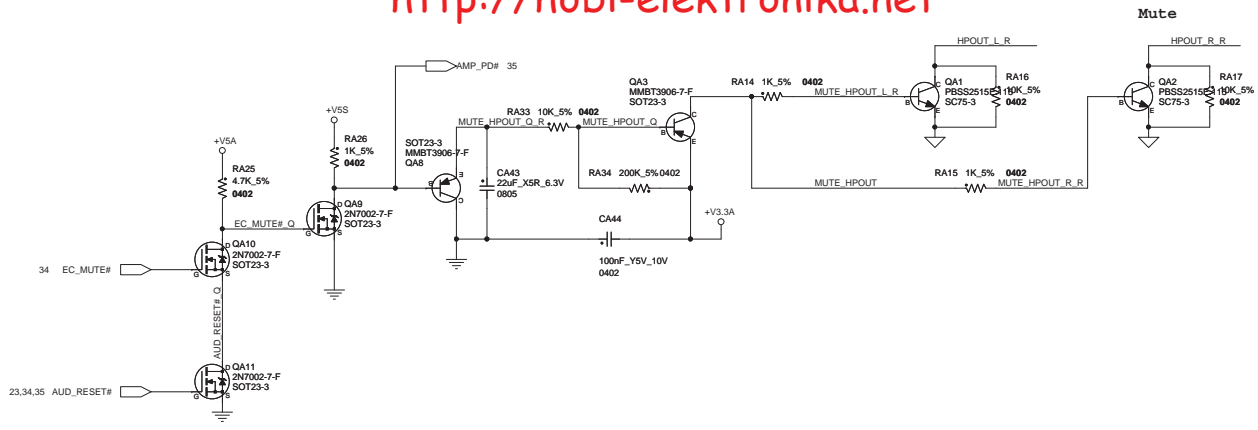


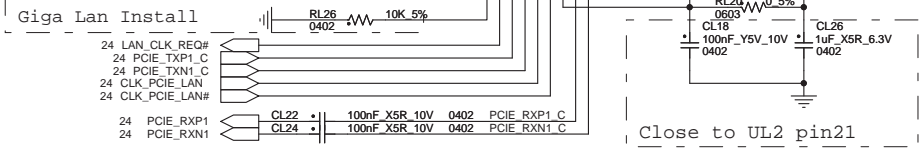
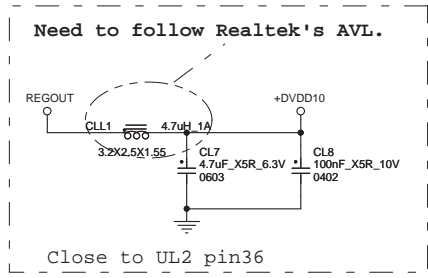
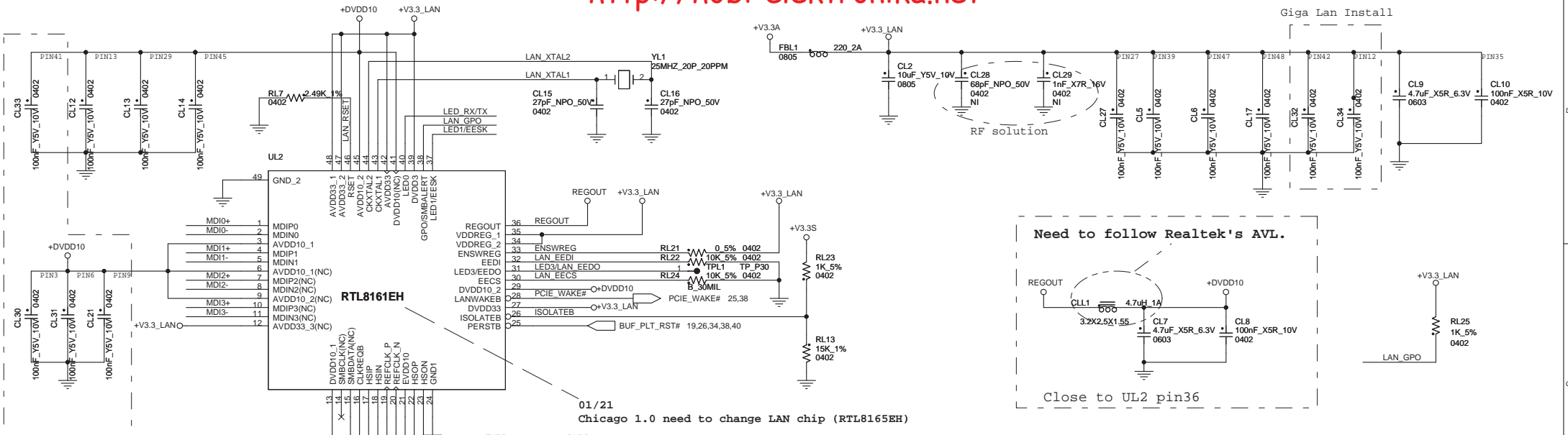
36 AMP_PD#
 PD#=0V : Power down Class D SPK amplifier
 PD#3.3V : Power up Class D SPK amplifier

<<Attention>>
 For power_on/off de-pop circuit and system booting warning signal: Please System BIOS Engineer Note :
 1. If you want the system make warning signal after power on, please let EC_MUTE# High first.
 2. When you want to exit your Bios Programming Code, please let the EC_MUTE# Low. (The programming is different from before.)

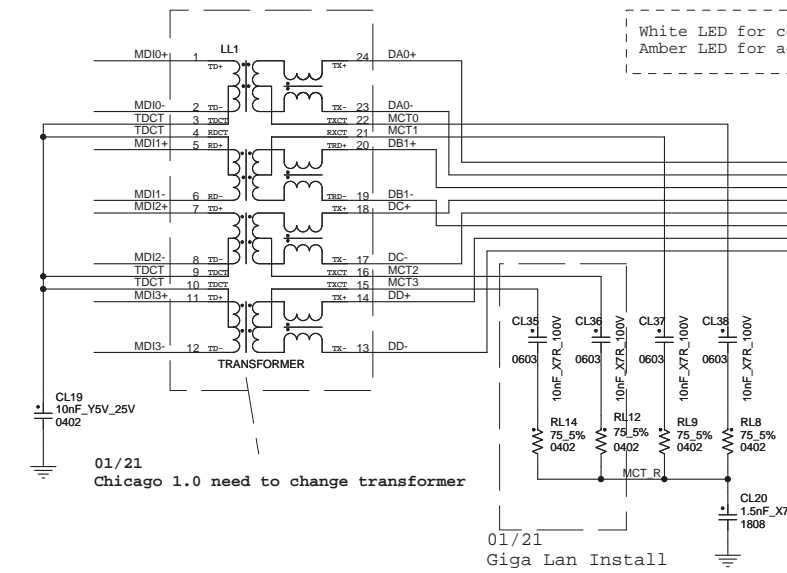
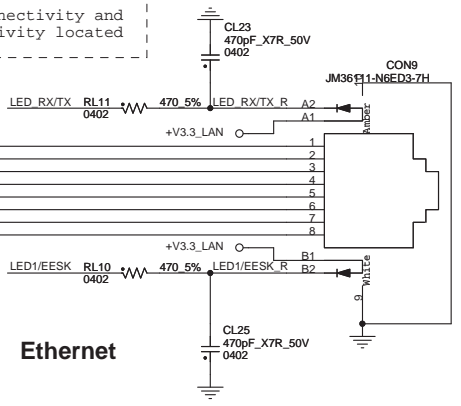


EMI solution

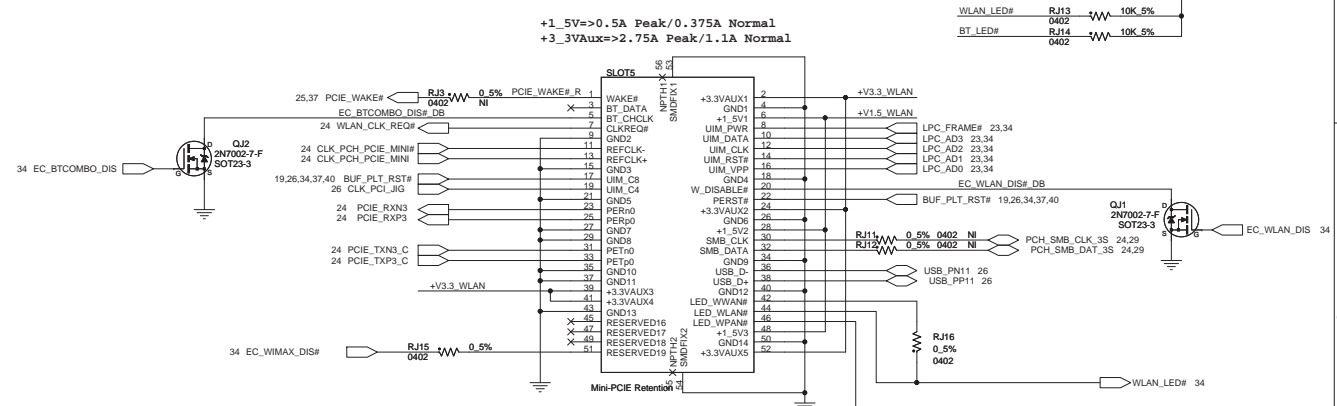
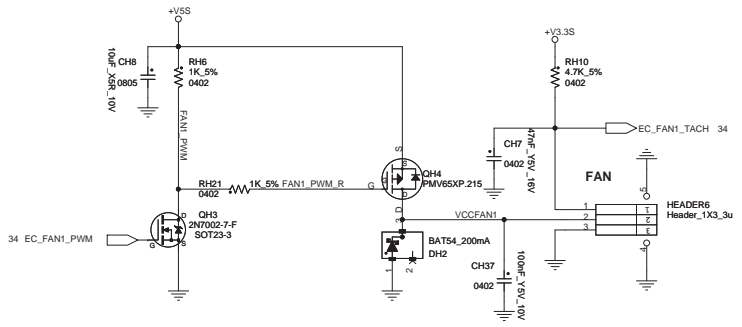
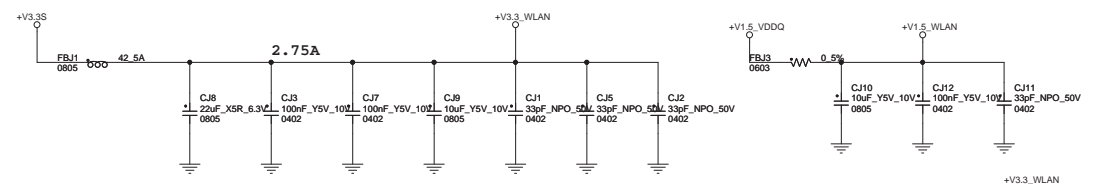
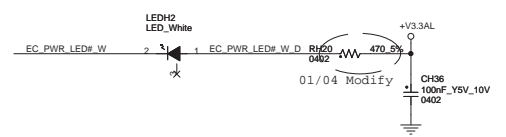




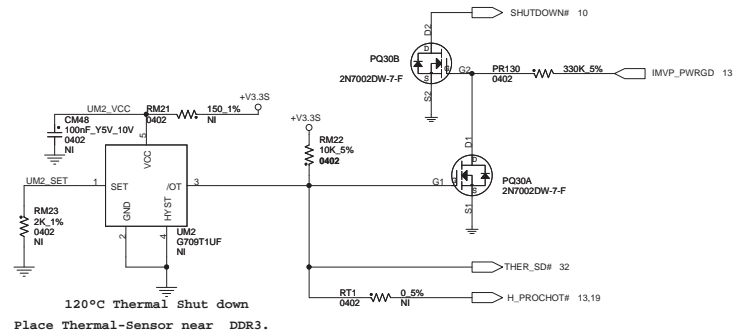
White LED for connectivity and Amber LED for activity located



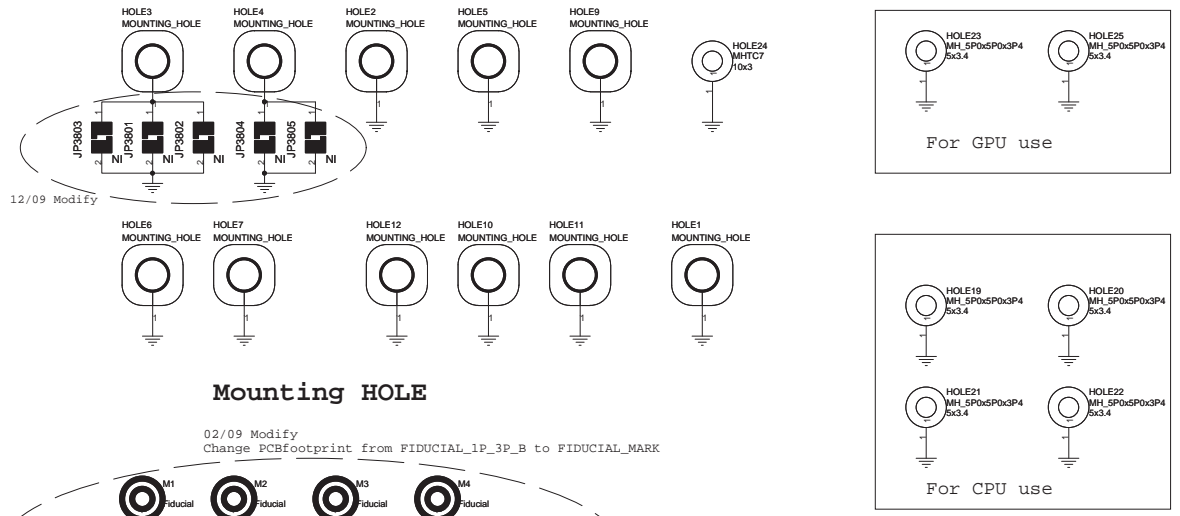
Foxconn eMS Inc. HNB D R&D phone: +886-2-2799-6111		Hon Hai Precision Industry Co. Ltd.	
Title: LAN (RTL8165EH)			
Size	Document Number	Rev	
Custom			
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Half Mini Card for WLAN



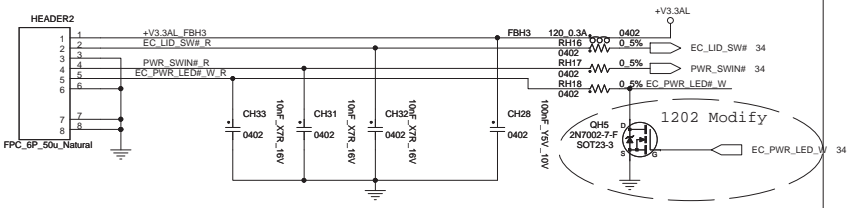
120°C Thermal Shut down
Place Thermal-Sensor near DDR3.



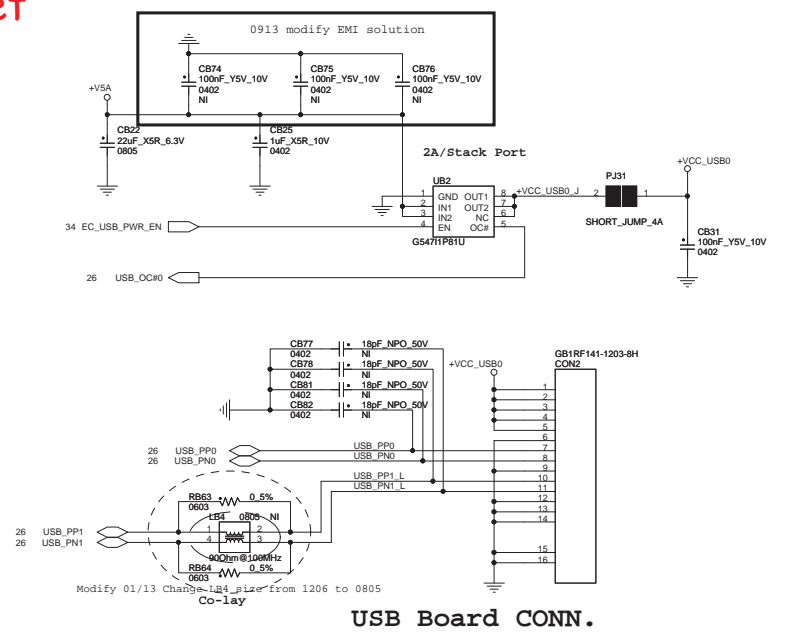
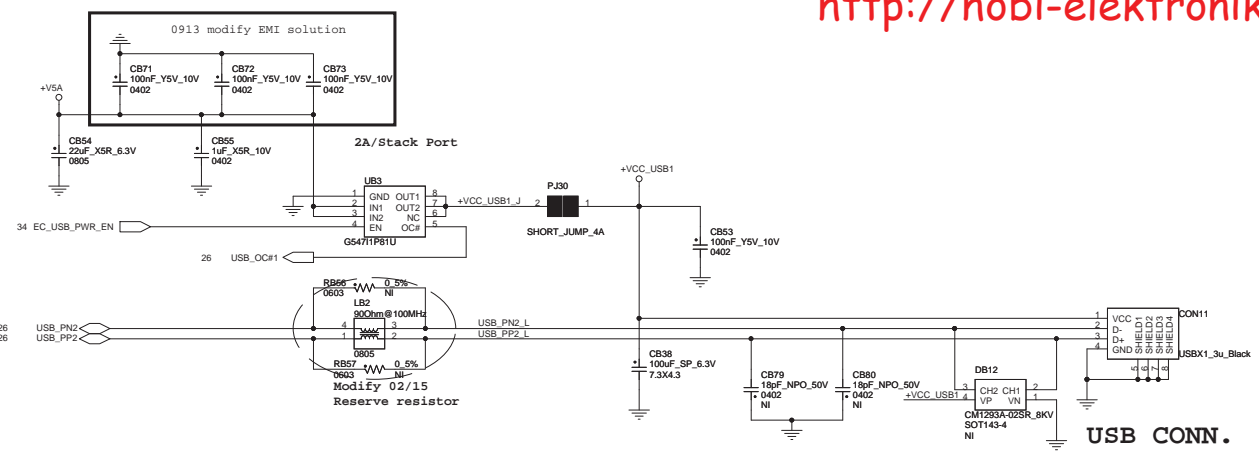
Mounting HOLE

Fiducial Mark

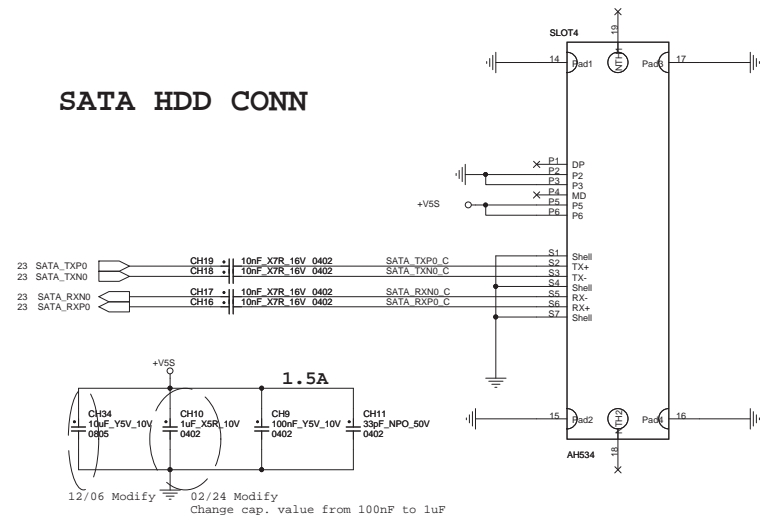
PWR Board CONN.



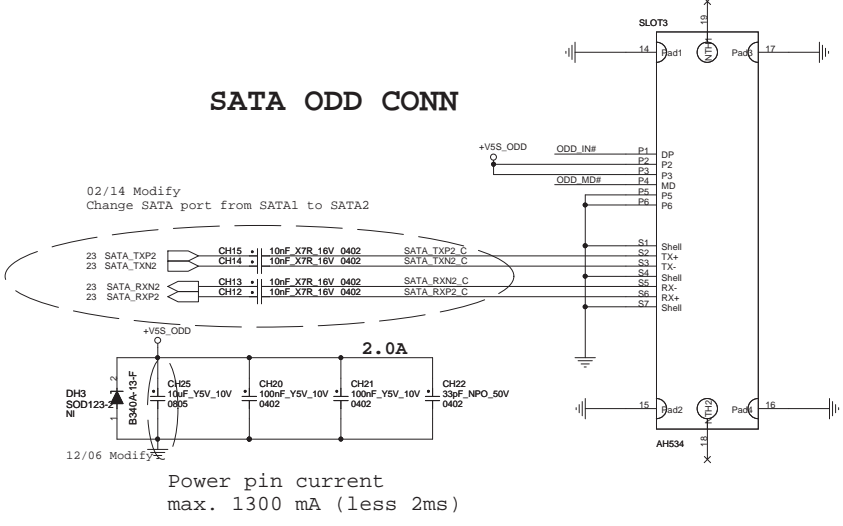
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Title Mini PCIe & RJ11 & BT		phone: +886-2-2799-6111	
Size	Document Number	Rev	
Custom			
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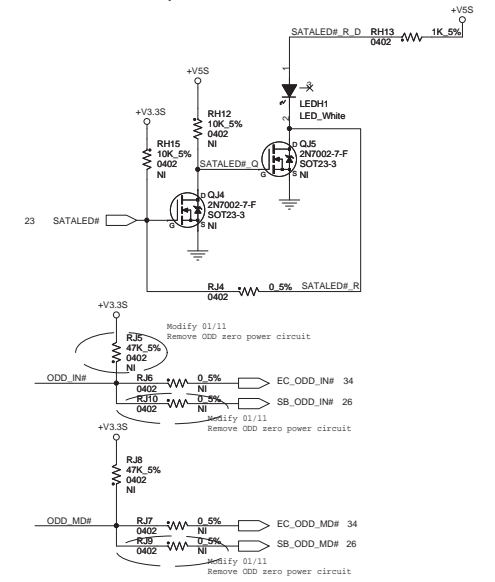
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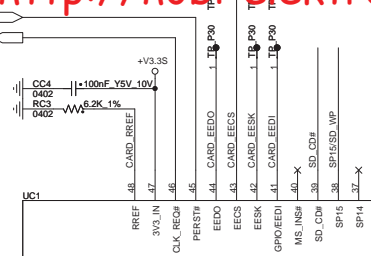
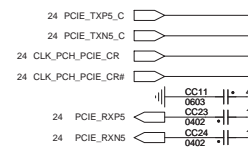
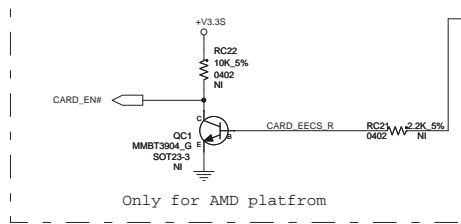


SATA ODD CONN

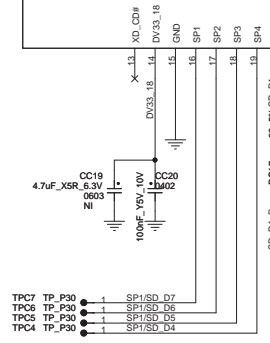


HDD/ODD Status LED



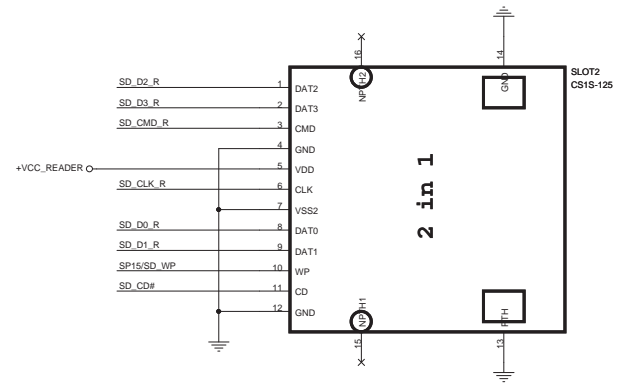
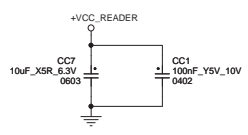


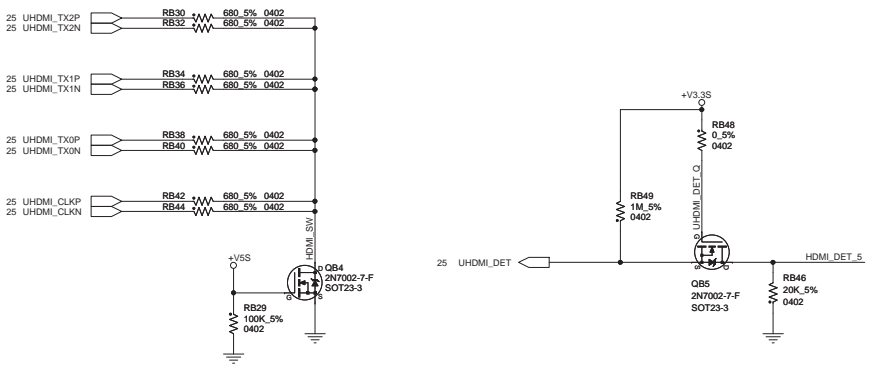
RTS5219-GR



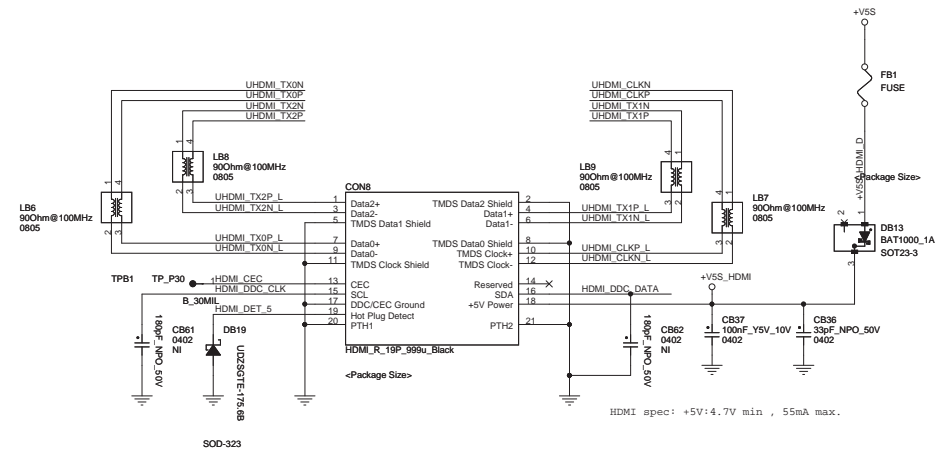
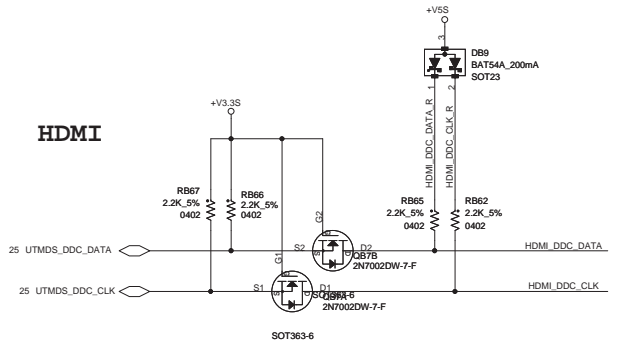
RC14-RC19, CC10 close to chip pin!

1/24 Modify Check with EMI/ESDMax

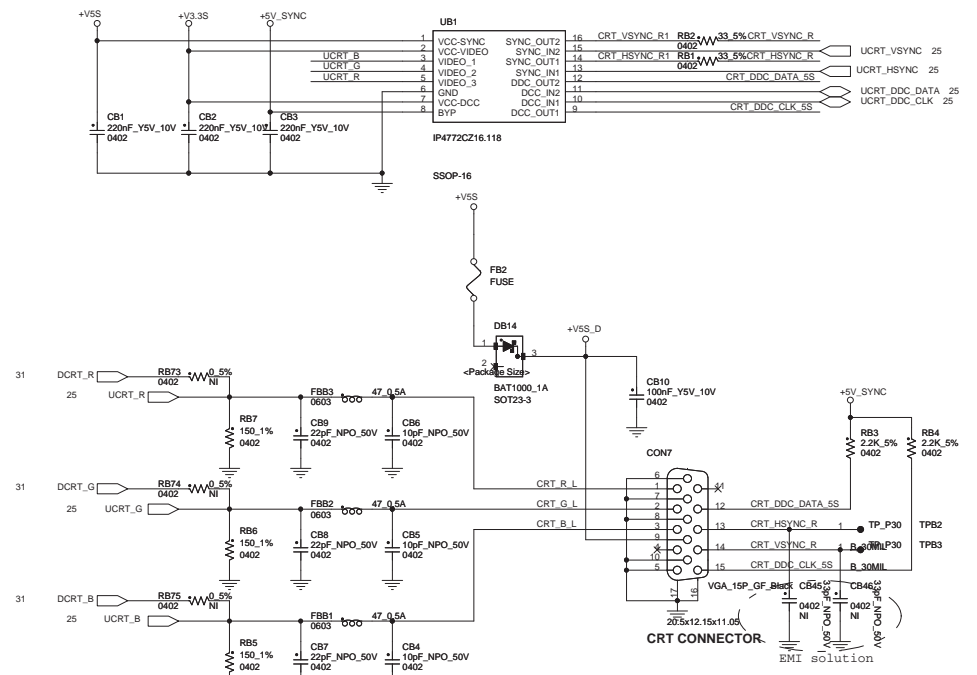


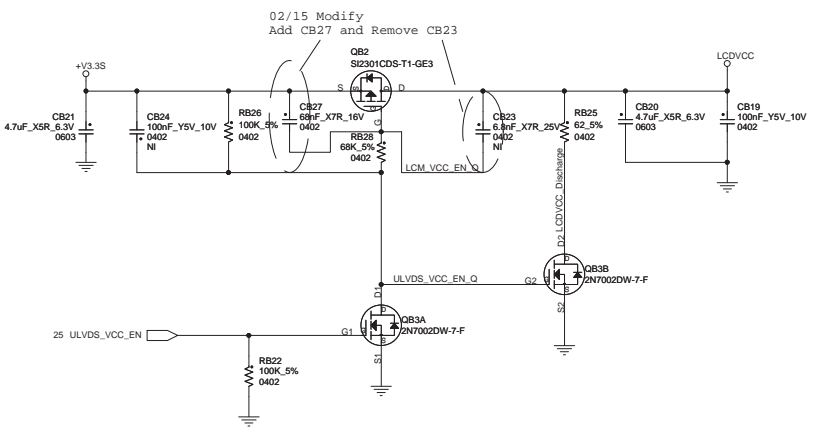
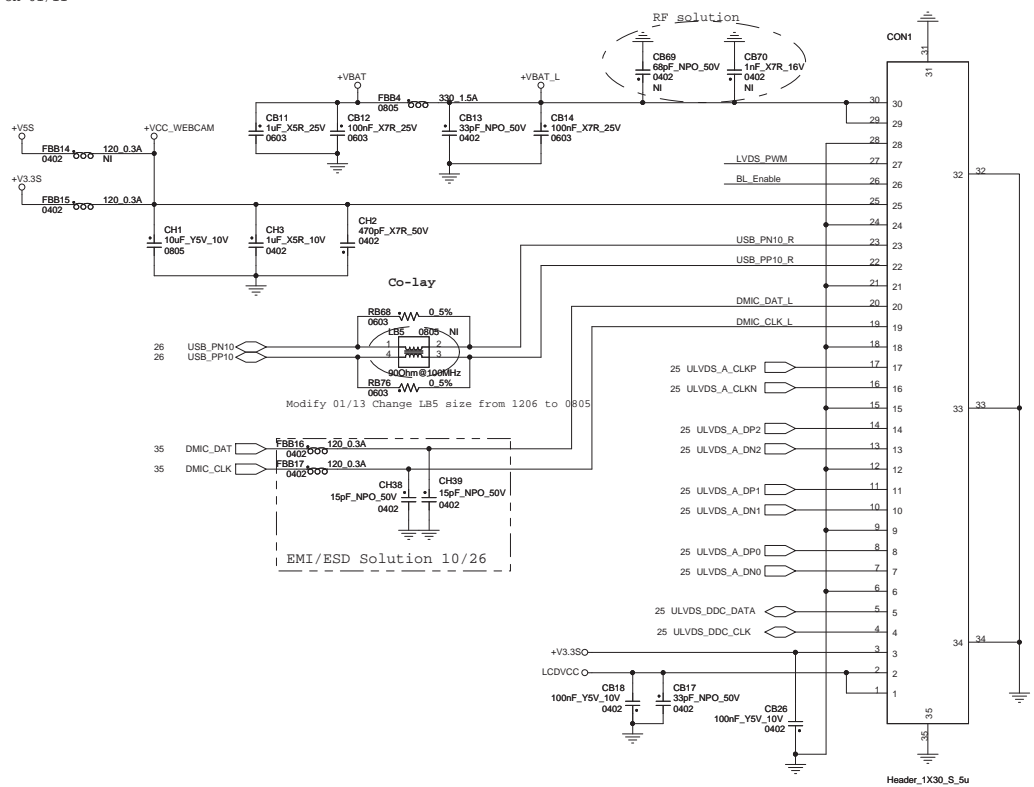
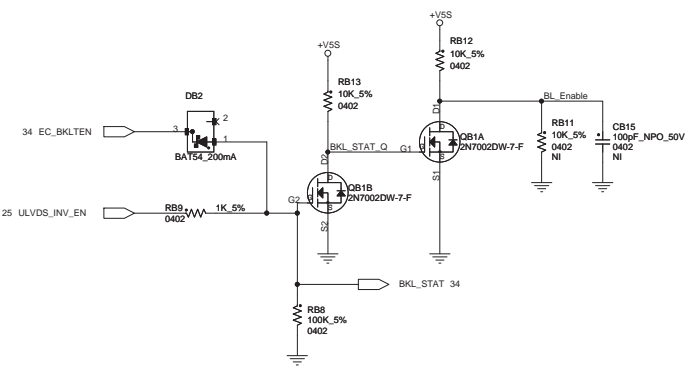
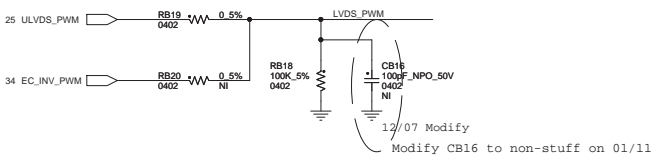


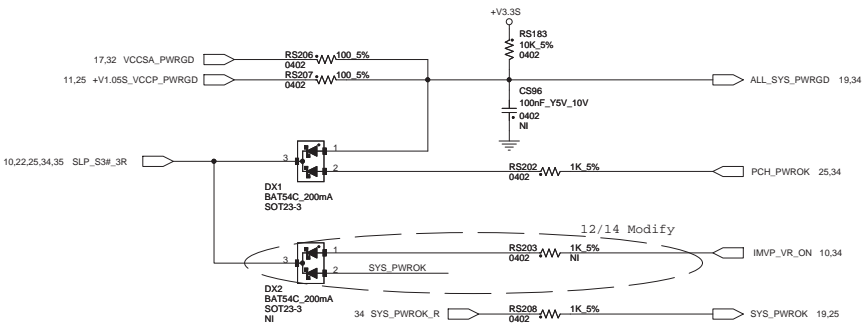
HDMI



CRT



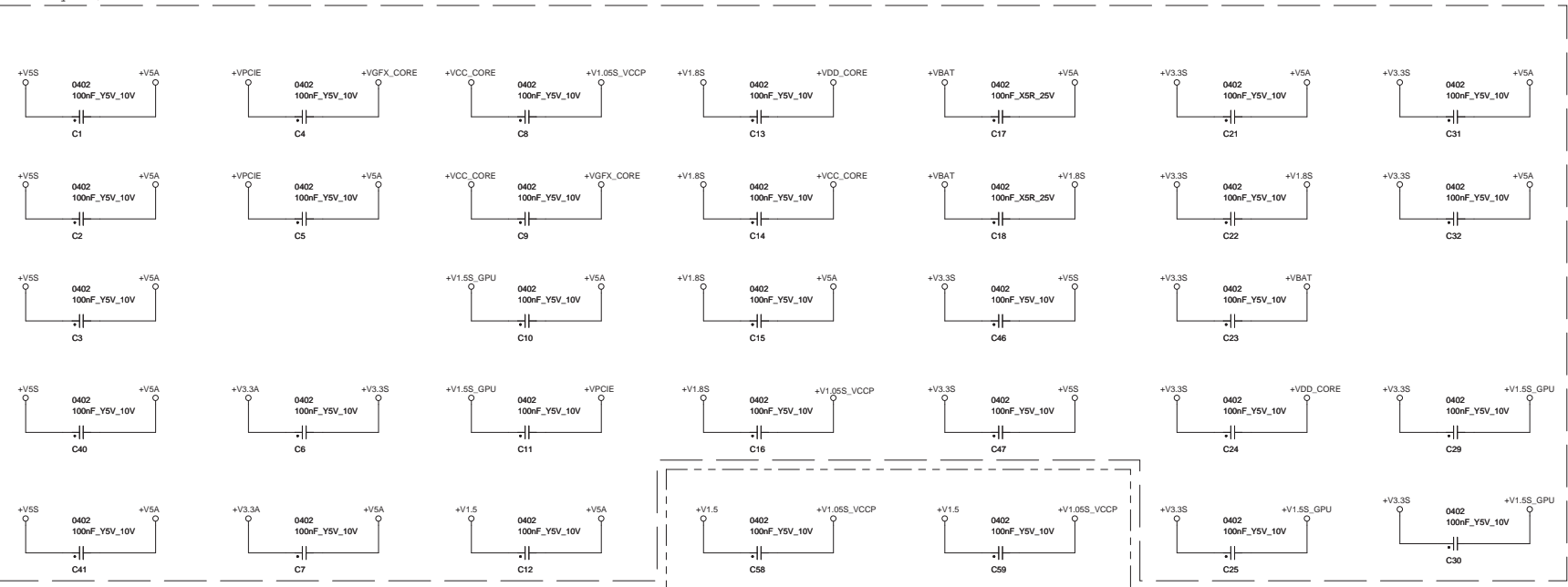
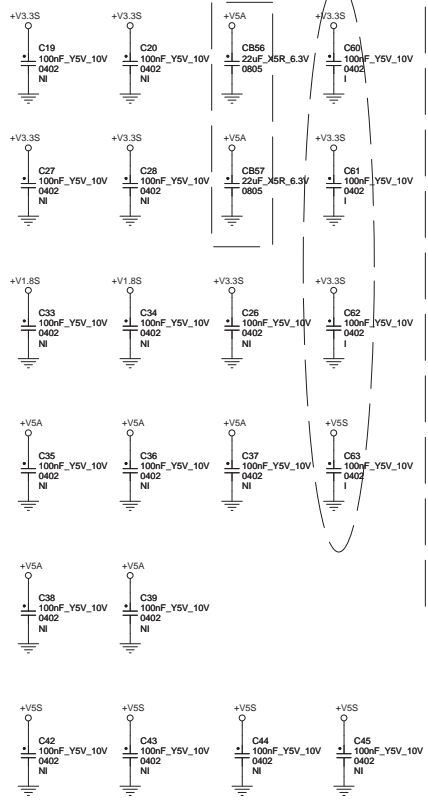




12/07 Modify

Modify 11/11

1117 Modify



EMI/ESD Solution 10/26

