

Danzka Dubra 08

SI1_BUILD

HP 6530B 6730B
6050A2219901-MB-A03

2007.11.02

DATE	CHANGE NO.	REV

DRAWER	EE	DATE	POWER	DATE	INVENTEC					
DESIGN					TITLE DD08					
CHECK					SIZE = 3					
RESPONSIBLE					FILE NAME: XXXX-XXXXXX-XX	VER:	SIZE A3	CODE CS	DOC. NUMBER	REV
					P/N: XXXXXXXXXXXXX		SHEET	1	OF	69

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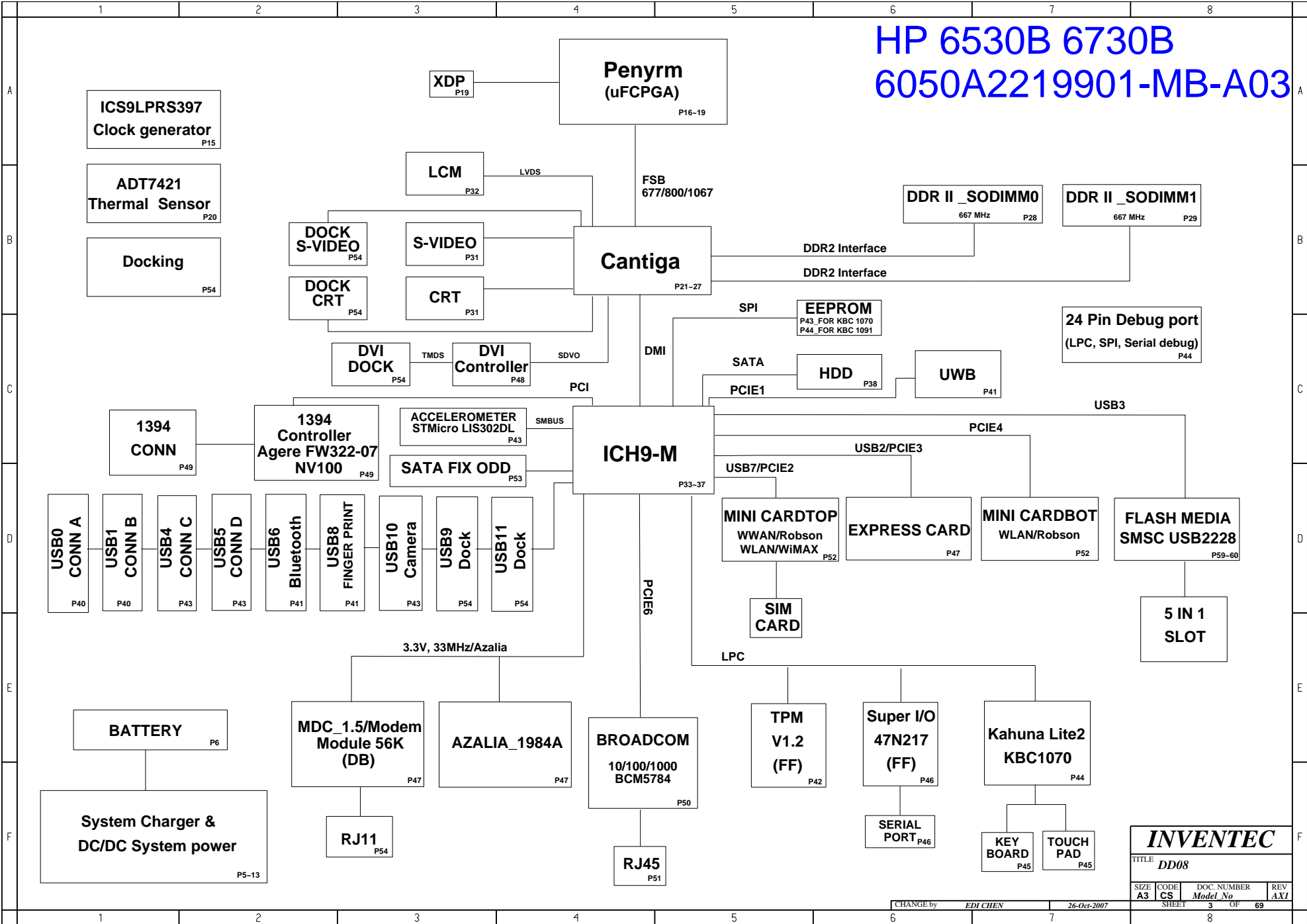
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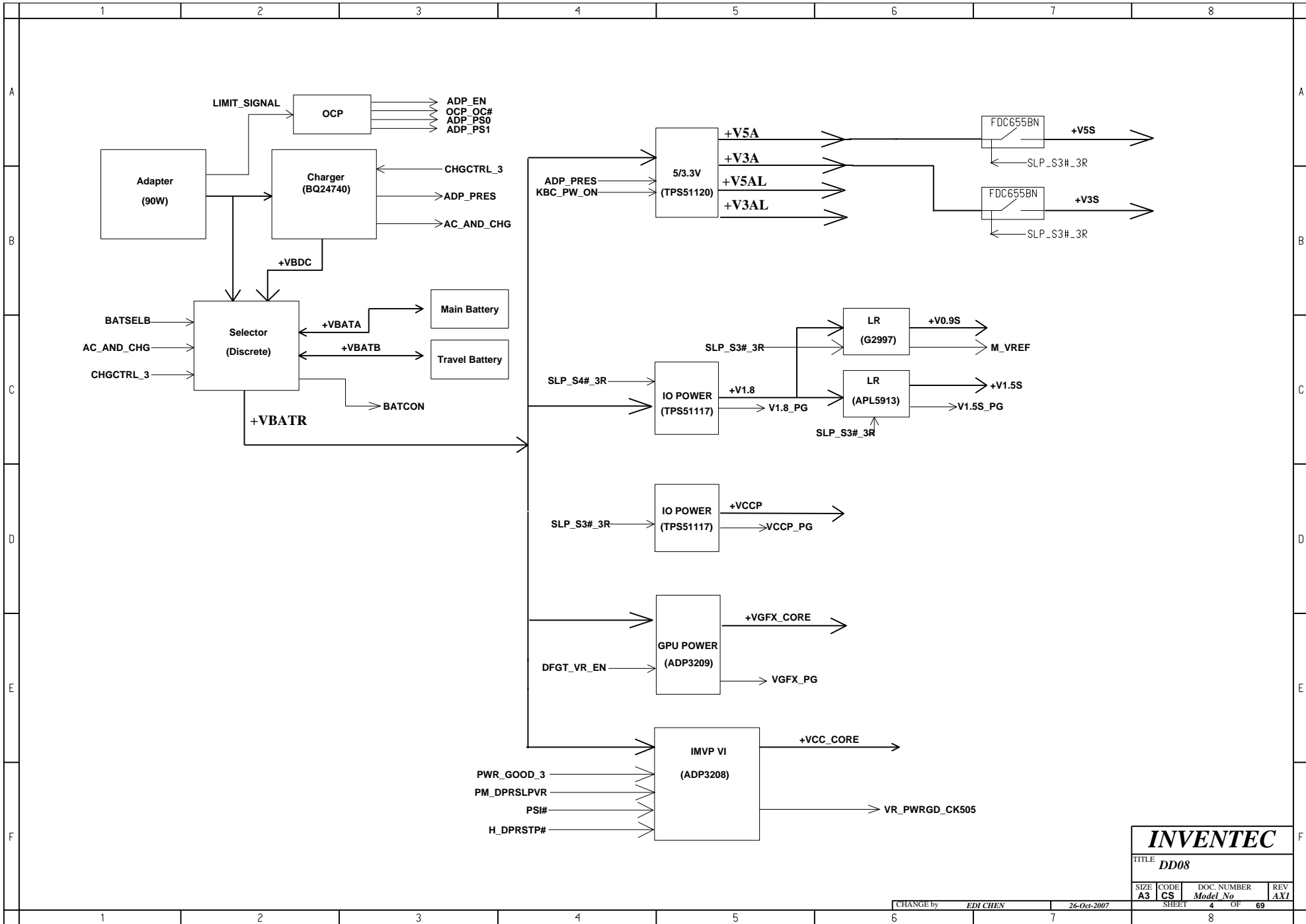
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CHANGE by *EDI CHEN* 26-Oct-2007

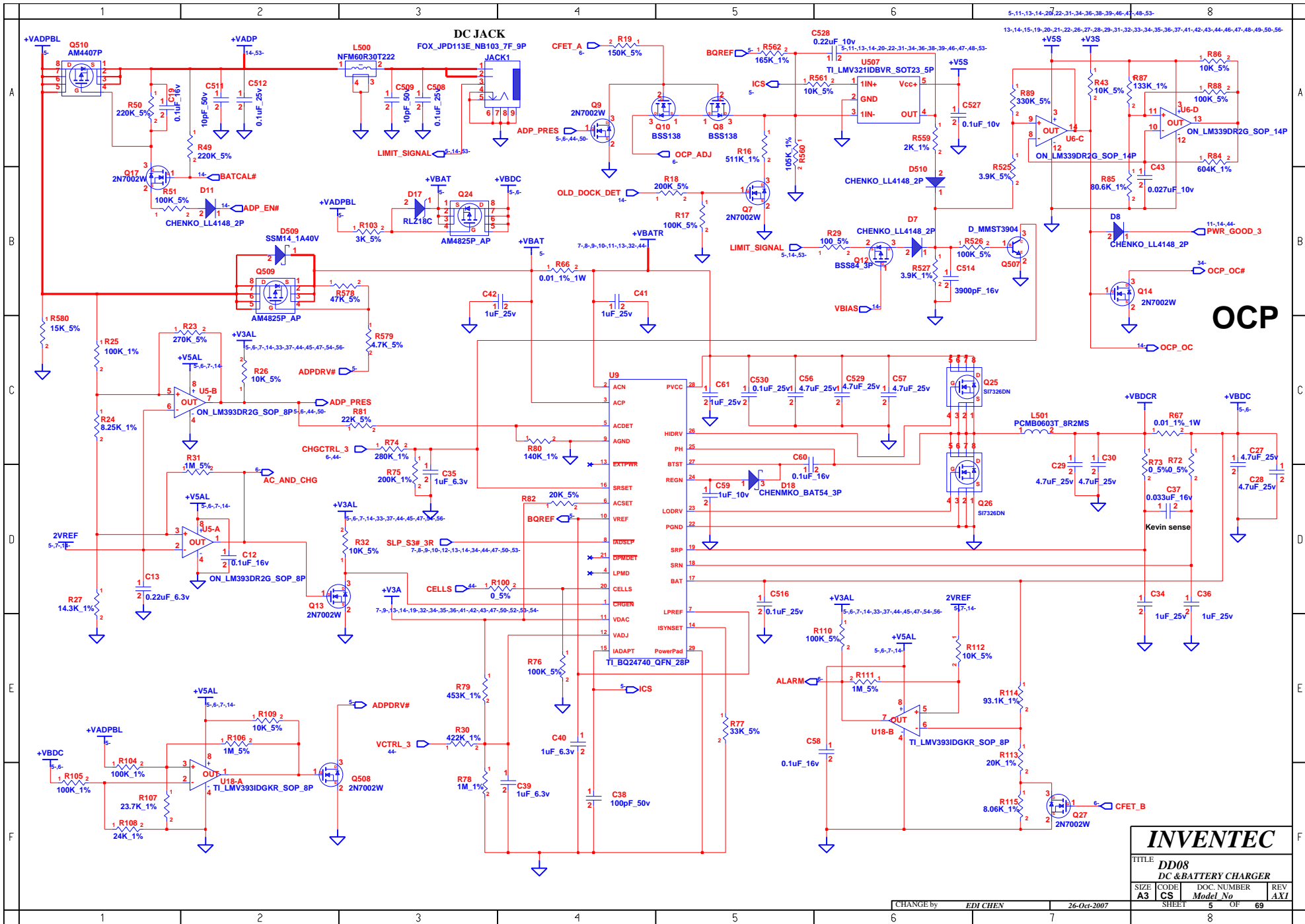
HP 6530B 6730B 6050A2219901-MB-A03



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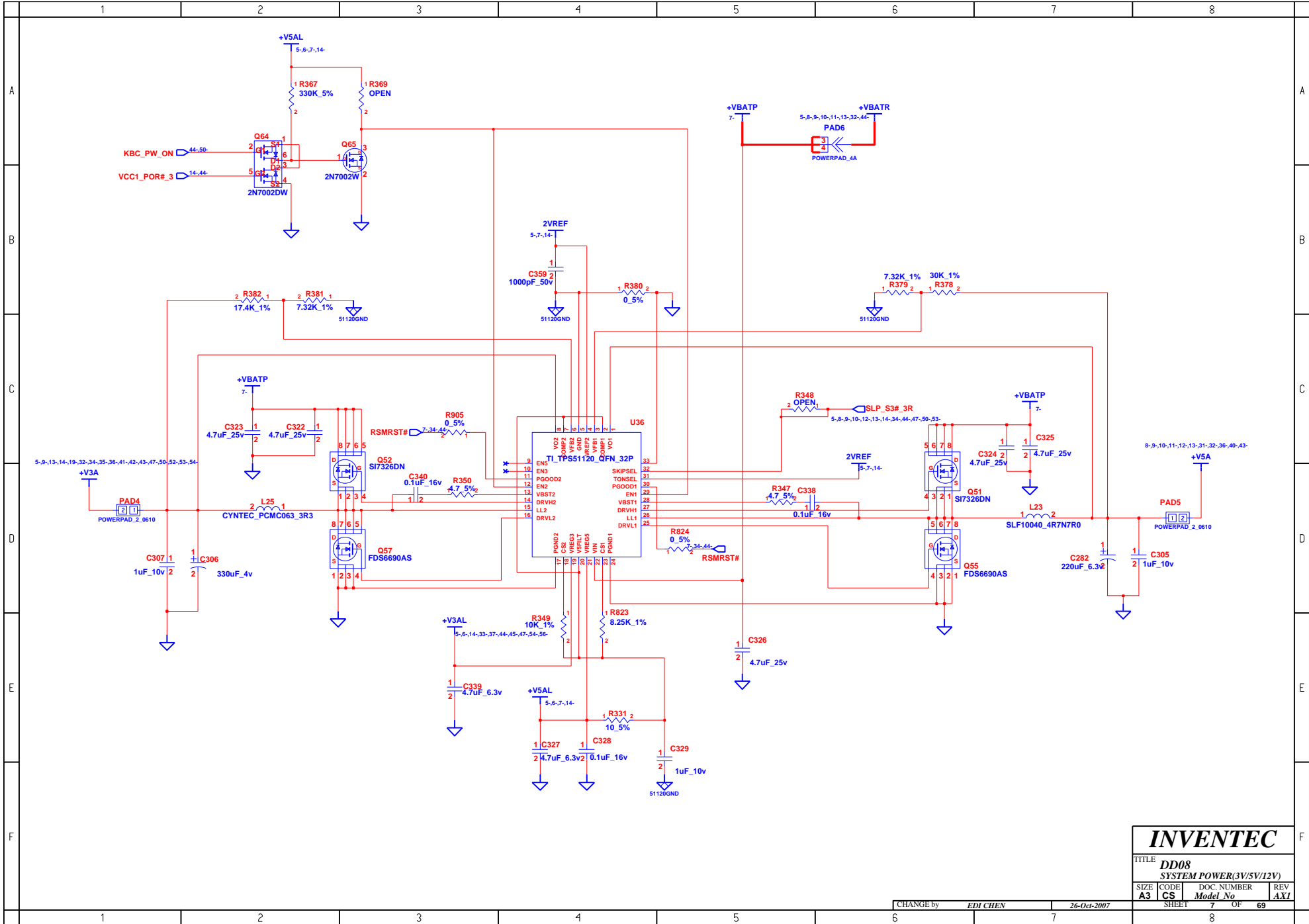


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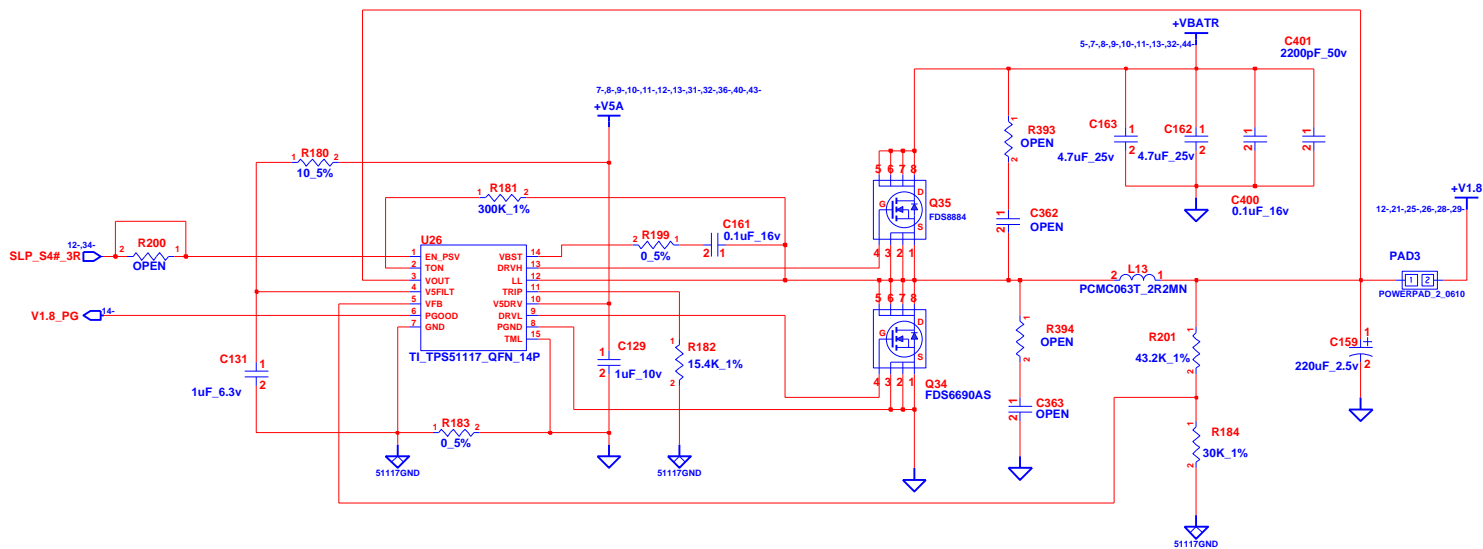
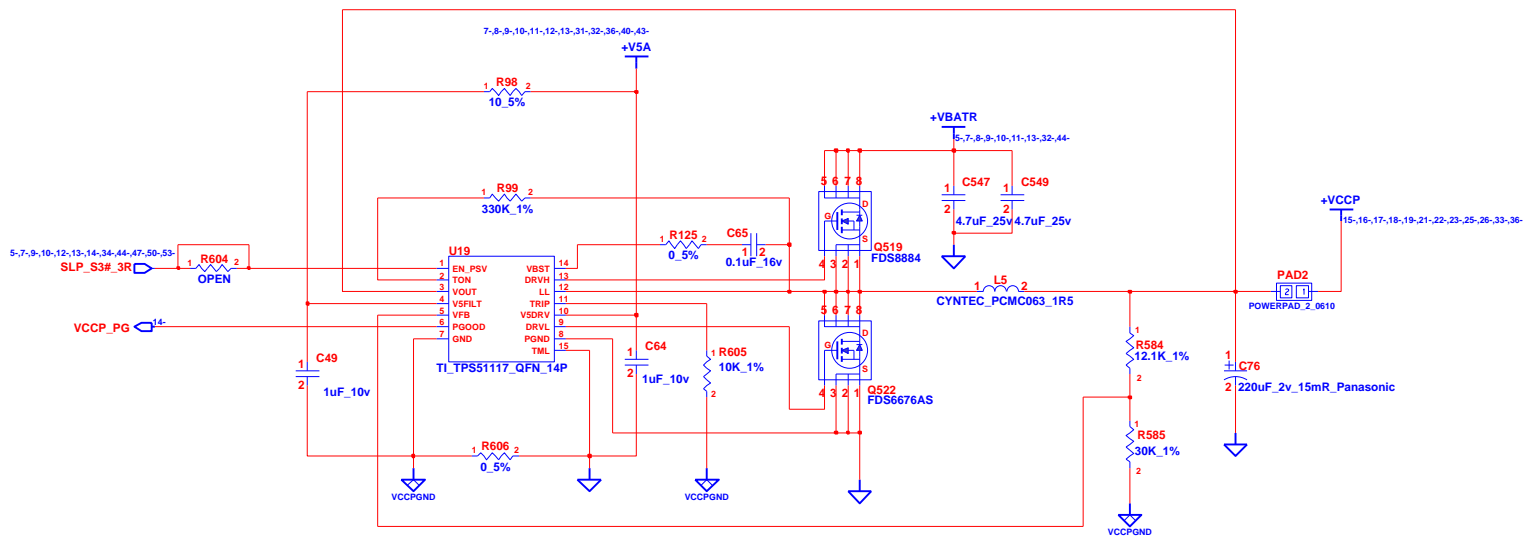


OCP

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A3	CS	Model No.	AX1
CHANGE by		EDJ CHEN	26-Oct-2007
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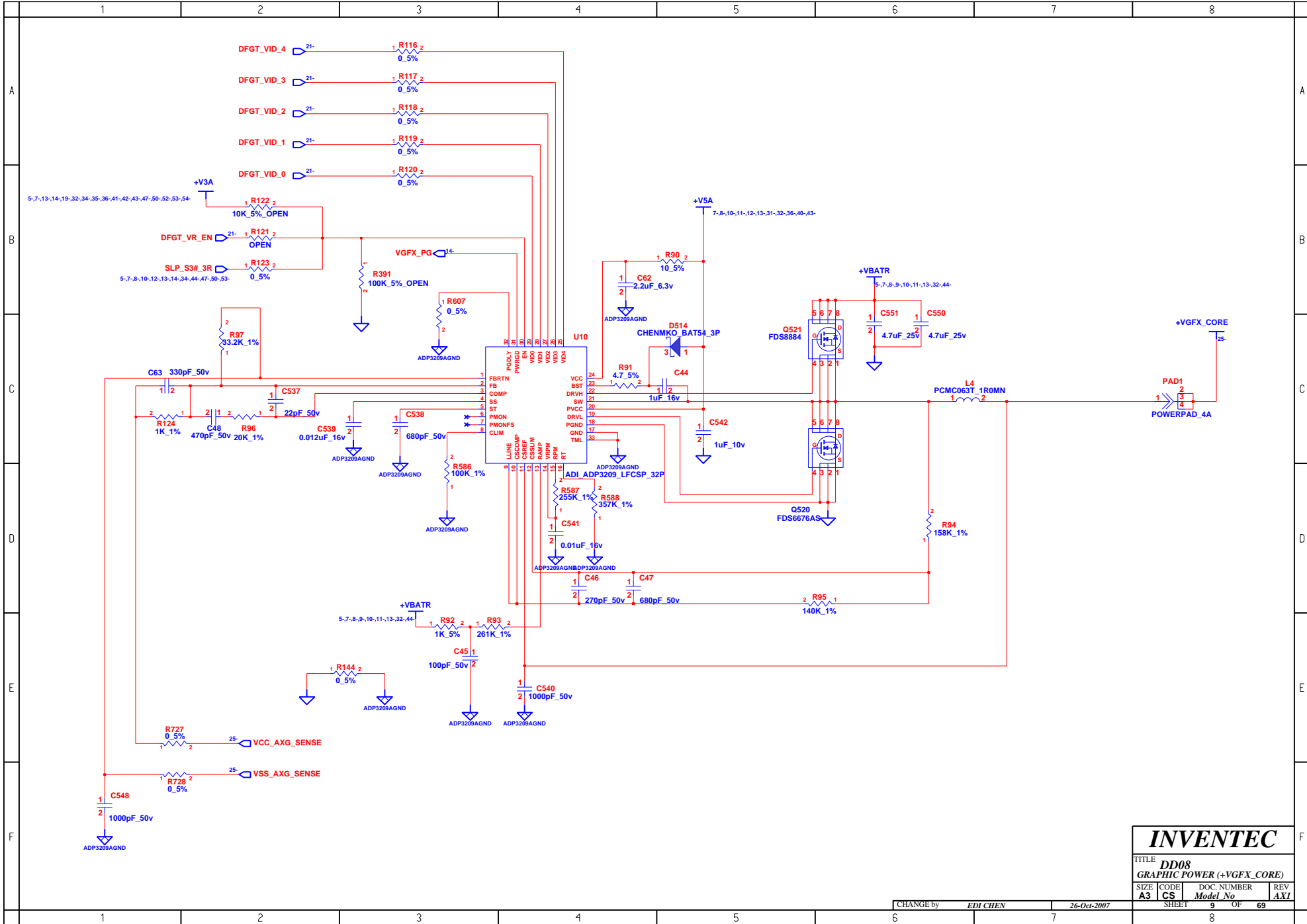


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SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI
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TITLE DD08			
SYSTEM POWER(+V1.8/+V1.25S)			
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CHANGE by EDI CHEN 26-Oct-2007

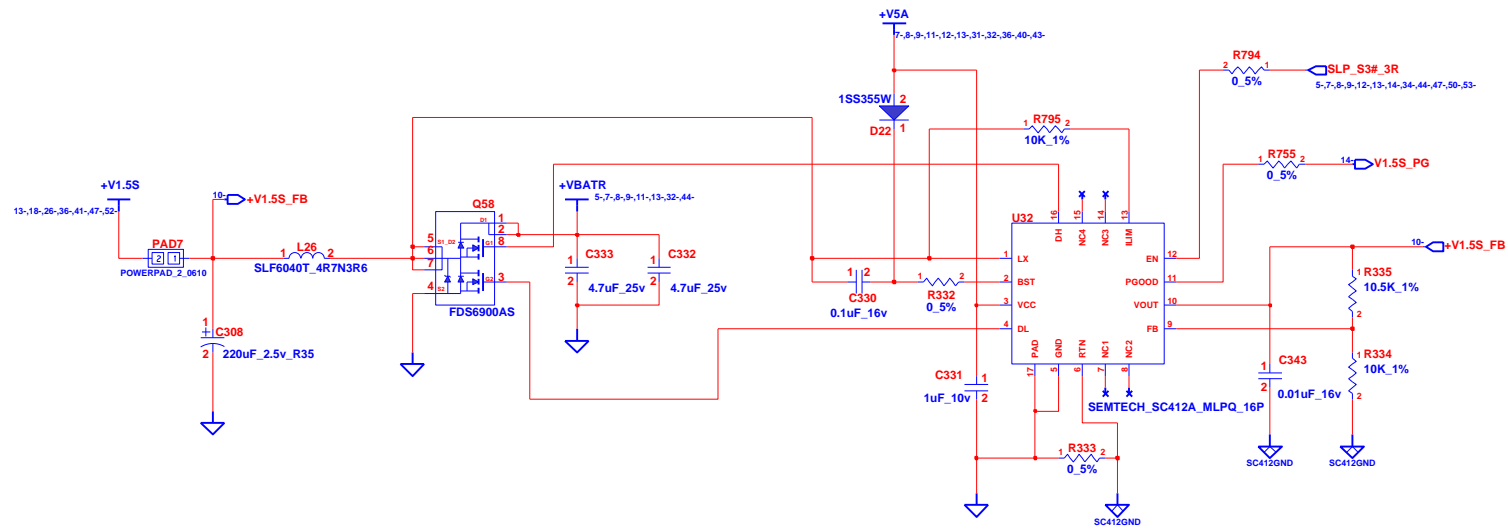


INVENTEC

TITLE **DD08**
GRAPHIC POWER (+VGFX_CORE)

SIZE A3	CODE CS	DOC. NUMBER Model No	REV AXI
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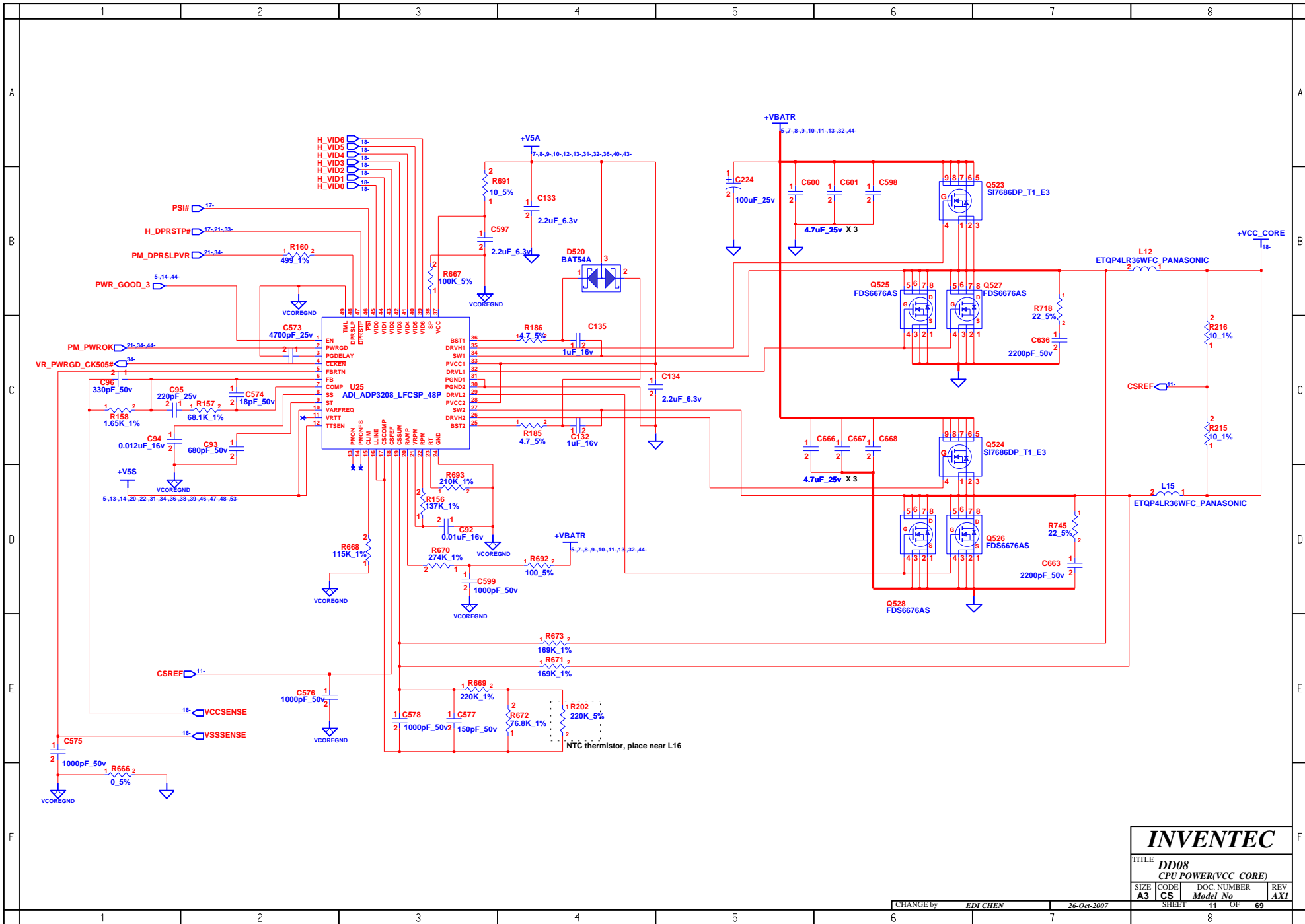


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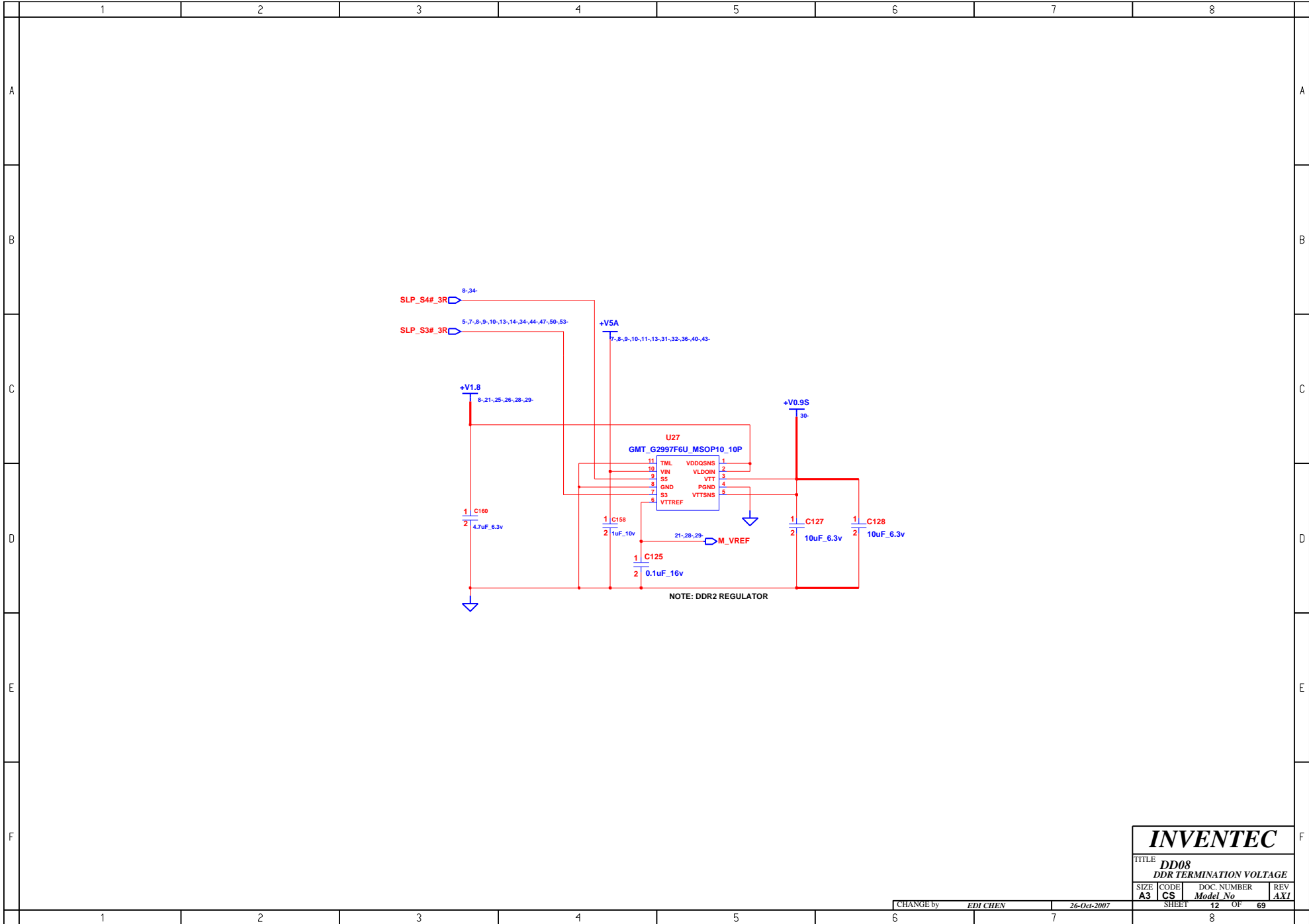
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DD08			
SYSTEM POWER(+VCCP/+V1.5S)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

CHANGE by EDI CHEN 26-Oct-2007

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INVENTEC			
TITLE DD08 CPU POWER(VCC_CORE)			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX1
CHANGE by EDI CHEN		26-Oct-2007	
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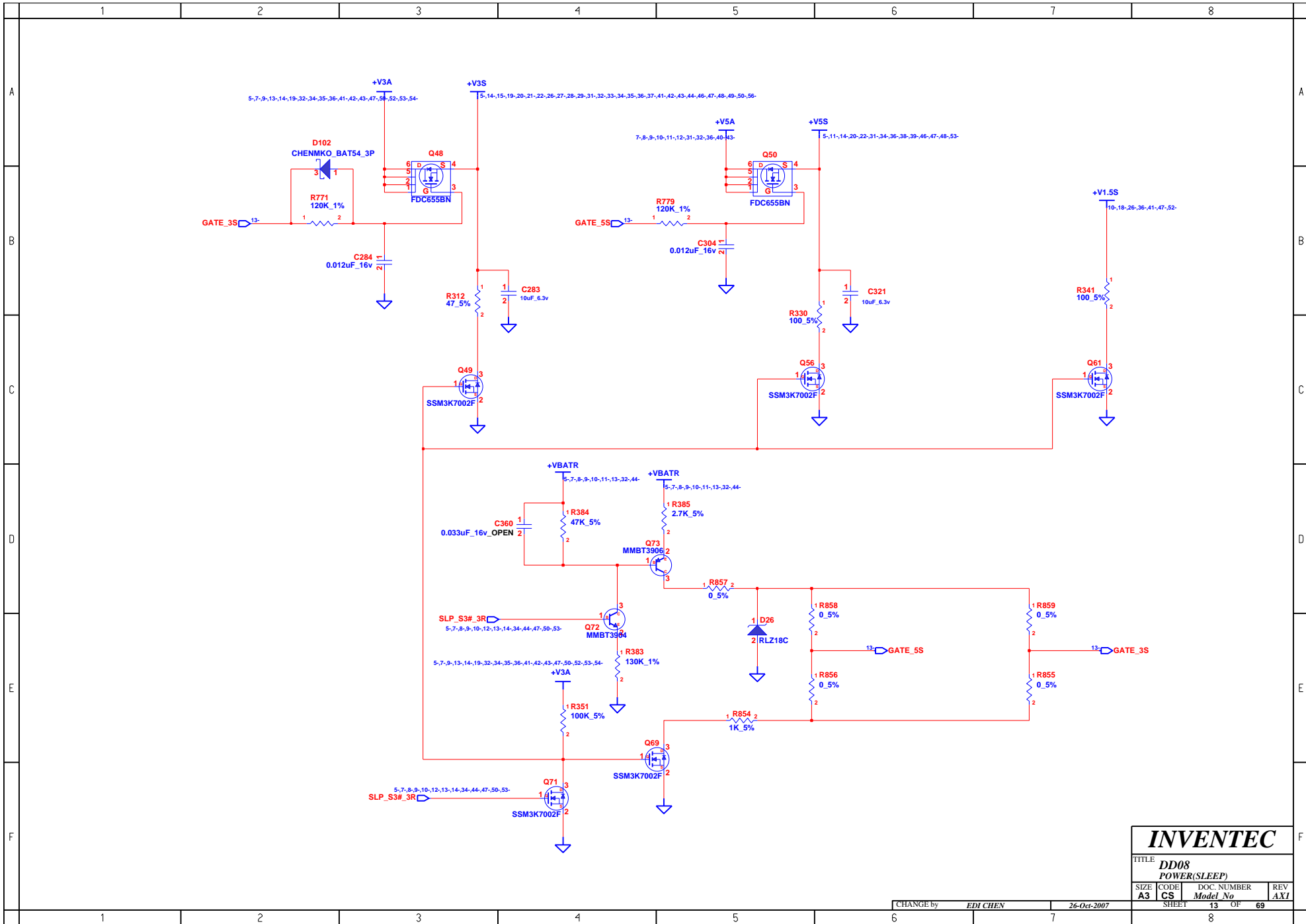
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DDR TERMINATION VOLTAGE

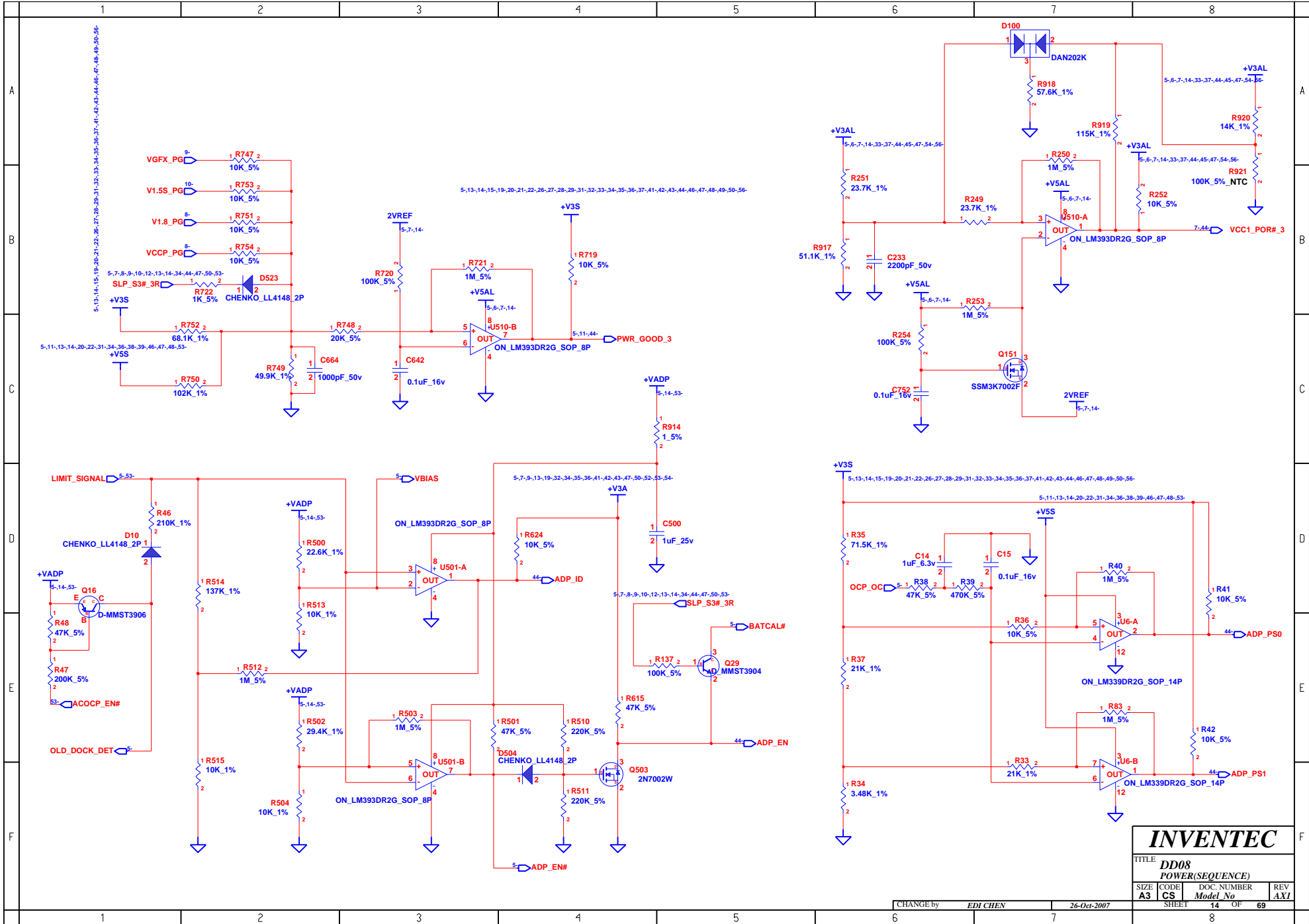
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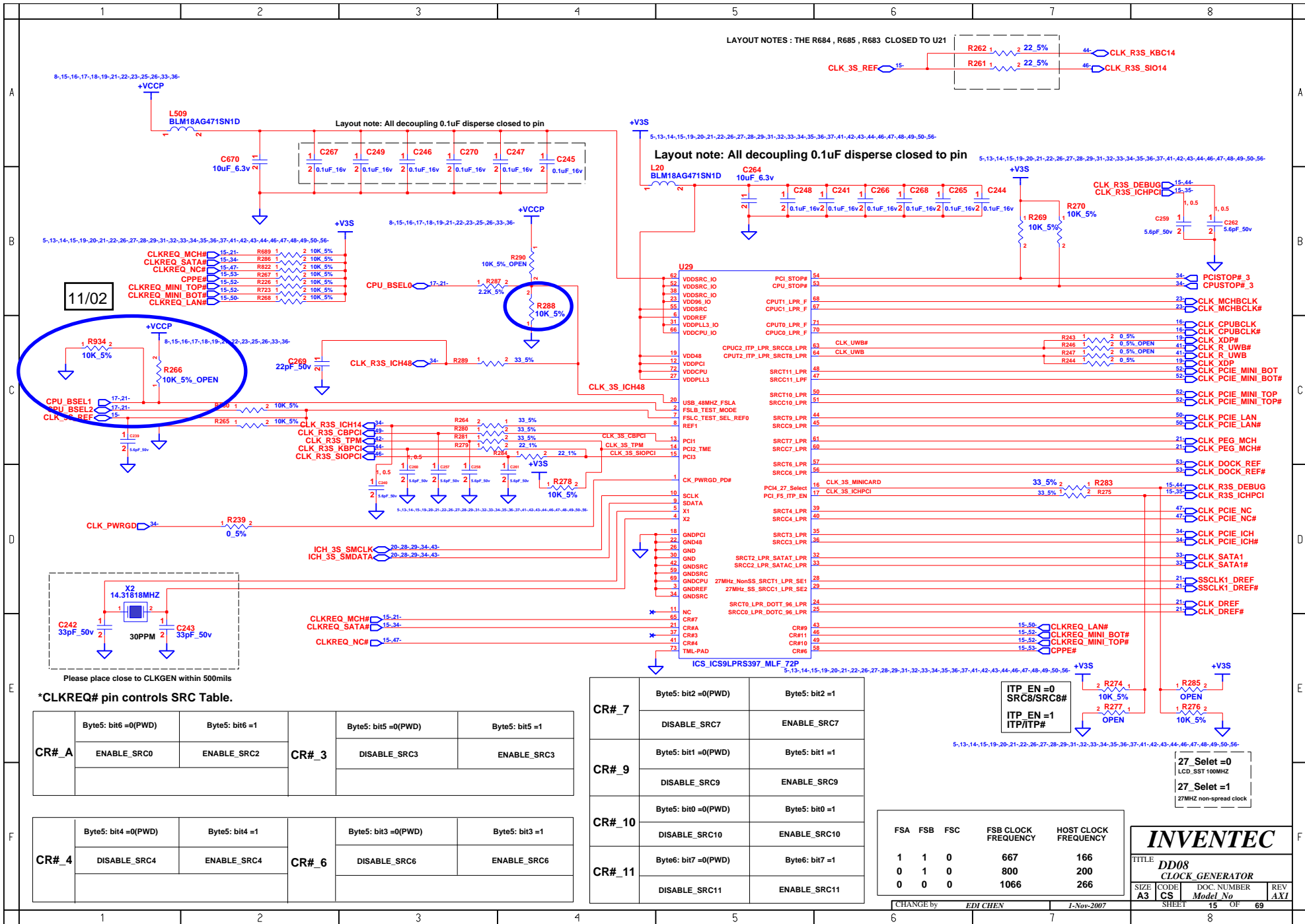
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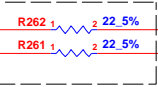
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CHANGE by EDI CHEN		26-Oct-2007	
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INVENTEC			
TITLE DD08 POWER(SEQUENCE)			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AXI
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LAYOUT NOTES : THE R684 , R685 , R683 CLOSED TO U21



Layout note: All decoupling 0.1uF disperse closed to pin

Layout note: All decoupling 0.1uF disperse closed to pin

11/02

*CLKREQ# pin controls SRC Table.

CR#_A	Byte5: bit6 =0(PWD)	Byte5: bit6 =1	CR#_3	Byte5: bit5 =0(PWD)	Byte5: bit5 =1
	ENABLE_SRC0	ENABLE_SRC2		DISABLE_SRC3	ENABLE_SRC3

CR#_4	Byte5: bit4 =0(PWD)	Byte5: bit4 =1	CR#_6	Byte5: bit3 =0(PWD)	Byte5: bit3 =1
	DISABLE_SRC4	ENABLE_SRC4		DISABLE_SRC6	ENABLE_SRC6

CR#_7	Byte5: bit2 =0(PWD)	Byte5: bit2 =1
	DISABLE_SRC7	ENABLE_SRC7
CR#_9	Byte5: bit1 =0(PWD)	Byte5: bit1 =1
	DISABLE_SRC9	ENABLE_SRC9
CR#_10	Byte5: bit0 =0(PWD)	Byte5: bit0 =1
	DISABLE_SRC10	ENABLE_SRC10
CR#_11	Byte6: bit7 =0(PWD)	Byte6: bit7 =1
	DISABLE_SRC11	ENABLE_SRC11

FSA	FSB	FSC	FSB CLOCK FREQUENCY	HOST CLOCK FREQUENCY
1	1	0	667	166
0	1	0	800	200
0	0	0	1066	266

INVENTEC

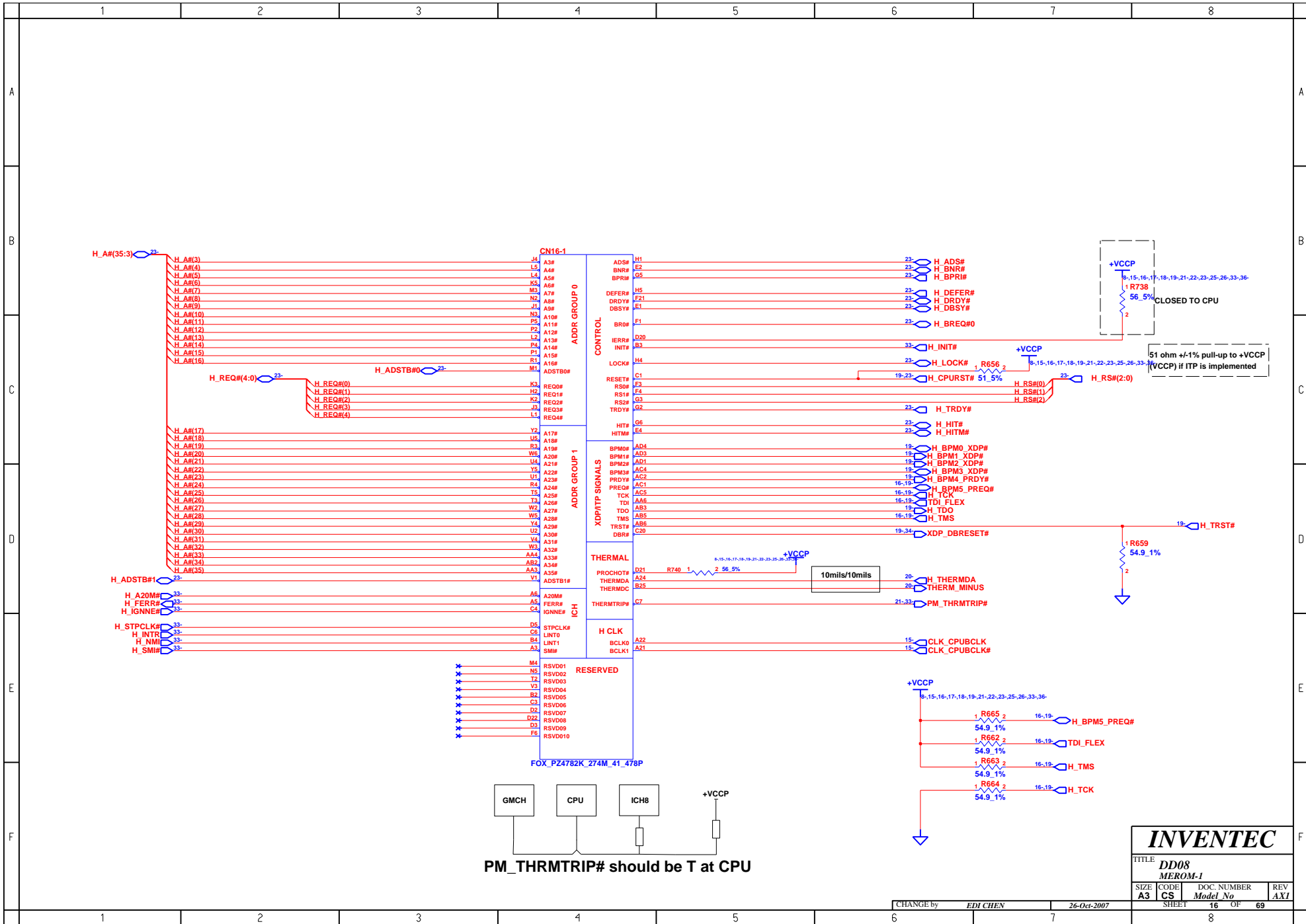
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CLOCK GENERATOR

SIZE **A3** CODE **CS** DOC. NUMBER **Model No** REV **AXI**

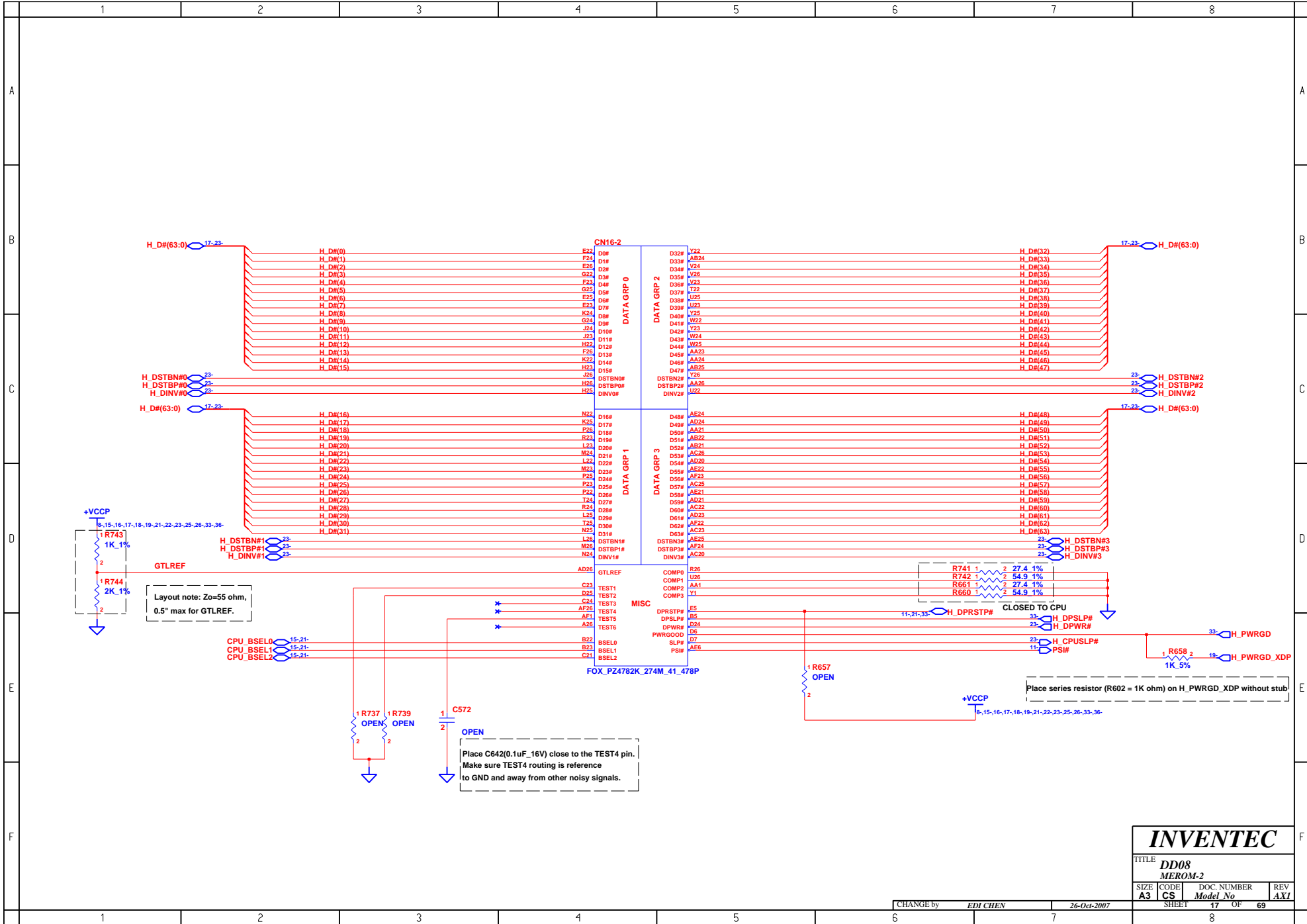
CHANGE by **EDI CHEN** **1-Nov-2007** SHEET **15** OF **69**

27_Selet =0
LCD_SST 100MHZ

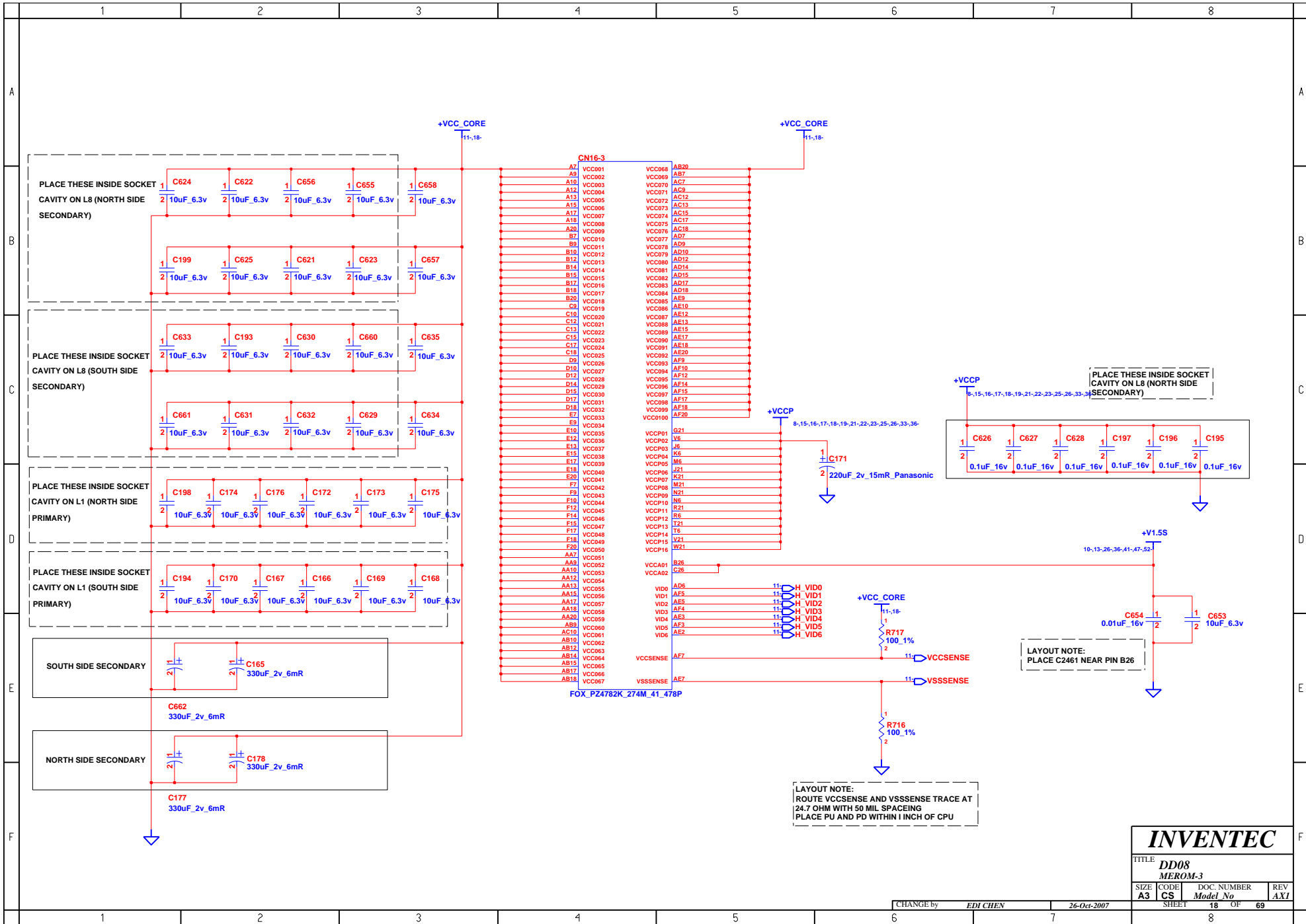
27_Selet =1
27MHZ non-spread clock



INVENTEC			
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CHANGE by EDI CHEN		26-Oct-2007	
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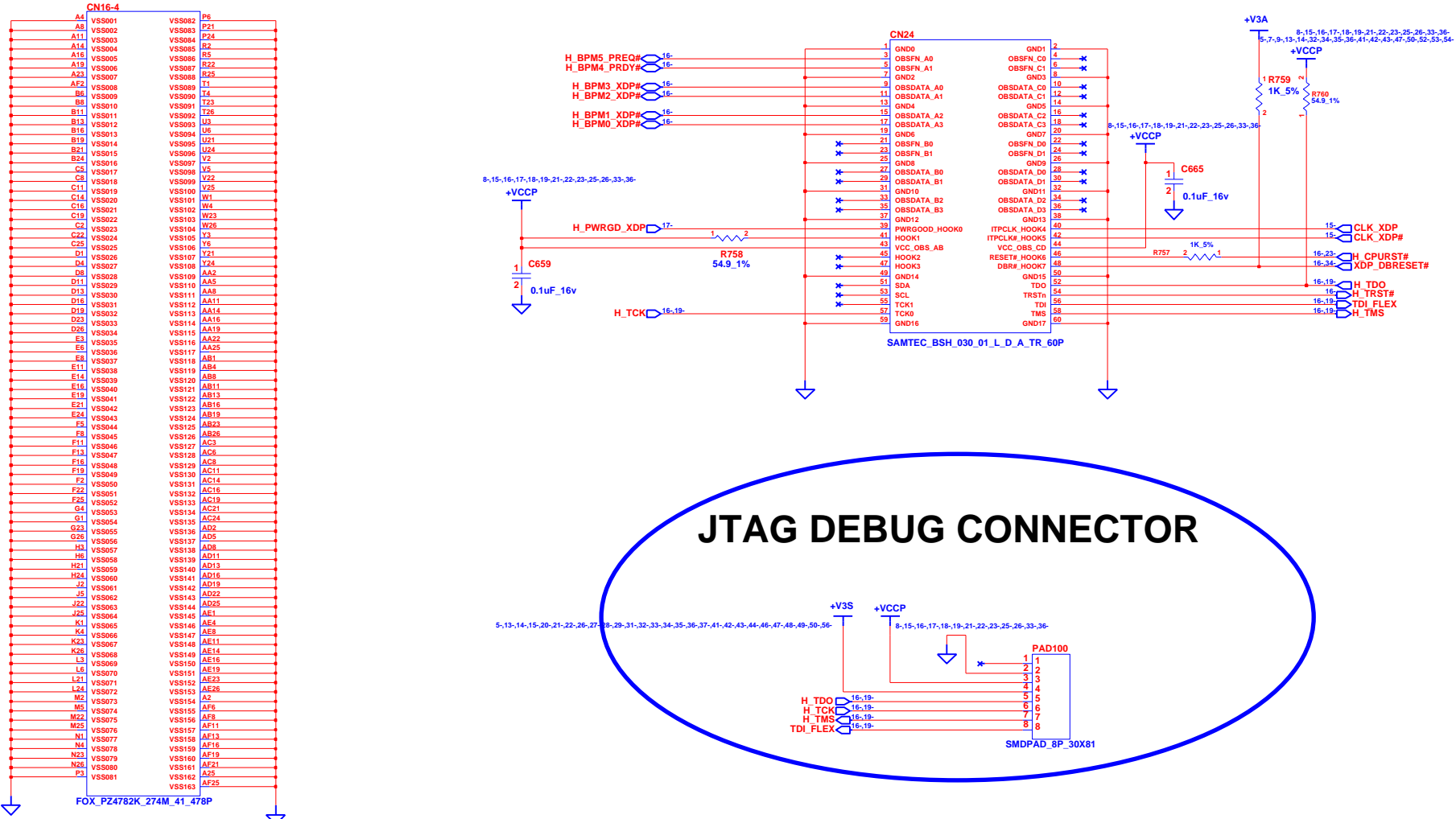


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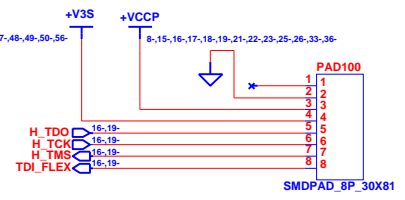


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SIZE A3	CODE CS	DOC. NUMBER Model No	REV AXI
CHANGE by EDI CHEN		26-Oct-2007	SHEET 18 OF 69

XDP CONNECTOR

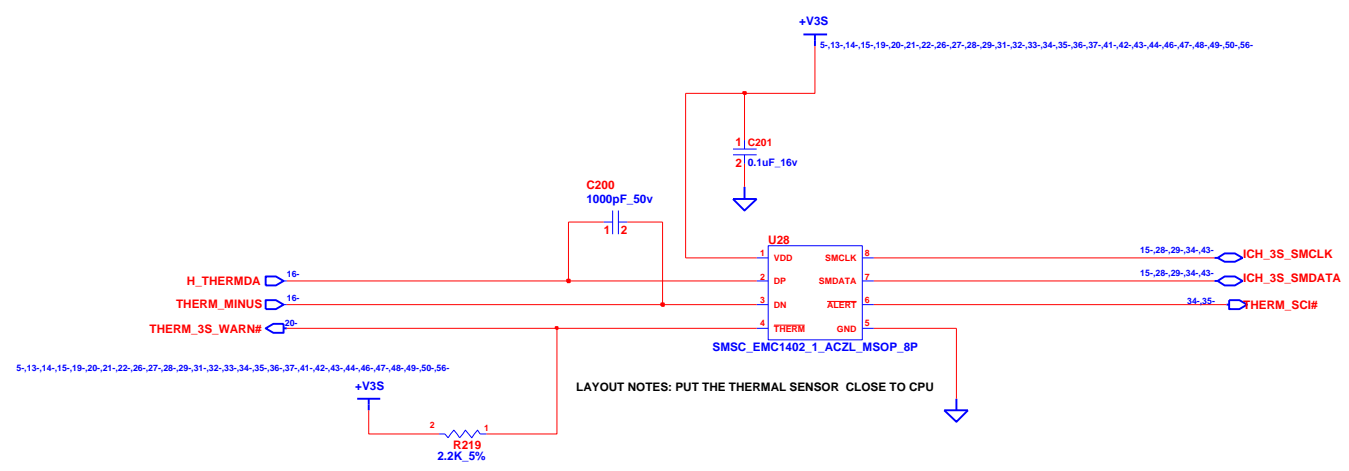
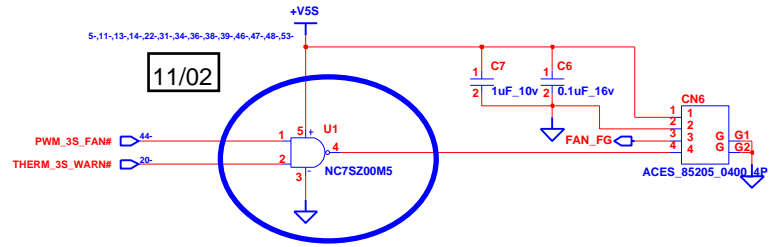


JTAG DEBUG CONNECTOR



INVENTEC

TITLE			
DD08			
MEROM-4			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI
CHANGE by		EDJ CHEN	2-Nov-2007
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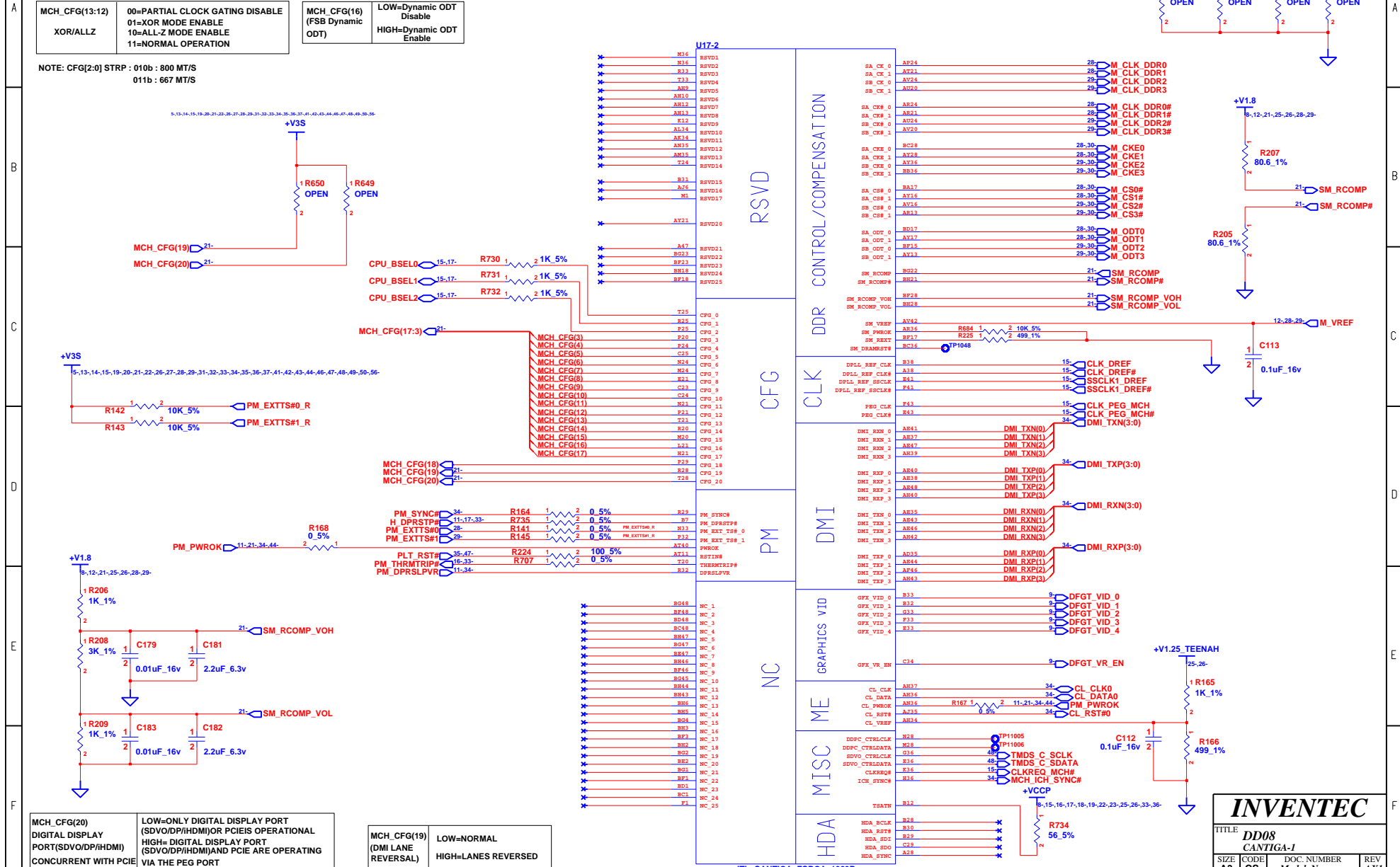
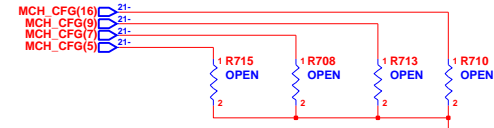
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TITLE DD08 THERMAL&FAN CONTROLLER			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX1
CHANGE by EDI CHEN		26-Oct-2007	
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MCH_CFG(5)	LOW=DMiX2 HIGH=DMiX4	MCH_CFG(7) (CPU Strap)	LOW=RSVD HIGH=Mobile CPU	MCH_CFG(9) PCIE Graphics Lane	LOW=Reverse Lane HIGH=Normal operation
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MCH_CFG(13:12) XOR/ALLZ	00=PARTIAL CLOCK GATING DISABLE 01=XOR MODE ENABLE 10=ALL-Z MODE ENABLE 11=NORMAL OPERATION	MCH_CFG(16) (FSB Dynamic ODT)	LOW=Dynamic ODT Disable HIGH=Dynamic ODT Enable
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NOTE: CFG[2:0] STRP : 010b : 800 MT/S
011b : 667 MT/S

NOTE : USE 4K-OHM RESISTOR WHEN INSTALLING PULL-UP/PULL-DOWN RESISTOR ON ANY MCH-CFG CONNECTION/PINS.



MCH_CFG(20)	LOW=ONLY DIGITAL DISPLAY PORT (SDVO/DP/HDMI) OR PCIE'S OPERATIONAL HIGH= DIGITAL DISPLAY PORT (SDVO/DP/HDMI) AND PCIE ARE OPERATING CONCURRENT WITH PCIE VIA THE PEG PORT
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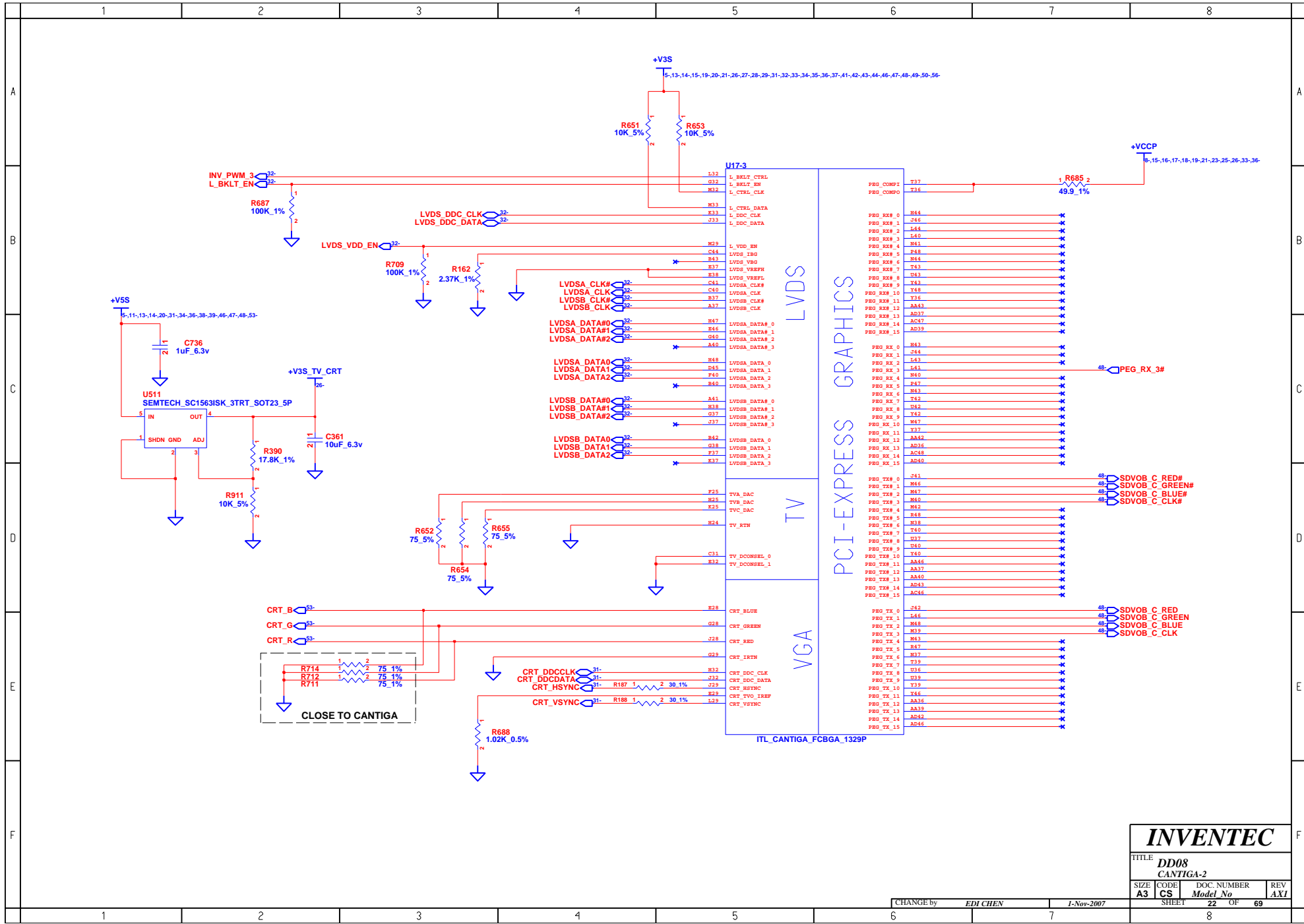
MCH_CFG(19)	LOW=NORMAL HIGH=LANES REVERSED
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INVENTEC

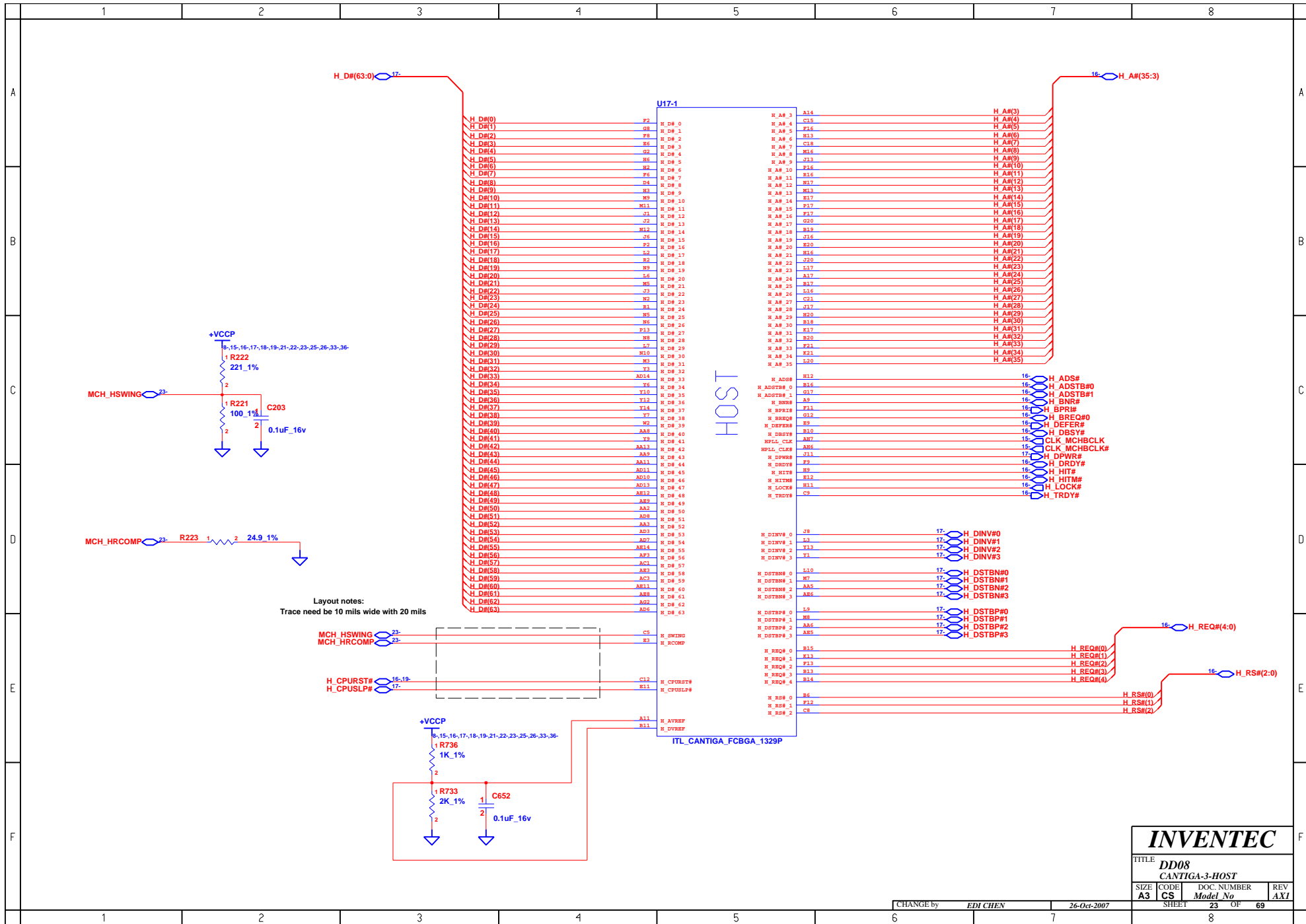
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A3	CS	Model No	AXI

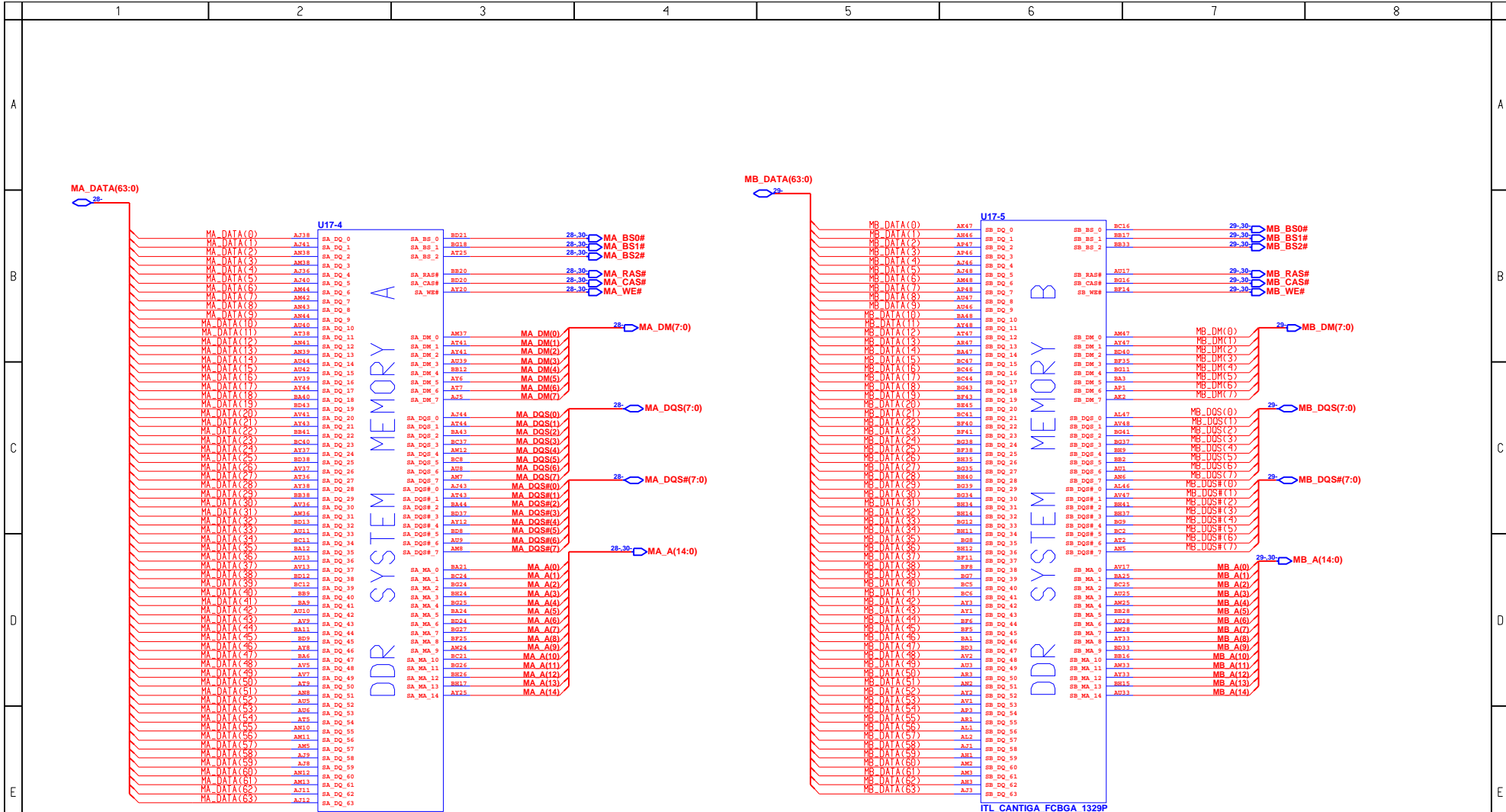
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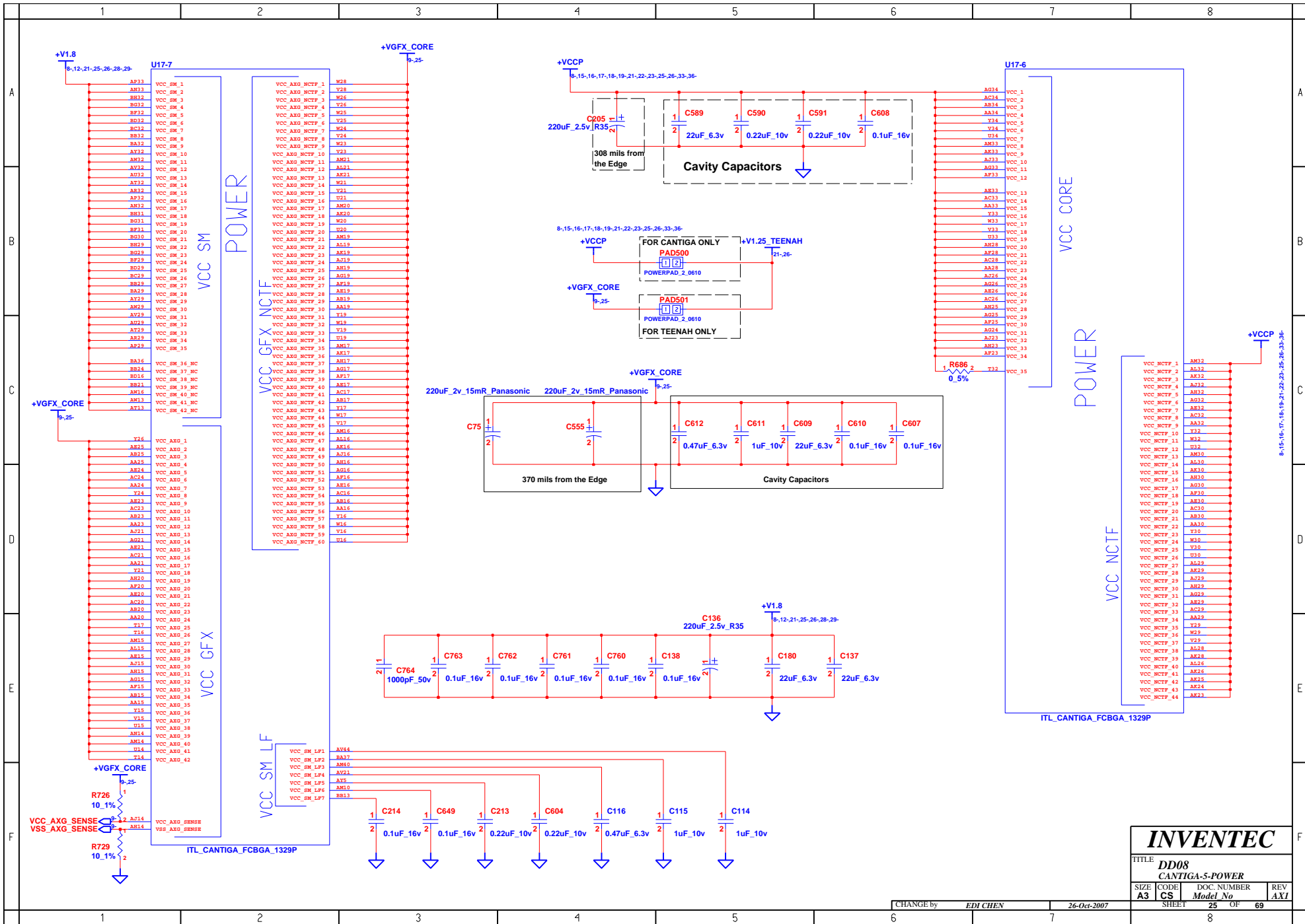
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CHANGE by EDI CHEN		1-Nov-2007	
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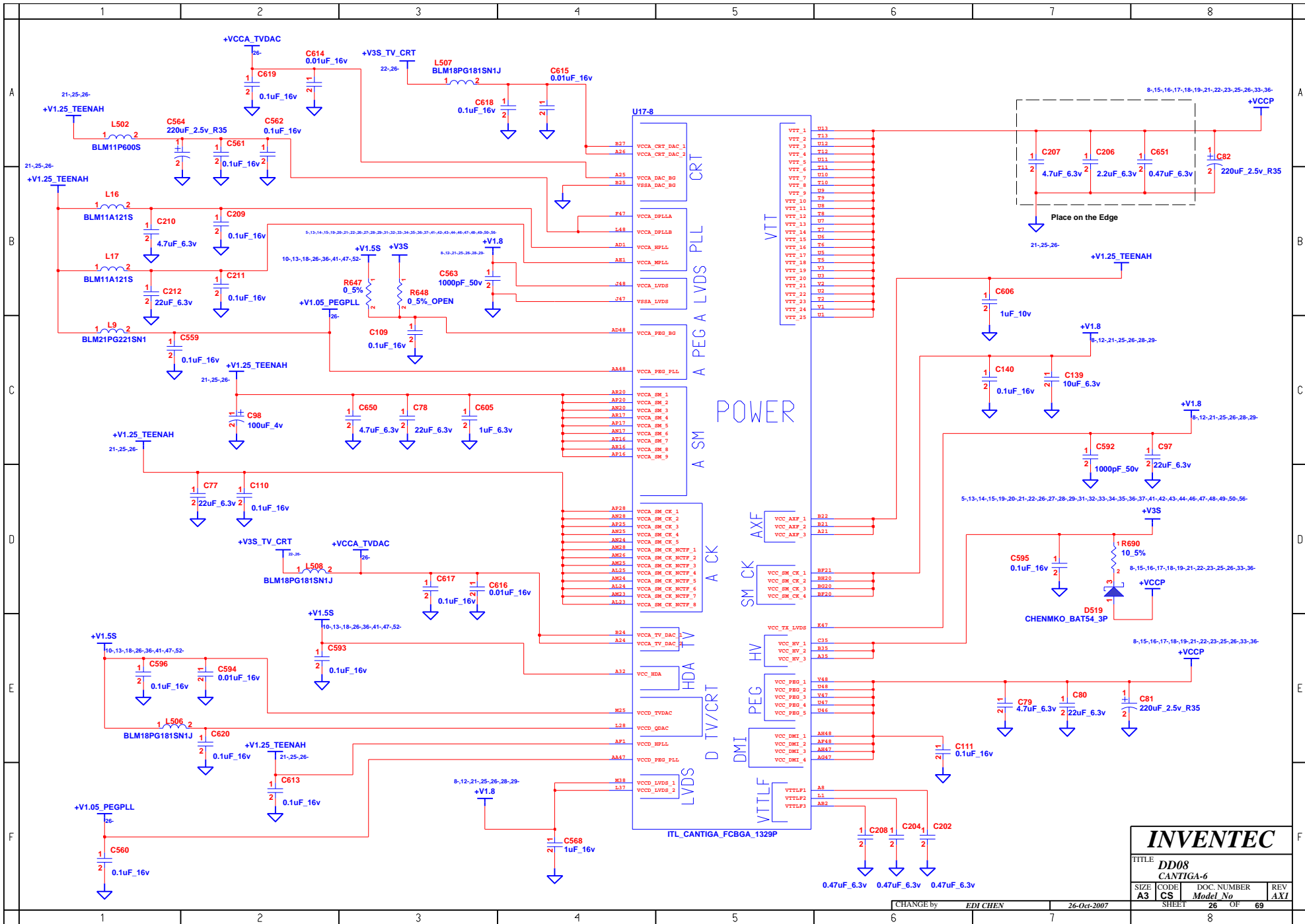
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TITLE DD08 CANTIGA-3-HOST			
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INVENTEC			
TITLE DD08 CANTIGA-4-DDR2			
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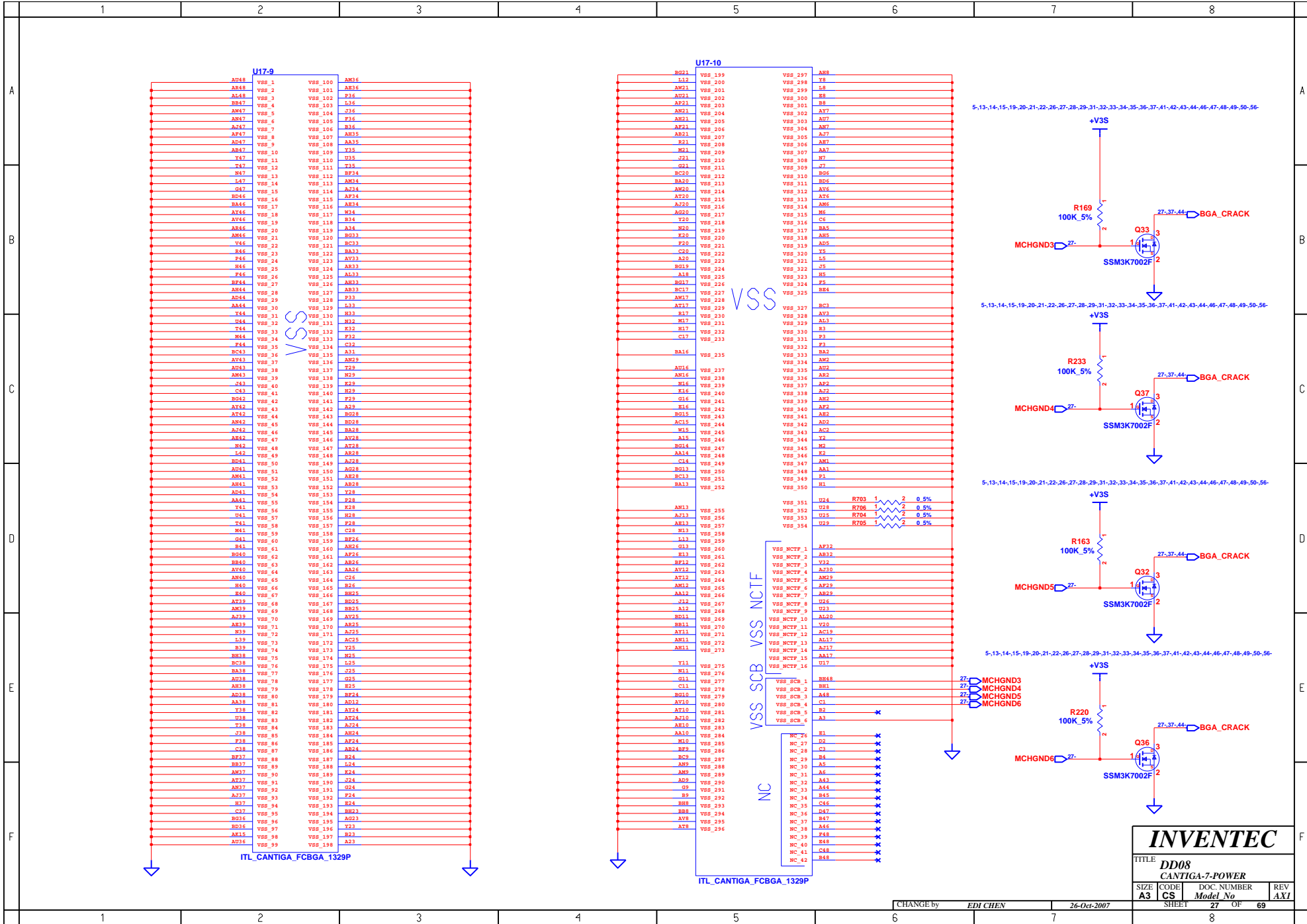


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CHANGE by EDI CHEN		26-Oct-2007	
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POWER

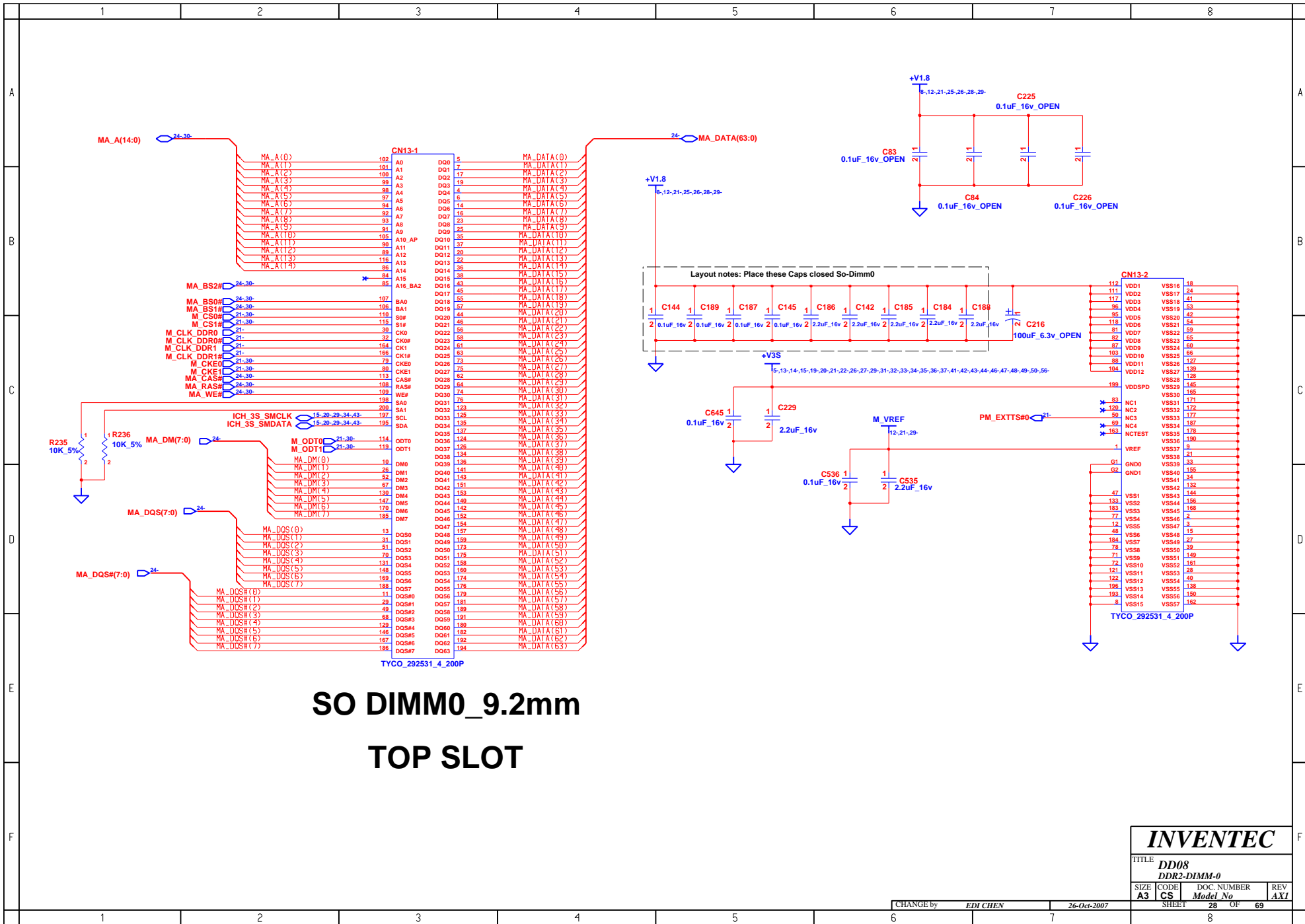
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SIZE A3	CODE CS	DOC NUMBER Model No	REV AXI
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INVENTEC

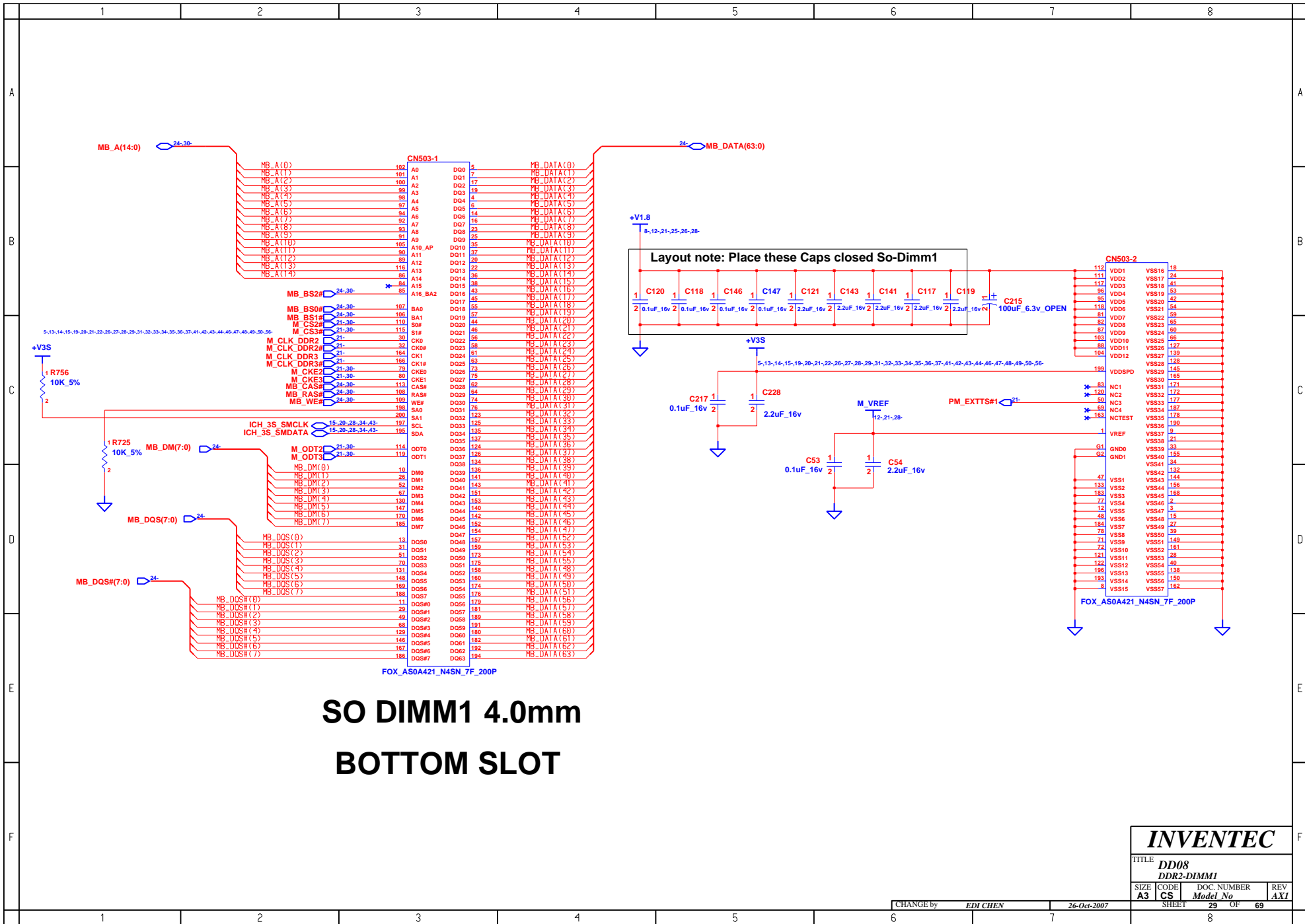
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DD08 CANTIGA-7-POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX1
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CHANGE by EDI CHEN 26-Oct-2007



SO DIMM0_9.2mm TOP SLOT

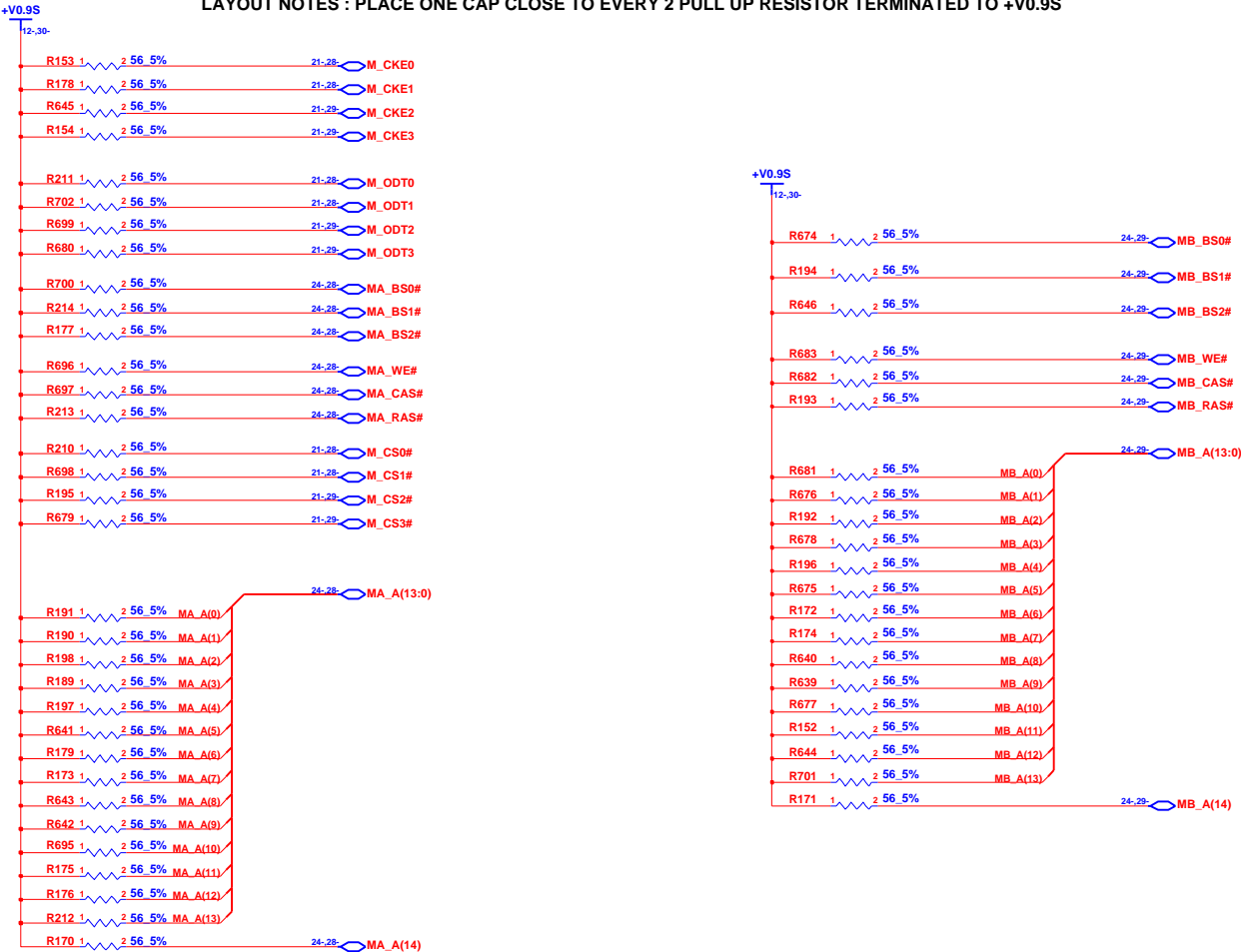
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CHANGE by EDI CHEN		26-Oct-2007	
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INVENTEC				
TITLE DD08 DDR2-DIMM1				
SIZE	CODE	DOC. NUMBER	REV	
A3	CS	Model No	AX1	
CHANGE by			EDJ CHEN	26-Oct-2007
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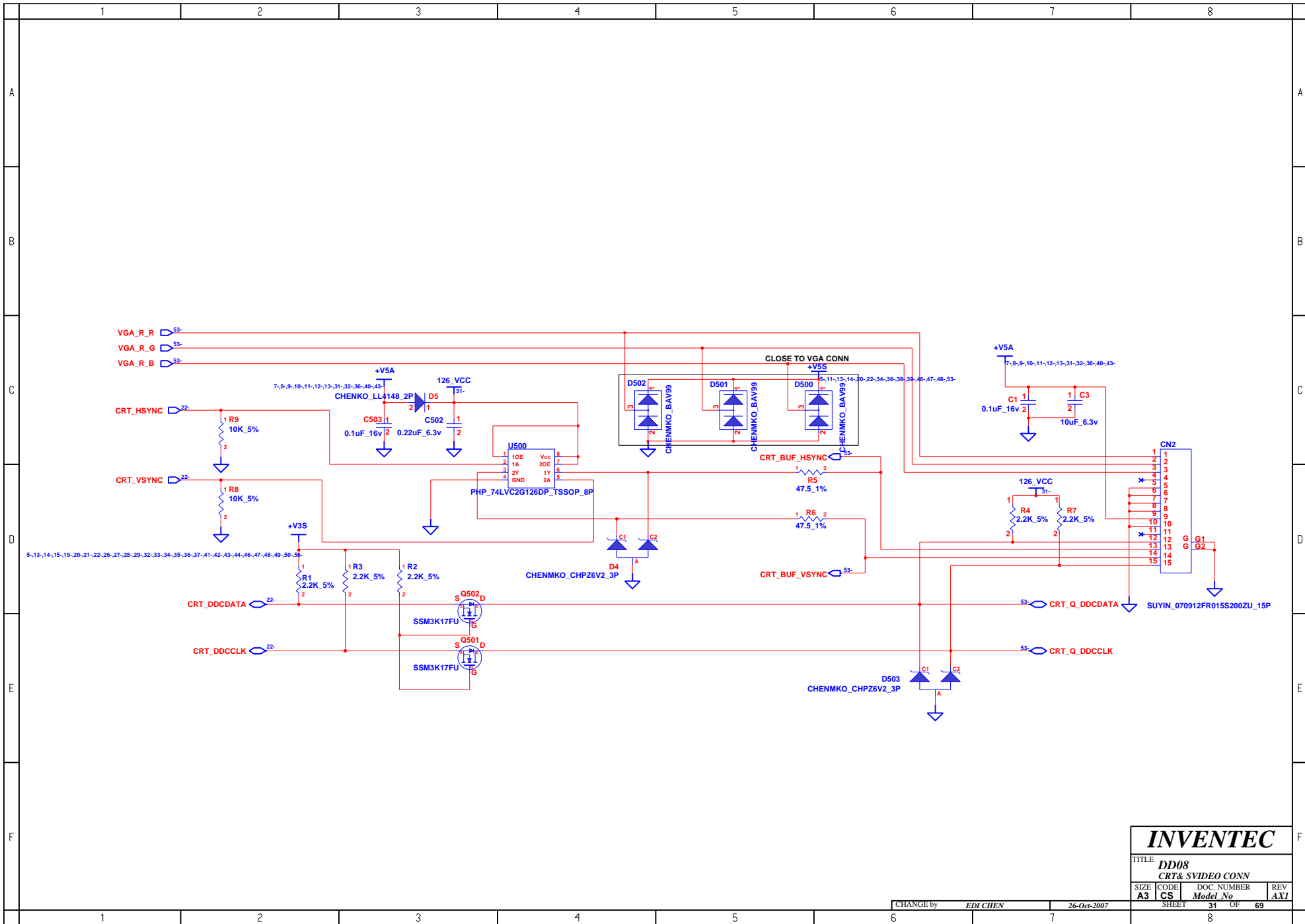


LAYOUT NOTES : PLACE ONE CAP CLOSE TO EVERY 2 PULL UP RESISTOR TERMINATED TO +V0.9S



INVENTEC

TITLE			
DD08			
DDR2-DAMPING			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI
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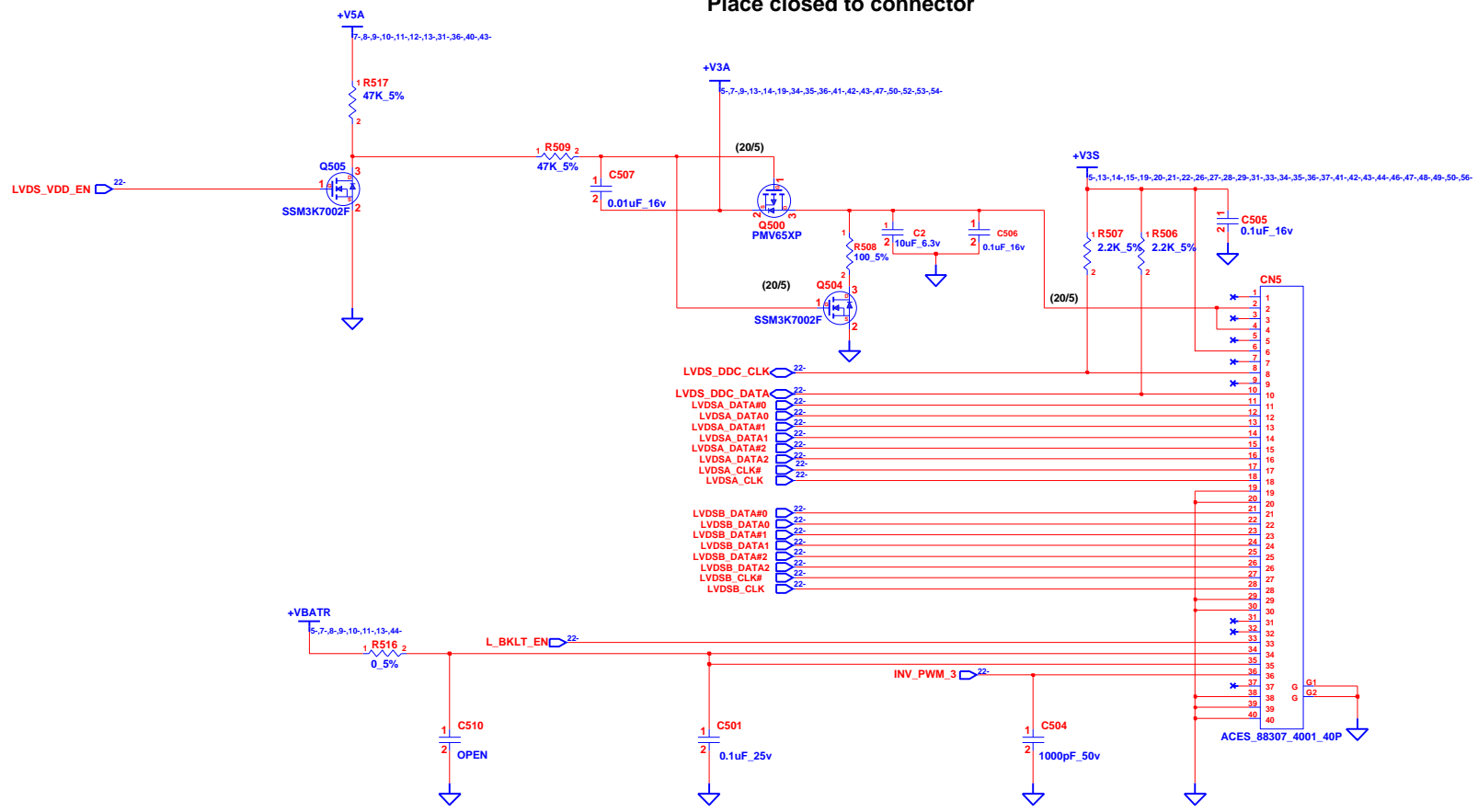
INVENTEC

TITLE
DD08
CRT & SVIDEO CONN

SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV <i>AXI</i>
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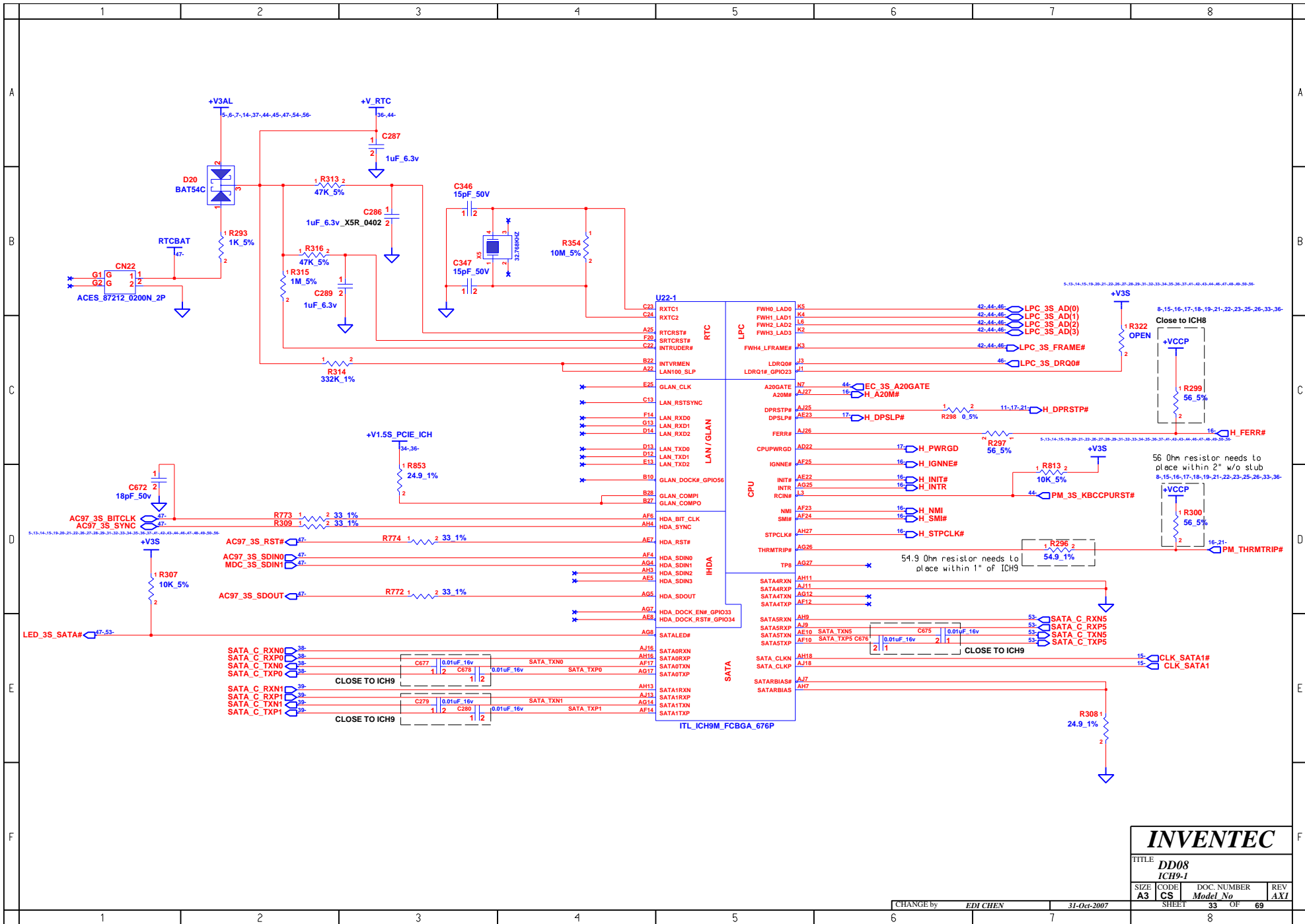
CHANGE by **EDI CHEN** 26-Oct-2007 SHEET 31 OF 69

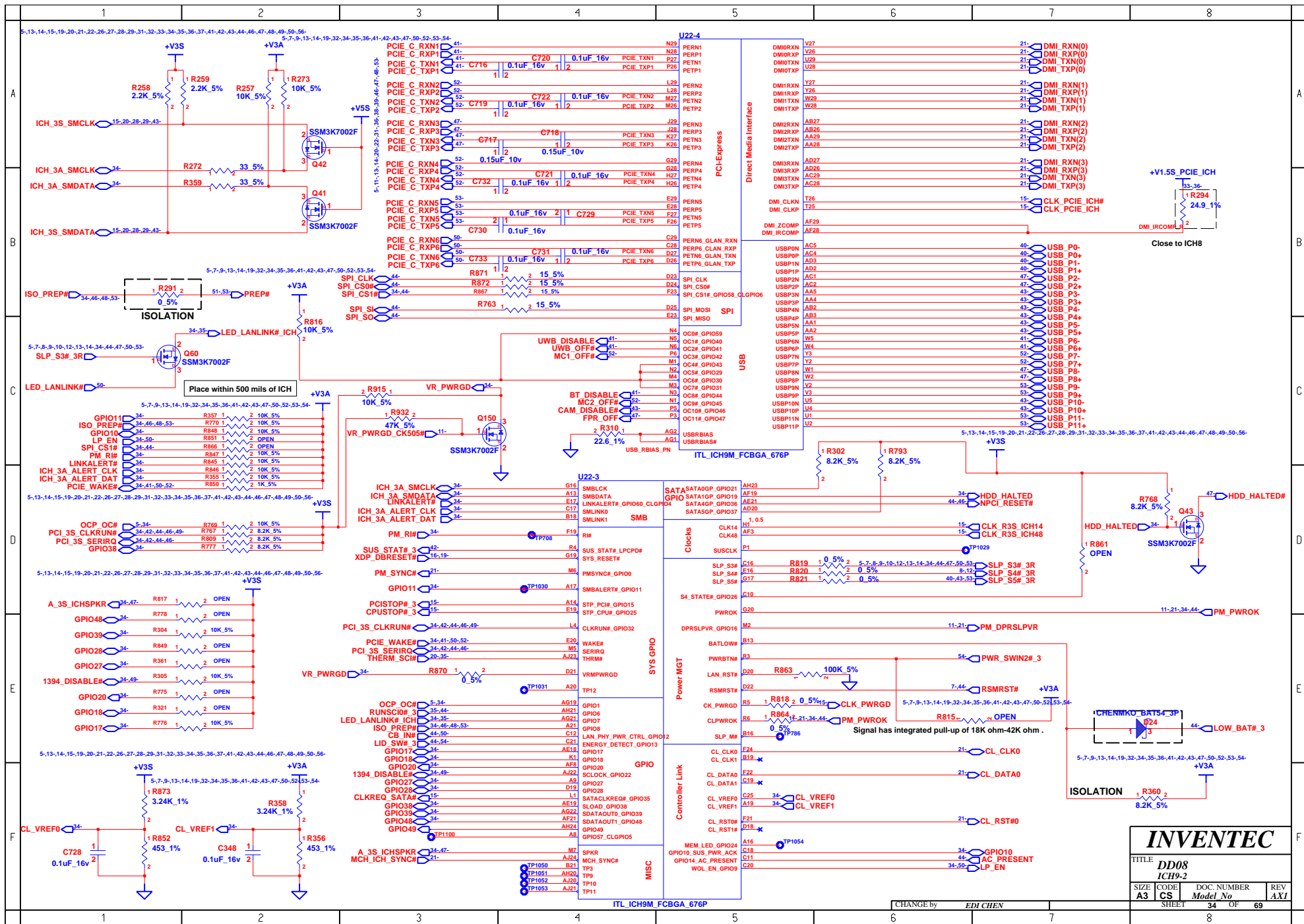
Place closed to connector



R516/C510 Place closed to LVDS connector

INVENTEC			
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SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX1
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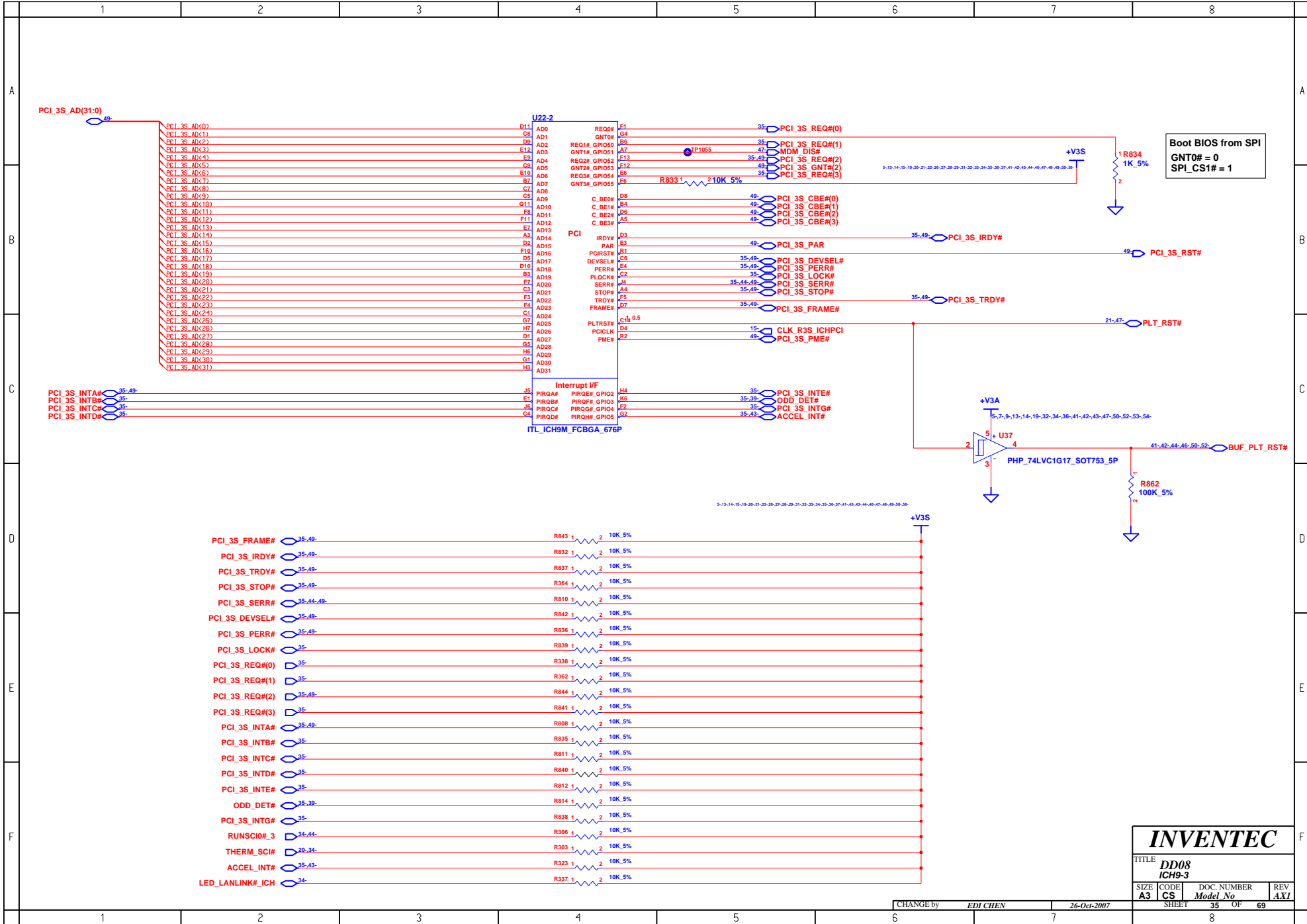


INVENTEC

TITLE **DD08 ICH9-2**

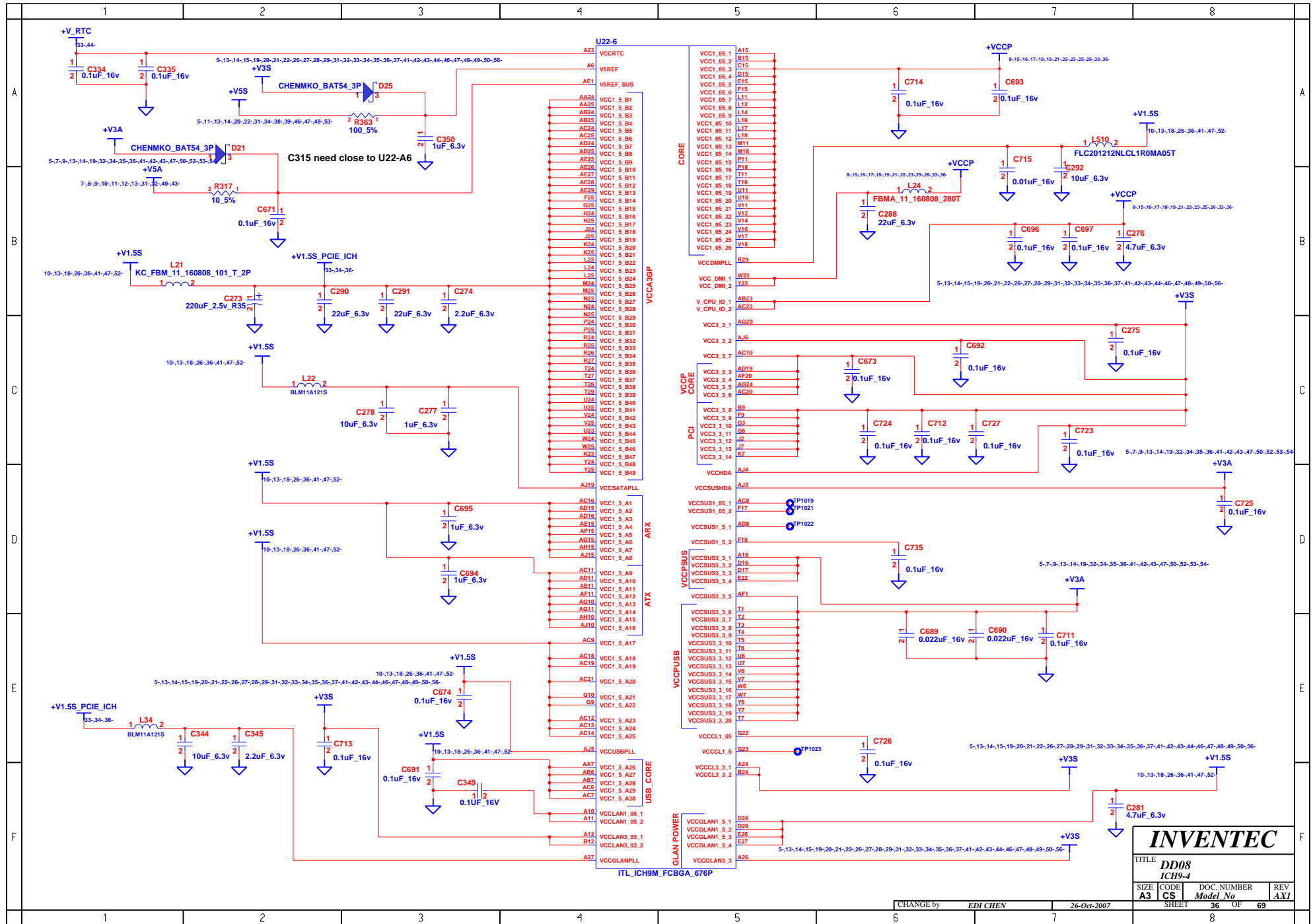
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A3	CS	Model No	AX1

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INVENTEC			
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SIZE A3	CODE CS	DOC. NUMBER Model No	REV AXI
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CHANGE by EDI CHEN 26-Oct-2007

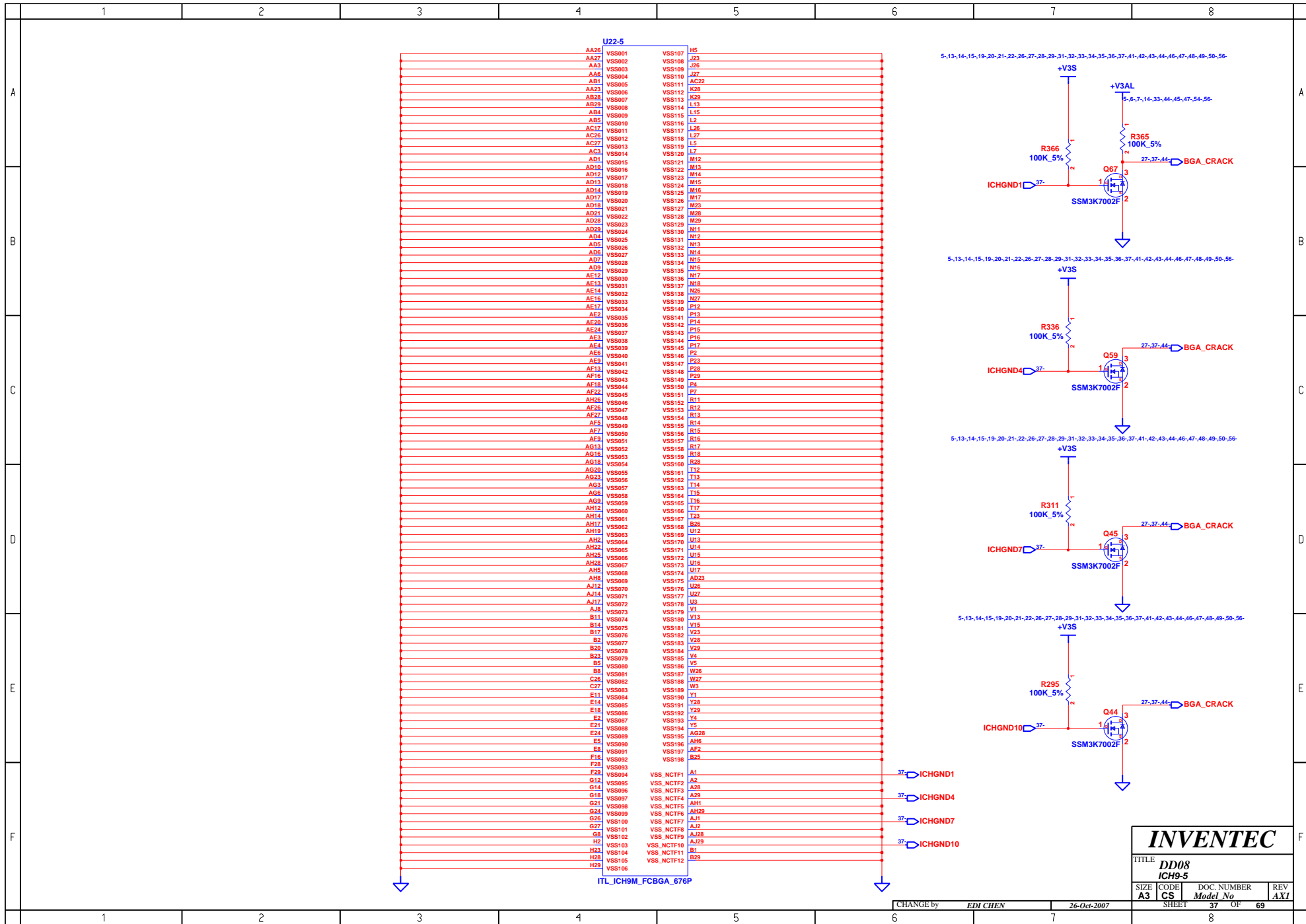


U22-6

A33	VCORTC
A6	VSREF
AE1	VSREF_SUS
AA34	VCC1_S_B1
AA35	VCC1_S_B2
AB24	VCC1_S_B3
AB25	VCC1_S_B4
AC24	VCC1_S_B5
AC25	VCC1_S_B6
AD24	VCC1_S_B7
AD25	VCC1_S_B8
AE25	VCC1_S_B9
AE26	VCC1_S_B10
AE27	VCC1_S_B11
AE28	VCC1_S_B12
AE29	VCC1_S_B13
F25	VCC1_S_B14
G25	VCC1_S_B15
H24	VCC1_S_B16
H25	VCC1_S_B17
J24	VCC1_S_B18
J25	VCC1_S_B19
K24	VCC1_S_B20
K25	VCC1_S_B21
L23	VCC1_S_B22
L24	VCC1_S_B23
L25	VCC1_S_B24
M24	VCC1_S_B25
M25	VCC1_S_B26
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N24	VCC1_S_B28
N25	VCC1_S_B29
P24	VCC1_S_B30
P25	VCC1_S_B31
R24	VCC1_S_B32
R25	VCC1_S_B33
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V24	VCC1_S_B42
V25	VCC1_S_B43
W24	VCC1_S_B44
W25	VCC1_S_B45
X24	VCC1_S_B46
X25	VCC1_S_B47
Y24	VCC1_S_B48
Y25	VCC1_S_B49
AJ19	VCCSATAPLL
AC16	VCC1_S_A1
AD15	VCC1_S_A2
AE15	VCC1_S_A3
AF15	VCC1_S_A4
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AF11	VCC1_S_A12
AG10	VCC1_S_A13
AG11	VCC1_S_A14
AH10	VCC1_S_A15
AJ10	VCC1_S_A16
AC9	VCC1_S_A17
AC18	VCC1_S_A18
AC19	VCC1_S_A19
AC21	VCC1_S_A20
G10	VCC1_S_A21
G9	VCC1_S_A22
AC12	VCC1_S_A23
AC13	VCC1_S_A24
AC14	VCC1_S_A25
AJ5	VCCUSBPLL
AA7	VCC1_S_A26
AA8	VCC1_S_A27
AA7	VCC1_S_A28
AA8	VCC1_S_A29
AA7	VCC1_S_A30
A10	VCCLAN1_05_1
A11	VCCLAN1_05_2
A12	VCCLAN3_03_1
B12	VCCLAN3_03_2
A27	VCCGLANPLL

INVENTEC

TITLE			
DD08 ICH9-4			
SIZE	CODE	DOC NUMBER	REV
A3	CS	Model No	AX1

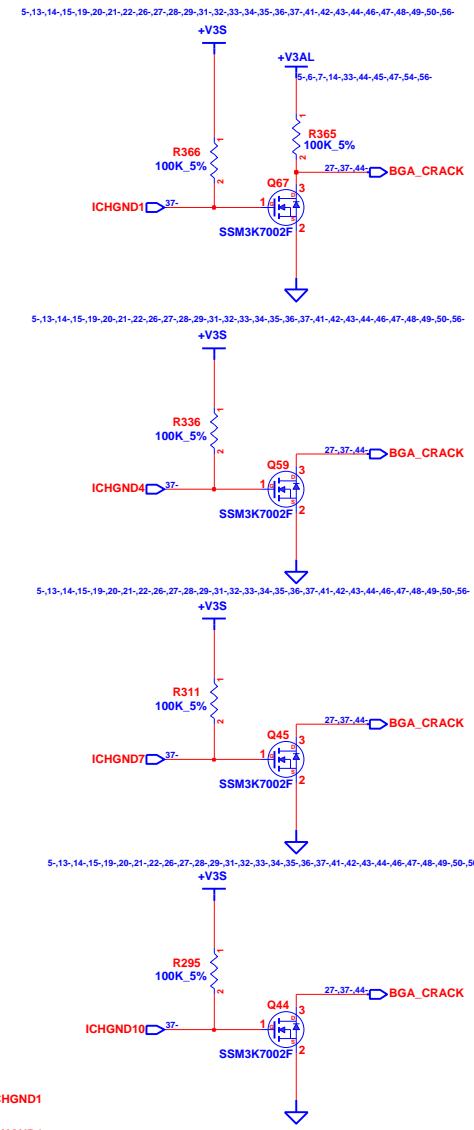


U22-5

- AA26 VSS001 VSS107 H5
- AA27 VSS002 VSS108 J23
- AA2 VSS003 VSS109 J26
- AA4 VSS004 VSS110 J27
- AB1 VSS005 VSS111 AC22
- AA23 VSS006 VSS112 K28
- AB26 VSS007 VSS113 K29
- AB29 VSS008 VSS114 L13
- AB4 VSS009 VSS115 L15
- AB5 VSS010 VSS116 L2
- AC17 VSS011 VSS117 L26
- AC26 VSS012 VSS118 L27
- AC27 VSS013 VSS119 L5
- AC3 VSS014 VSS120 L7
- AD3 VSS015 VSS121 M12
- AD10 VSS016 VSS122 M13
- AD12 VSS017 VSS123 M14
- AD13 VSS018 VSS124 M15
- AD14 VSS019 VSS125 M16
- AD17 VSS020 VSS126 M17
- AD18 VSS021 VSS127 M23
- AD21 VSS022 VSS128 M28
- AD25 VSS023 VSS129 M29
- AD29 VSS024 VSS130 N11
- AD4 VSS025 VSS131 N12
- AD6 VSS026 VSS132 N13
- AD8 VSS027 VSS133 N14
- AD28 VSS028 VSS134 N15
- AD9 VSS029 VSS135 N16
- AE19 VSS030 VSS136 N17
- AE18 VSS031 VSS137 N18
- AE14 VSS032 VSS138 N26
- AE16 VSS033 VSS139 N27
- AE17 VSS034 VSS140 P12
- AE3 VSS035 VSS141 P13
- AE20 VSS036 VSS142 P14
- AE24 VSS037 VSS143 P15
- AE3 VSS038 VSS144 P16
- AE39 VSS039 VSS145 P17
- AE6 VSS040 VSS146 P2
- AE9 VSS041 VSS147 P23
- AF13 VSS042 VSS148 P28
- AF16 VSS043 VSS149 P29
- AF18 VSS044 VSS150 P4
- AF24 VSS045 VSS151 P7
- AF26 VSS046 VSS152 R11
- AF28 VSS047 VSS153 R12
- AF27 VSS048 VSS154 R13
- AF3 VSS049 VSS155 R14
- AF7 VSS050 VSS156 R15
- AF9 VSS051 VSS157 R16
- AG13 VSS052 VSS158 R17
- AG16 VSS053 VSS159 R18
- AG18 VSS054 VSS160 R28
- AG20 VSS055 VSS161 T12
- AG23 VSS056 VSS162 T13
- AG3 VSS057 VSS163 T14
- AG5 VSS058 VSS164 T15
- AG9 VSS059 VSS165 T16
- AH12 VSS060 VSS166 T17
- AH14 VSS061 VSS167 T23
- AH17 VSS062 VSS168 B26
- AH19 VSS063 VSS169 U12
- AH2 VSS064 VSS170 U13
- AH22 VSS065 VSS171 U14
- AH26 VSS066 VSS172 U15
- AH28 VSS067 VSS173 U16
- AH8 VSS068 VSS174 U17
- AH8 VSS069 VSS175 AD23
- AJ12 VSS070 VSS176 U26
- AJ14 VSS071 VSS177 U27
- AJ17 VSS072 VSS178 U3
- AJ8 VSS073 VSS179 V1
- B11 VSS074 VSS180 V13
- B14 VSS075 VSS181 V15
- B17 VSS076 VSS182 V23
- B3 VSS077 VSS183 V28
- B20 VSS078 VSS184 V29
- B23 VSS079 VSS185 V4
- B5 VSS080 VSS186 V5
- B8 VSS081 VSS187 W26
- C28 VSS082 VSS188 W27
- C27 VSS083 VSS189 W3
- E11 VSS084 VSS190 Y1
- E14 VSS085 VSS191 Y28
- E18 VSS086 VSS192 Y29
- E2 VSS087 VSS193 Y4
- E21 VSS088 VSS194 Y5
- E24 VSS089 VSS195 AG28
- E5 VSS090 VSS196 AH6
- E8 VSS091 VSS197 AF2
- F16 VSS092 VSS198 B28
- F29 VSS093
- F29 VSS094 VSS_NCTF1 A1
- G12 VSS095 VSS_NCTF2 A2
- G14 VSS096 VSS_NCTF3 A28
- G19 VSS097 VSS_NCTF4 A29
- G21 VSS098 VSS_NCTF5 AH1
- G24 VSS099 VSS_NCTF6 AH29
- G26 VSS100 VSS_NCTF7 AJ1
- G27 VSS101 VSS_NCTF8 AJ2
- G8 VSS102 VSS_NCTF9 AJ28
- H5 VSS103 VSS_NCTF10 AJ29
- H28 VSS104 VSS_NCTF11 B1
- H28 VSS105 VSS_NCTF12 B29
- H28 VSS106

ITL_ICHSM_FCBGA_676P

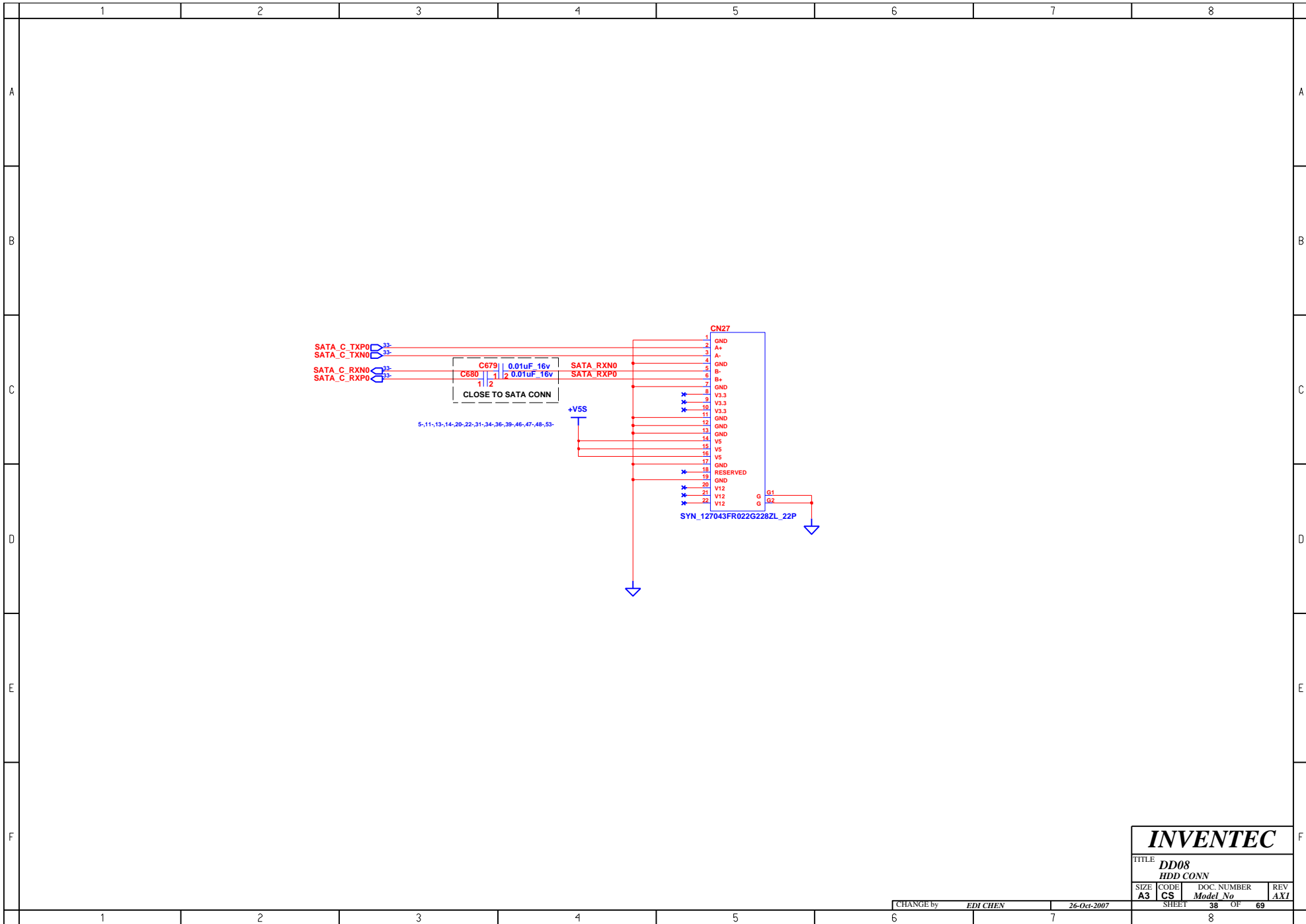
- 37- ICHGND1
- 37- ICHGND4
- 37- ICHGND7
- 37- ICHGND10



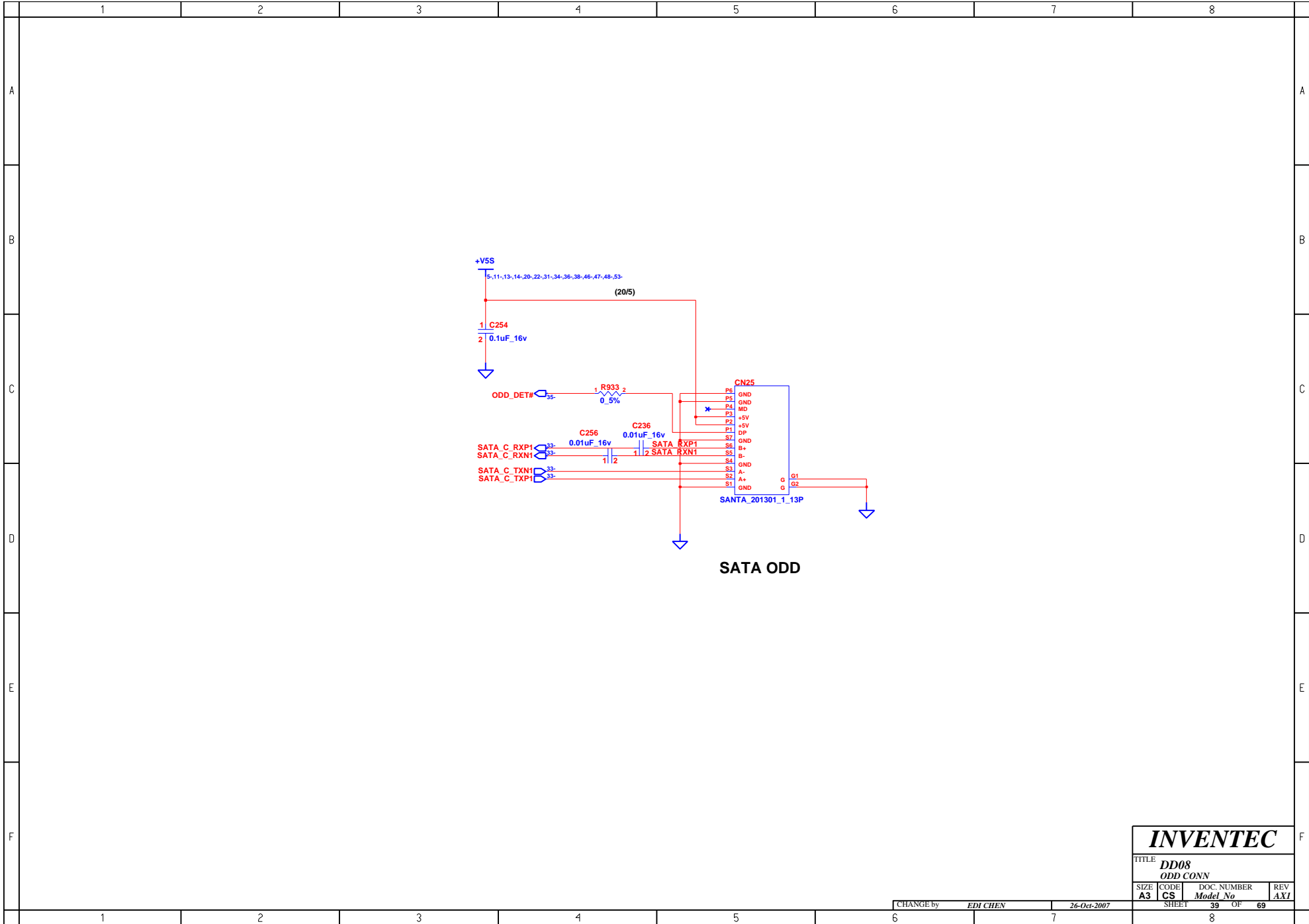
INVENTEC

TITLE			
DD08			
ICH9-5			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX1
SHEET			OF
37			69

CHANGE by EDI CHEN 26-Oct-2007



INVENTEC			
TITLE DD08 HDD CONN			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX1
CHANGE by EDI CHEN		26-Oct-2007	
SHEET 38 OF 69		8	



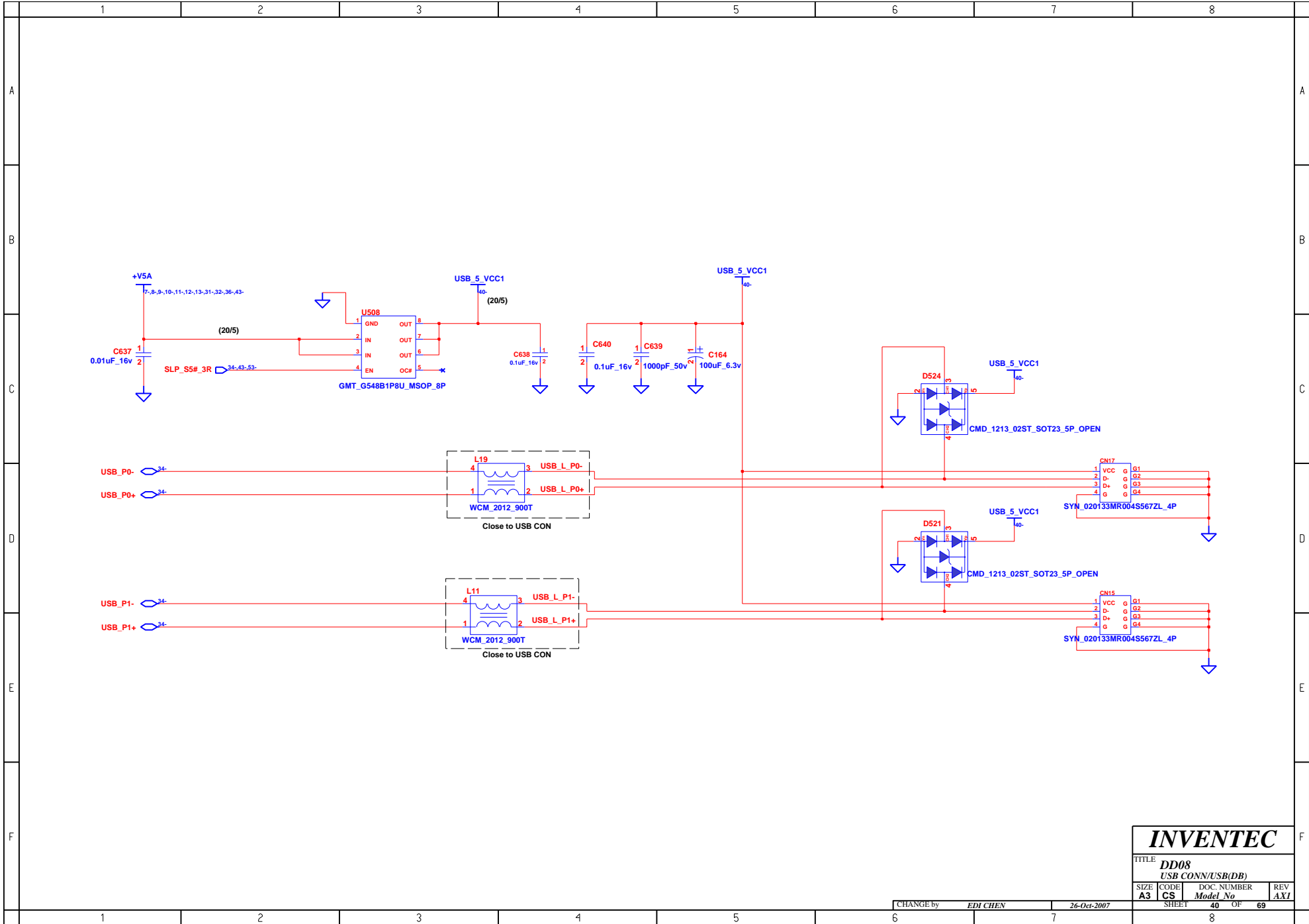
SATA ODD

INVENTEC

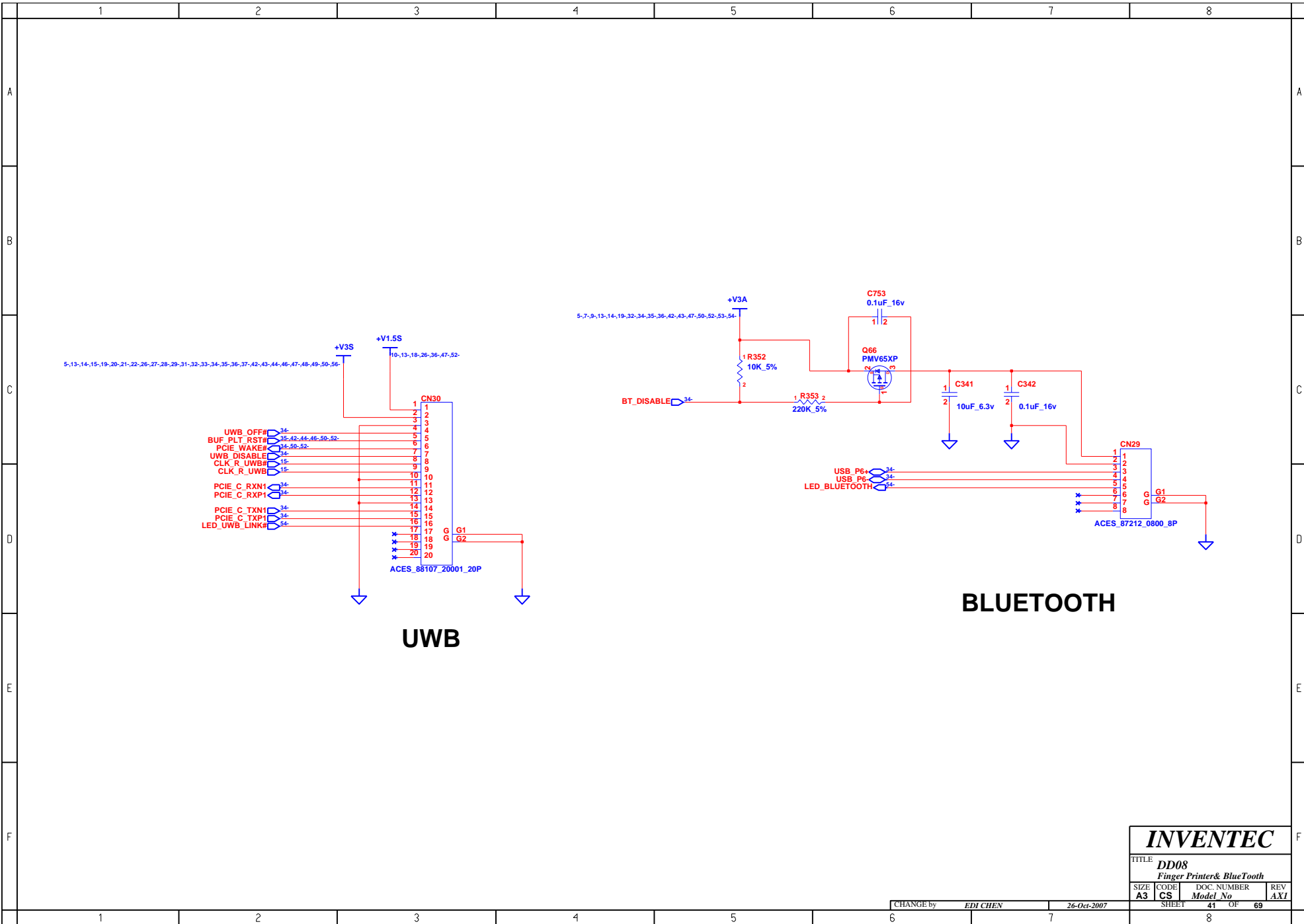
TITLE			
DD08			
ODD CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

CHANGE by EDI CHEN 26-Oct-2007

SHEET 39 OF 69



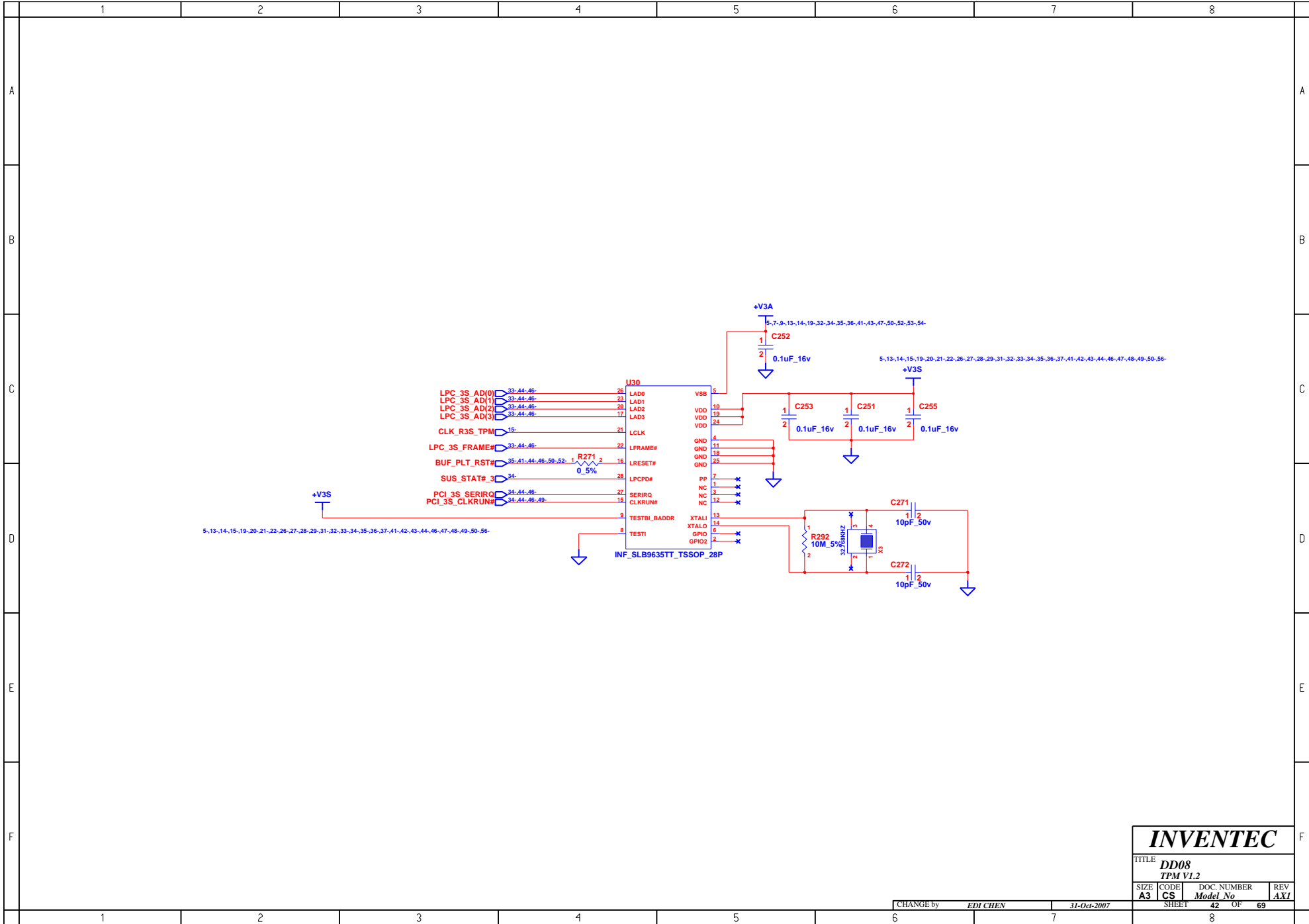
INVENTEC			
TITLE DD08 USB CONN/USB(DB)			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AXI
CHANGE by EDI CHEN		26-Oct-2007	SHEET 40 OF 69



UWB

BLUETOOTH

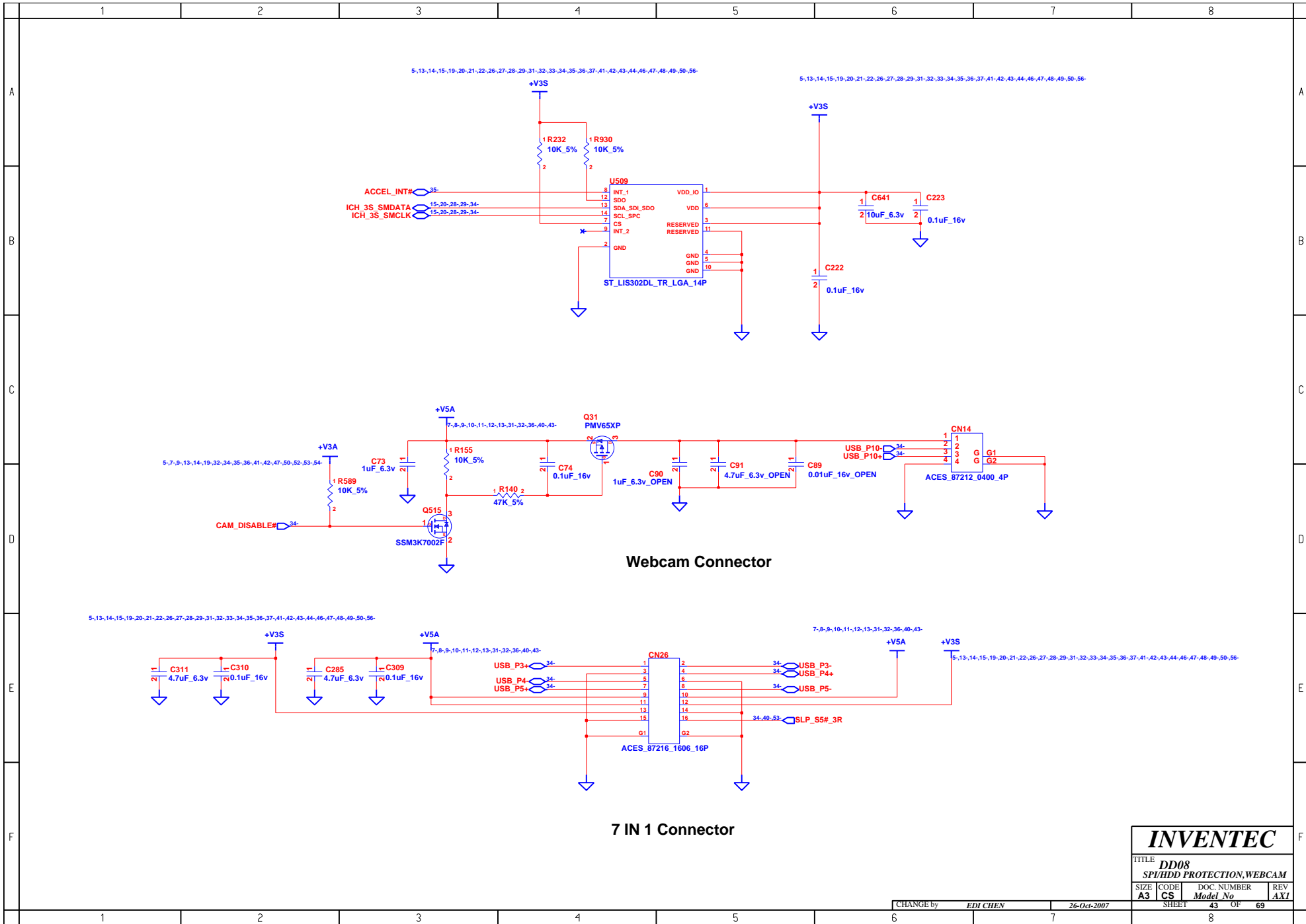
INVENTEC			
TITLE DD08 <i>Finger Printer & BlueTooth</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX1
CHANGE by EDI CHEN		26-Oct-2007	SHEET 41 OF 69



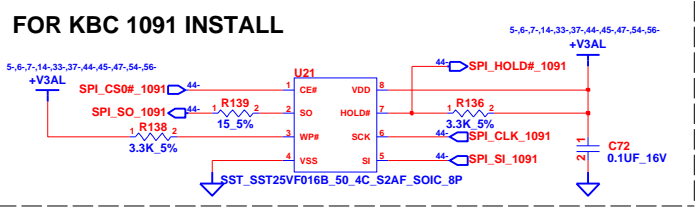
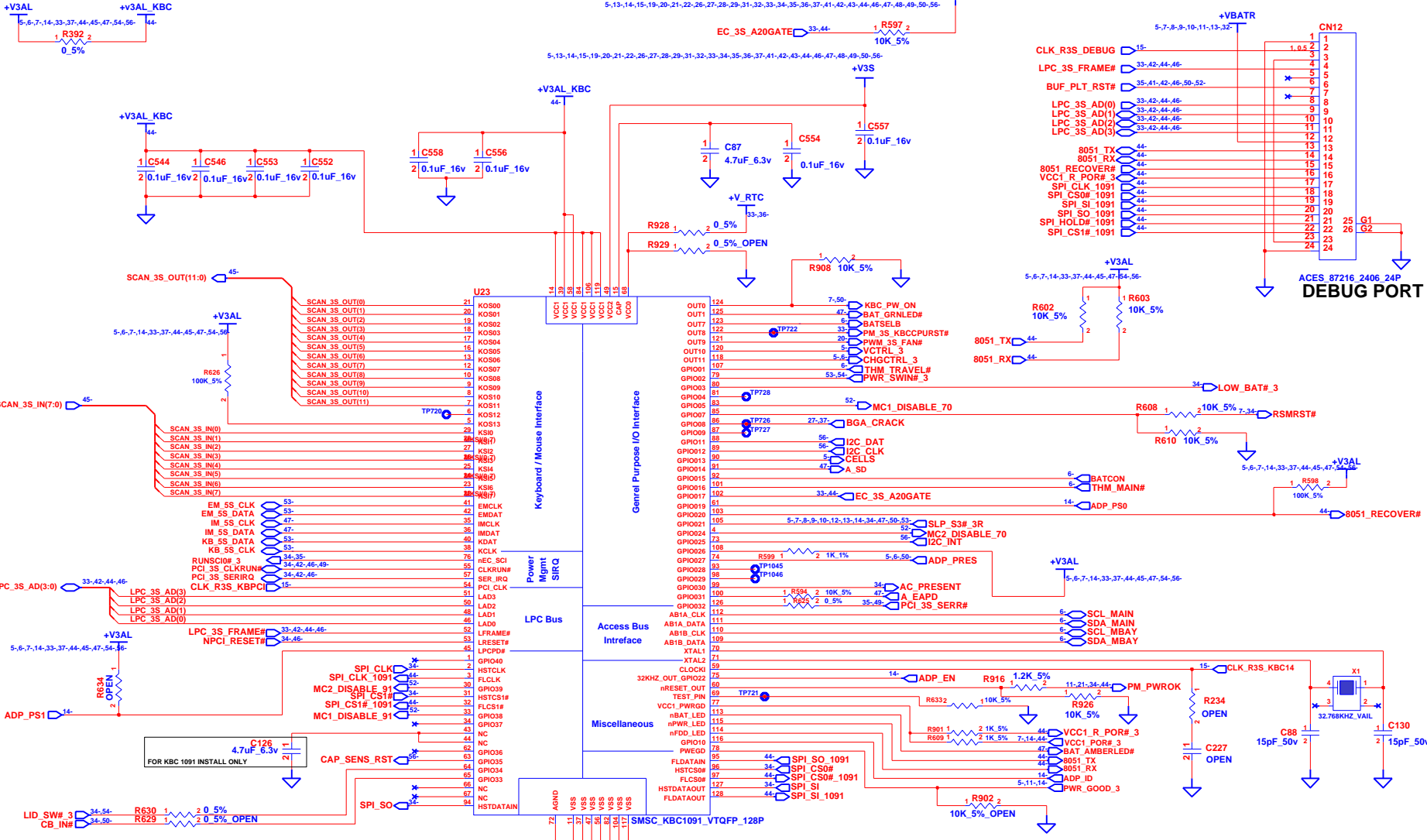
INVENTEC

TITLE DD08
TPM V1.2

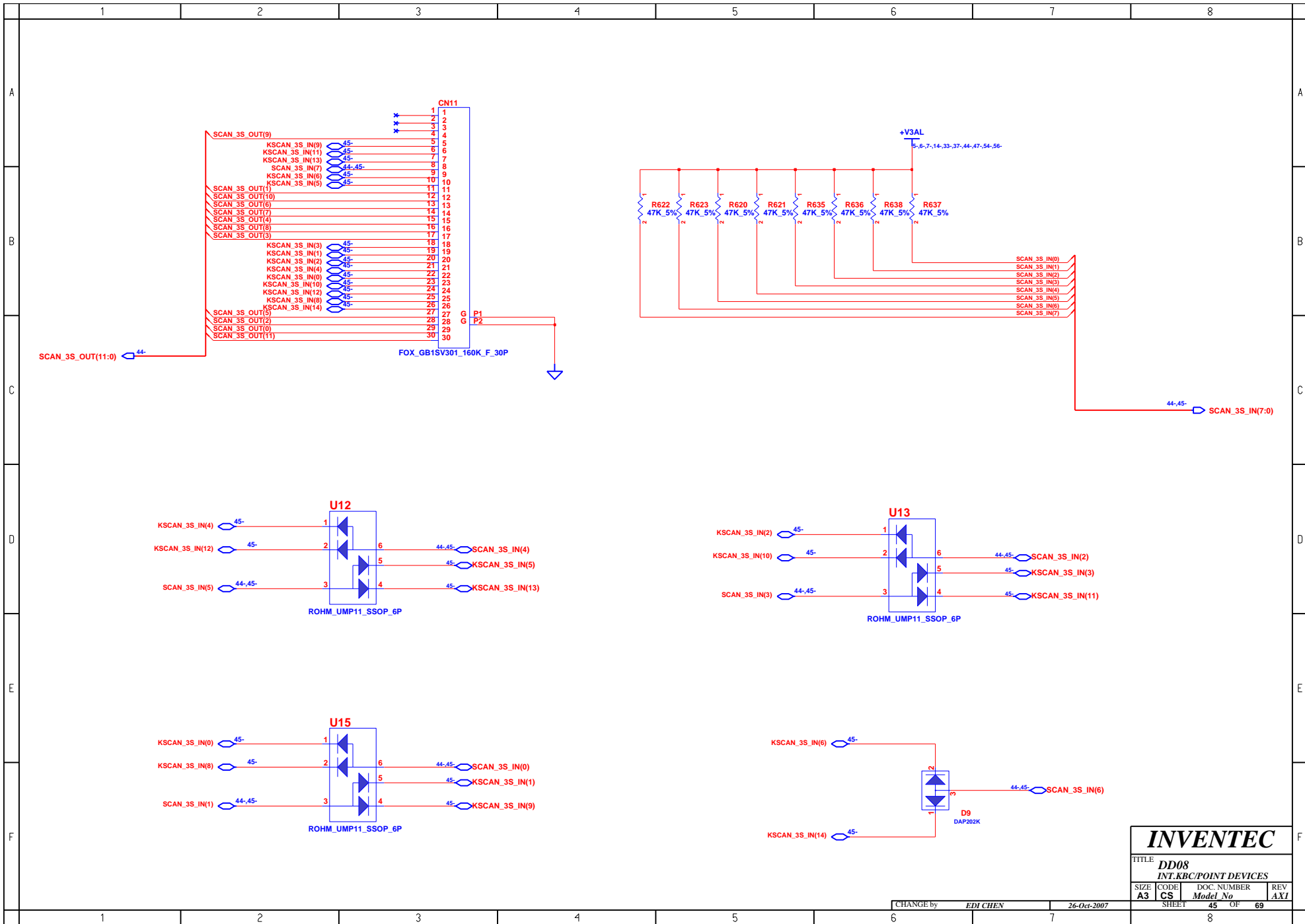
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX1
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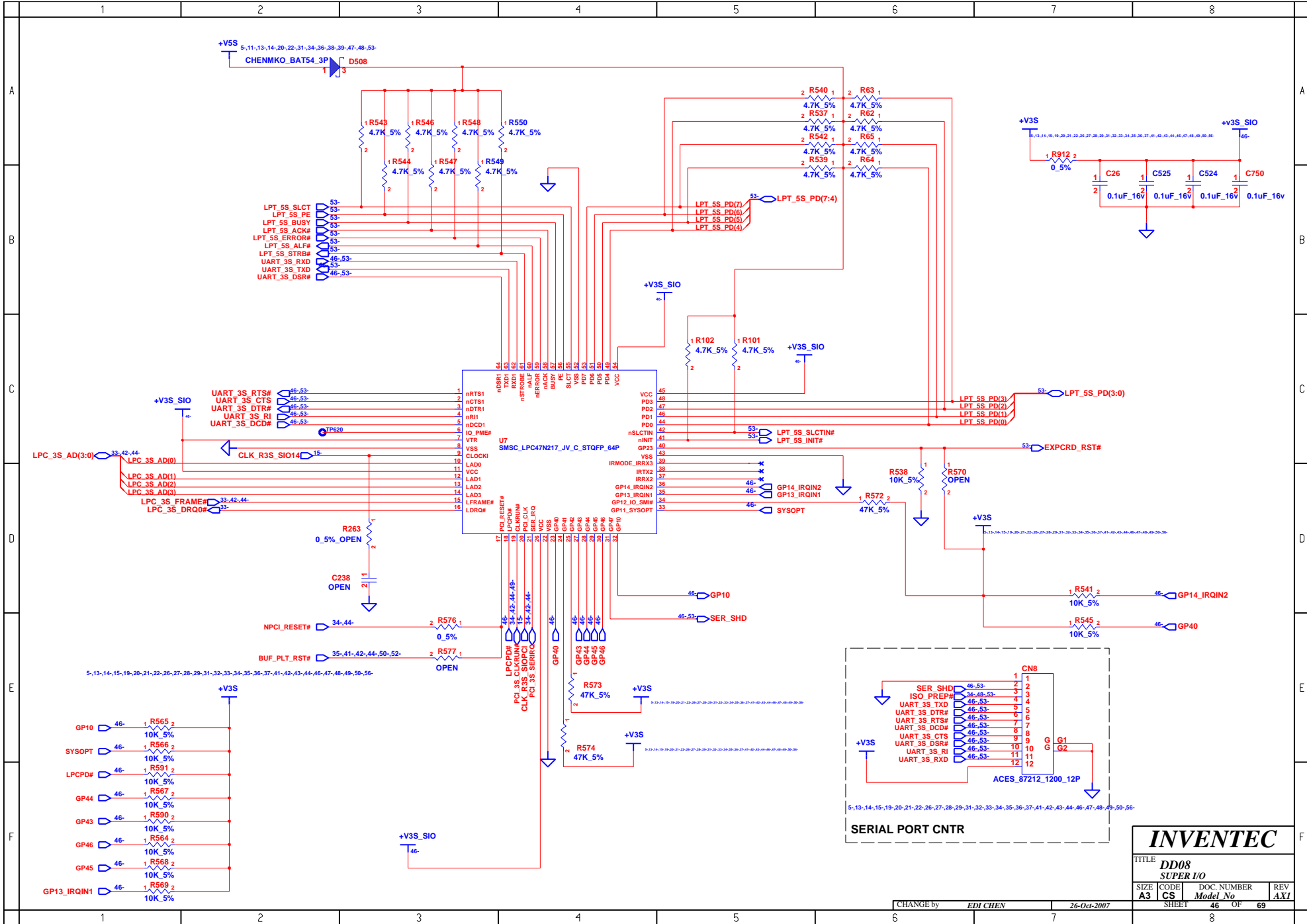
INVENTEC			
TITLE DD08			
SP/ HDD PROTECTION, WEBCAM			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX1
CHANGE by EDI CHEN		26-Oct-2007	SHEET 43 OF 69



INVENTEC			
TITLE DD08 KBC			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX1
CHANGE by EDI CHEN		31-Oct-2007	
SHEET 44		OF 69	



INVENTEC			
TITLE DD08 INT.KBC/POINT DEVICES			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX1
CHANGE by EDI CHEN		26-Oct-2007	
SHEET 45		OF 69	

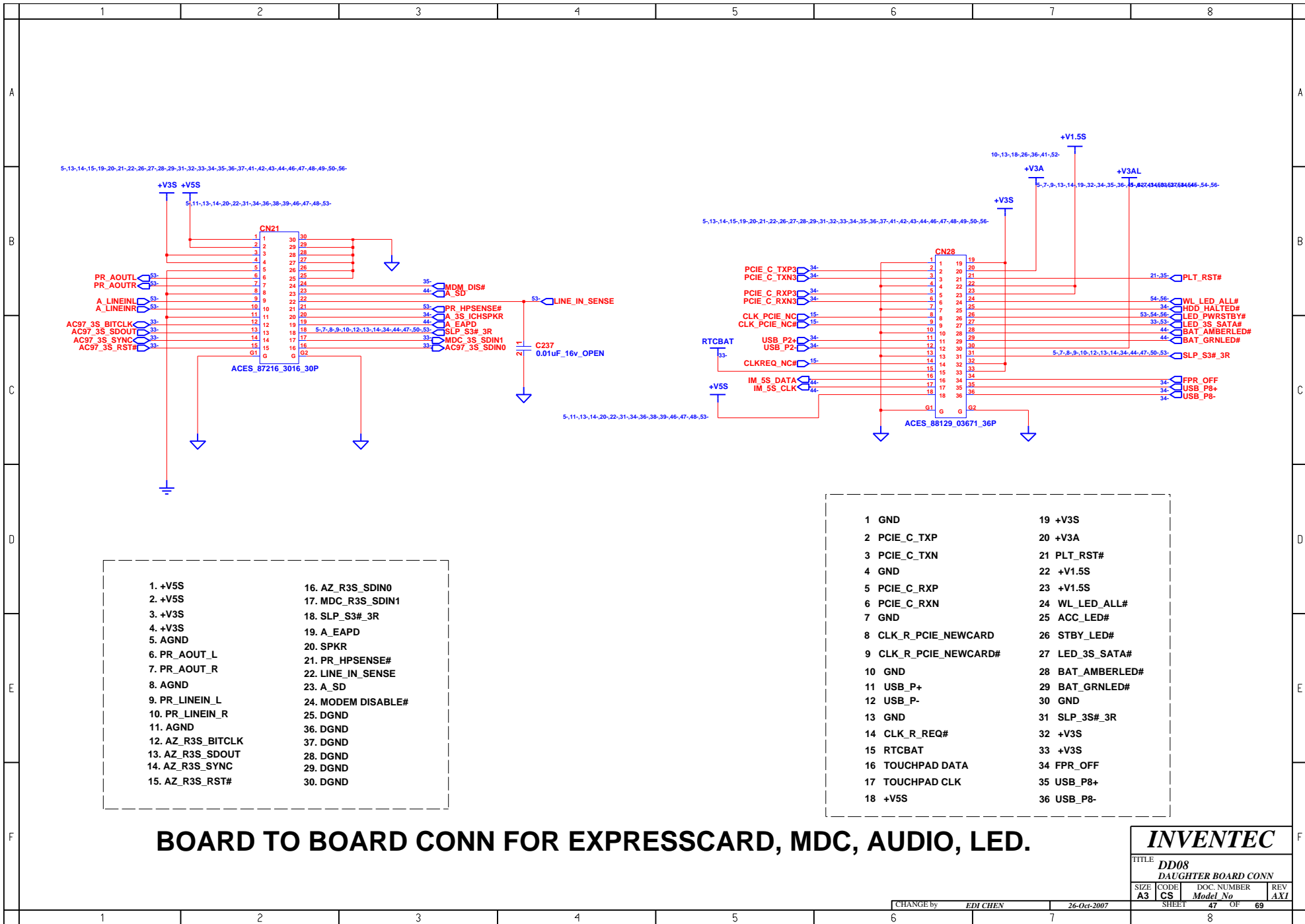


INVENTEC

TITLE			
DD08 SUPER I/O			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

CHANGE by EDI CHEN 26-Oct-2007

SHEET 46 OF 69

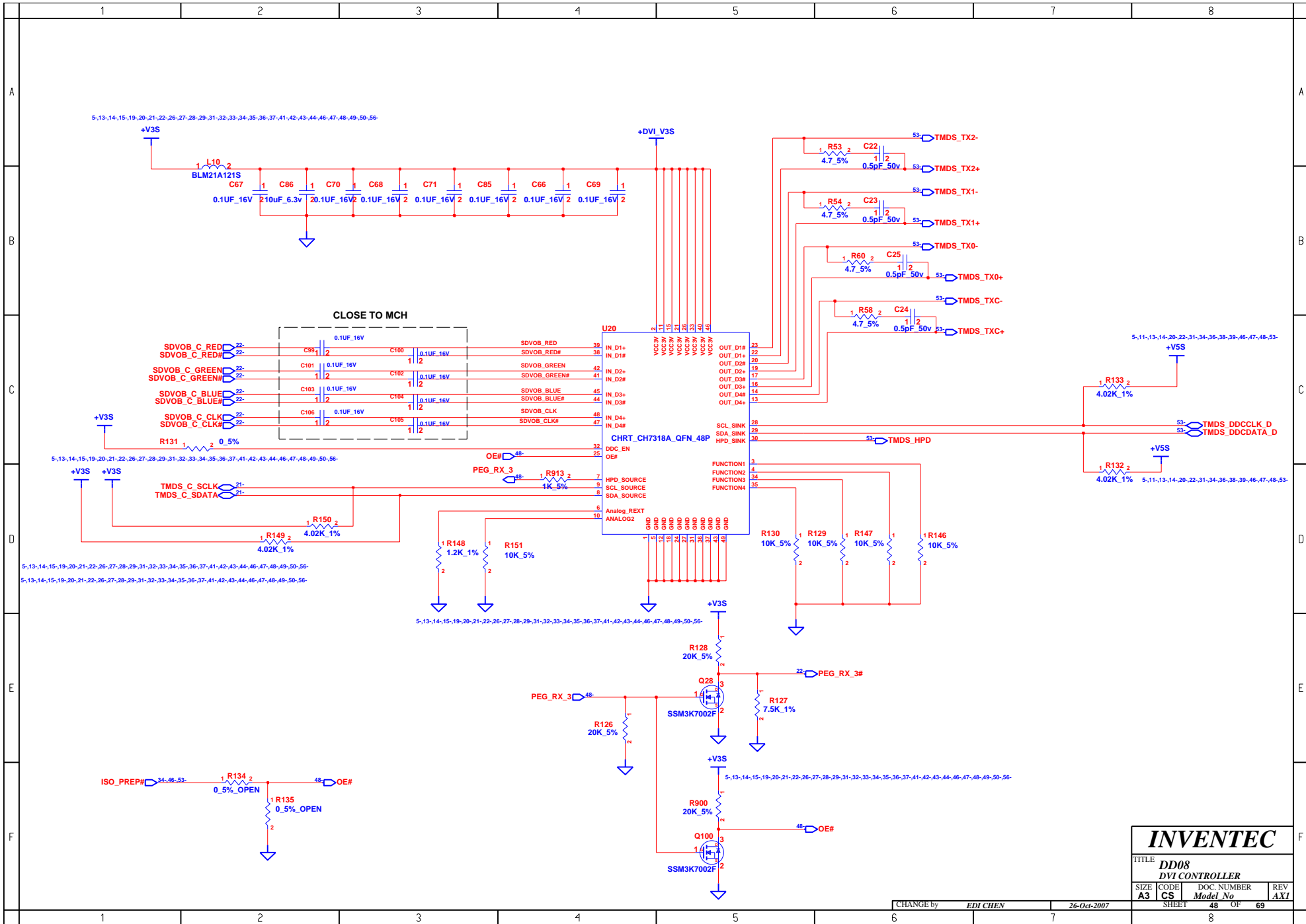


INVENTEC

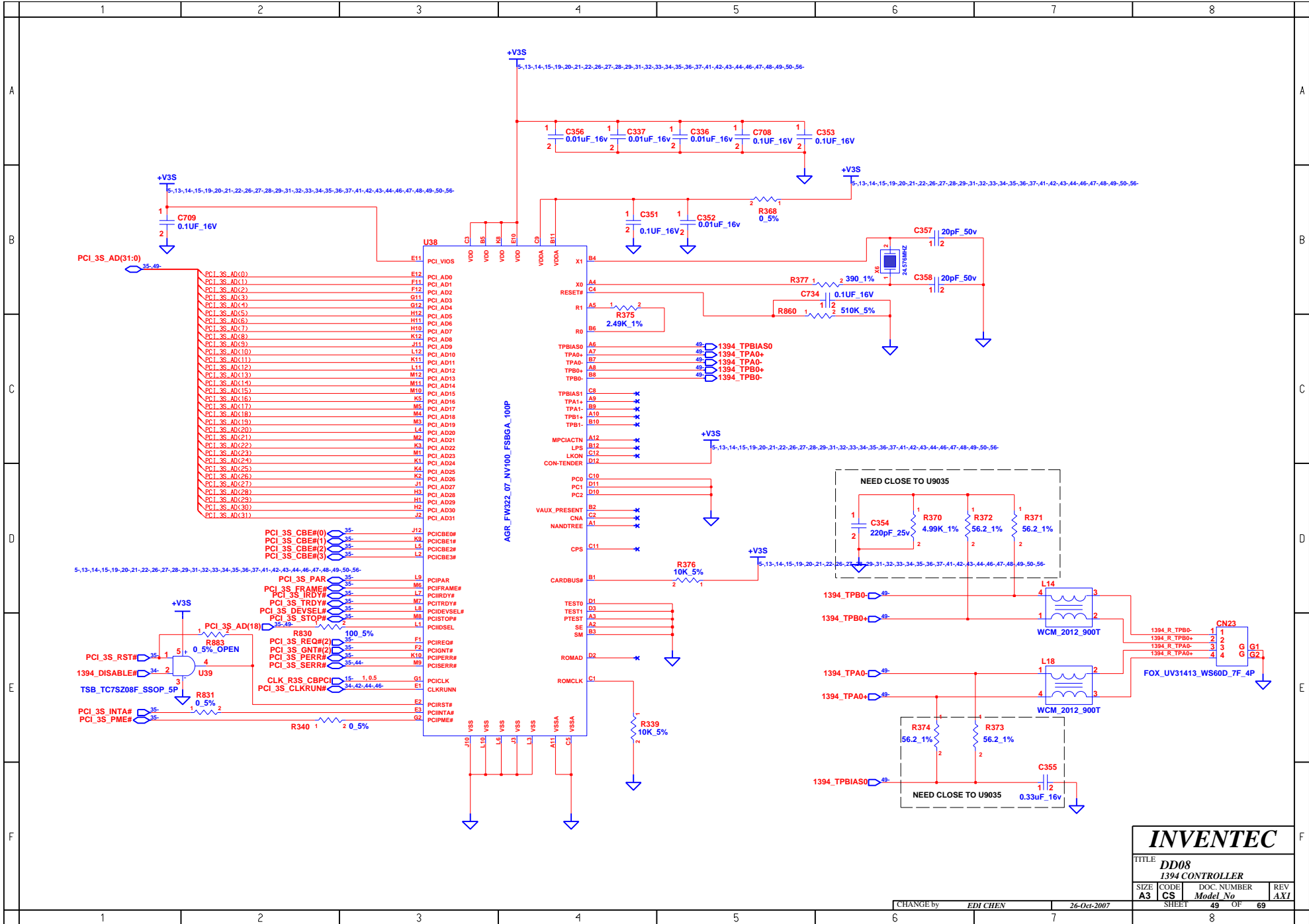
TITLE: **DD08 DAUGHTER BOARD CONN**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

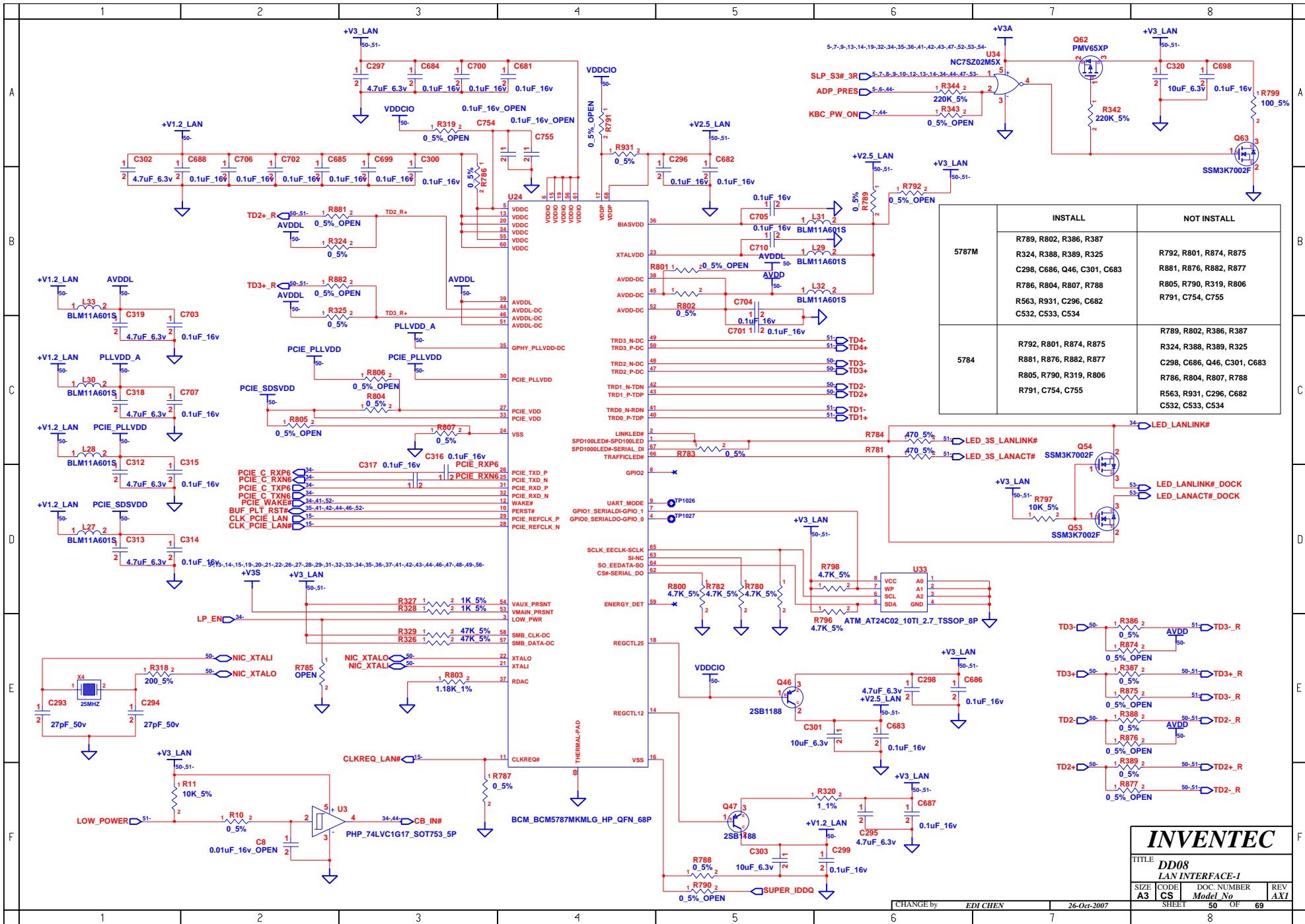
CHANGE by: EDI CHEN 26-Oct-2007 SHEET 47 OF 69



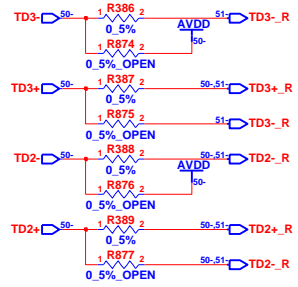
INVENTEC			
TITLE DD08 DVI CONTROLLER			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No.</i>	REV AXI
CHANGE by EDI CHEN		26-Oct-2007	
SHEET 48 OF 69		8	



INVENTEC			
TITLE			
DD08 1394 CONTROLLER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI
CHANGE by		SHEET	
EDI CHEN		49 OF 69	
		26-Oct-2007	



	INSTALL	NOT INSTALL
5787M	R789, R802, R386, R387 R324, R388, R389, R325 C298, C686, Q46, C301, C683 R786, R804, R807, R788 R563, R931, C296, C682 C532, C533, C534	R792, R801, R874, R875 R881, R876, R882, R877 R805, R790, R319, R806 R791, C754, C755
5784	R792, R801, R874, R875 R881, R876, R882, R877 R805, R790, R319, R806 R791, C754, C755	R789, R802, R386, R387 R324, R388, R389, R325 C298, C686, Q46, C301, C683 R786, R804, R807, R788 R563, R931, C296, C682 C532, C533, C534

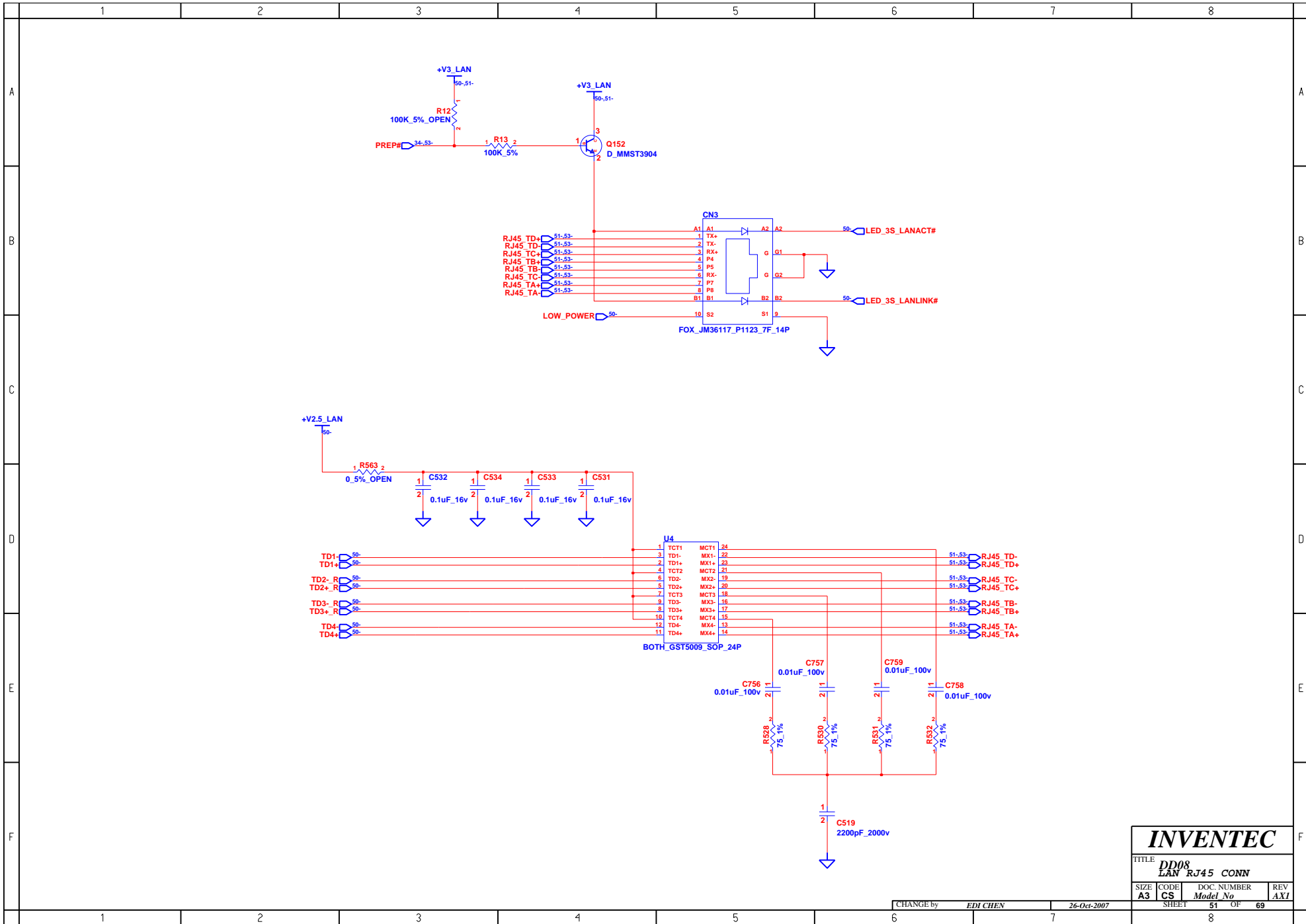


INVENTEC

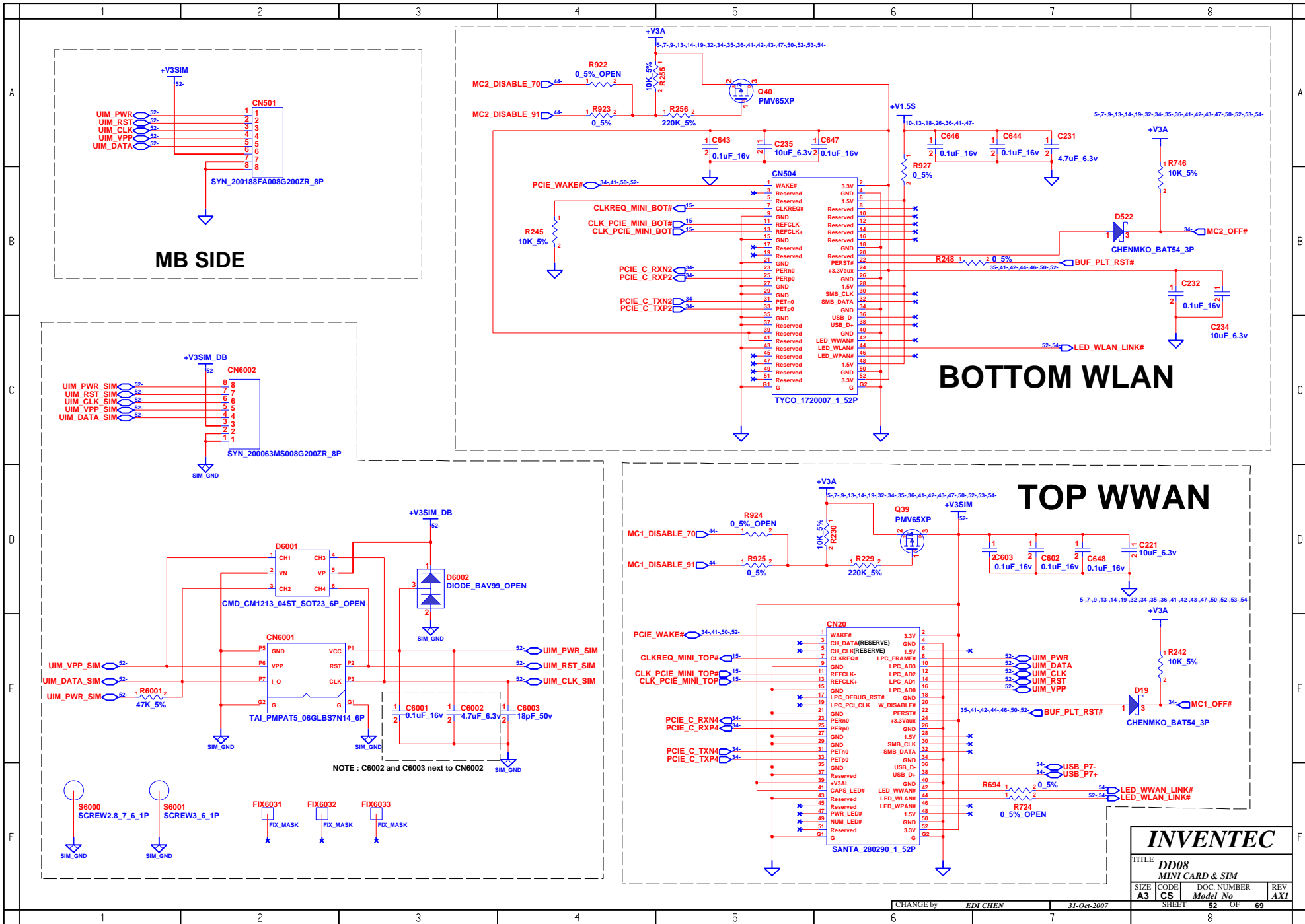
TITLE: DD08
LAN INTERFACE-1

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

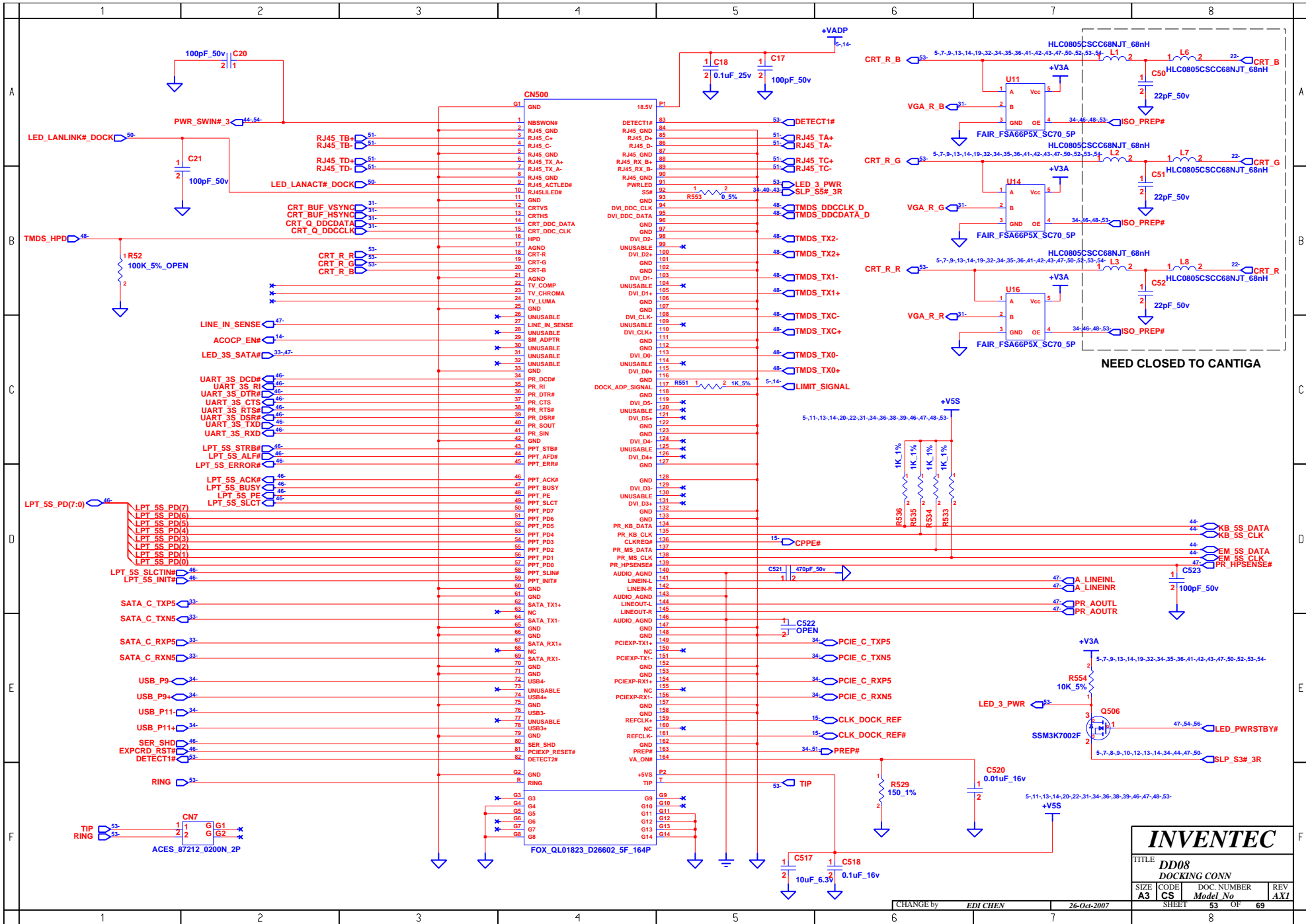
SHEET 50 OF 69



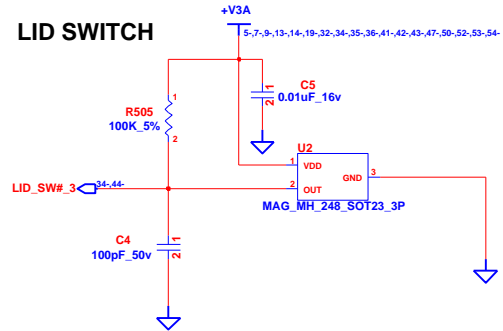
INVENTEC			
TITLE DD08 LAN RJ45 CONN			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX1
CHANGE by EDI CHEN		26-Oct-2007	SHEET 51 OF 69



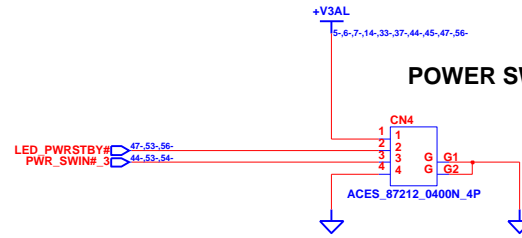
INVENTEC			
TITLE DD08 MINI CARD & SIM			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX1
CHANGE by EDI CHEN		31-Oct-2007	SHEET 52 OF 69



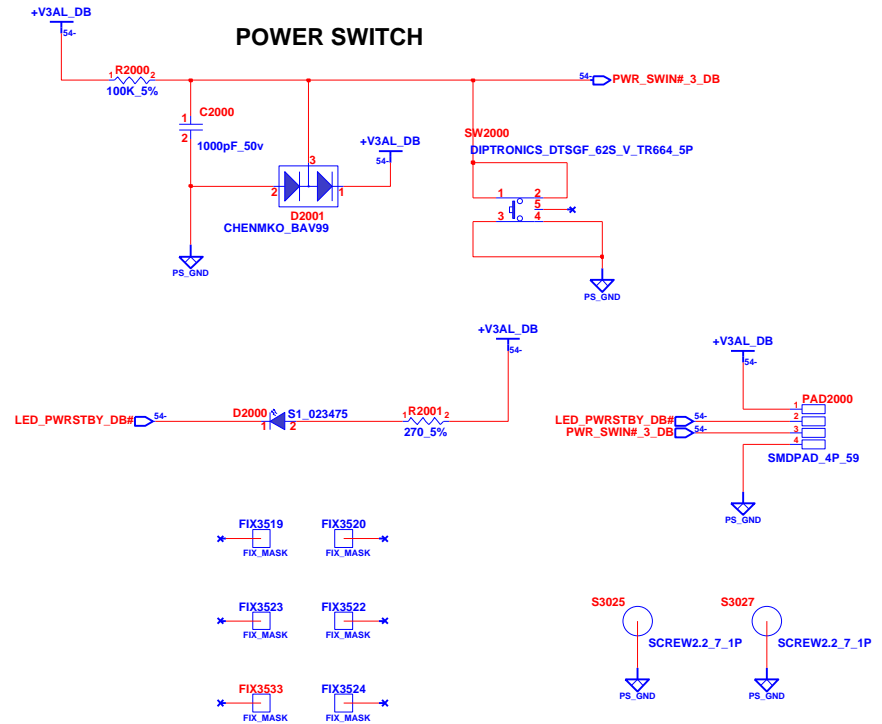
LID SWITCH



POWER SWITCH CNTR

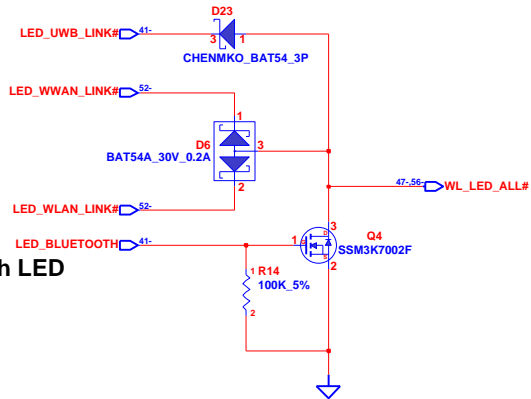


POWER SWITCH

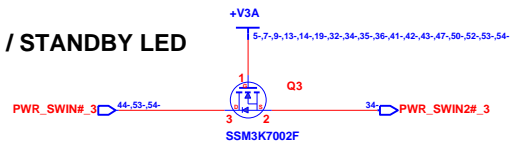


POWER BUTTON BOARD

WLAN_Bluetooth LED



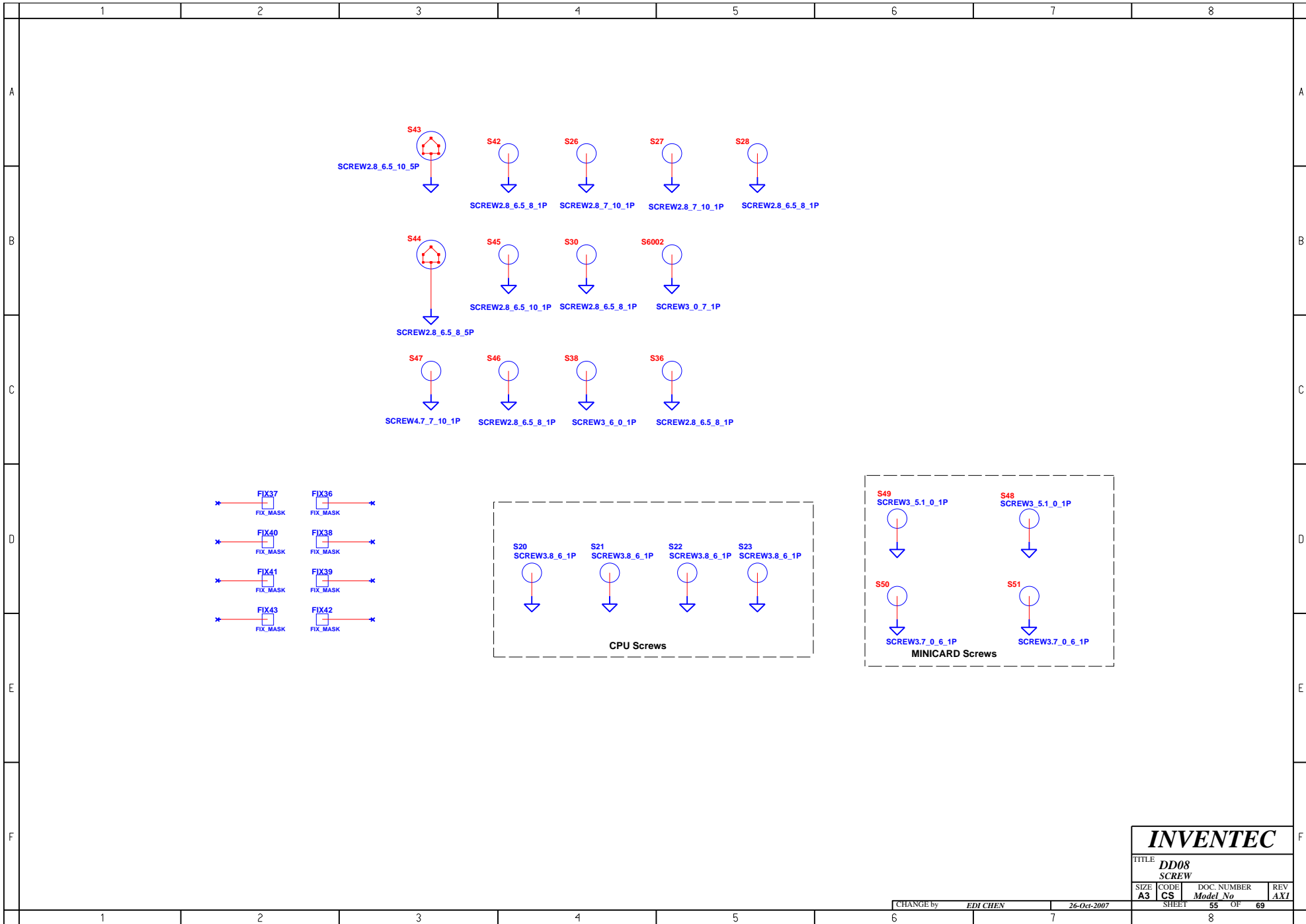
POWER / STANDBY LED



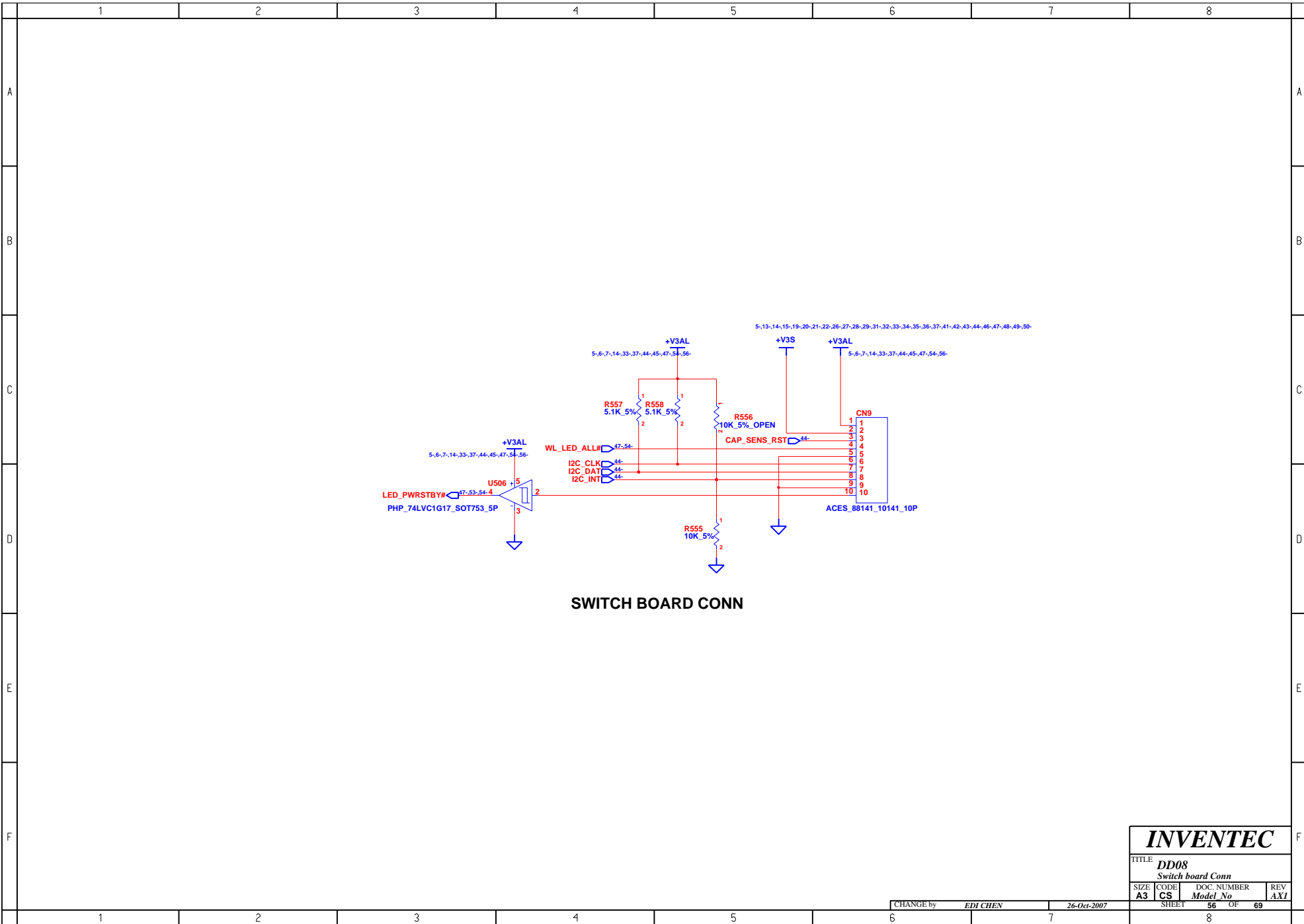
INVENTEC

TITLE			
DD08			
BUTTON&SWITCH&LED			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI
SHEET		54	OF 69

CHANGE by EDI CHEN 26-Oct-2007



INVENTEC			
TITLE DD08 SCREW			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX1
CHANGE by EDI CHEN		26-Oct-2007	
SHEET		55	OF 69



SWITCH BOARD CONN

INVENTEC

TITLE			
DD08			
Switch board Conn			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

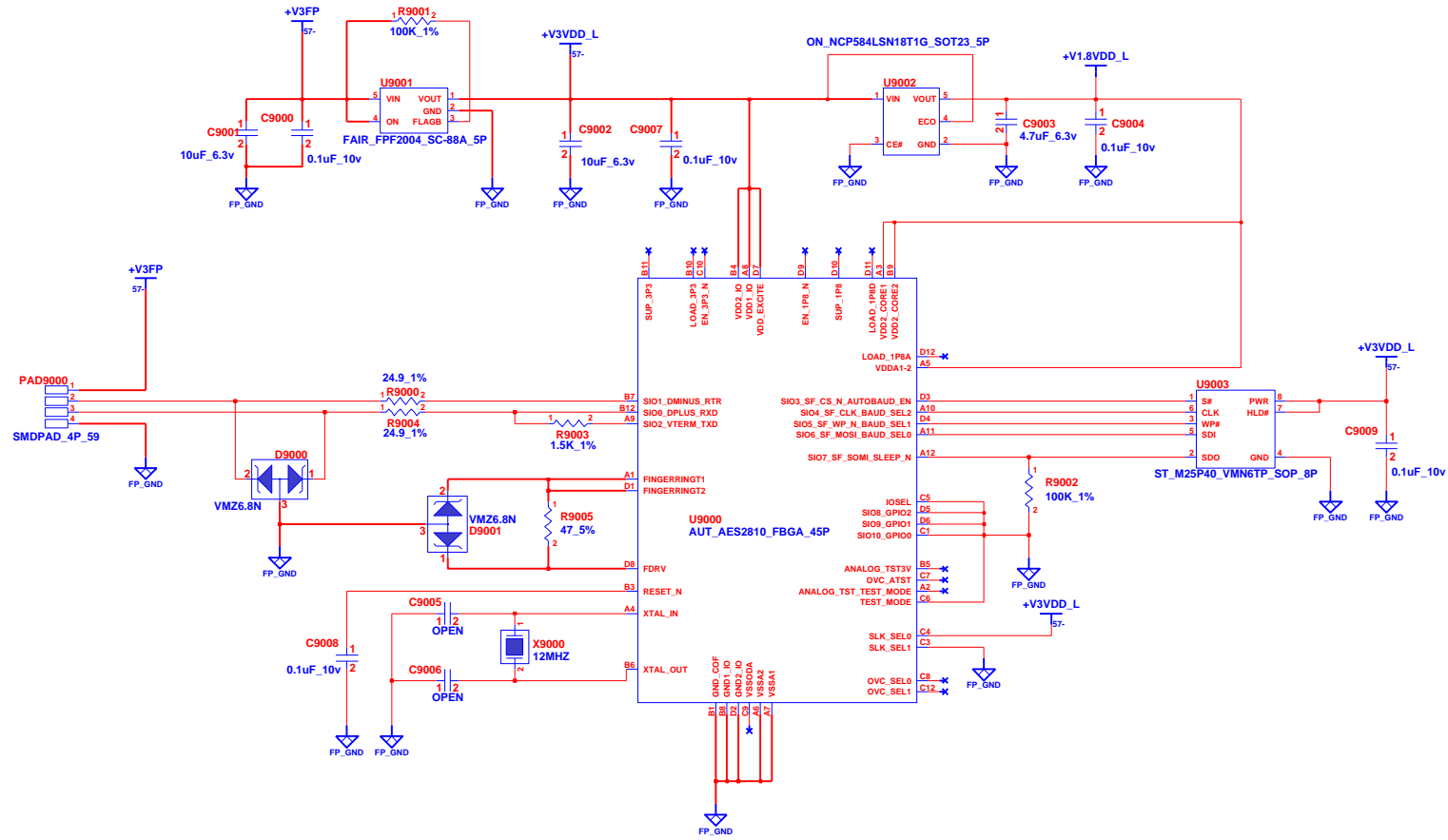
CHANGE by EDI CHEN 26-Oct-2007

SHEET 56 OF 69

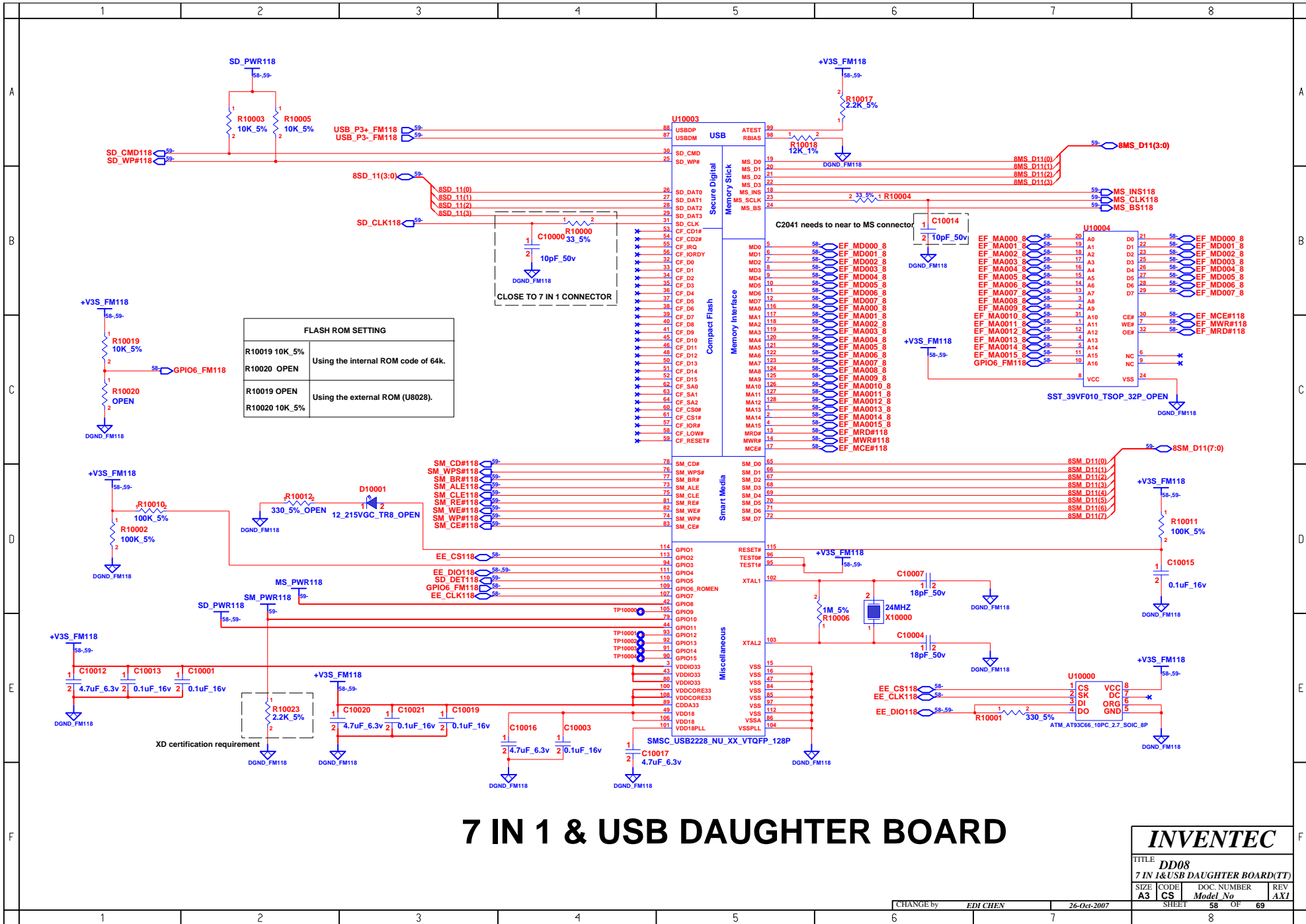
Layout notes :

1. Placed R5 close to U1
2. Placed D1 closed to R5
3. D1 & D2 pin_A to GND should be a short trace(20mil max) using three via to connect to GND.

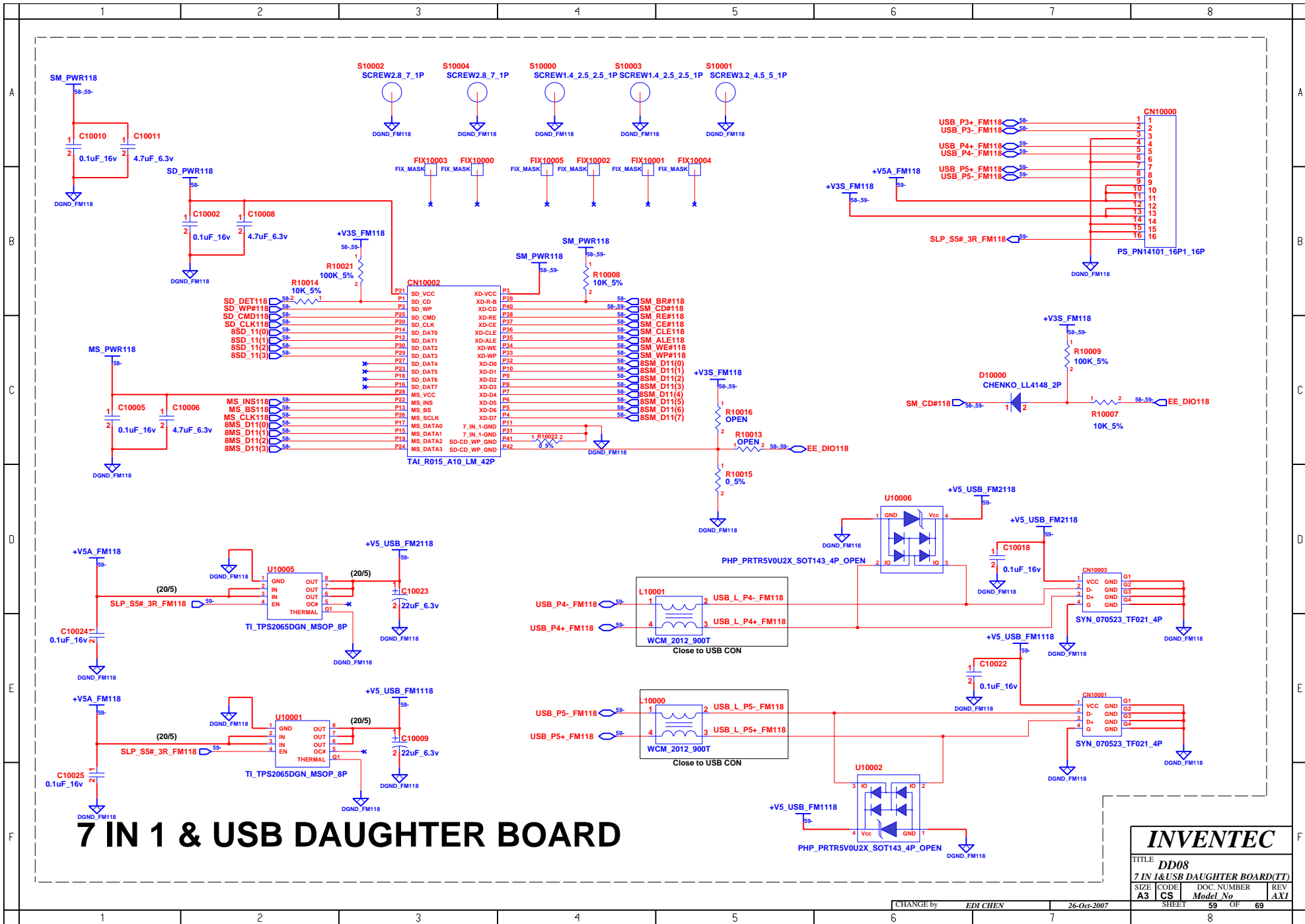
Finger Print Reader DB



INVENTEC			
TITLE DD08 Finger Print board			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX1
CHANGE by EDI CHEN		26-Oct-2007	
SHEET		OF 69	
8			

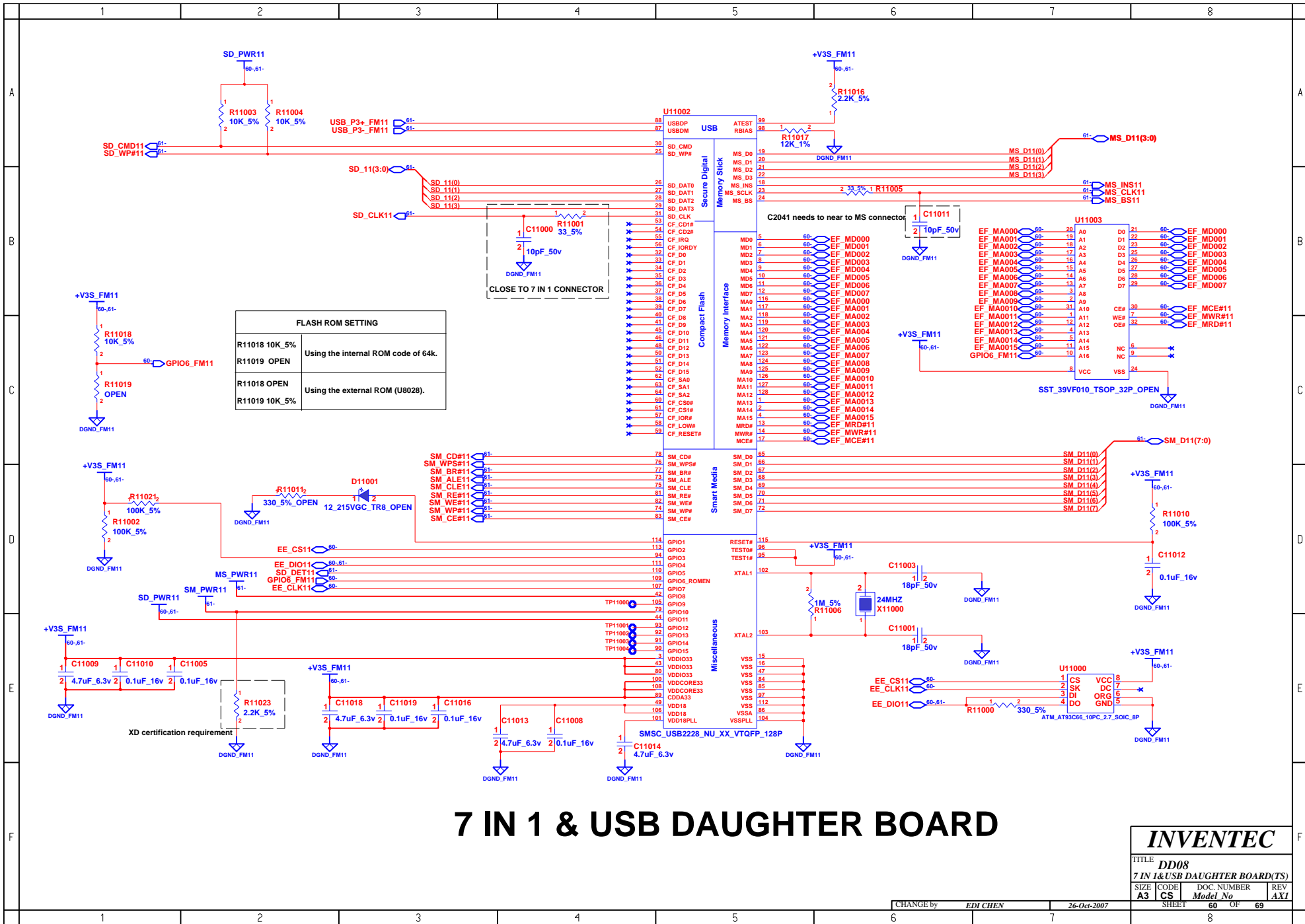


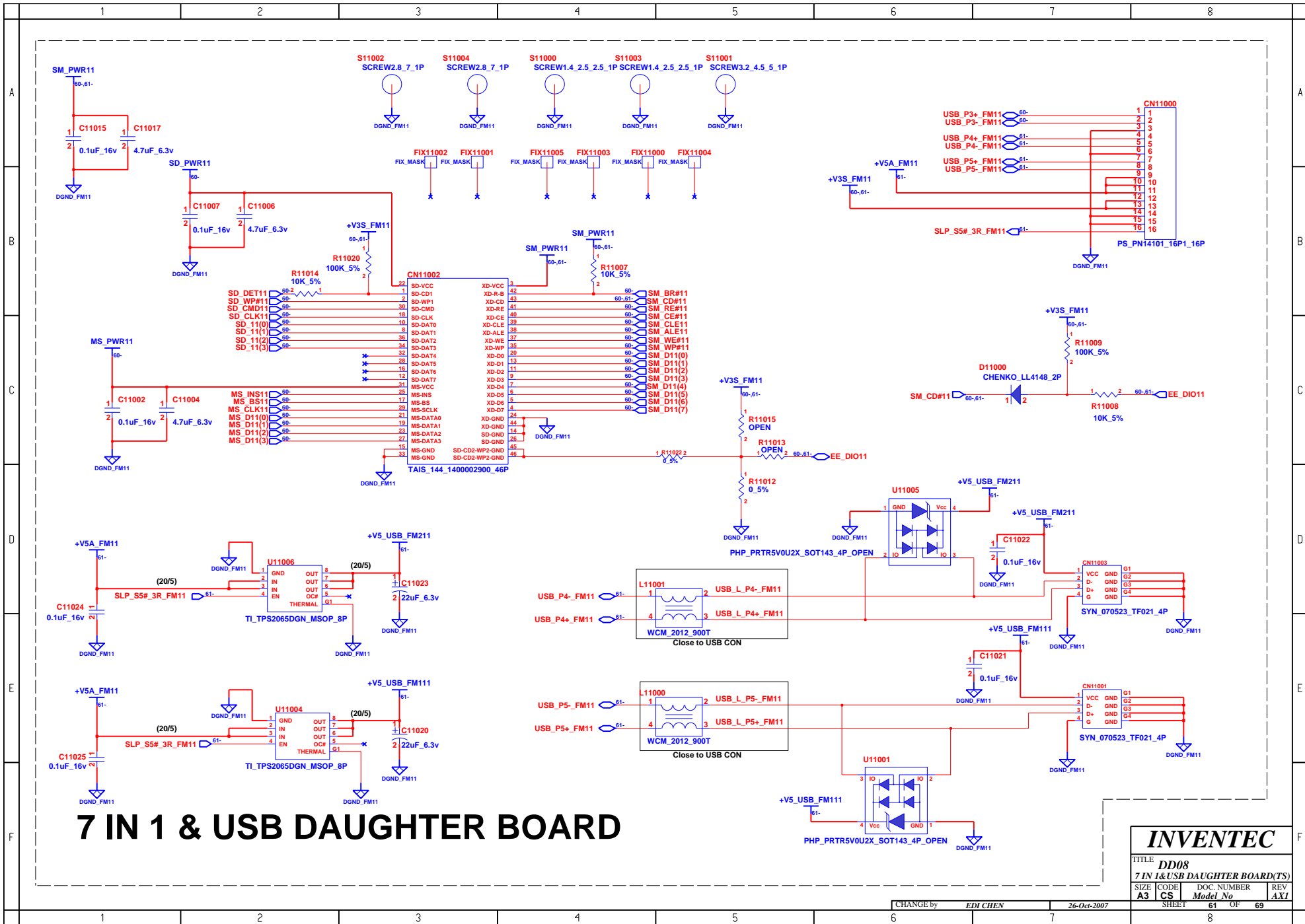
INVENTEC			
TITLE DD08			
7 IN 1&USB DAUGHTER BOARD(TT)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX1
CHANGE by EDI CHEN		26-Oct-2007	
SHEET 58		OF 69	



INVENTEC			
TITLE DD08			
7 IN 1 & USB DAUGHTER BOARD(TT)			
SIZE A3	CODE CS	DOC. NUMBER	REV AX1
Model No		SHEET 59 OF 69	

CHANGE by **EDI CHEN** 26-Oct-2007

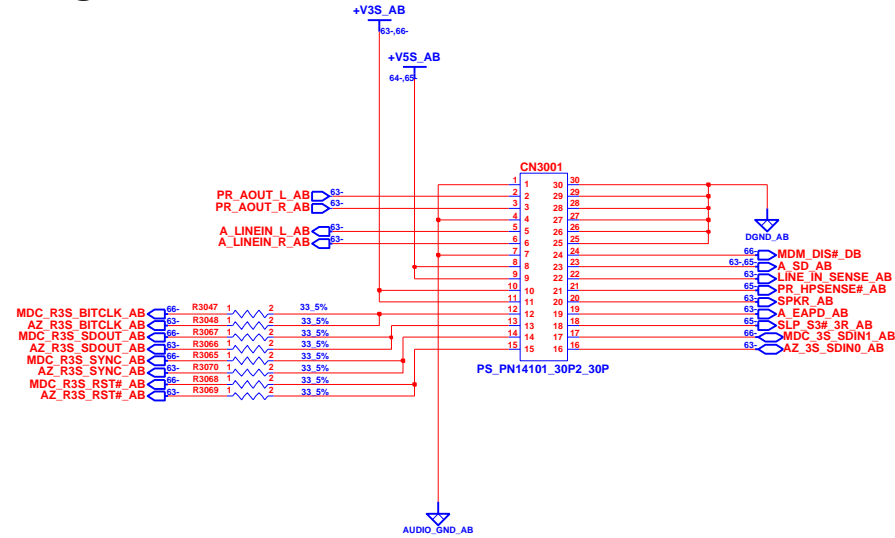




7 IN 1 & USB DAUGHTER BOARD

INVENTEC			
TITLE DD08			
7 IN 1 & USB DAUGHTER BOARD(TS)			
SIZE A3	CODE CS	DOC. NUMBER	REV AX1
CHANGE by EDI CHEN		DATE 26-Oct-2007	SHEET 61 OF 69

AUDIO DAUGHTER BOARD



Audio CONN

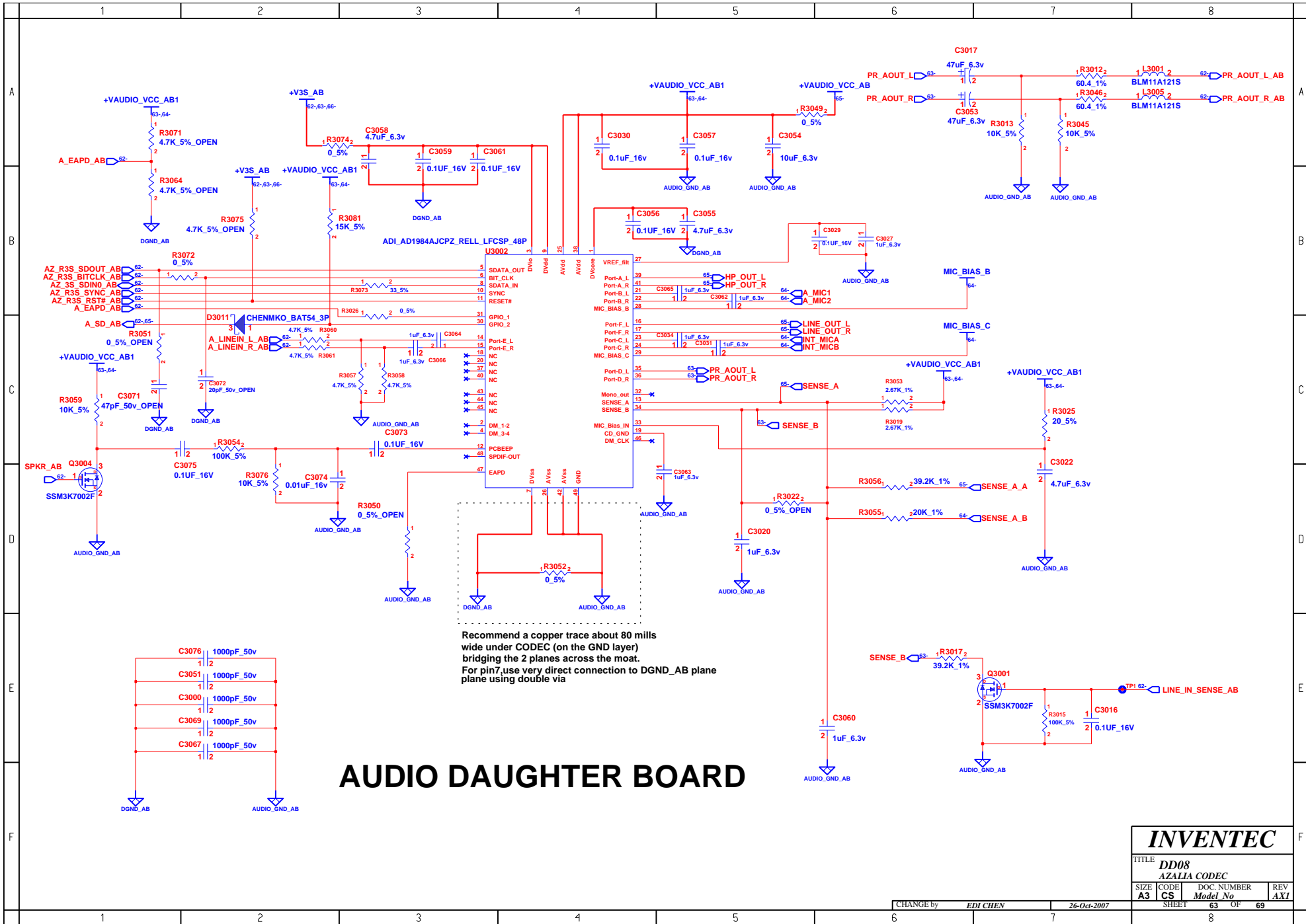
- | | |
|-----------------------------------|----------------------|
| 1. AGND | 16. AZ_3S_SDIN0_AB |
| 2. PR_AOUT_L | 17. MDC_3S_SDIN1_AB |
| 3. PR_AOUT_R | 18. SLP_S3#_3R_AB |
| 4. AGND | 19. A_EAPD_AB |
| 5. PR_LINEIN_L | 20. SPKR_AB |
| 6. PR_LINEIN_R | 21. PR_HPSENSE#_AB |
| 7. AGND | 22. LINE_IN_SENSE_AB |
| 8. +V5S | 23. A_SD_AB |
| 9. +V5S | 24. MODEM DISABLE# |
| 10. +V3S | 25. DGND |
| 11. +V3S | 26. DGND |
| 12. AZ_R3S_BITCLK(MDC_R3S_BITCLK) | 27. DGND |
| 13. AZ_R3S_SDOUT(MDC_R3S_SDOUT) | 28. DGND |
| 14. AZ_R3S_SYNC(MDC_R3S_SYNC) | 29. DGND |
| 15. AZ_R3S_RST#(MDC_R3S_RST#) | 30. DGND |

FIX3501 FIX3502 FIX3504
 * * *
 FIX_MASK FIX_MASK FIX_MASK
 FIX3531 FIX3532 FIX3505 FIX3506
 * * * *
 FIX_MASK FIX_MASK FIX_MASK FIX_MASK

INVENTEC

TITLE			
DD08			
AUDIO CNTR			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI
SHEET		OF	
62		69	

CHANGE by EDI CHEN 26-Oct-2007



Recommend a copper trace about 80 mils wide under CODEC (on the GND layer) bridging the 2 planes across the moat. For pin7, use very direct connection to DGND_AB plane using double via

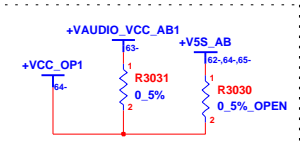
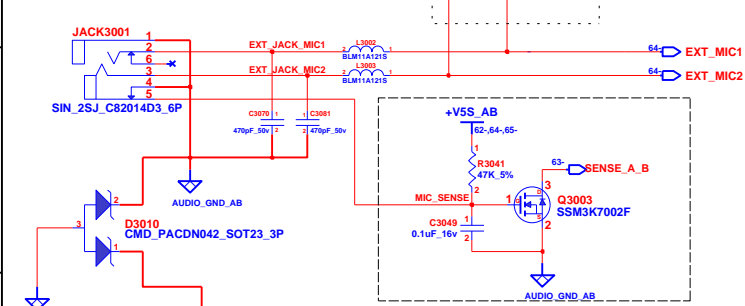
AUDIO DAUGHTER BOARD

- C3076 1000pF_50v
- C3051 1000pF_50v
- C3000 1000pF_50v
- C3069 1000pF_50v
- C3067 1000pF_50v

INVENTEC			
TITLE DD08 AZALIA CODEC			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AXI
CHANGE by EDI CHEN		26-Oct-2007	
SHEET 63		OF 69	

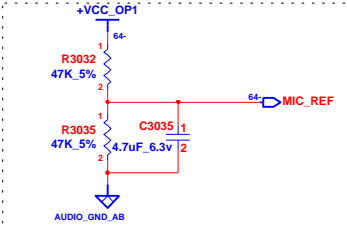
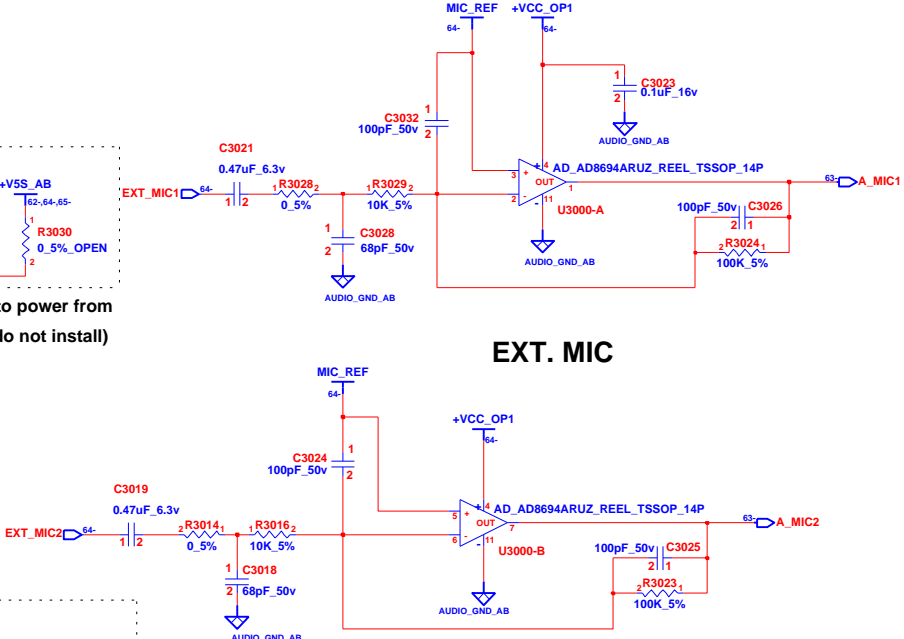
AUDIO DAUGHTER BOARD

MIC Jack



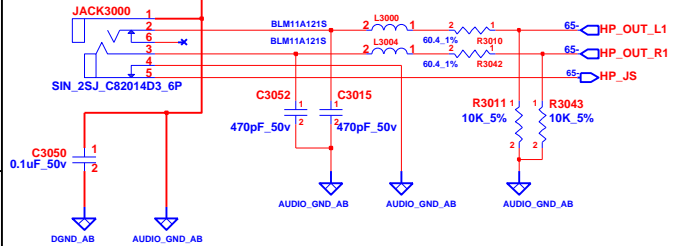
Add option resistor to power from +V5S_AB if needed(do not install)

EXT. MIC



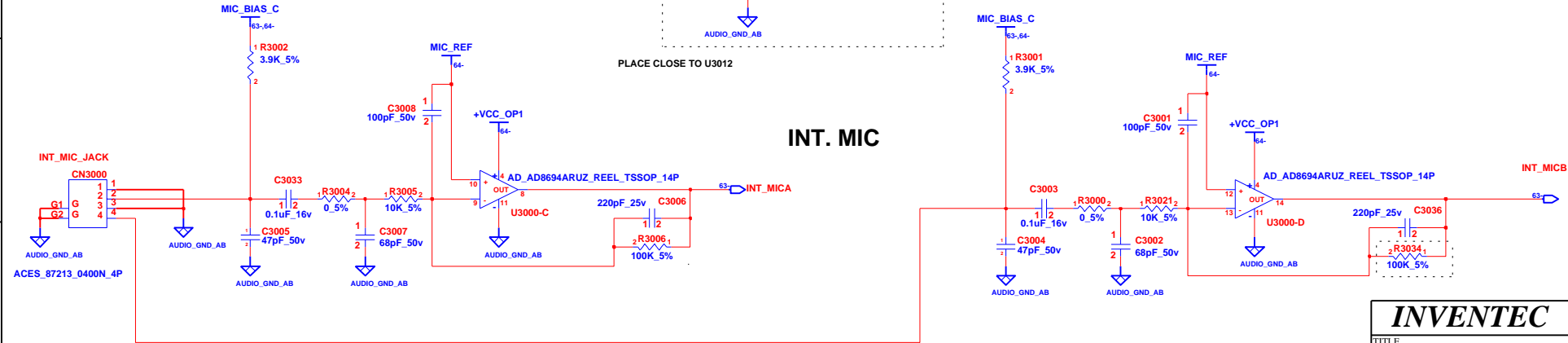
PLACE CLOSE TO U3012

Earphone Jack



R3176 need to be place very close to jack pin1 with heavy copper traces(20 mil min)

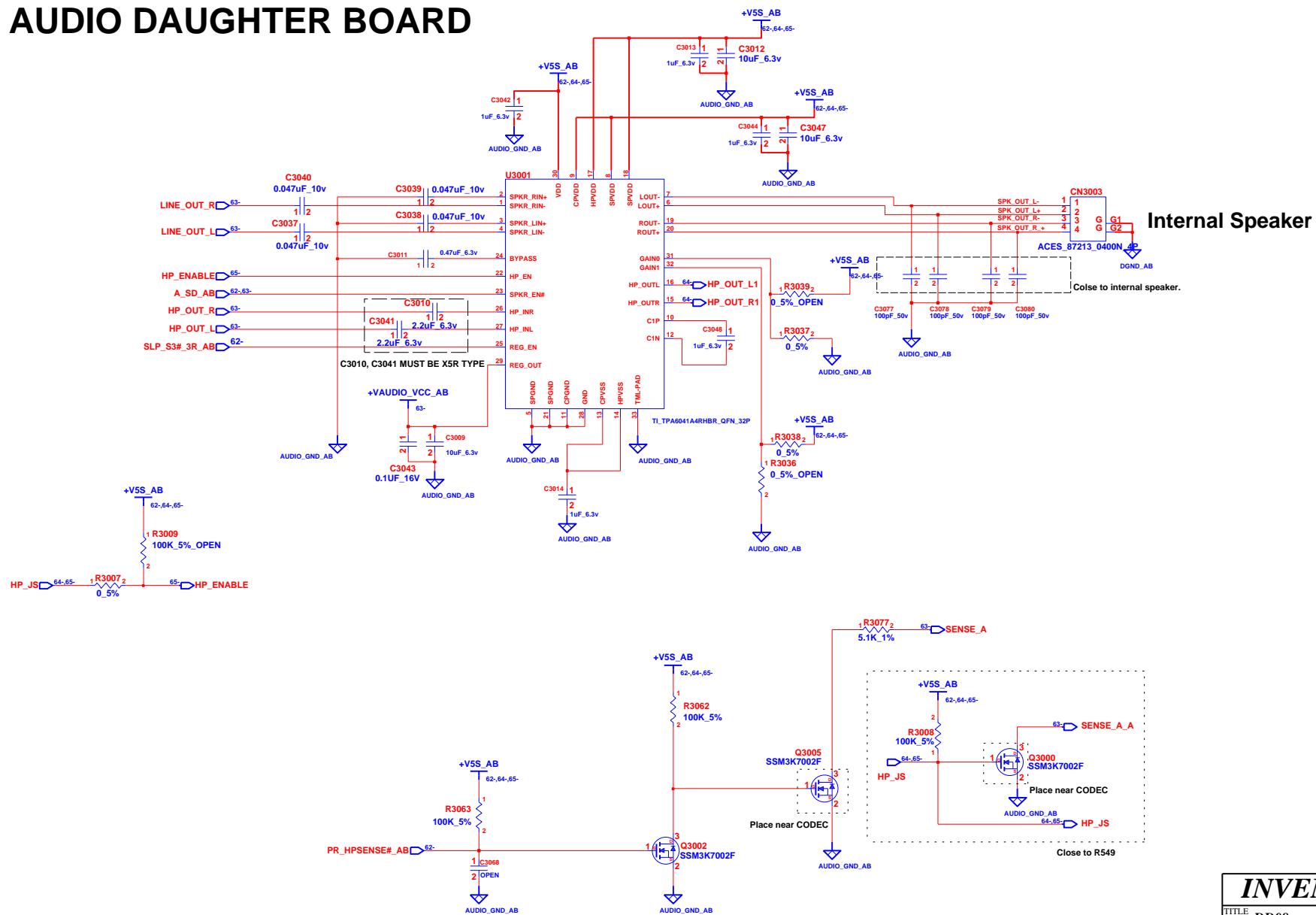
INT. MIC



INVENTEC

TITLE			
DD08			
EARPHONE & MIC JACK			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

AUDIO DAUGHTER BOARD



Internal Speaker

Close to internal speaker.

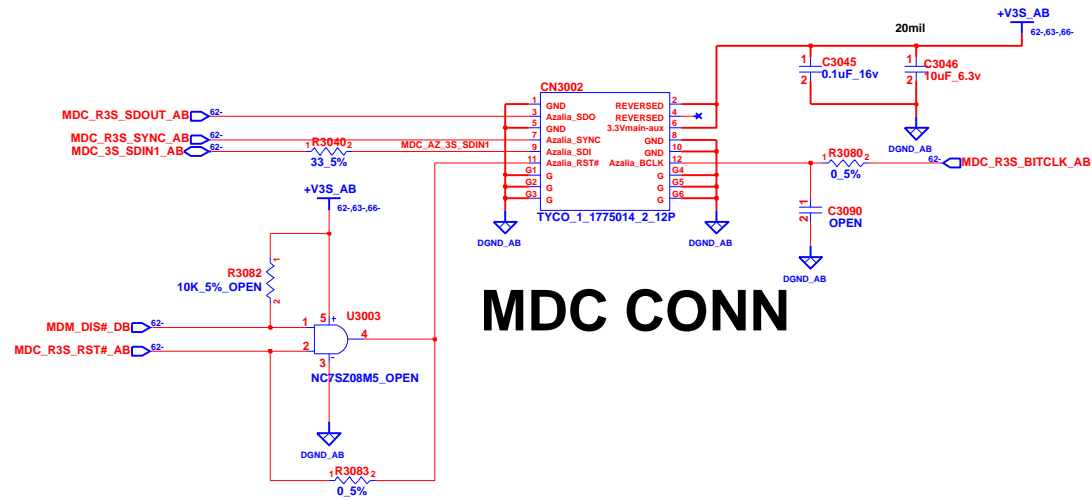
INVENTEC

TITLE			
DD08			
AUDIO AMP & HP JACK			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

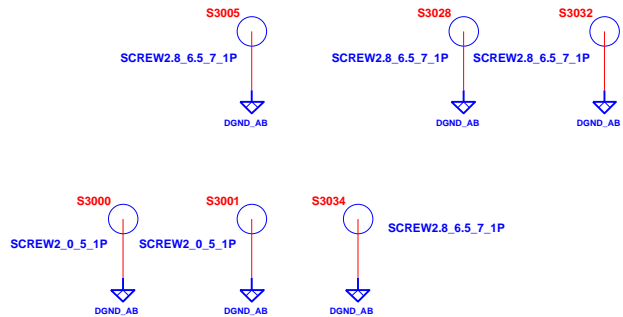
CHANGE by EDI CHEN 26-Oct-2007

SHEET 65 OF 69

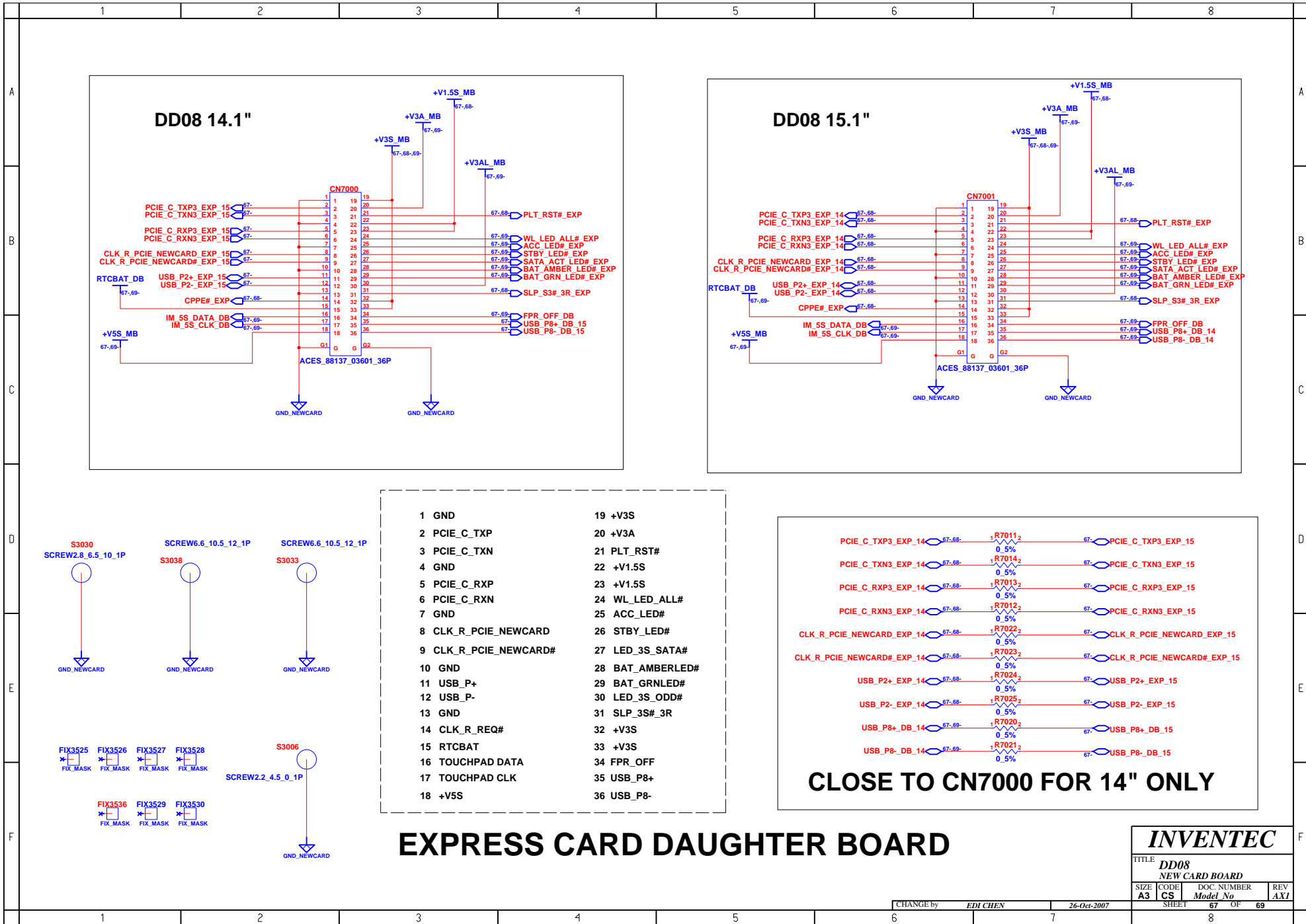
AUDIO DAUGHTER BOARD



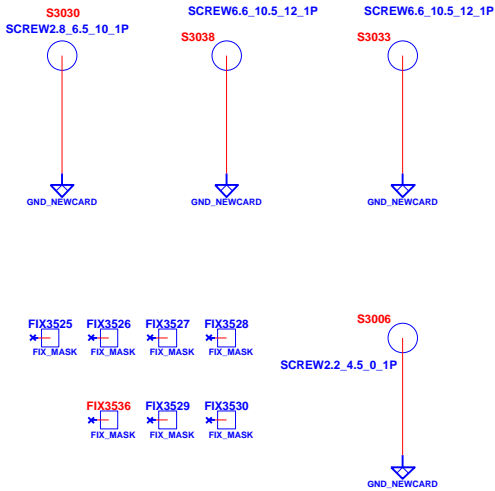
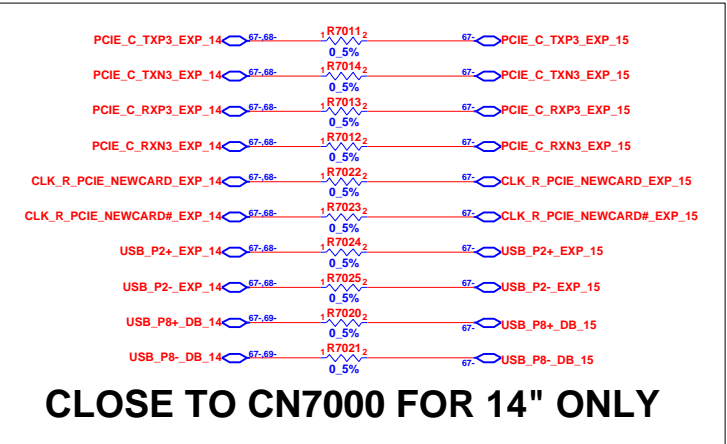
MDC CONN

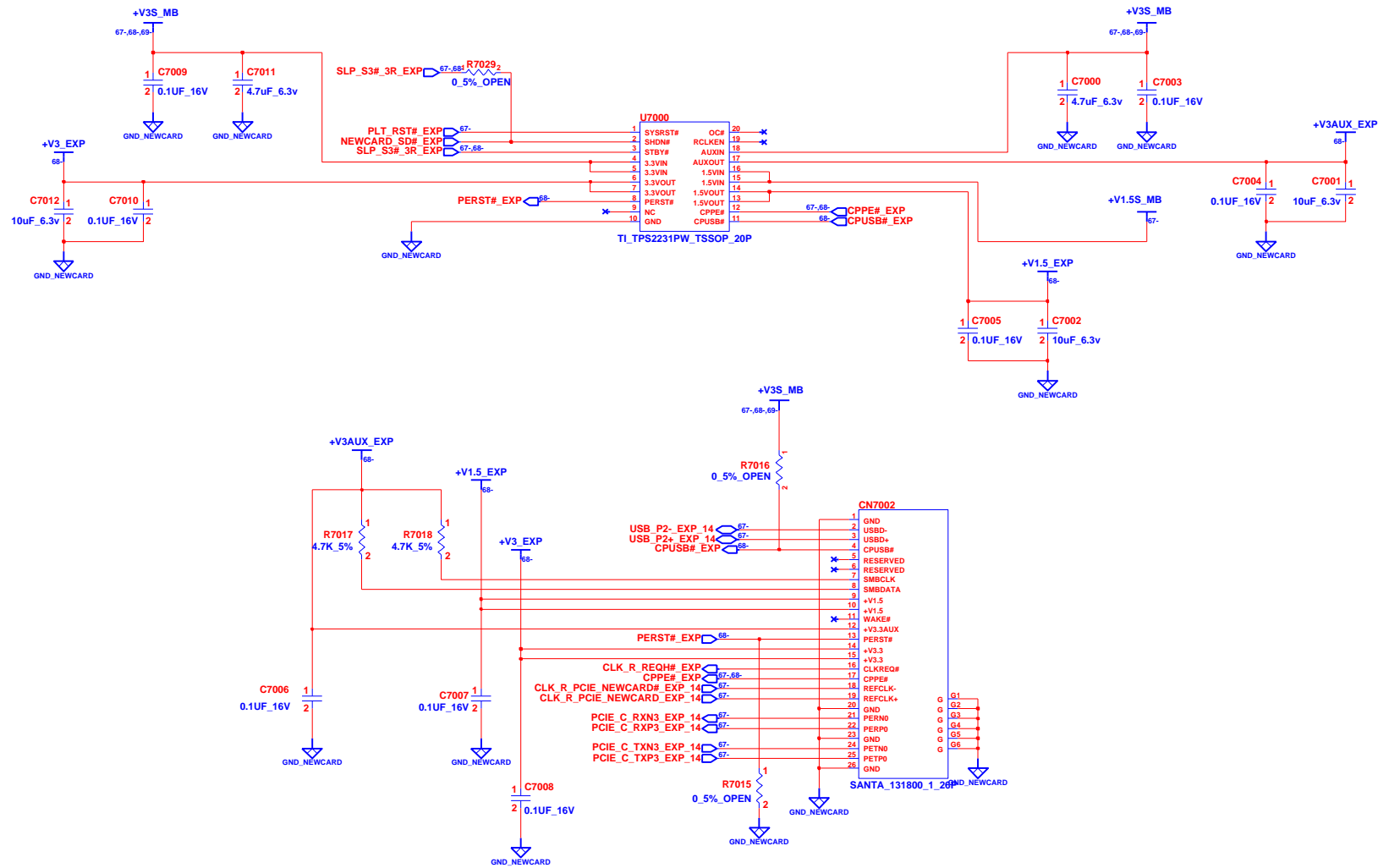


INVENTEC			
TITLE DD08 MDC CONN(Ver 1.5)			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No.</i>	REV AXI
CHANGE by EDI CHEN		26-Oct-2007	
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- | | |
|-----------------------|------------------|
| 1 GND | 19 +V3S |
| 2 PCIE_C_TXP | 20 +V3A |
| 3 PCIE_C_TXN | 21 PLT_RST# |
| 4 GND | 22 +V1.5S |
| 5 PCIE_C_RXP | 23 +V1.5S |
| 6 PCIE_C_RXN | 24 WL_LED_ALL# |
| 7 GND | 25 ACC_LED# |
| 8 CLK_R_PCIE_NEWCARD | 26 STBY_LED# |
| 9 CLK_R_PCIE_NEWCARD# | 27 LED_3S_SATA# |
| 10 GND | 28 BAT_AMBERLED# |
| 11 USB_P+ | 29 BAT_GRNLED# |
| 12 USB_P- | 30 LED_3S_ODD# |
| 13 GND | 31 SLP_3S#_3R |
| 14 CLK_R_REQ# | 32 +V3S |
| 15 RTCBAT | 33 +V3S |
| 16 TOUCHPAD DATA | 34 FPR_OFF |
| 17 TOUCHPAD CLK | 35 USB_P8+ |
| 18 +V5S | 36 USB_P8- |





EXPRESS CARD DAUGHTER BOARD

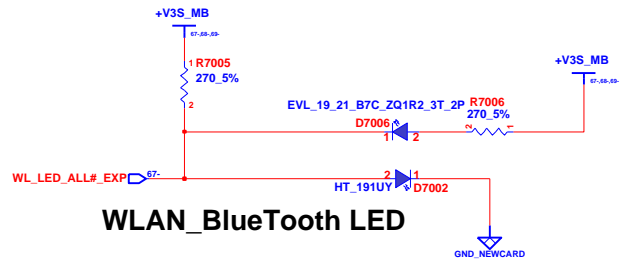
INVENTEC

TITLE			
DD08			
NEW CARD CNTR			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

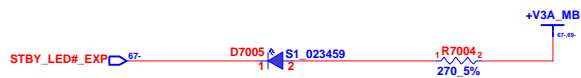
CHANGE by EDI CHEN 26-Oct-2007

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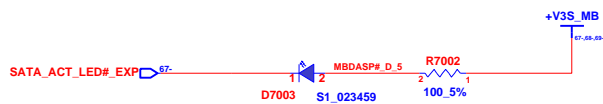
EXPRESS CARD DAUGHTER BOARD



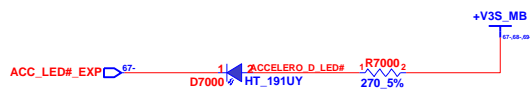
WLAN_BlueTooth LED



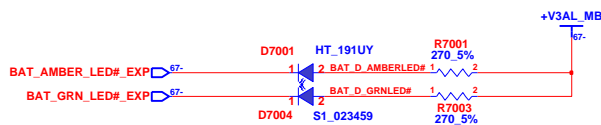
Power / Standby LED



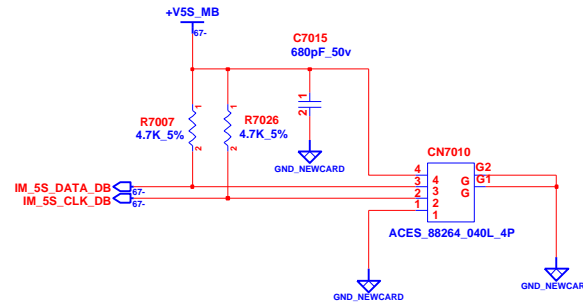
HDD & ODD LED



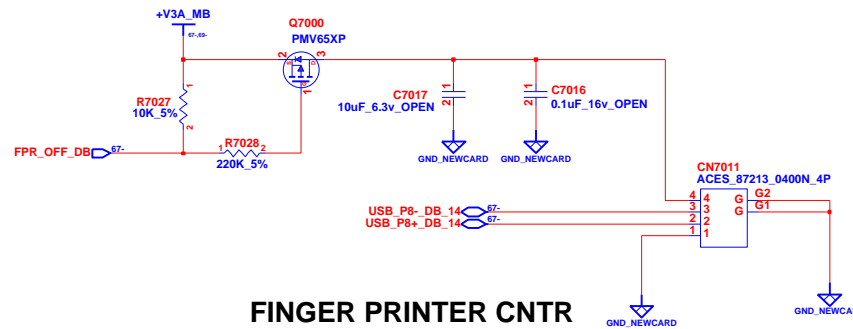
Accelero-Meter LED



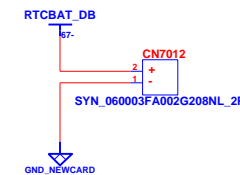
Battery-Charge LED



TOUCH PAD CNTR



FINGER PRINTER CNTR



RTC CNTR

INVENTEC			
TITLE DD08 LED SIGNAL			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AXI
CHANGE by EDI CHEN		26-Oct-2007	
SHEET 69		OF 69	