

# VULCAIN Discrete

## SI Build

2008.12.26

DATE	CHANGE NO.	REV

DRAWER	EE	DATE	POWER	DATE	<b>INVENTEC</b>			
DESIGN								
CHECK								
RESPONSIBLE								
TITLE					VV Discrete			
SIZE =	3	VER :						
FILE NAME :	XXXX XXXXXX-XX				SIZE	CODE	DOC NUMBER	REV
P/N	XXXXXXXXXXXX				A3	CS	J310A22294-0-MTR	A01
					SHEET	1	OF	54

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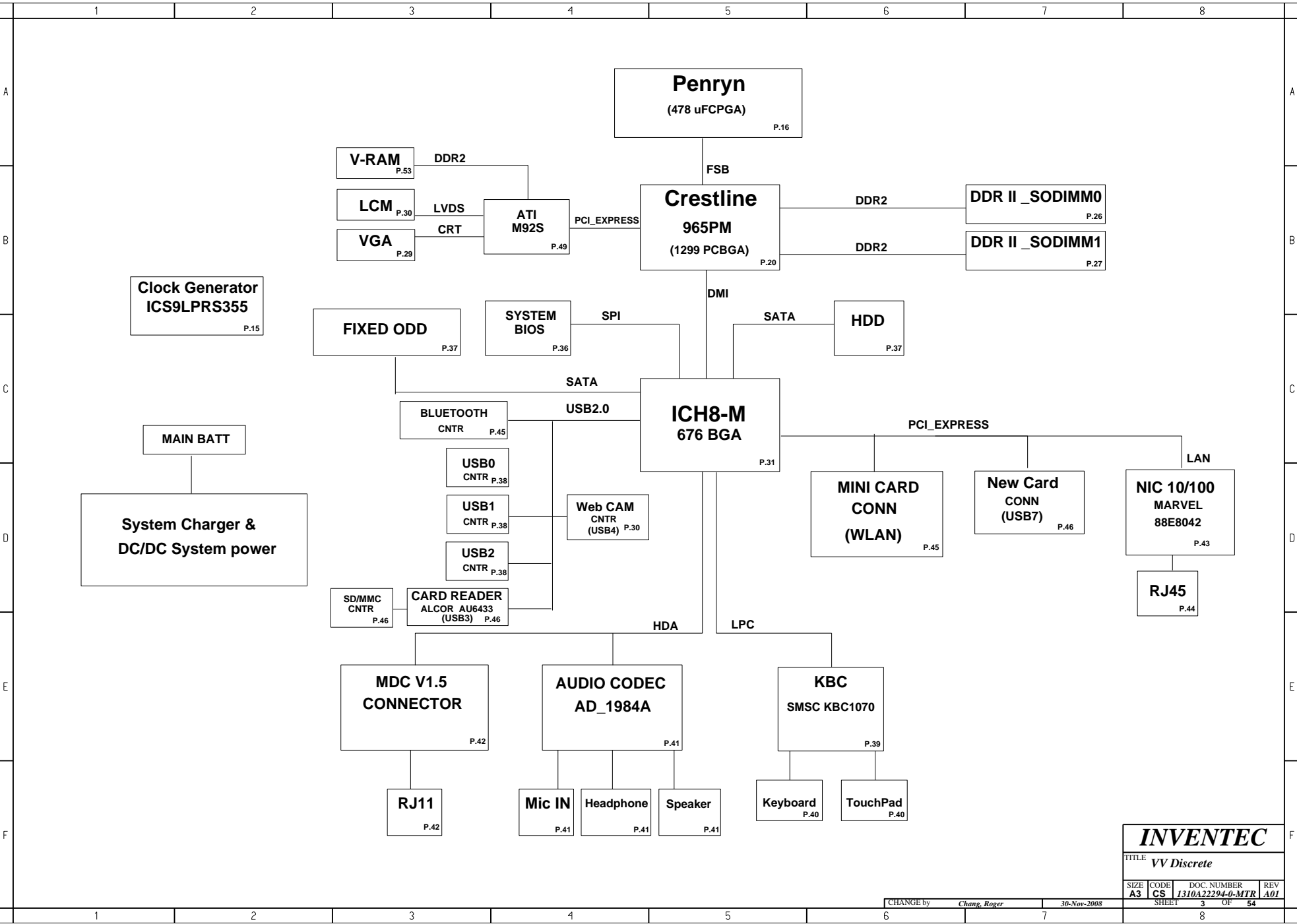
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	33- ICH8-3	54- VIDEO RAM-2
	34- ICH8-4	
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**INVENTEC**

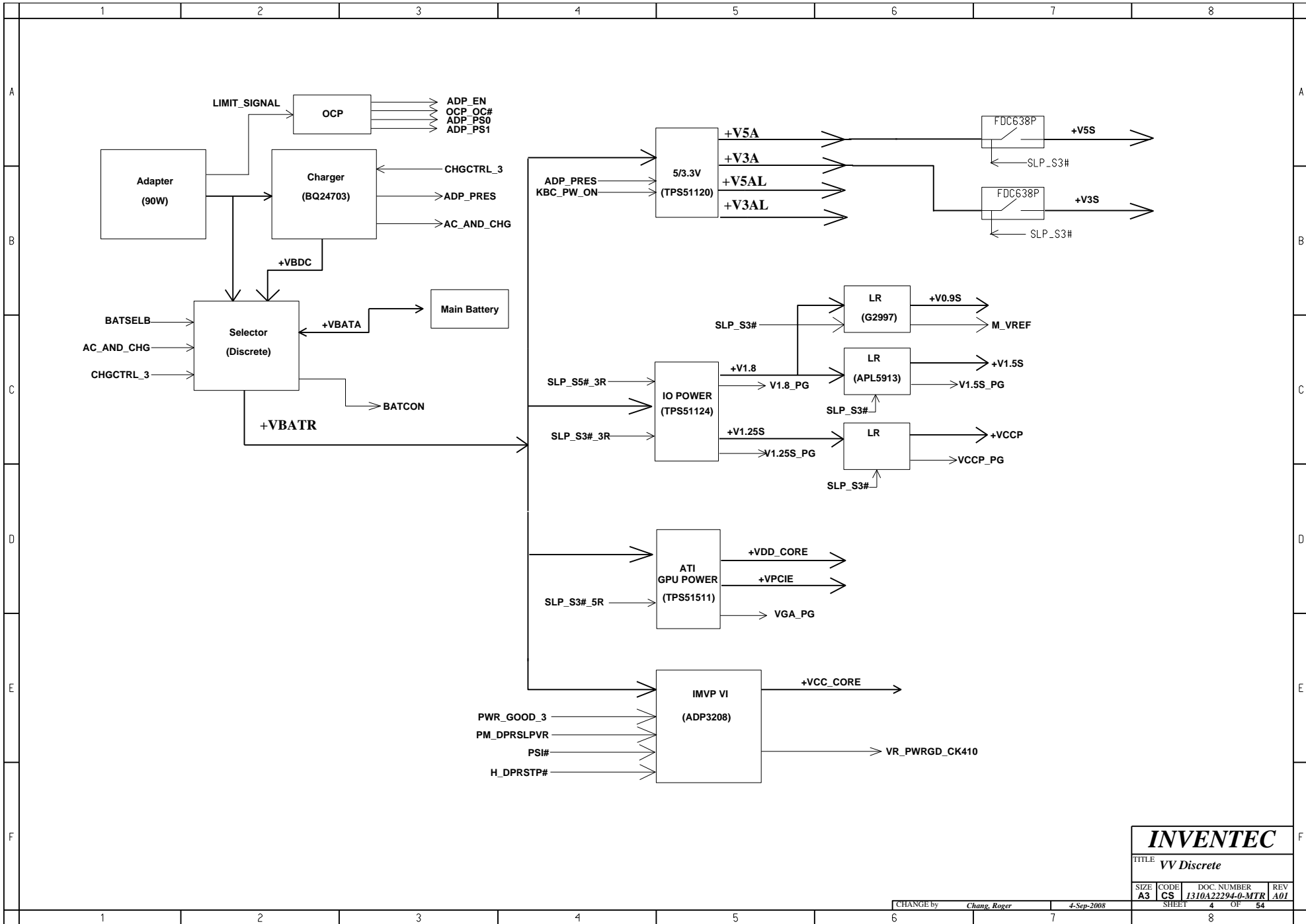
TITLE *VV Discrete*

SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <i>1310A22294-0-MTR</i>	REV <b>A01</b>
SHEET		2	OF 54

CHANGE by *Chang, Roger* 30-Nov-2008

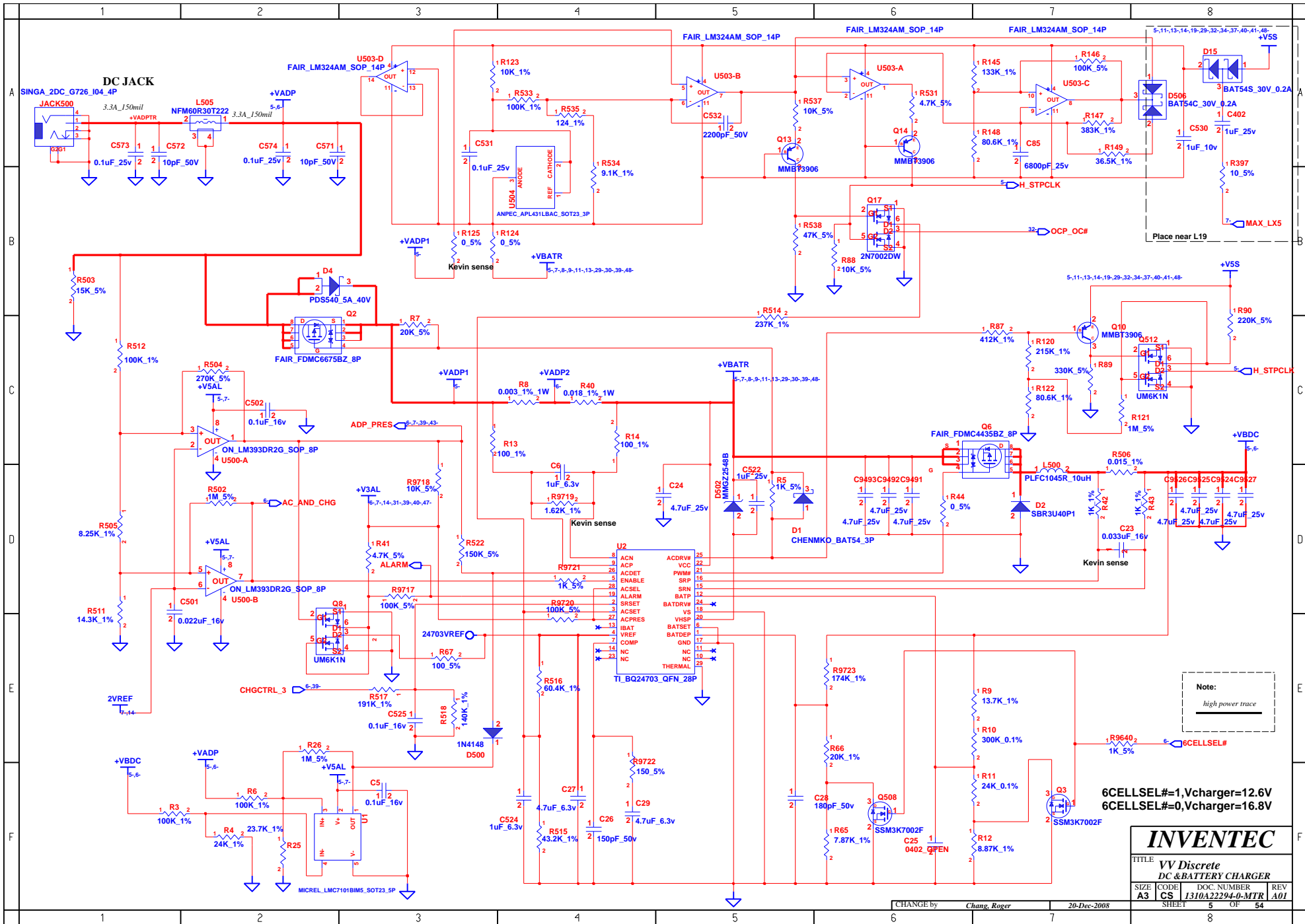


<b>INVENTEC</b>			
TITLE <i>VV Discrete</i>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <i>1310A22294-0-MTR</i>	REV <b>A01</b>
CHANGE by <i>Chang, Roger</i>		30-Nov-2008	
SHEET <b>3</b>		OF <b>54</b>	



<b>INVENTEC</b>			
TITLE <b>VV Discrete</b>			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22294-0-MTR	A01
CHANGE by		SHEET	OF
Chang, Roger		4	54

4-Sep-2008

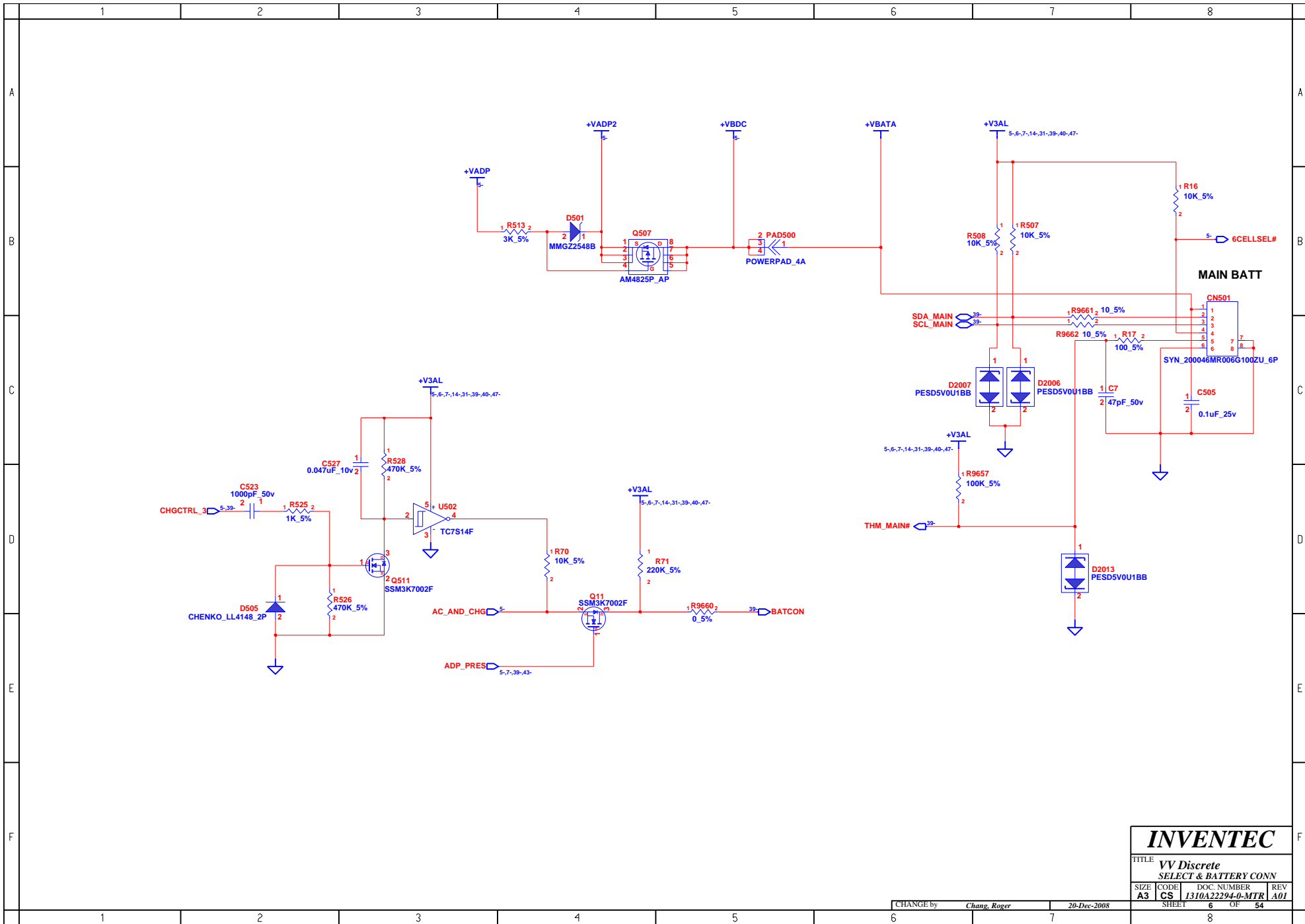


6CELLSEL#=1,Vcharger=12.6V  
6CELLSEL#=0,Vcharger=16.8V

**INVENTEC**

TITLE				VV Discrete DC & BATTERY CHARGER			
SIZE	CODE	DOC NUMBER	REV				
A3	CS	1310A22294-0-MTR	A01				
SHEET		5		OF		54	

CHANGE by Chang, Roger, 20-Dec-2008

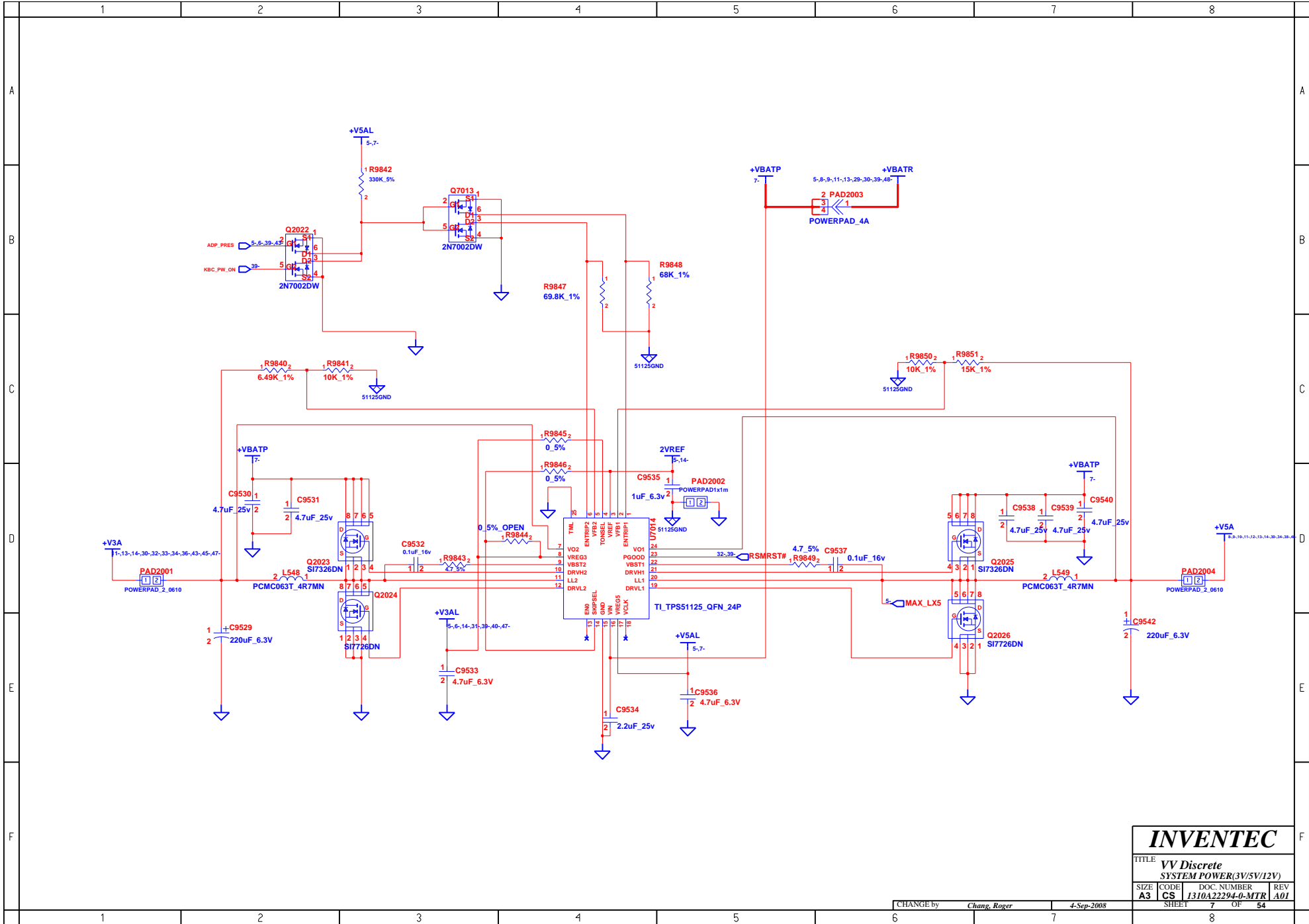


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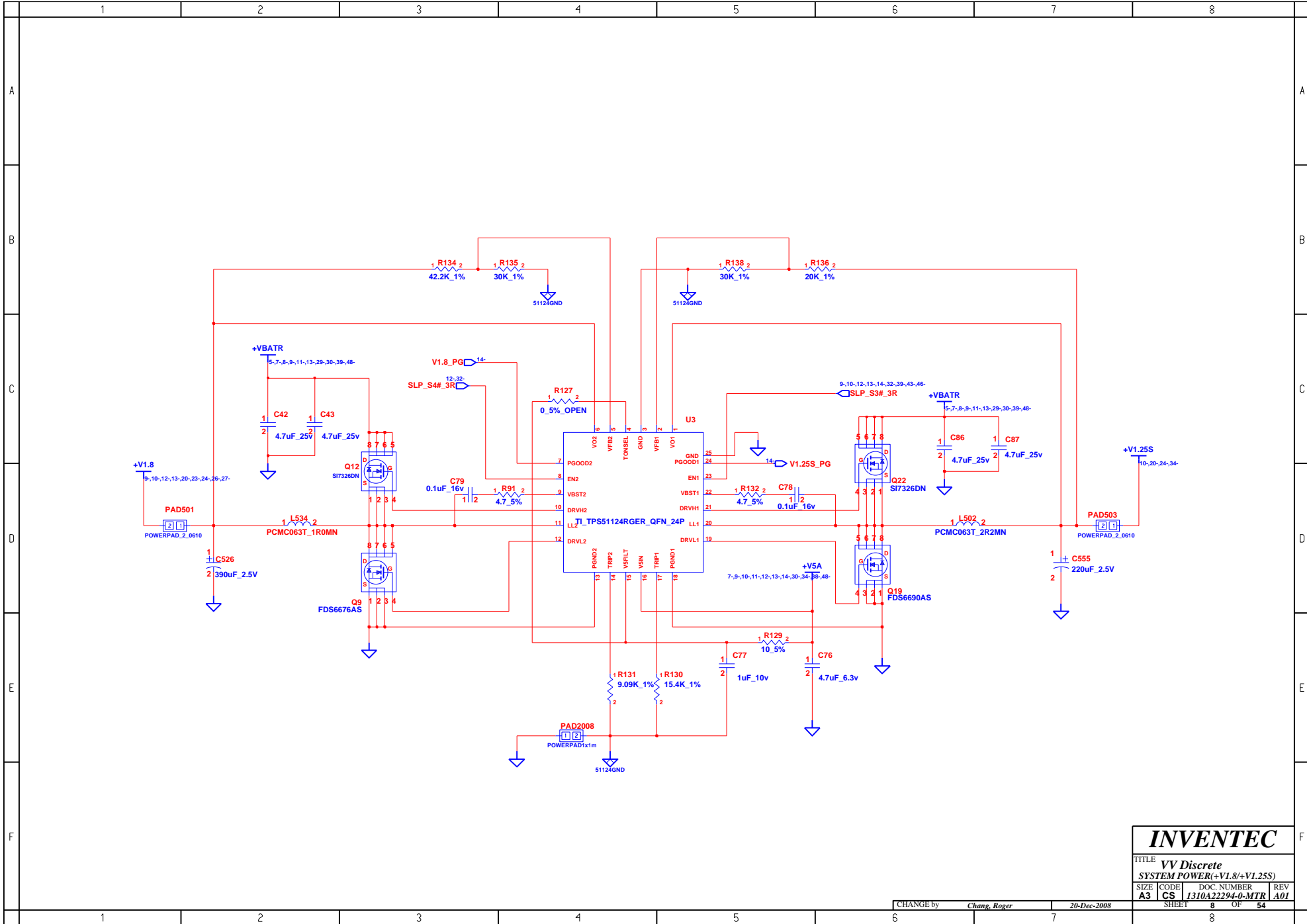
TITLE **VV Discrete  
SELECT & BATTERY CONN**

SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A22294-0-MTR	A01

CHANGE by	Chang, Roger	20-Dec-2008
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<b>INVENTEC</b>			
TITLE <b>VV Discrete SYSTEM POWER(3V/5V/12V)</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC NUMBER <b>1310A22294-0-MTR</b>	REV <b>A01</b>
CHANGE by <i>Chang, Roger</i>		4-Sep-2008	
SHEET <b>7</b>		OF <b>54</b>	



# INVENTEC

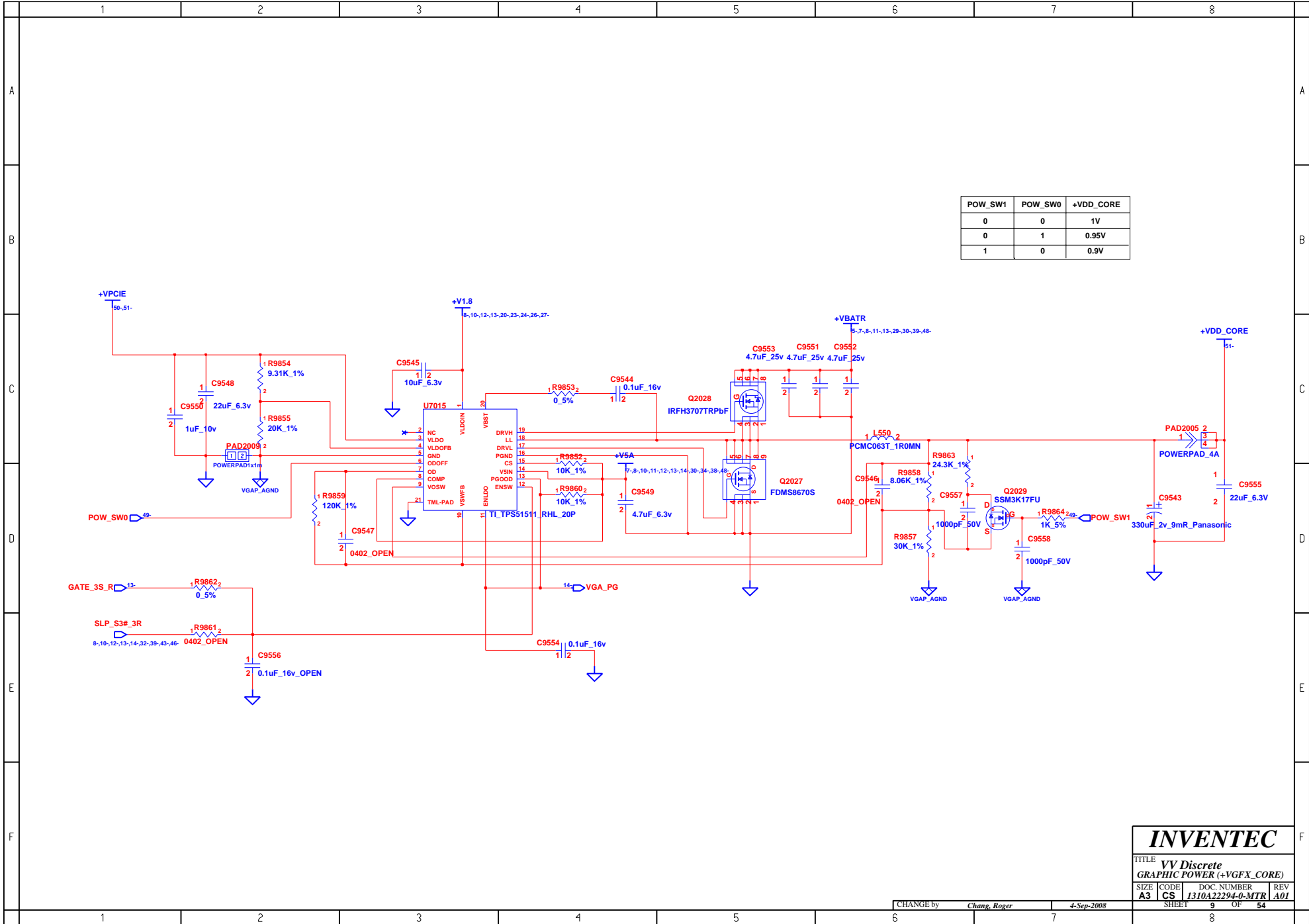
TITLE **VV Discrete**  
**SYSTEM POWER(+V1.8/+V1.25S)**

SIZE <b>A3</b>	CODE <b>CS</b>	DOC NUMBER <b>1310A22294-0-MTR</b>	REV <b>A01</b>
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CHANGE by *Chang, Roger* 20-Dec-2008

SHEET <b>8</b>	OF <b>54</b>
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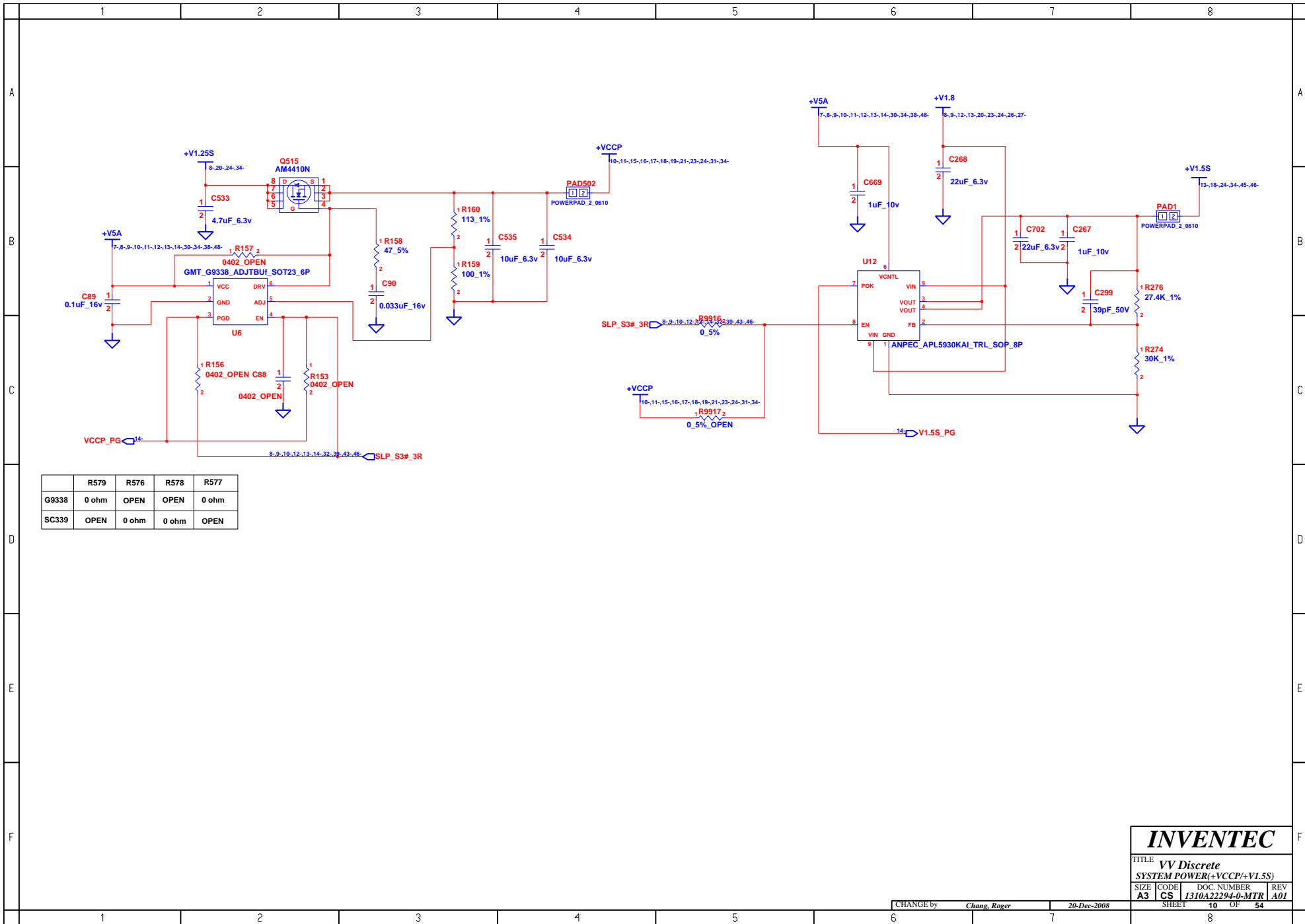
POW_SW1	POW_SW0	+VDD_CORE
0	0	1V
0	1	0.95V
1	0	0.9V

**INVENTEC**

TITLE *VV Discrete*  
*GRAPHIC POWER (+VGF\_X\_CORE)*

SIZE <b>A3</b>	CODE <b>CS</b>	DOC NUMBER <i>1310A22294-0-MTR</i>	REV <b>A01</b>
CHANGE by <i>Chang, Roger</i>		4-Sep-2008	
SHEET <b>9</b>		OF <b>54</b>	

CHANGE by *Chang, Roger* 4-Sep-2008



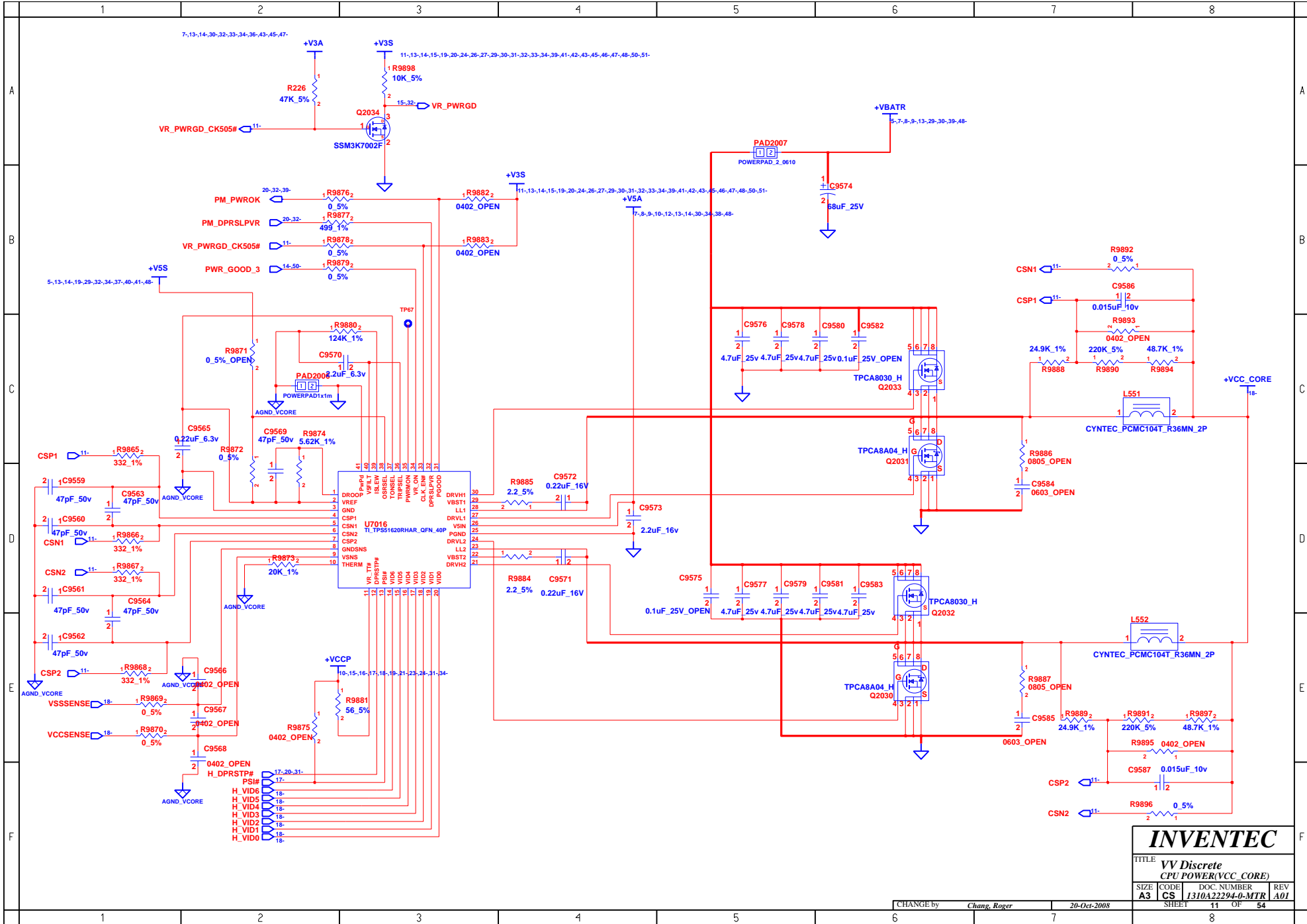
**INVENTEC**

TITLE **VV Discrete  
SYSTEM POWER(+VCCP/+V1.5S)**

SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A22294-0-MTR	A01

CHANGE by *Chang, Roger* 20-Dec-2008

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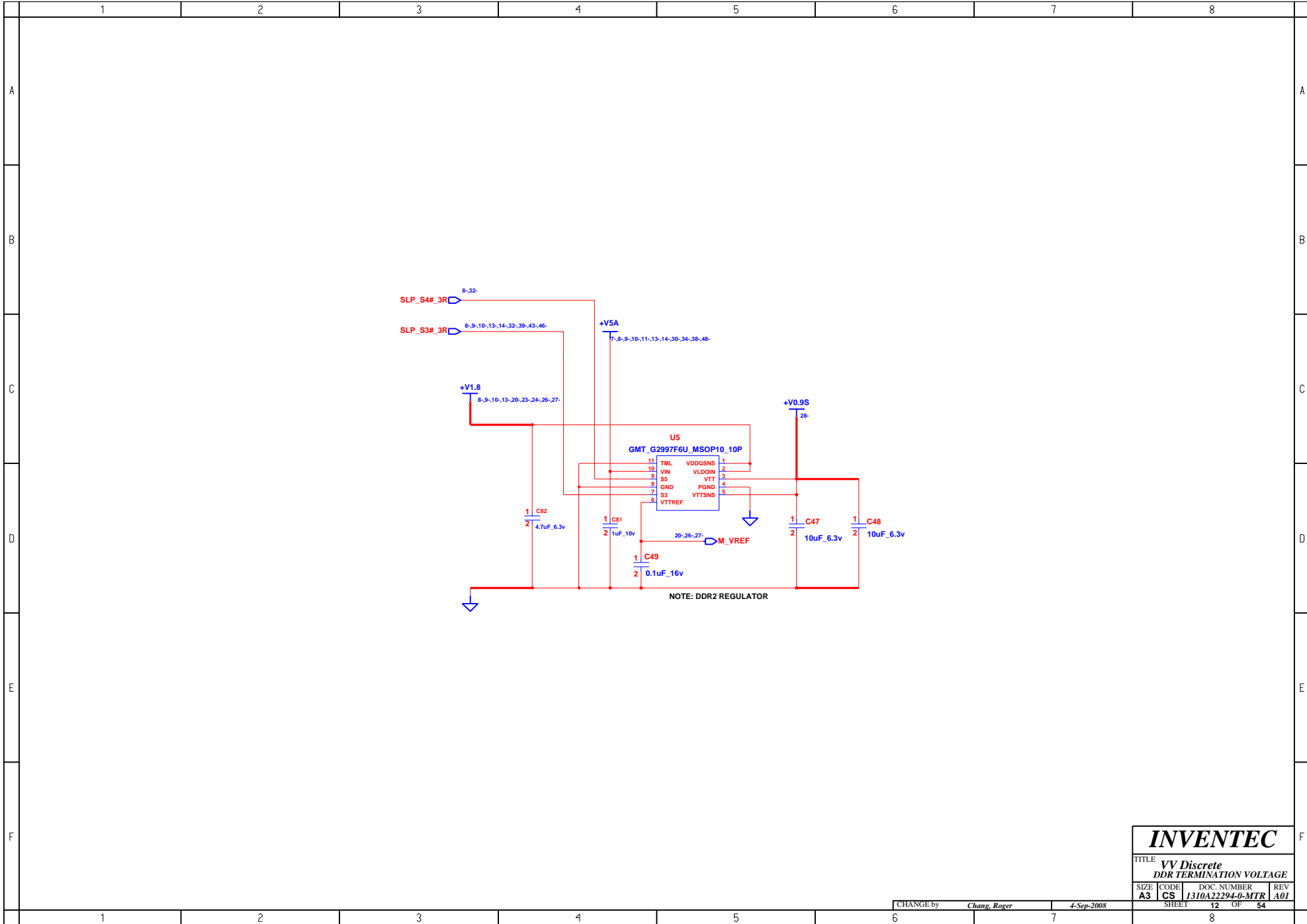


**INVENTEC**

TITLE: **VV Discrete CPU POWER(VCC\_CORE)**

SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A2294-0-MTR	A01

CHANGE by: *Chang, Roger*    20-Oct-2008    SHEET 11 OF 54



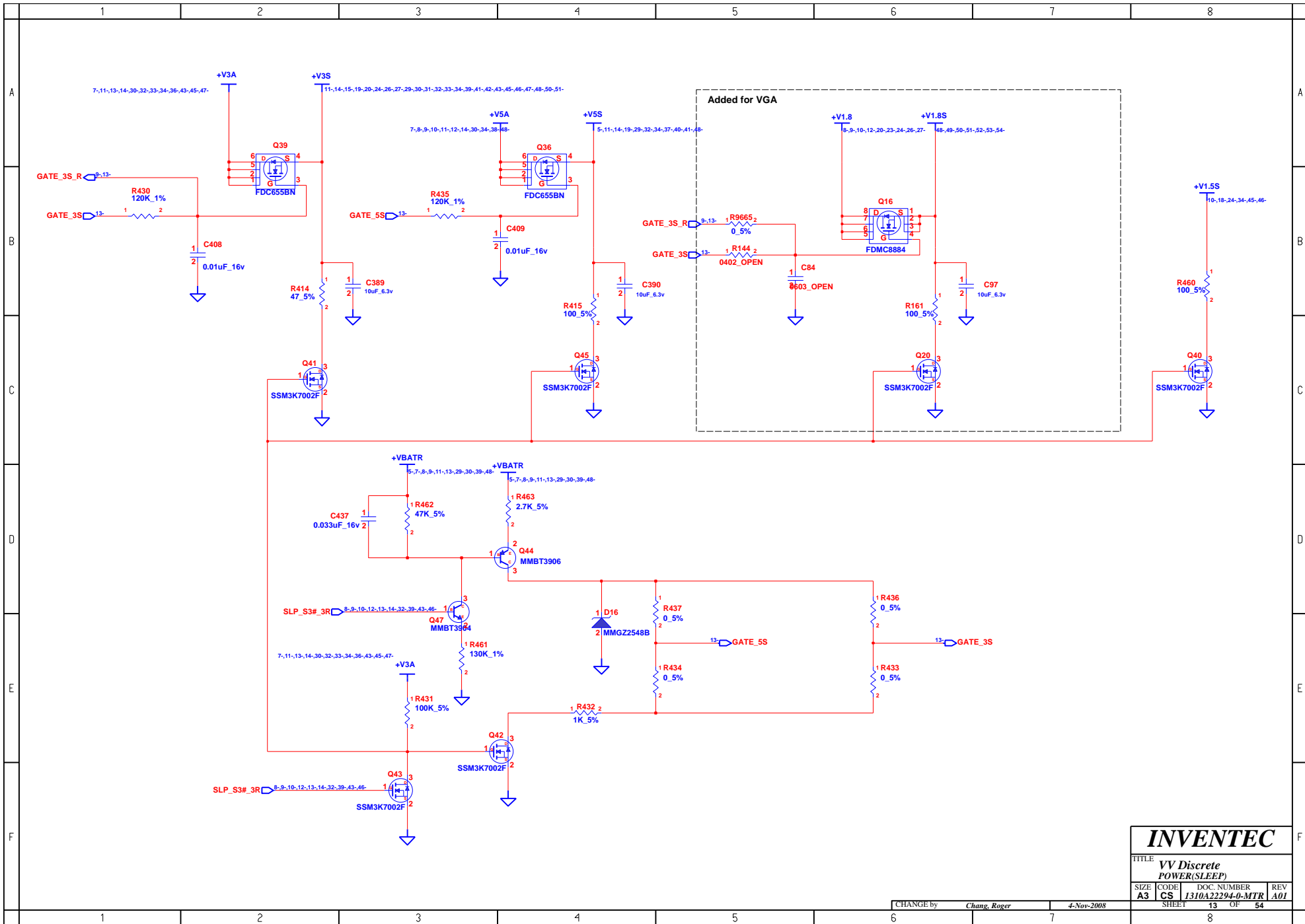
**INVENTEC**

TITLE *VV Discrete*  
**DDR TERMINATION VOLTAGE**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22294-0-MTR	A01

CHANGE by Chang, Roger 4-Sep-2008

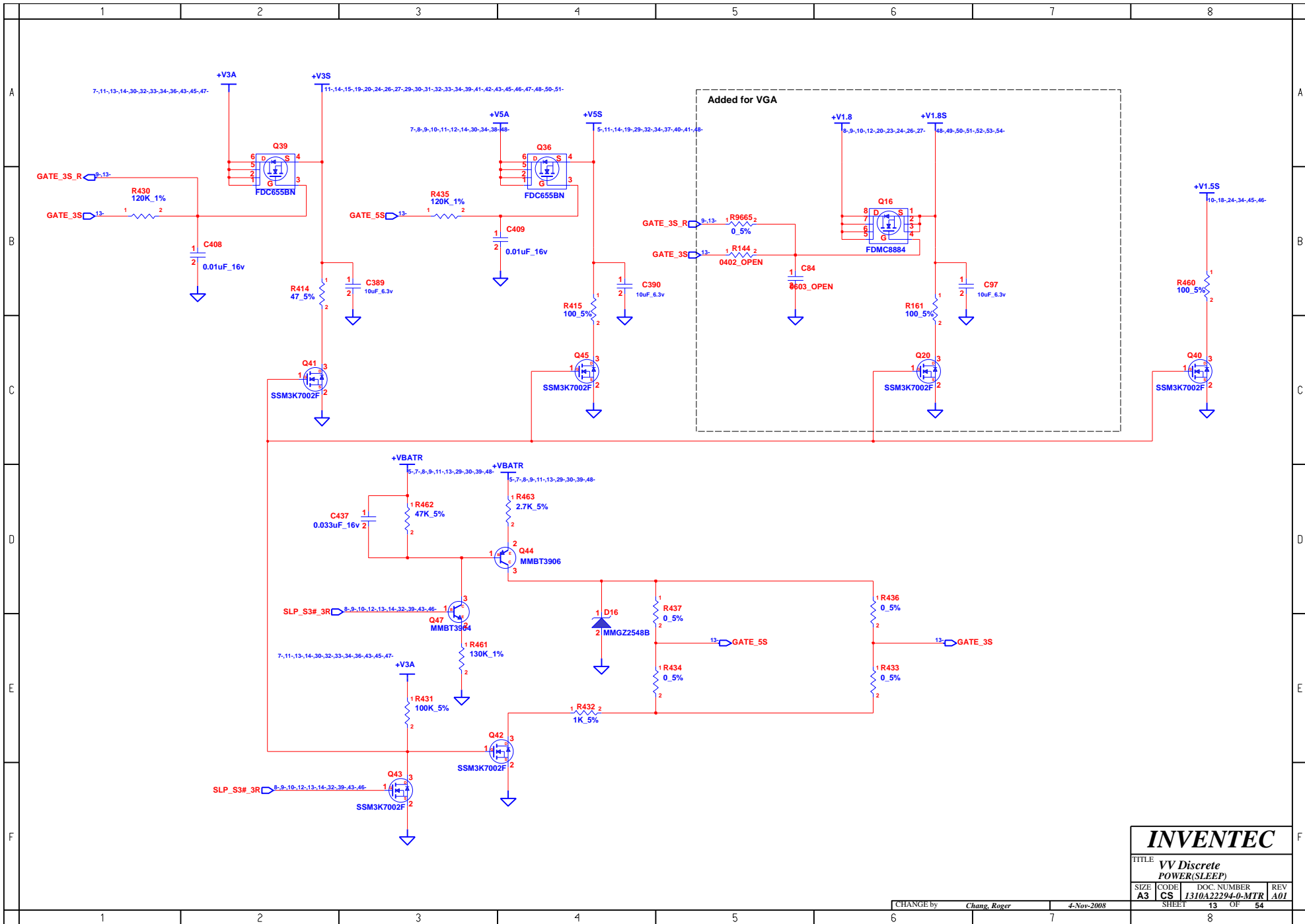
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# INVENTEC

TITLE			
VV Discrete POWER(SLEEP)			
SIZE	CODE	DOC NUMBER	REV
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13		54	

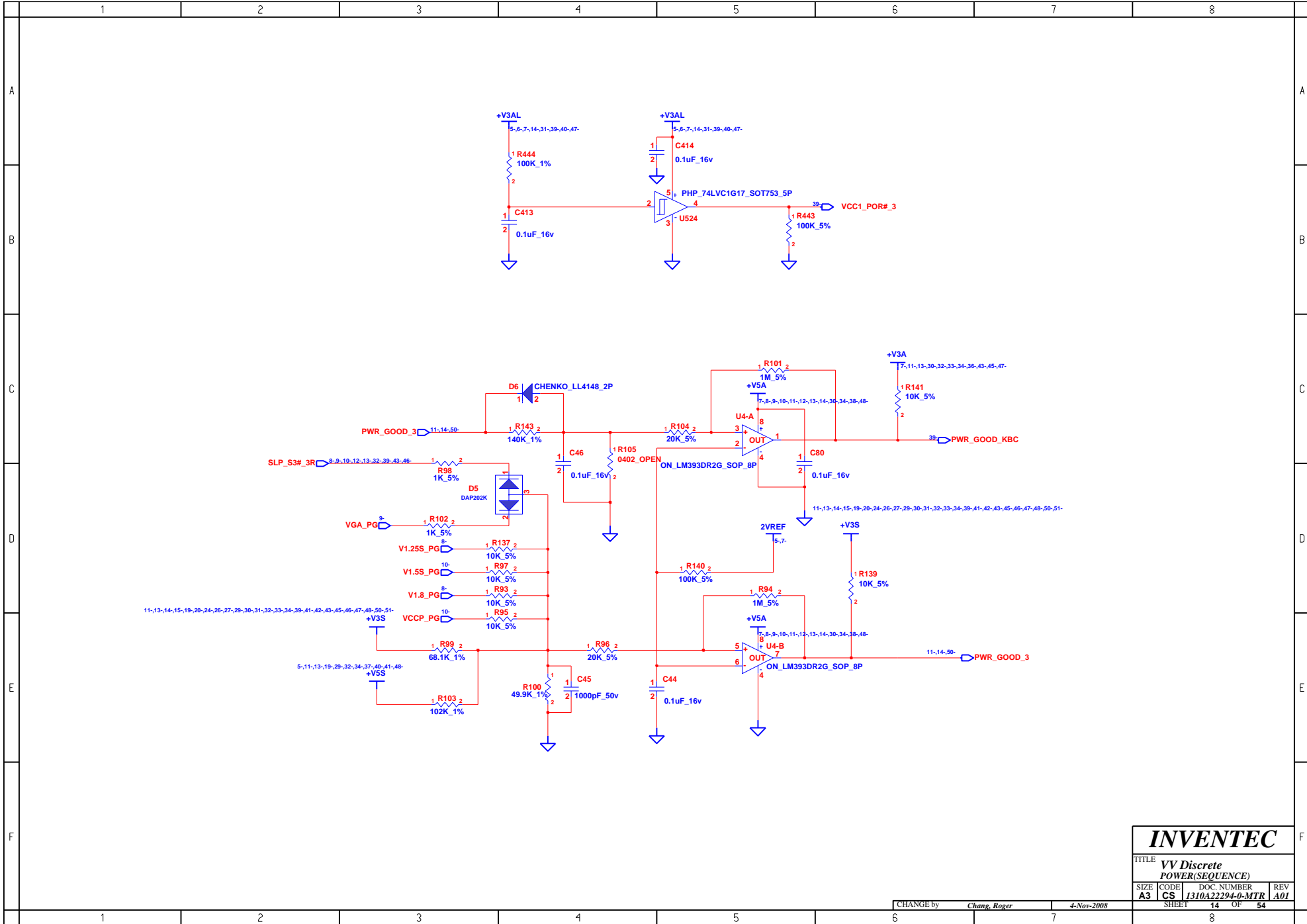
CHANGE by *Chang, Roger* 4-Nov-2008



# INVENTEC

TITLE			
VV Discrete POWER(SLEEP)			
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SHEET		OF	
13		54	

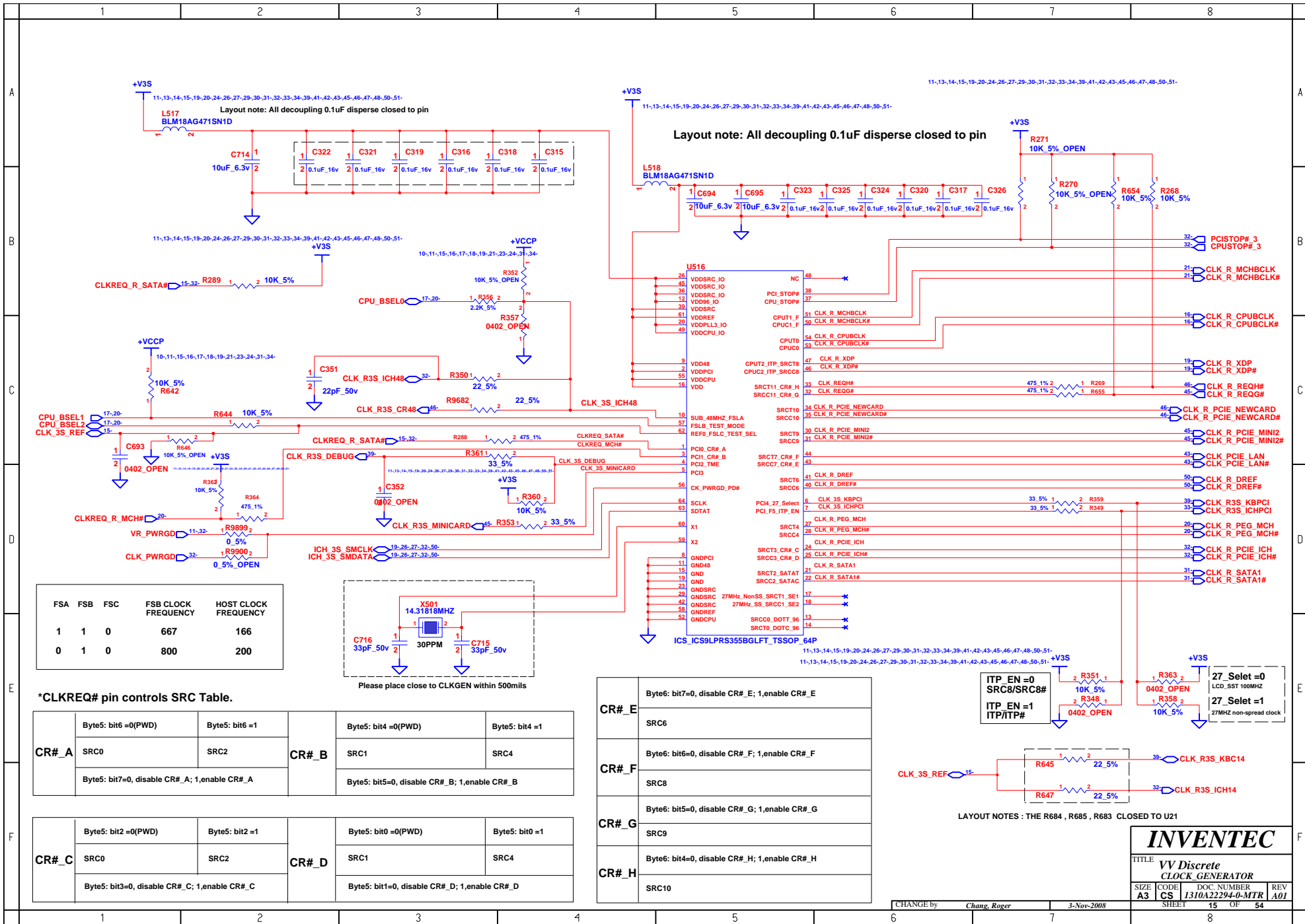
CHANGE by *Chang, Roger* 4-Nov-2008



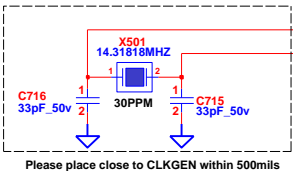
# INVENTEC

TITLE			
VV Discrete POWER(SEQUENCE)			
SIZE	CODE	DOC NUMBER	REV
A3	CS	I310A22294-0-MTR	A01
SHEET		OF	
14		54	

CHANGE by *Chang, Roger* 4-Nov-2008



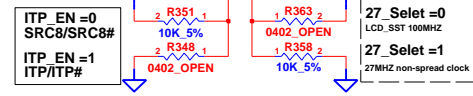
FSA	FSB	FSC	FSB CLOCK FREQUENCY	HOST CLOCK FREQUENCY
1	1	0	667	166
0	1	0	800	200



\*CLKREQ# pin controls SRC Table.

CR#_A	Byte5: bit6 =0(PWD)		Byte5: bit6 =1		CR#_B	Byte5: bit4 =0(PWD)		Byte5: bit4 =1	
	SRC0	SRC2	SRC1	SRC4		SRC1	SRC4		
Byte5: bit7=0, disable CR#_A; 1,enable CR#_A		Byte5: bit5=0, disable CR#_B; 1,enable CR#_B		CR#_C		Byte5: bit2 =0(PWD)		Byte5: bit2 =1	
SRC0	SRC2	SRC1	SRC4	CR#_D		Byte5: bit0 =0(PWD)		Byte5: bit0 =1	
Byte5: bit3=0, disable CR#_C; 1,enable CR#_C		Byte5: bit1=0, disable CR#_D; 1,enable CR#_D		CR#_E		Byte5: bit7=0, disable CR#_E; 1,enable CR#_E		CR#_F	
Byte5: bit3=0, disable CR#_C; 1,enable CR#_C		Byte5: bit1=0, disable CR#_D; 1,enable CR#_D		CR#_G		Byte5: bit6=0, disable CR#_F; 1,enable CR#_F		CR#_H	
Byte5: bit3=0, disable CR#_C; 1,enable CR#_C		Byte5: bit1=0, disable CR#_D; 1,enable CR#_D		CR#_I		Byte5: bit5=0, disable CR#_G; 1,enable CR#_G		CR#_J	
Byte5: bit3=0, disable CR#_C; 1,enable CR#_C		Byte5: bit1=0, disable CR#_D; 1,enable CR#_D		CR#_K		Byte5: bit4=0, disable CR#_H; 1,enable CR#_H		CR#_L	
Byte5: bit3=0, disable CR#_C; 1,enable CR#_C		Byte5: bit1=0, disable CR#_D; 1,enable CR#_D		CR#_M		Byte5: bit3=0, disable CR#_I; 1,enable CR#_I		CR#_N	
Byte5: bit3=0, disable CR#_C; 1,enable CR#_C		Byte5: bit1=0, disable CR#_D; 1,enable CR#_D		CR#_O		Byte5: bit2=0, disable CR#_J; 1,enable CR#_J		CR#_P	
Byte5: bit3=0, disable CR#_C; 1,enable CR#_C		Byte5: bit1=0, disable CR#_D; 1,enable CR#_D		CR#_Q		Byte5: bit1=0, disable CR#_K; 1,enable CR#_K		CR#_R	
Byte5: bit3=0, disable CR#_C; 1,enable CR#_C		Byte5: bit1=0, disable CR#_D; 1,enable CR#_D		CR#_S		Byte5: bit0=0, disable CR#_L; 1,enable CR#_L		CR#_T	
Byte5: bit3=0, disable CR#_C; 1,enable CR#_C		Byte5: bit1=0, disable CR#_D; 1,enable CR#_D		CR#_U		Byte5: bit0=0, disable CR#_M; 1,enable CR#_M		CR#_V	
Byte5: bit3=0, disable CR#_C; 1,enable CR#_C		Byte5: bit1=0, disable CR#_D; 1,enable CR#_D		CR#_W		Byte5: bit0=0, disable CR#_N; 1,enable CR#_N		CR#_X	
Byte5: bit3=0, disable CR#_C; 1,enable CR#_C		Byte5: bit1=0, disable CR#_D; 1,enable CR#_D		CR#_Y		Byte5: bit0=0, disable CR#_O; 1,enable CR#_O		CR#_Z	

CR#_E	Byte6: bit7=0, disable CR#_E; 1,enable CR#_E
SRC6	
CR#_F	Byte6: bit6=0, disable CR#_F; 1,enable CR#_F
SRC8	
CR#_G	Byte6: bit5=0, disable CR#_G; 1,enable CR#_G
SRC9	
CR#_H	Byte6: bit4=0, disable CR#_H; 1,enable CR#_H
SRC10	



LAYOUT NOTES : THE R684 , R685 , R683 CLOSED TO U21

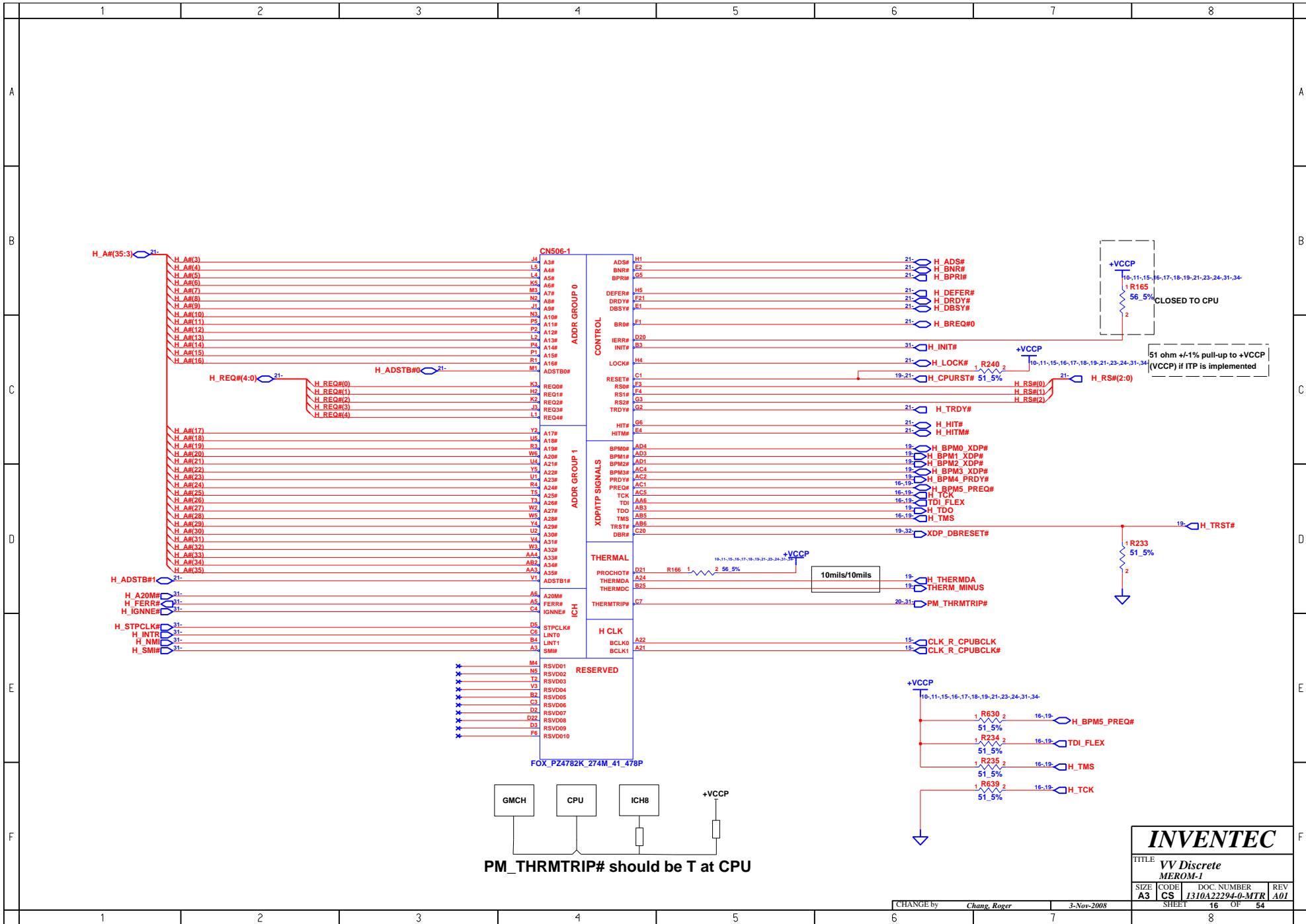
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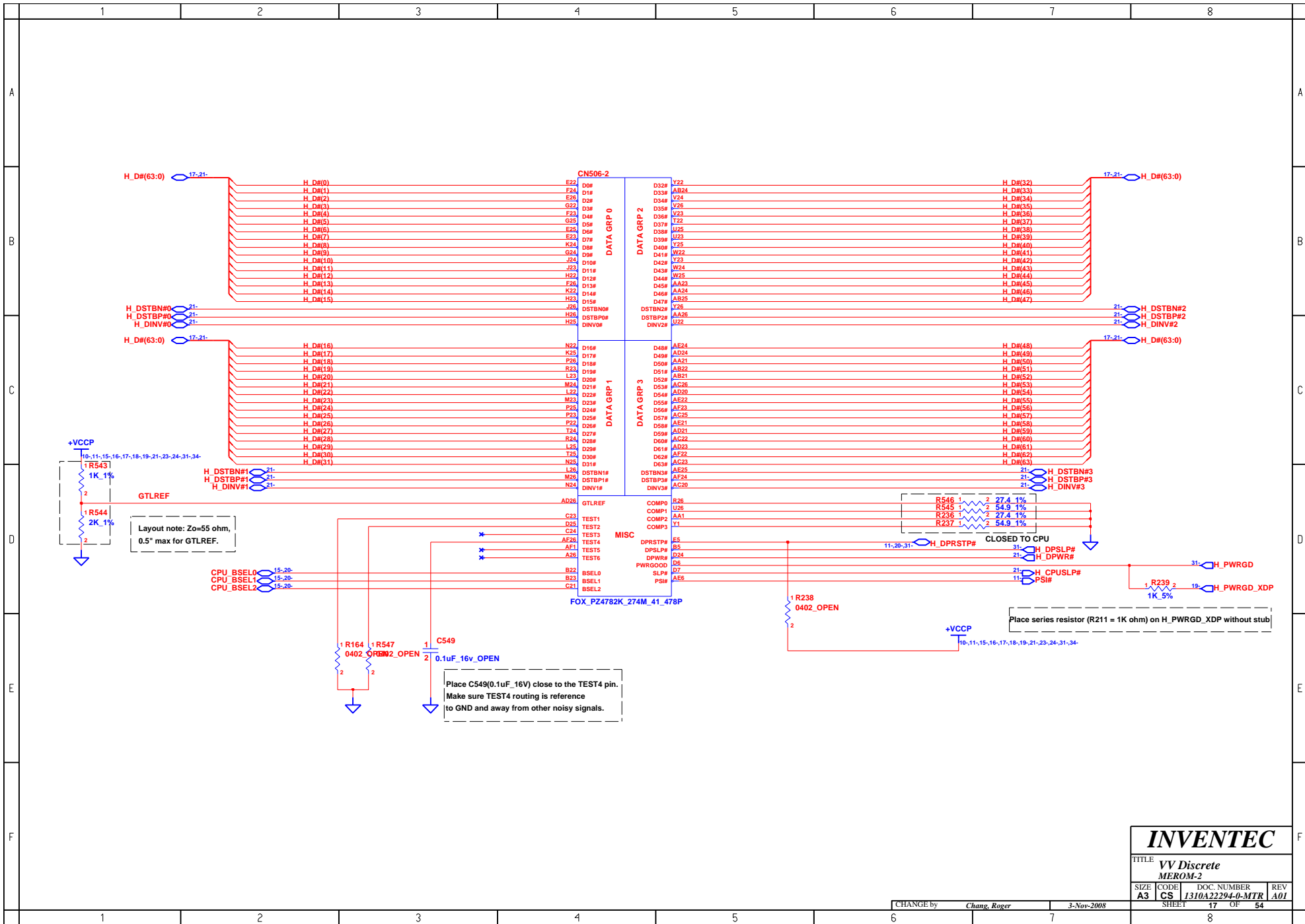
TITLE: VV Discrete CLOCK GENERATOR

SIZE: A3 CODE: CS DOC NUMBER: 1310A22294-0-MTR REV: A01

SHEET: 15 OF 54



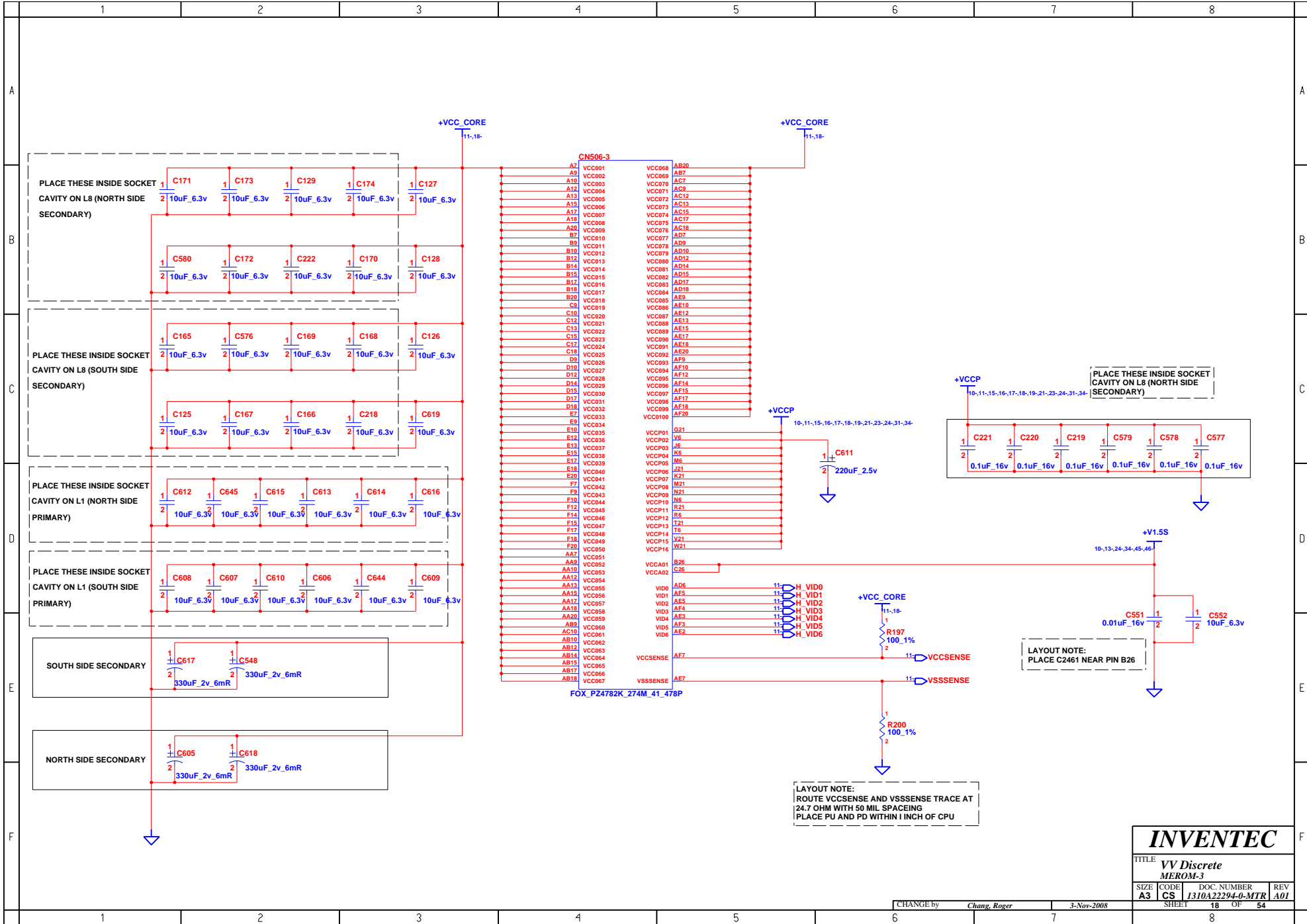


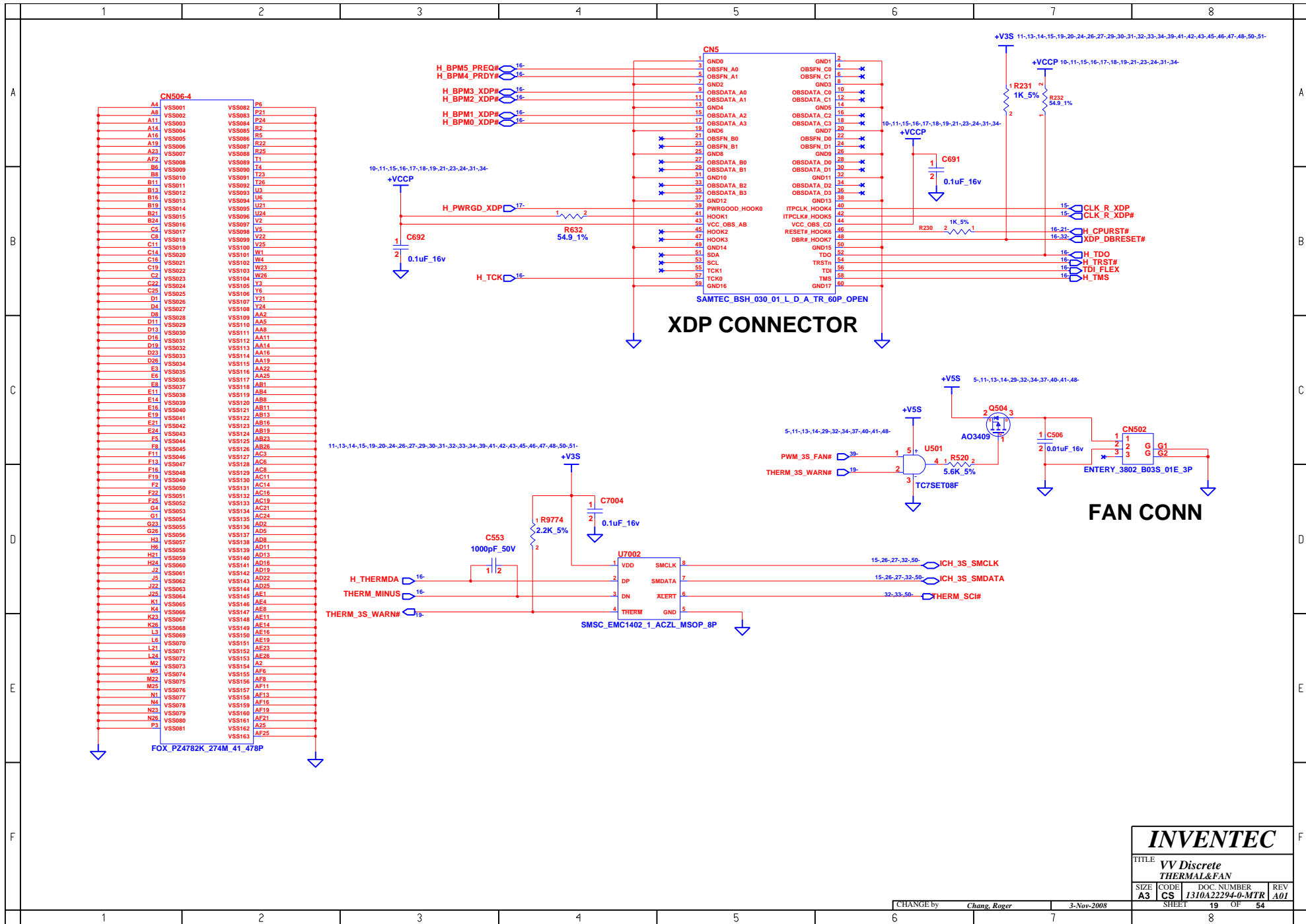


# INVENTEC

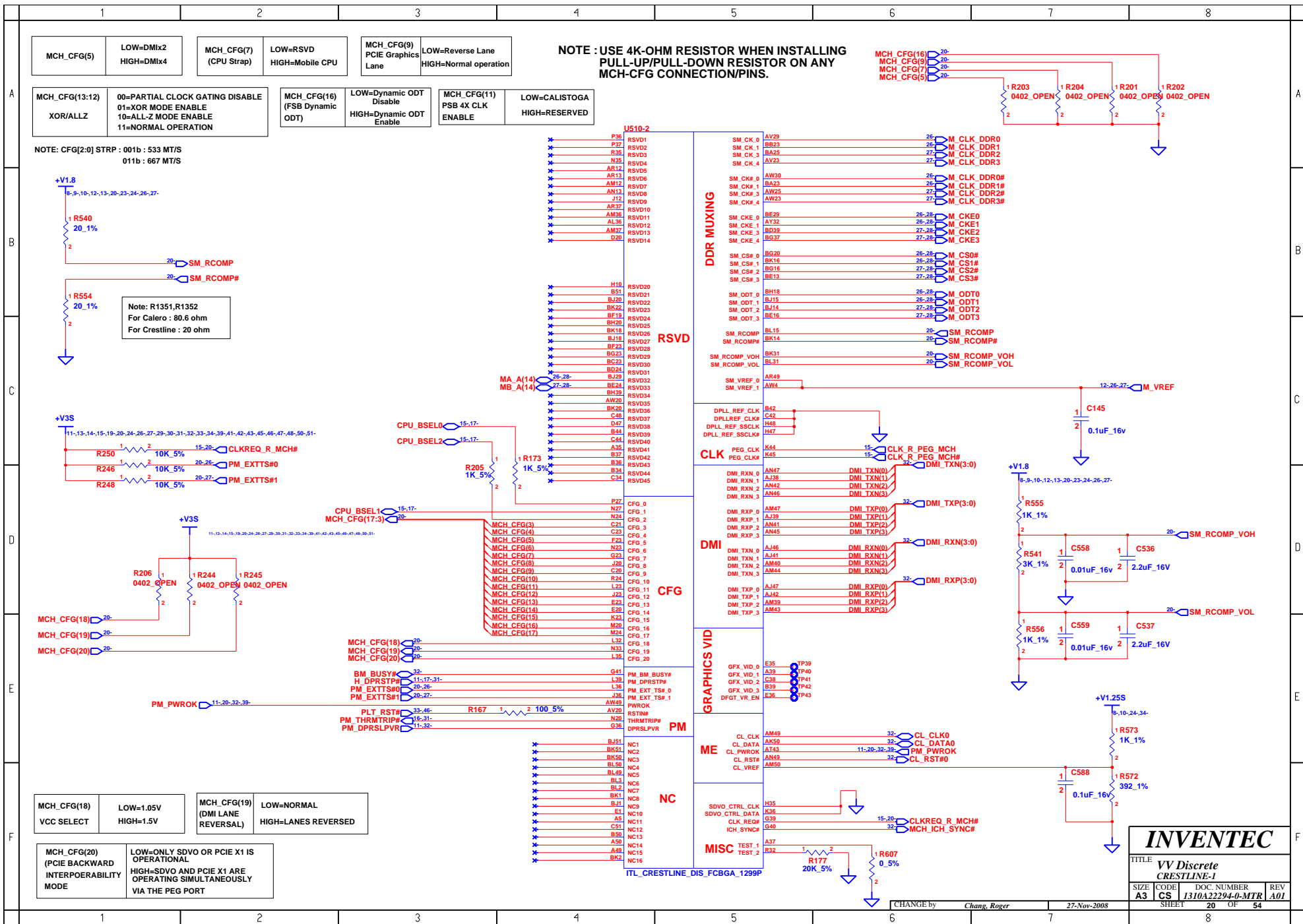
TITLE **VV Discrete MEROM-2**

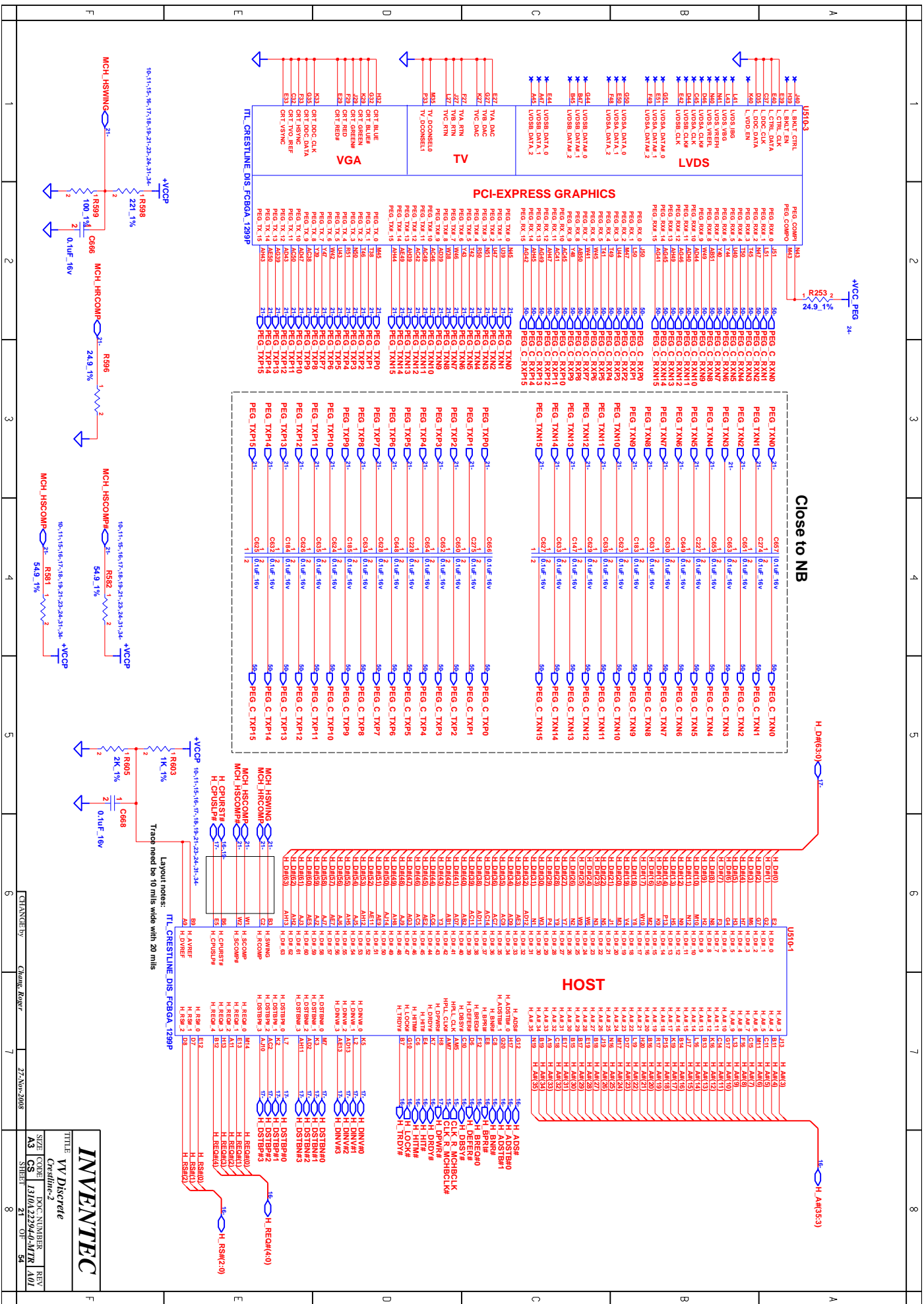
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A22294-0-MTR</b>	REV <b>A01</b>
SHEET <b>17</b>		OF <b>54</b>	





<b>INVENTEC</b>			
TITLE: VV Discrete THERMAL&FAN			
SIZE: A3	CODE: CS	DOC NUMBER: 1310A22294-0-MTR	REV: A01
CHANGE by: Chang, Roger		3-Nov-2008	
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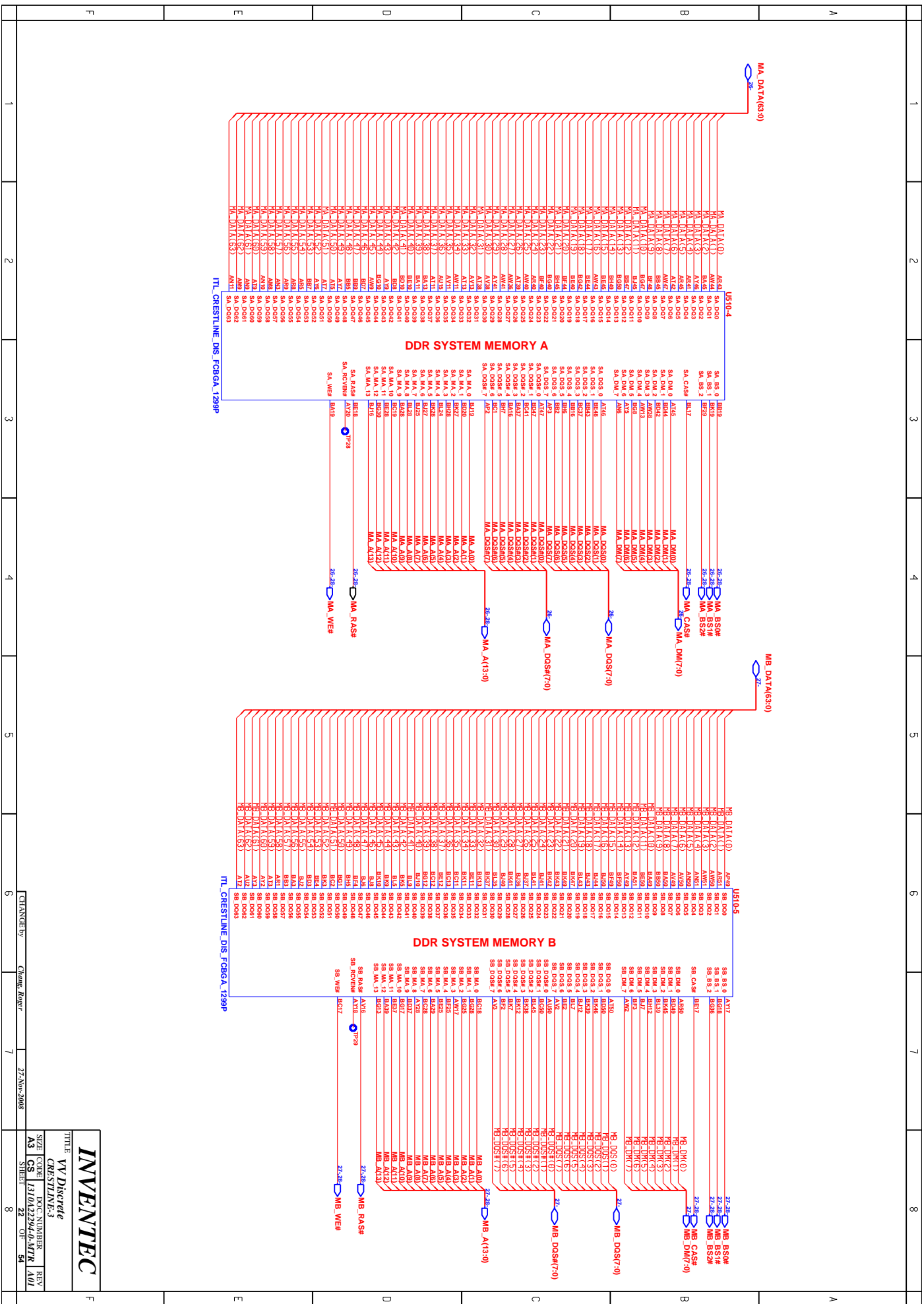
Close to NB

Layout notes:  
TTL\_CRESTLINE\_DIS\_FCBGA\_1299P  
Trace need be 10 mils wide with 20 mils

CHANGE BY: Change Range 27-Nov-2008

TITLE		VV Discrete	
SIZE	CODE	DOC NUMBER	REV
A3	CS1	1310A222942MTR	A01
SHEET		21 OF	54

**INVENTEC**



**DDR SYSTEM MEMORY A**

**DDR SYSTEM MEMORY B**

ITL\_CRESTLINE\_DIS\_FCBGA\_1299P

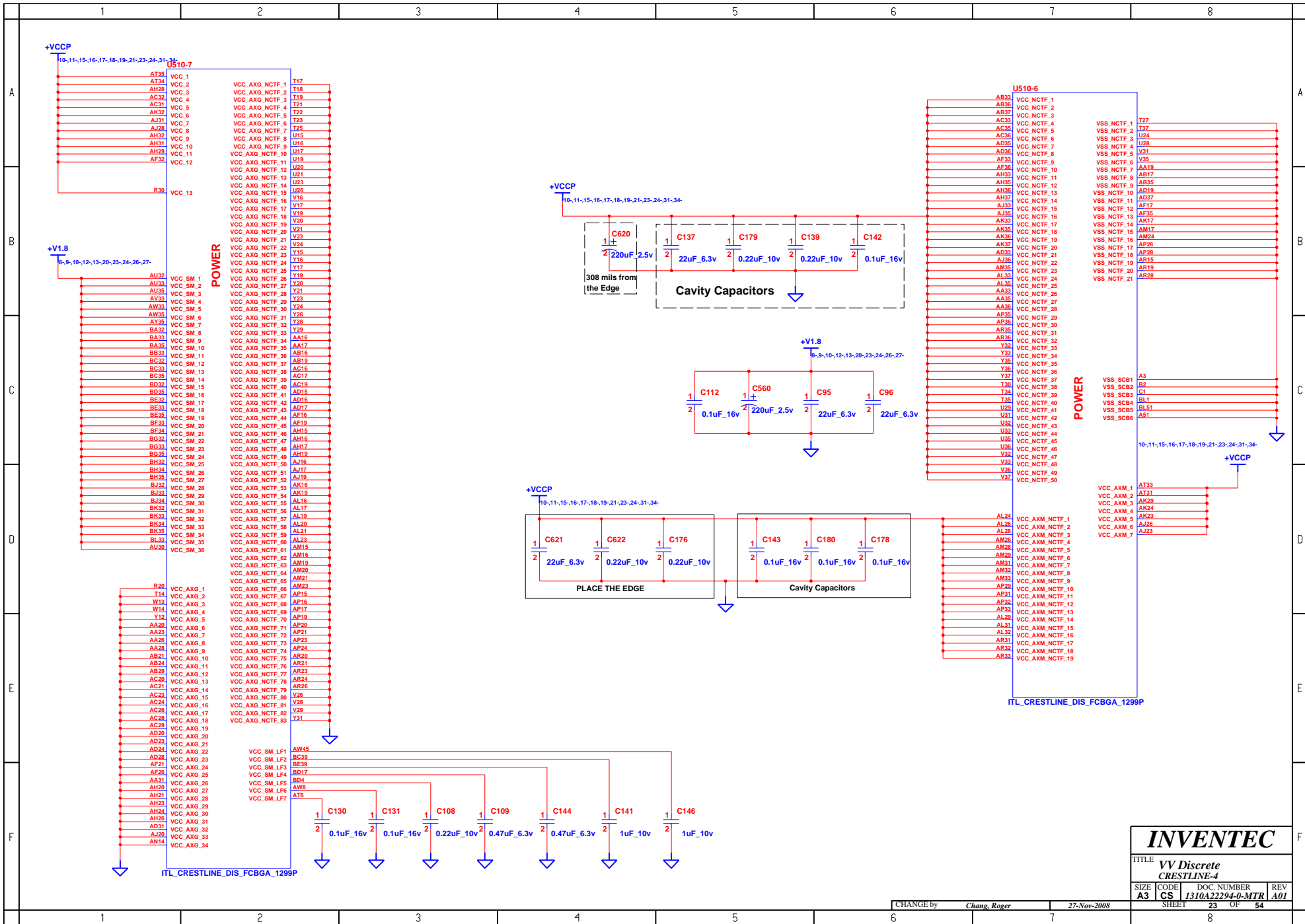
ITL\_CRESTLINE\_DIS\_FCBGA\_1299P

CHANGED BY: *Chang Rongz* 27-Nov-2008

**INVENTEC**

VV Discrete  
CRESTLINE-V3

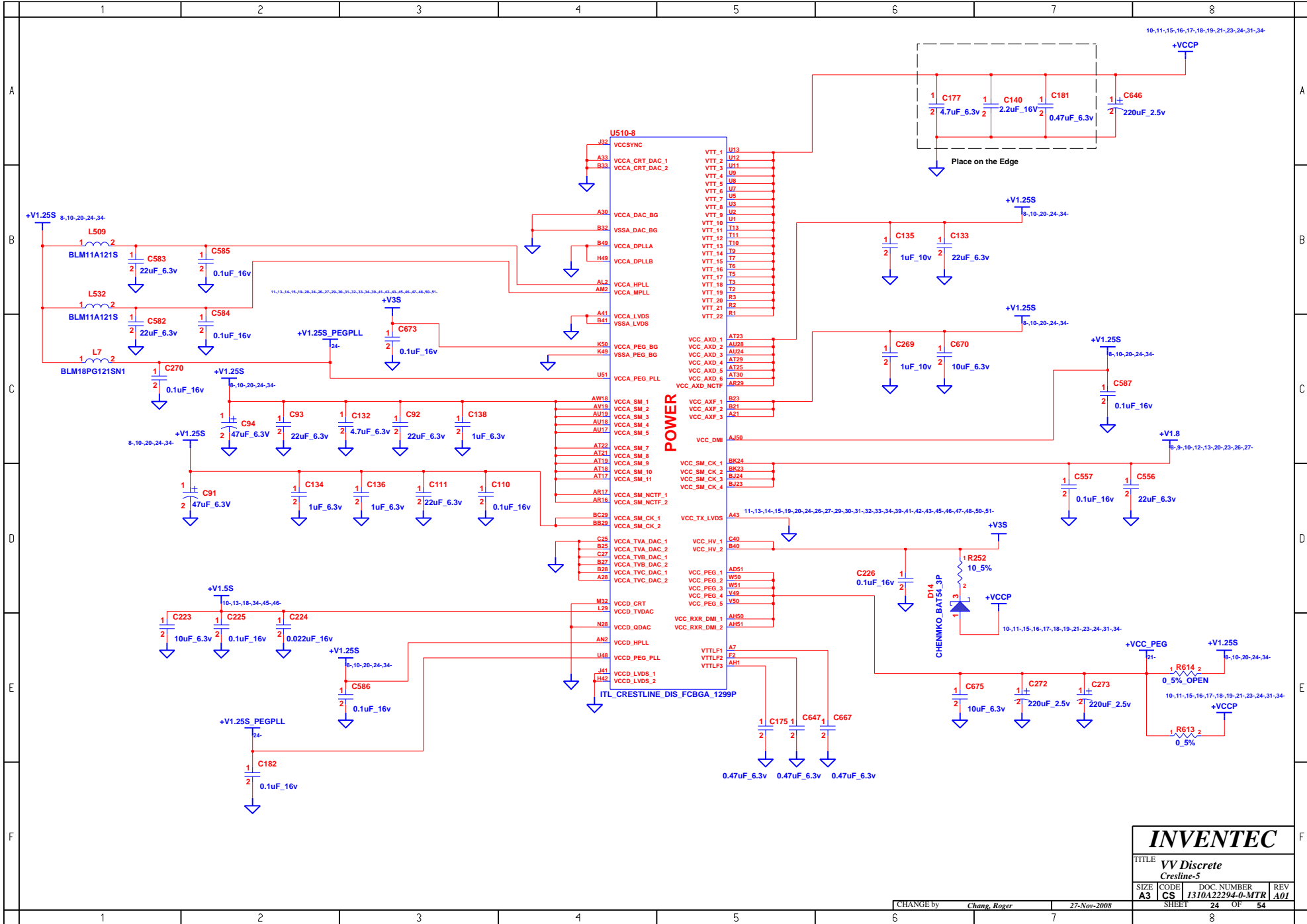
SIZE	DOC NUMBER	REV
A3	1310A2204-1MR	A01
AS	SHIELD	Z2-01-54



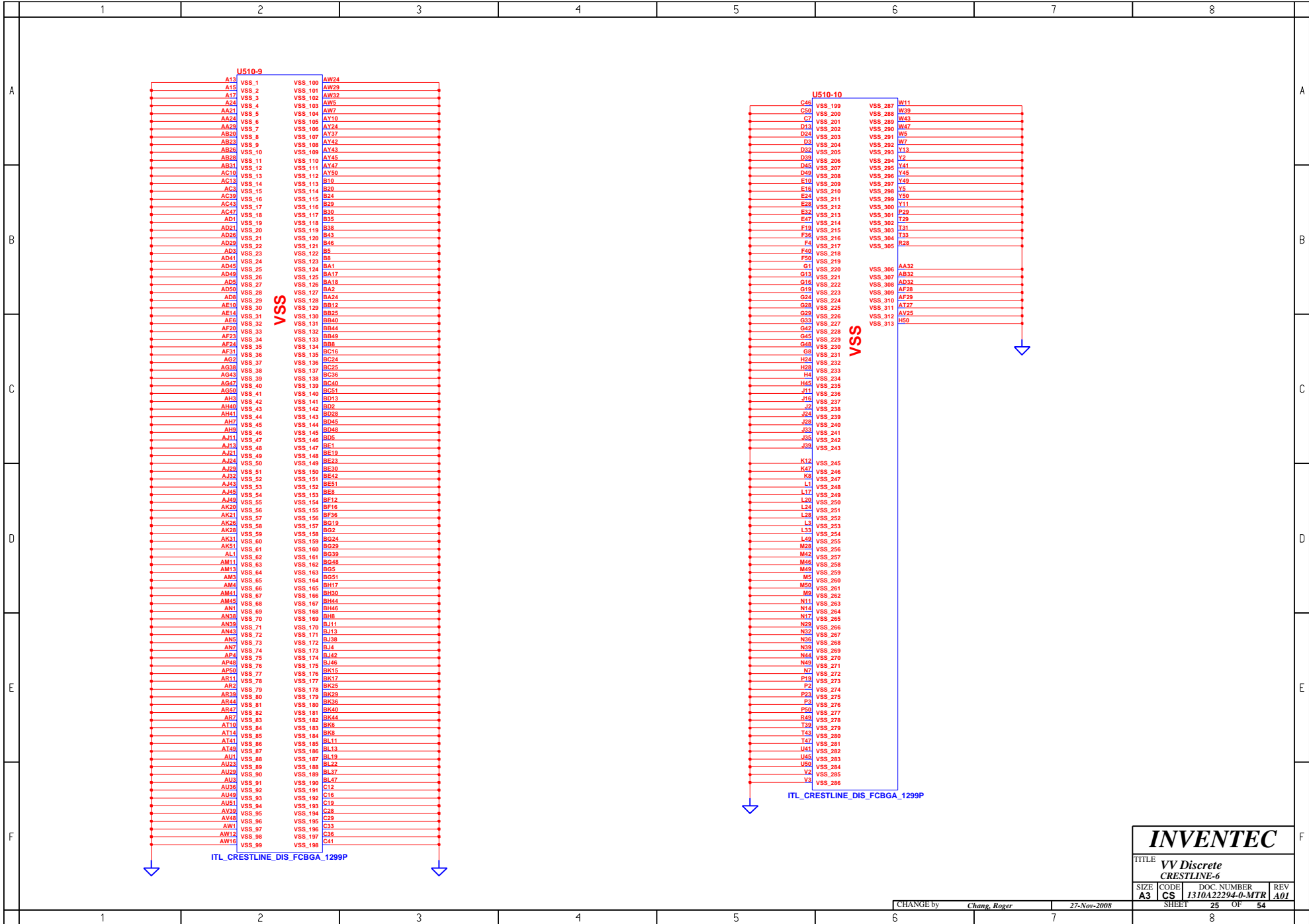
<b>INVENTEC</b>			
TITLE <b>VV Discrete CRESTLINE-4</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC NUMBER <b>1310A22294-0-MTR</b>	REV <b>A01</b>
SHEET <b>23</b>		OF <b>54</b>	

CHANGE by *Chang, Roger* 27-Nov-2008

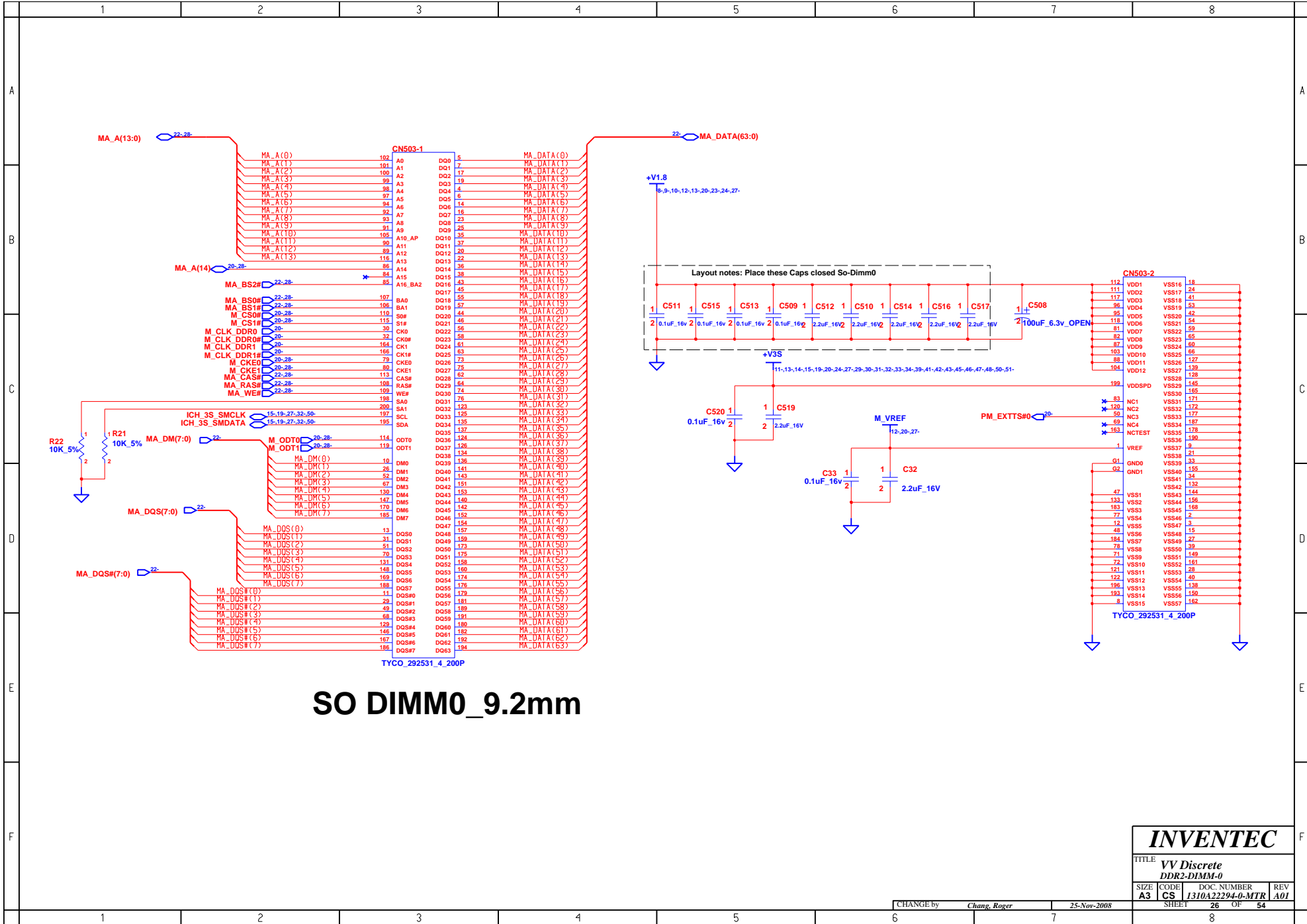




<b>INVENTEC</b>			
TITLE <i>VV Discrete Cresline-5</i>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC NUMBER <b>1310A2294-0-MTR</b>	REV <b>A01</b>
CHANGE by <i>Chang, Roger</i>		27-Nov-2008	
SHEET <b>24</b>		OF <b>54</b>	



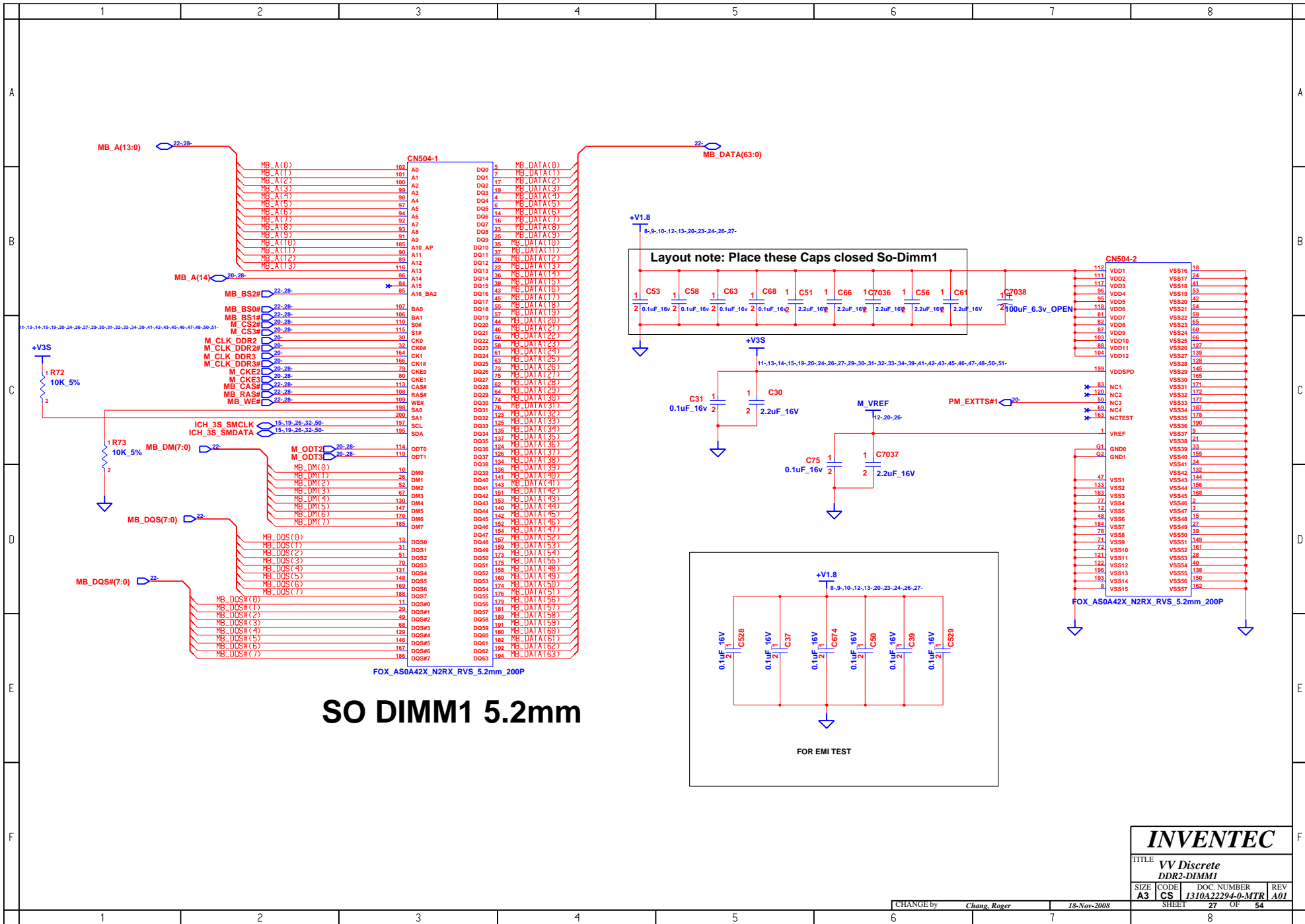
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TITLE <b>VV Discrete CRESTLINE-6</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A22294-0-MTR</b>	REV <b>A01</b>
CHANGE by <b>Chang, Roger</b>		27-Nov-2008	
SHEET <b>25</b>		OF <b>54</b>	



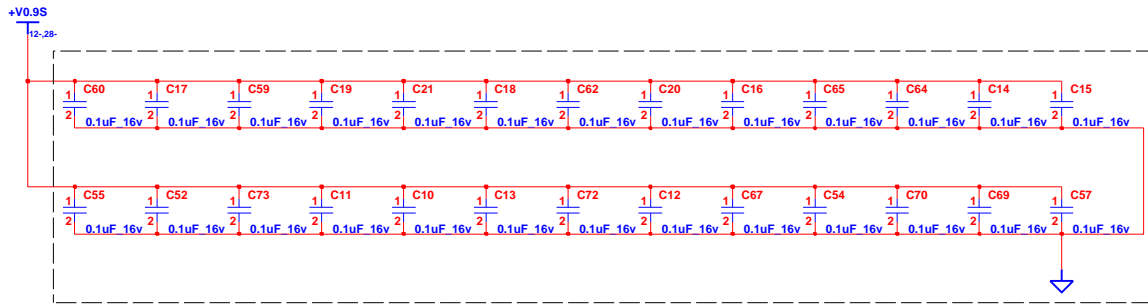
# SO DIMM0\_9.2mm

## INVENTEC

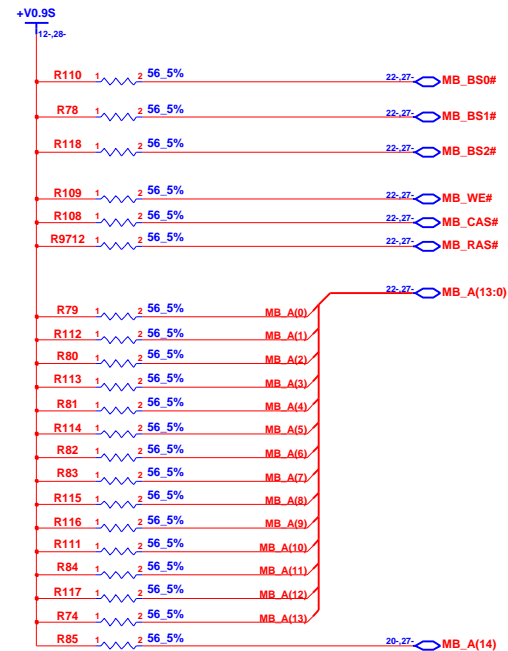
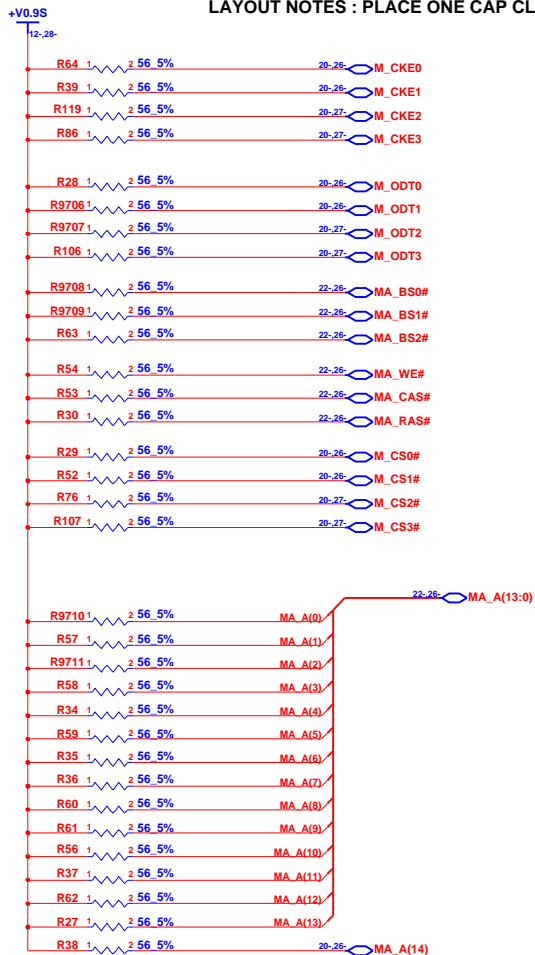
TITLE			
VV Discrete			
DDR2-DIMM-0			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A22294-0-MTR	A01
CHANGE by			SHEET
Chang, Roger			26
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<b>INVENTEC</b>			
TITLE <b>VV Discrete DDR2-DIMM1</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC NUMBER <b>1310A2294-0-MTR</b>	REV <b>A01</b>
CHANGE by <i>Chang, Roger</i>		18-Nov-2008	
SHEET <b>27</b>		OF <b>54</b>	



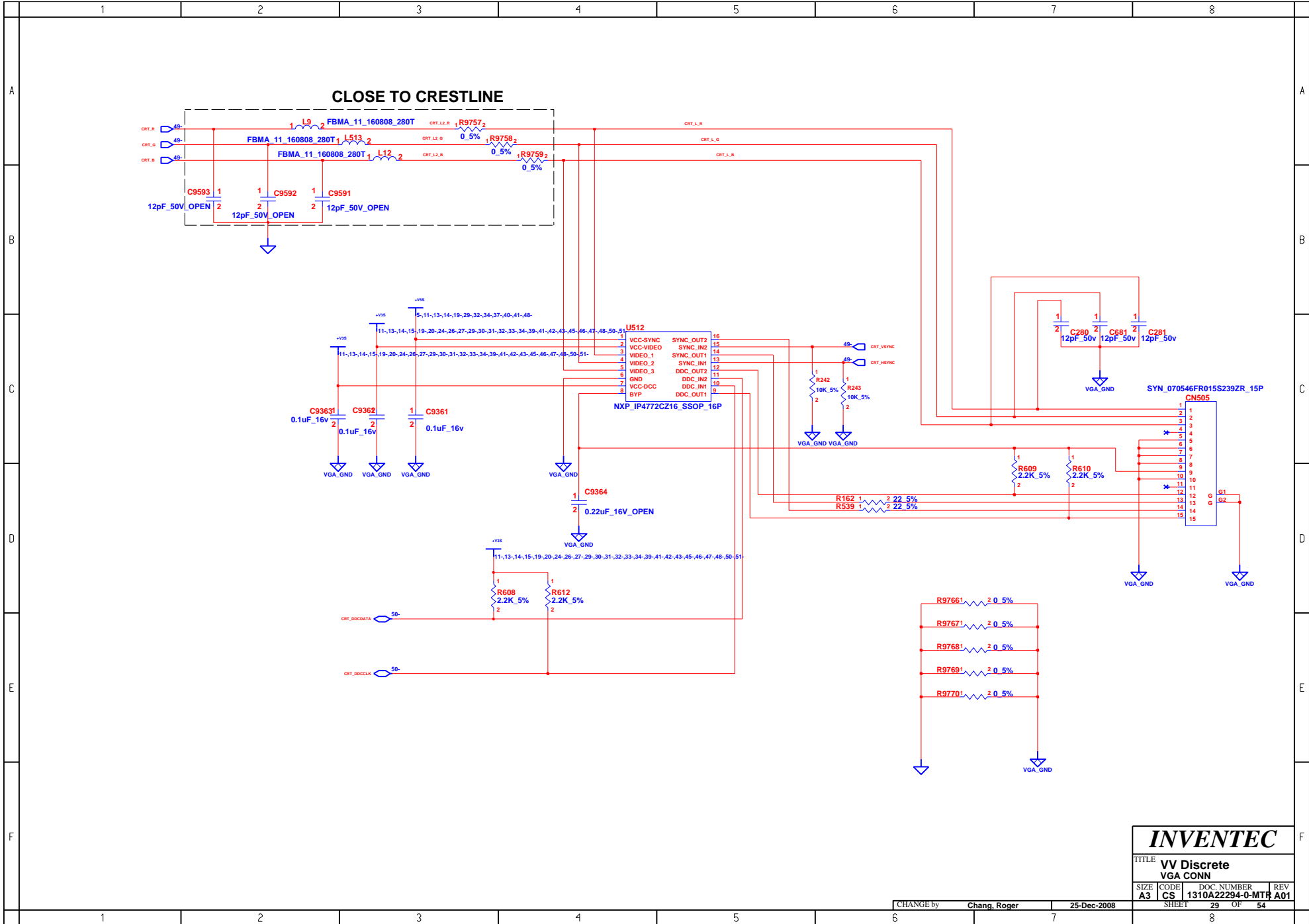
LAYOUT NOTES : PLACE ONE CAP CLOSE TO EVERY 2 PULL UP RESISTOR TERMINATED TO +V0.9S



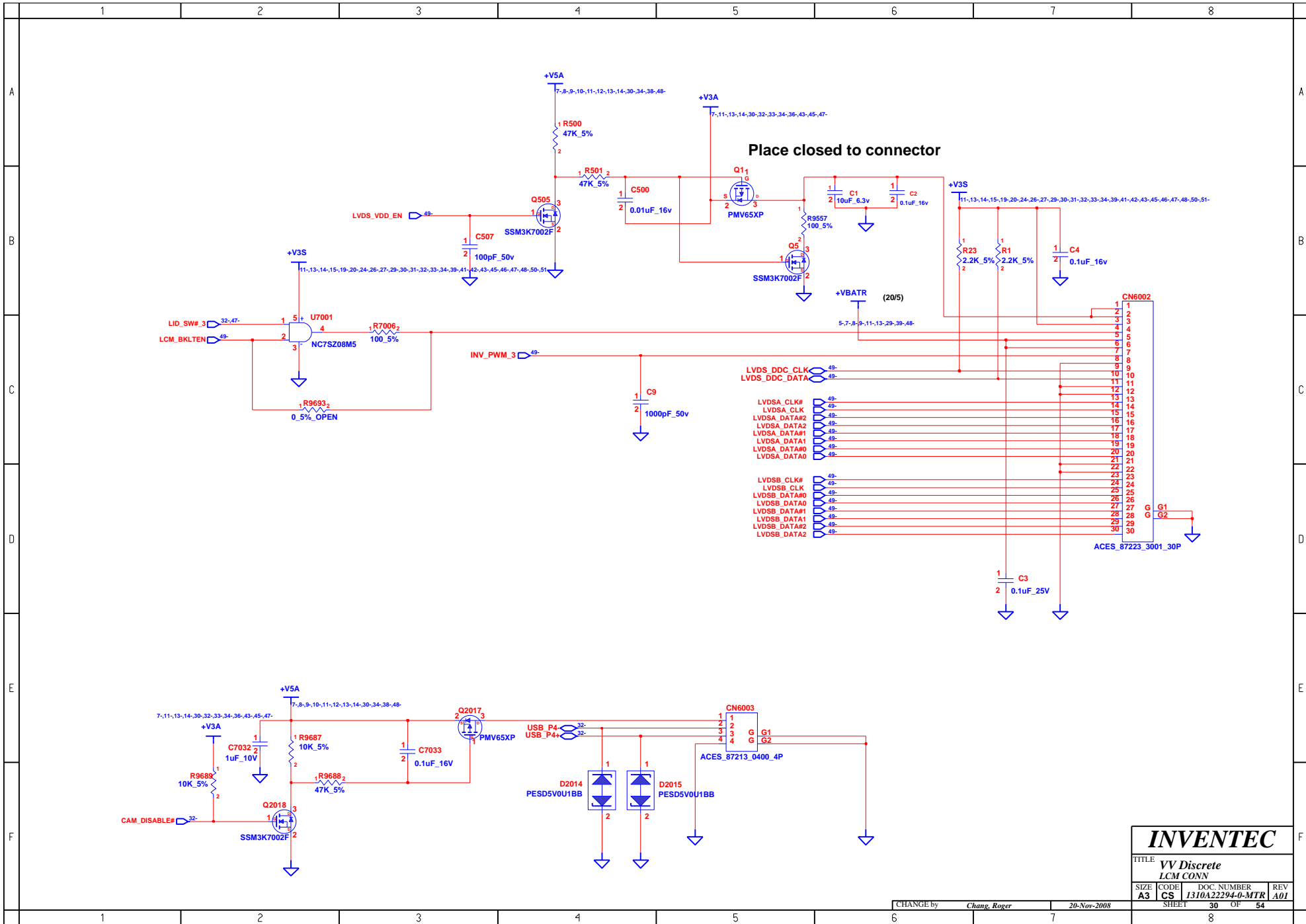
# INVENTEC

TITLE			
VV Discrete			
DDR2-DAMPING			
SIZE	CODE	DOC NUMBER	REV
A3	CS	I310A22294-0-MTR	RA01
SHEET		OF	
28		54	

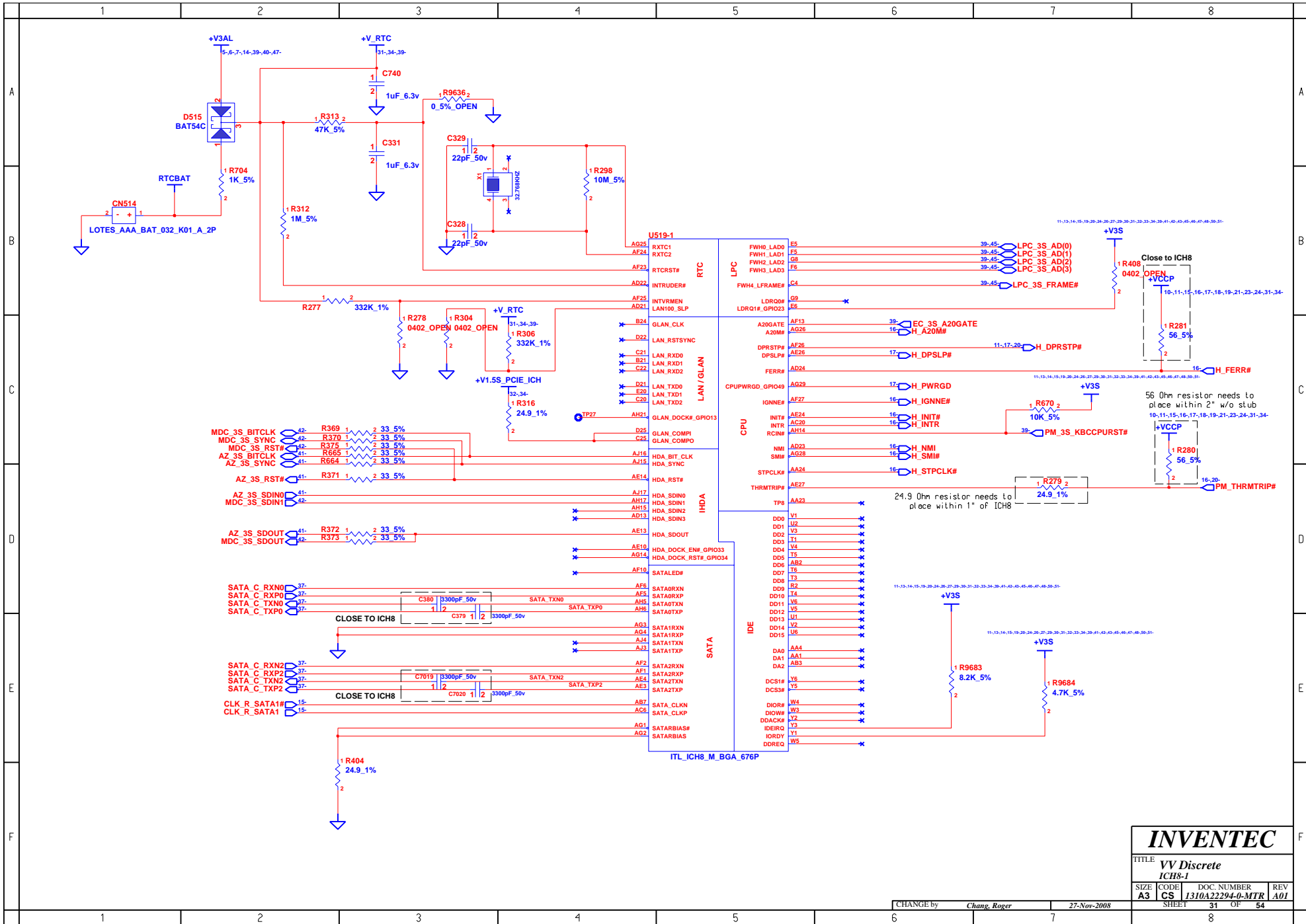
CHANGE by *Chang, Roger* 27-Sep-2003



<b>INVENTEC</b>			
TITLE VV Discrete VGA CONN			
SIZE A3	CODE CS	DOC NUMBER 1310A22294-0-MTR	REV A01
CHANGE by Chang, Roger		25-Dec-2008	
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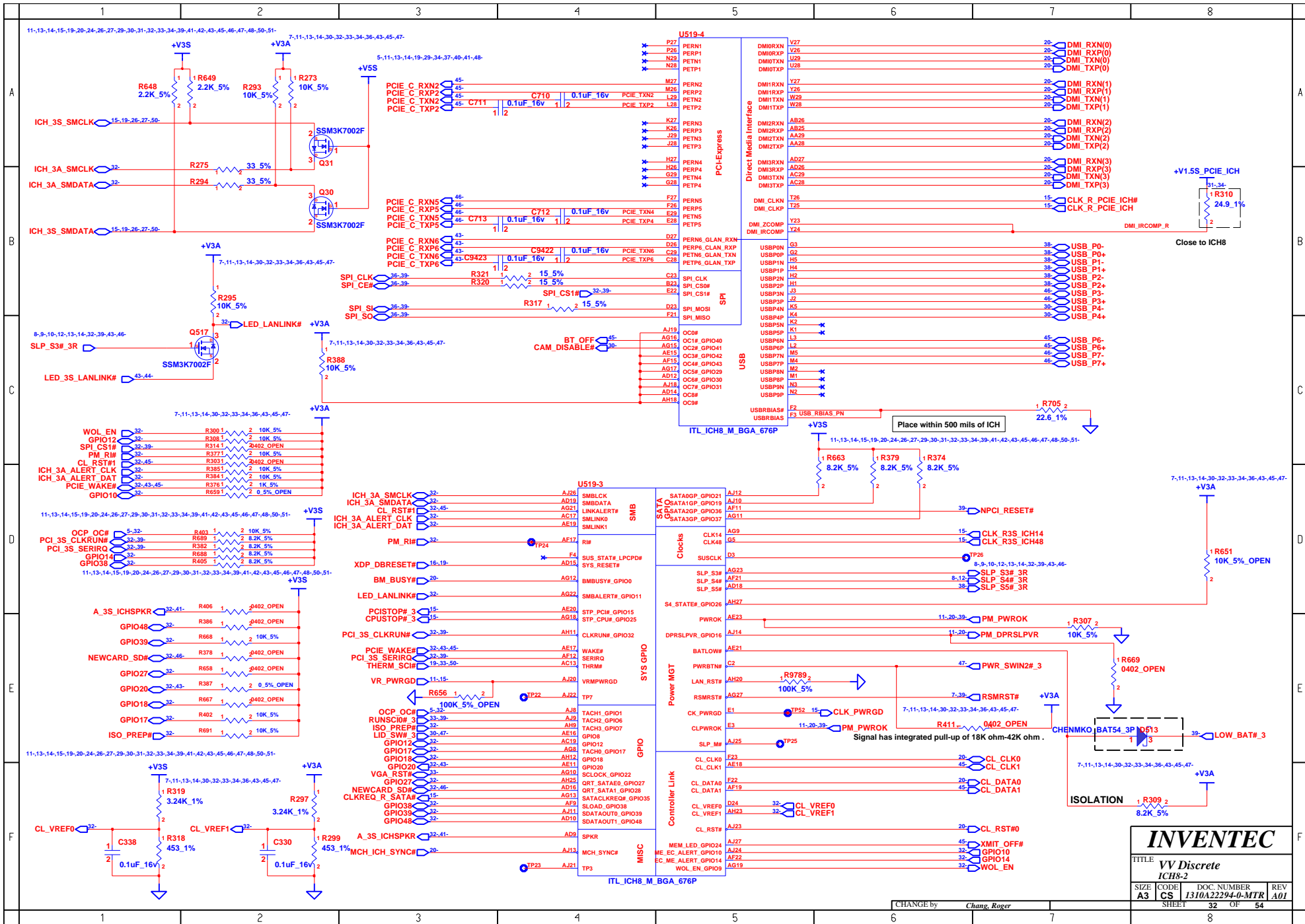
<b>INVENTEC</b>			
TITLE <b>VV Discrete LCM CONN</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC NUMBER <b>1310A22294-0-MTR</b>	REV <b>A01</b>
CHANGE by <i>Chang, Roger</i>		20-Nov-2008	
SHEET <b>30</b>		OF <b>54</b>	



<b>INVENTEC</b>			
TITLE <b>VV Discrete ICH8-1</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A22294-0-MTR</b>	REV <b>A01</b>
SHEET <b>31</b>		OF <b>54</b>	

CHANGE by *Chang, Roger* 27-Nov-2008



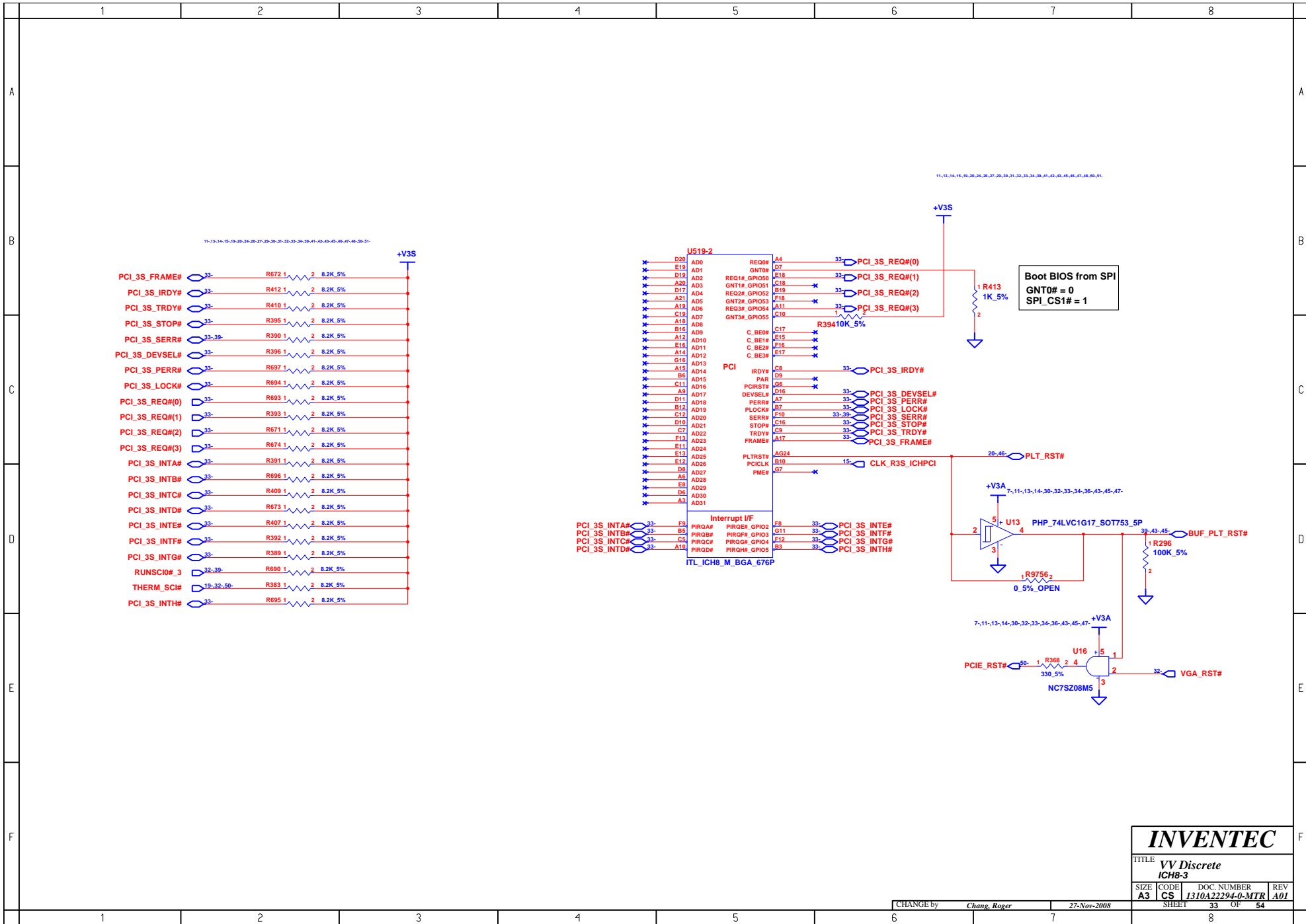


**INVENTEC**

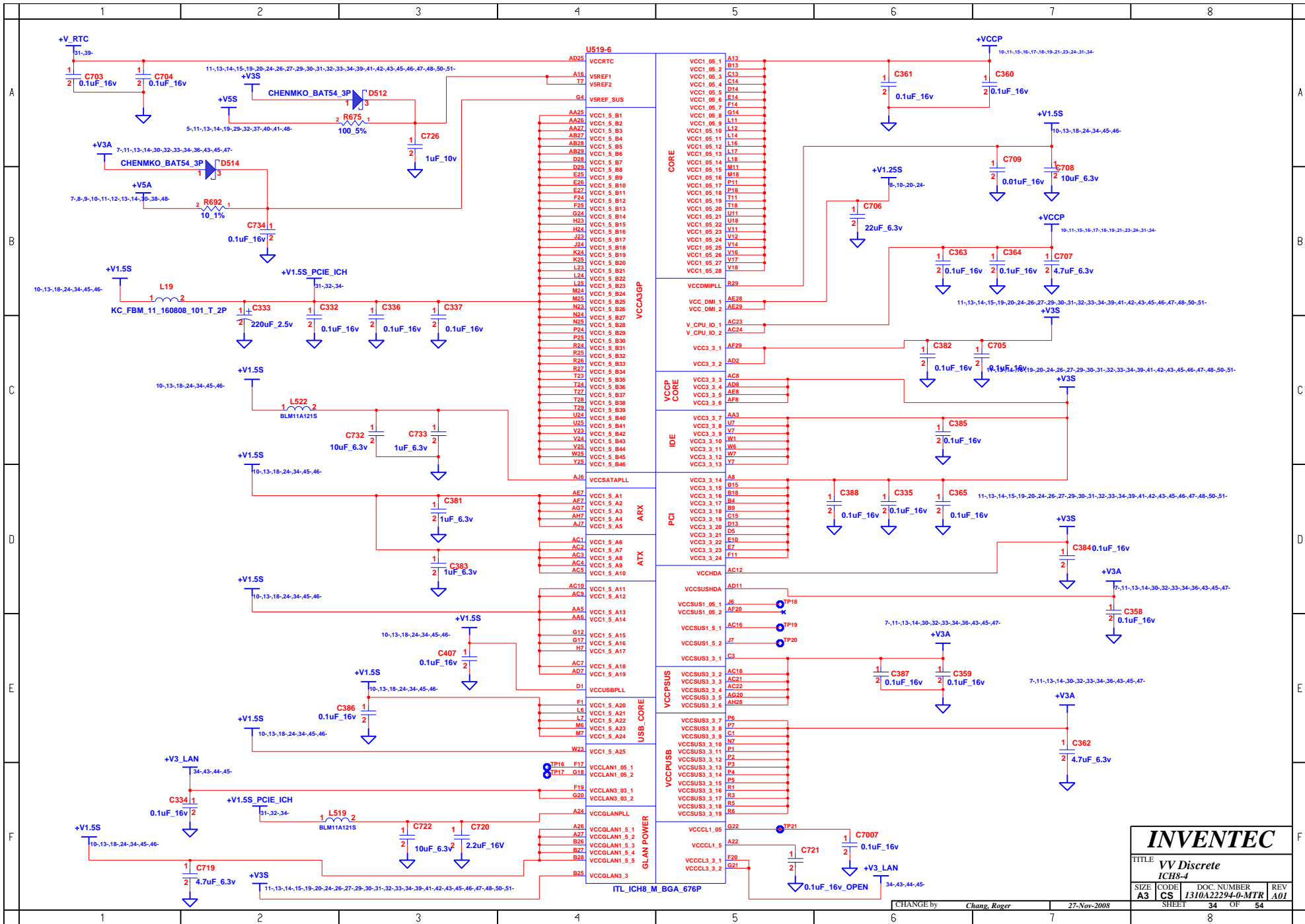
TITLE: **VV Discrete ICH8-2**

SIZE: <b>A3</b>	CODE: <b>CS</b>	DOC. NUMBER: <b>1310A22294-0-MTR</b>	REV: <b>A01</b>
SHEET: <b>32</b>		OF: <b>54</b>	

CHANGE by *Chang, Roger*



<b>INVENTEC</b>			
TITLE <b>VV Discrete ICH8-3</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A22394-0-MTR</b>	REV <b>A01</b>
CHANGE by <b>Chang, Roger</b>		27-Nov-2008	
SHEET <b>33</b>		OF <b>54</b>	



**INVENTEC**

TITLE **VV Discrete ICH8-4**

SIZE <b>A3</b>	CODE <b>CS</b>	DOC NUMBER <b>1310A22294-0-MTR</b>	REV <b>A01</b>
SHEET <b>34</b>		OF <b>54</b>	

CHANGE by *Chang, Roger* 27-Nov-2008

	1	2	3	4	5	6	7	8	
A									
B									
C									
D									
E									
F									

U519-5

AA1	VSS001	VSS099	K7
AA2	VSS002	VSS100	L1
AA3	VSS003	VSS101	L13
AA4	VSS004	VSS102	L15
AA5	VSS005	VSS103	L26
AA6	VSS006	VSS104	L27
AA7	VSS007	VSS105	L4
AA8	VSS008	VSS106	L5
AA9	VSS009	VSS107	M12
AA10	VSS010	VSS108	M13
AA11	VSS011	VSS109	M14
AA12	VSS012	VSS110	M15
AA13	VSS013	VSS111	M16
AA14	VSS014	VSS112	M17
AA15	VSS015	VSS113	M23
AA16	VSS016	VSS114	M28
AA17	VSS017	VSS115	M3
AA18	VSS018	VSS116	M32
AA19	VSS019	VSS117	N1
AA20	VSS020	VSS118	N11
AA21	VSS021	VSS119	N12
AA22	VSS022	VSS120	N13
AA23	VSS023	VSS121	N14
AA24	VSS024	VSS122	N15
AA25	VSS025	VSS123	N16
AA26	VSS026	VSS124	N17
AA27	VSS027	VSS125	N18
AA28	VSS028	VSS126	N26
AA29	VSS029	VSS127	N27
AA30	VSS030	VSS128	N4
AA31	VSS031	VSS129	N5
AA32	VSS032	VSS130	N6
AA33	VSS033	VSS131	P12
AA34	VSS034	VSS132	P13
AA35	VSS035	VSS133	P14
AA36	VSS036	VSS134	P15
AA37	VSS037	VSS135	P16
AA38	VSS038	VSS136	P17
AA39	VSS039	VSS137	P23
AA40	VSS040	VSS138	P28
AA41	VSS041	VSS139	P29
AA42	VSS042	VSS140	R11
AA43	VSS043	VSS141	R12
AA44	VSS044	VSS142	R13
AA45	VSS045	VSS143	R14
AA46	VSS046	VSS144	R15
AA47	VSS047	VSS145	R16
AA48	VSS048	VSS146	R17
AA49	VSS049	VSS147	R18
AA50	VSS050	VSS148	R28
AA51	VSS051	VSS149	R4
AA52	VSS052	VSS150	T12
AA53	VSS053	VSS151	T13
AA54	VSS054	VSS152	T14
AA55	VSS055	VSS153	T15
AA56	VSS056	VSS154	T16
AA57	VSS057	VSS155	T17
AA58	VSS058	VSS156	T2
AA59	VSS059	VSS157	U12
AA60	VSS060	VSS158	U13
AA61	VSS061	VSS159	U14
AA62	VSS062	VSS160	U15
AA63	VSS063	VSS161	U16
AA64	VSS064	VSS162	U17
AA65	VSS065	VSS163	U23
AA66	VSS066	VSS164	U26
AA67	VSS067	VSS165	U27
AA68	VSS068	VSS166	U3
AA69	VSS069	VSS167	U5
AA70	VSS070	VSS168	V13
AA71	VSS071	VSS169	V15
AA72	VSS072	VSS170	V28
AA73	VSS073	VSS171	V29
AA74	VSS074	VSS172	W2
AA75	VSS075	VSS173	W26
AA76	VSS076	VSS174	W27
AA77	VSS077	VSS175	Y28
AA78	VSS078	VSS176	Y29
AA79	VSS079	VSS177	Y4
AA80	VSS080	VSS178	AB4
AA81	VSS081	VSS179	AB23
AA82	VSS082	VSS180	AB5
AA83	VSS083	VSS181	AB6
AA84	VSS084	VSS182	AD5
AA85	VSS085	VSS183	U4
AA86	VSS086	VSS184	W24
AA87	VSS087	VSS_NCTF_01	A1
AA88	VSS088	VSS_NCTF_02	A2
AA89	VSS089	VSS_NCTF_03	A28
AA90	VSS090	VSS_NCTF_04	A29
AA91	VSS091	VSS_NCTF_05	AH1
AA92	VSS092	VSS_NCTF_06	AH29
AA93	VSS093	VSS_NCTF_07	AJ1
AA94	VSS094	VSS_NCTF_08	AJ2
AA95	VSS095	VSS_NCTF_09	AJ28
AA96	VSS096	VSS_NCTF_10	AJ29
AA97	VSS097	VSS_NCTF_011	B1
AA98	VSS098	VSS_NCTF_012	B29

ITL\_ICH8\_M\_BGA\_676P

**INVENTEC**

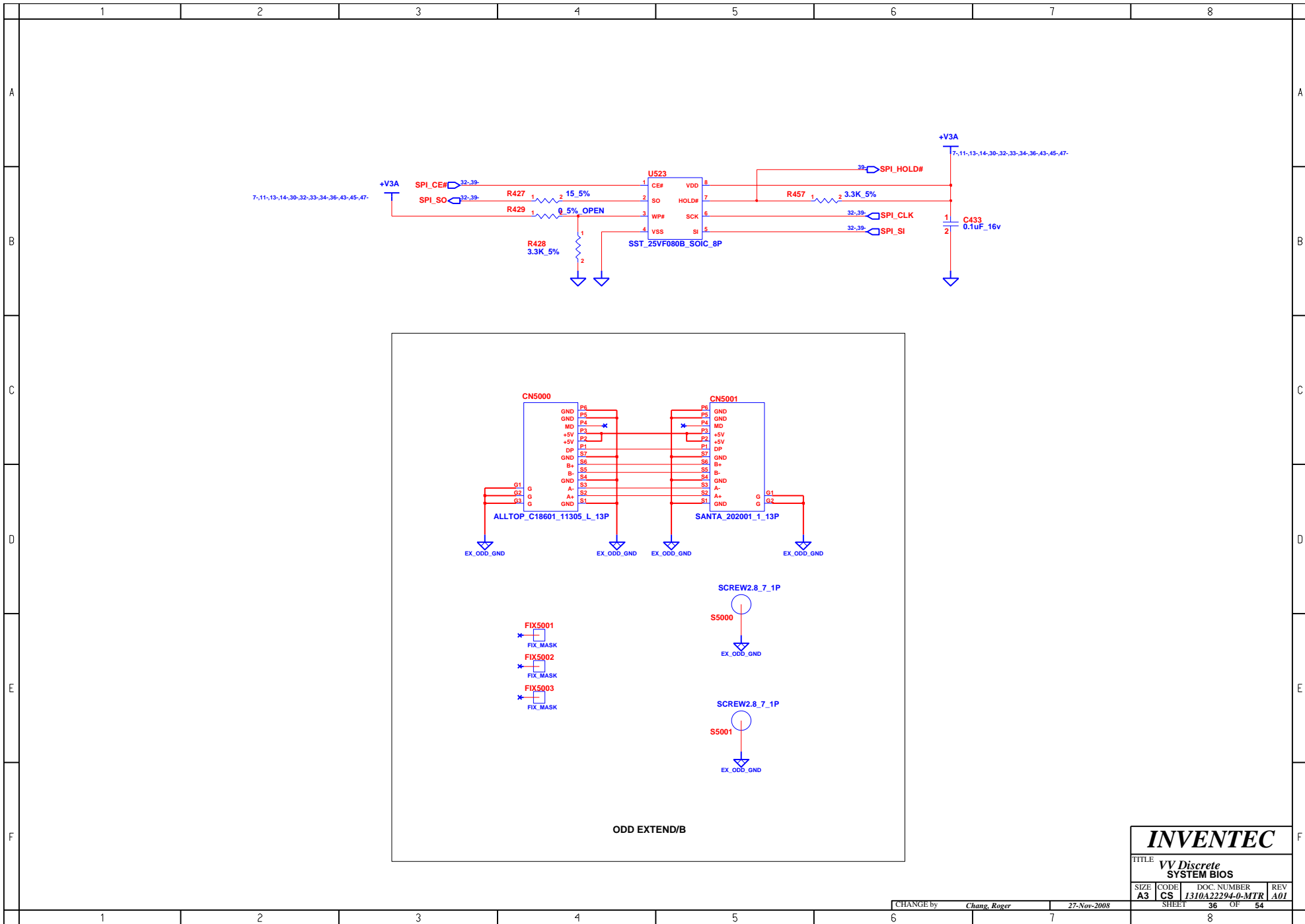
TITLE **VV Discrete**  
**ICH8-5**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22294-0-MTR	A01

SHEET 35 OF 54

CHANGE by *Chang, Roger* 27-Nov-2008

	1	2	3	4	5	6	7	8	
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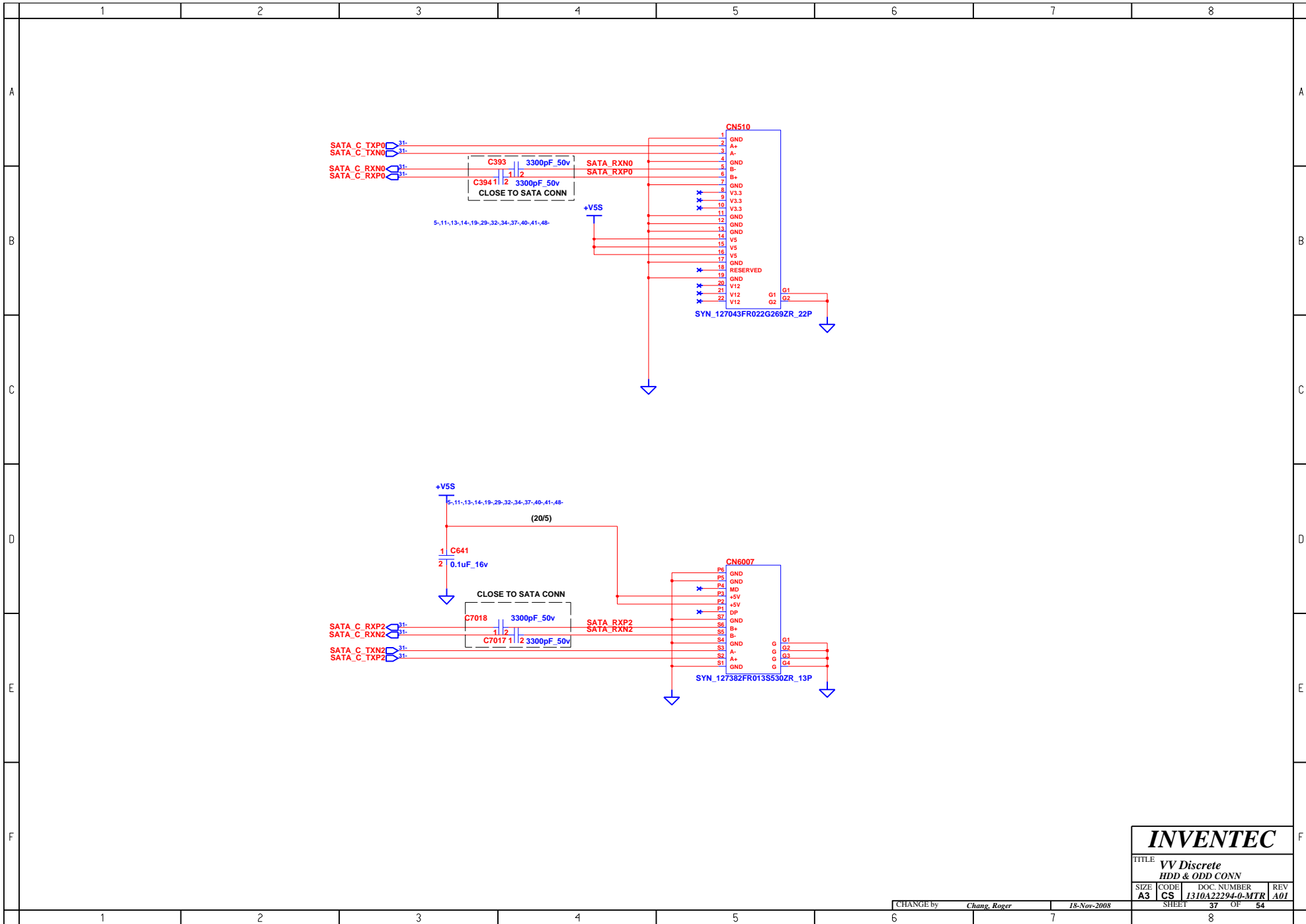
# INVENTEC

TITLE **VV Discrete SYSTEM BIOS**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22294-0-MTR	A01

CHANGE by *Chang, Roger* 27-Nov-2008

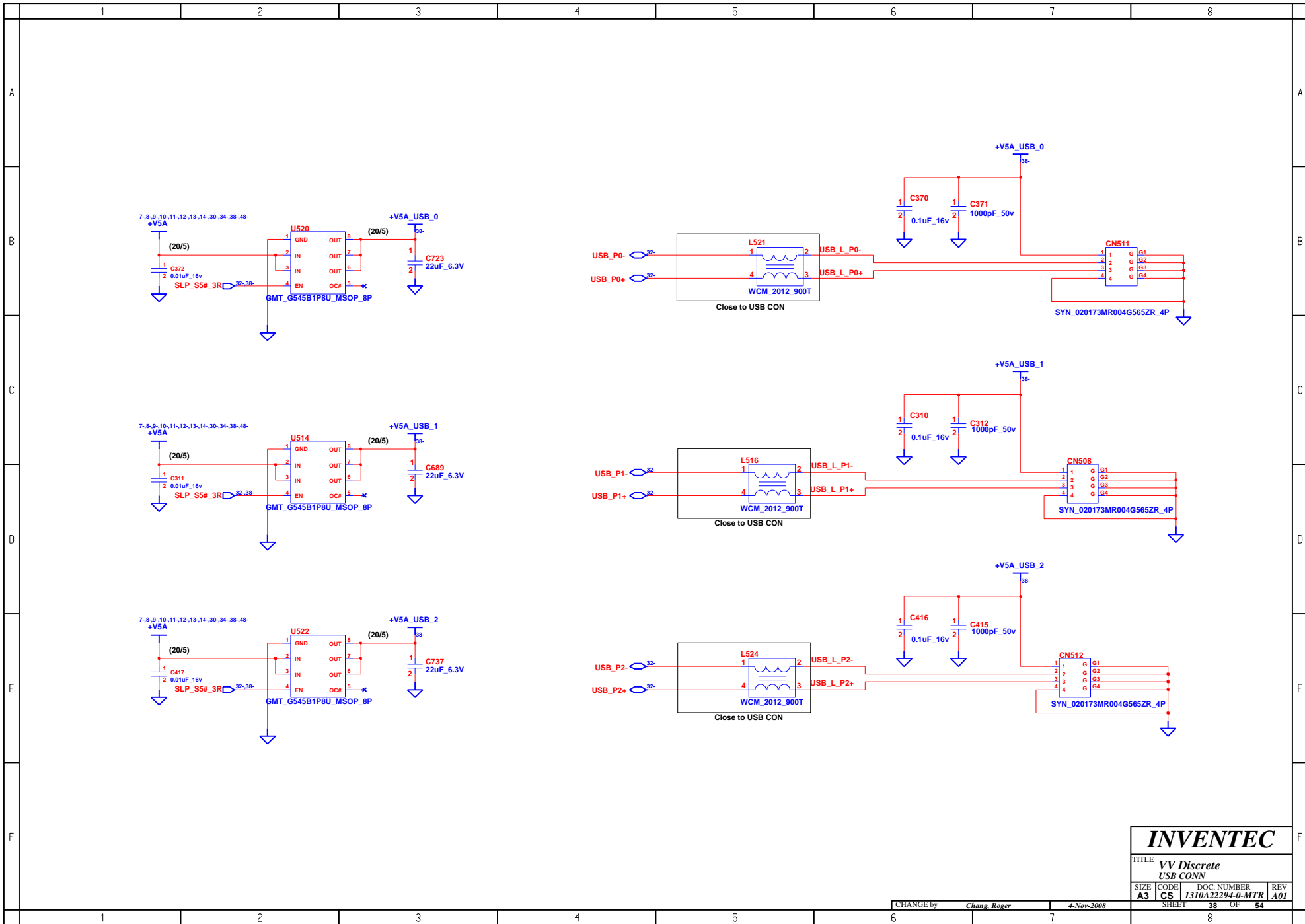
SHEET 36 OF 54



# INVENTEC

TITLE			
VV Discrete HDD & ODD CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22294-0-MTR	A01
SHEET		37	OF 54

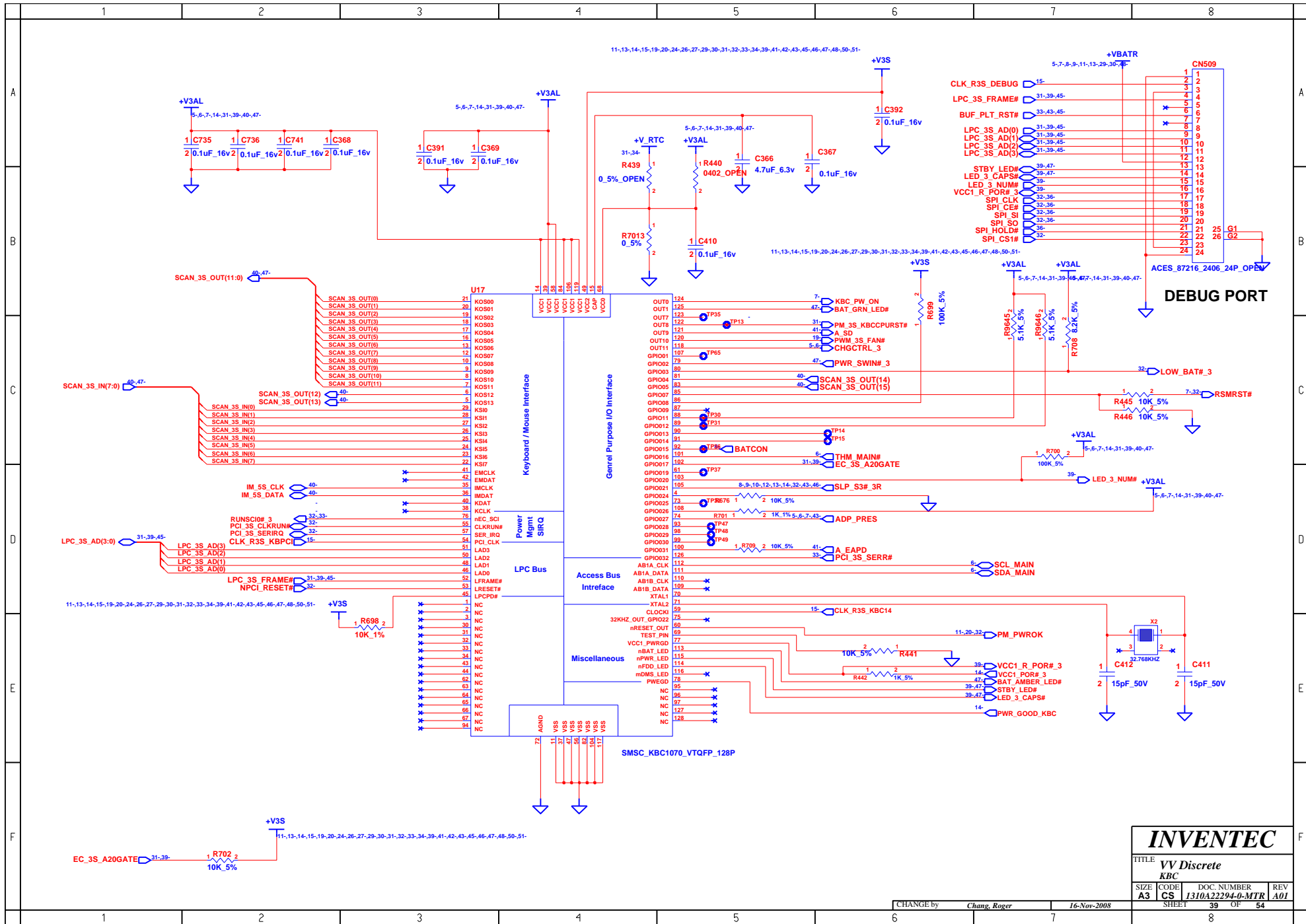
CHANGE by Chang, Roger 18-Nov-2008



# INVENTEC

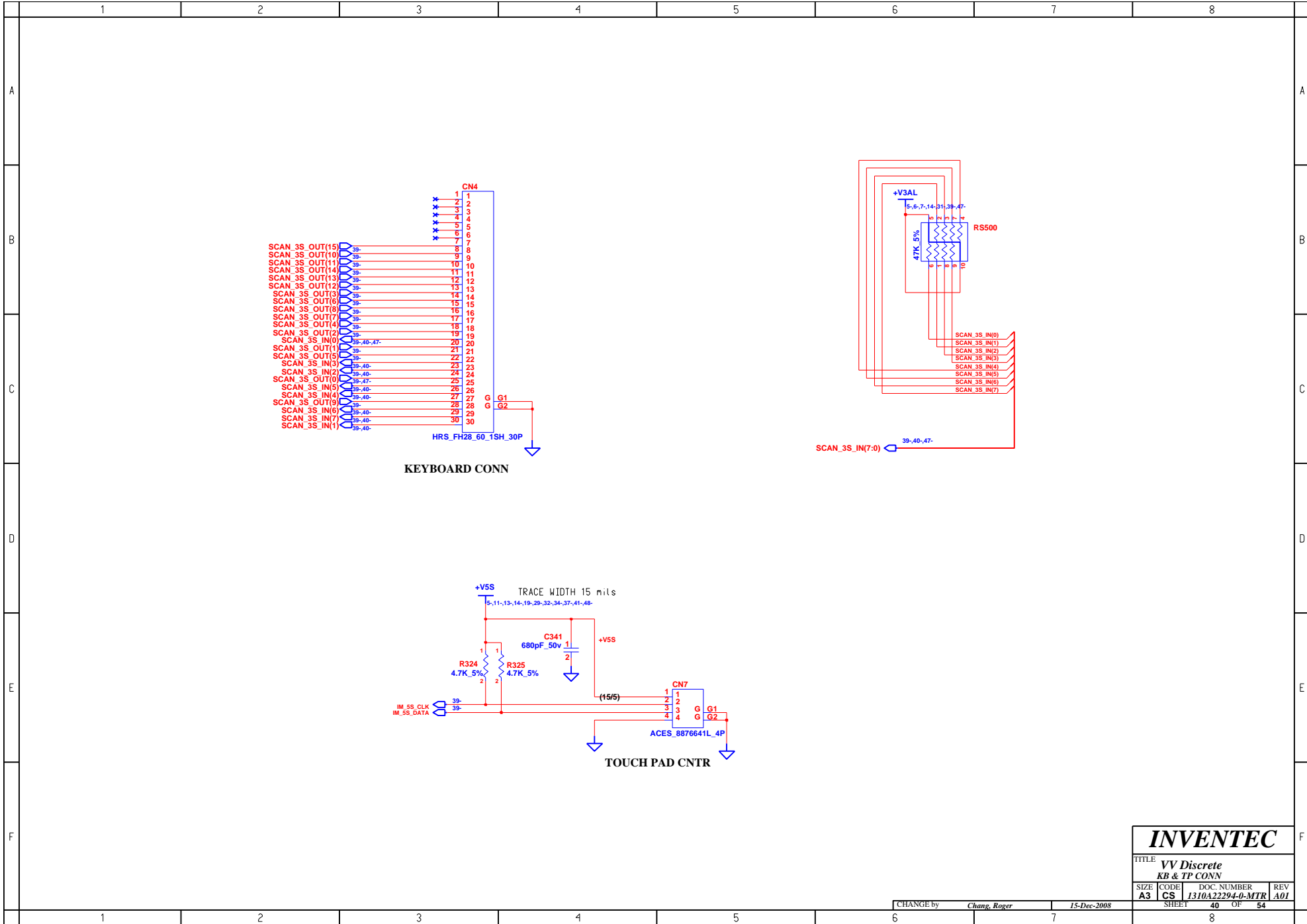
TITLE			
VV Discrete			
USB CONN			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A22294-0-MTR	A01
SHEET		OF	
38		54	

CHANGE by *Chang, Roger* 4-Nov-2008



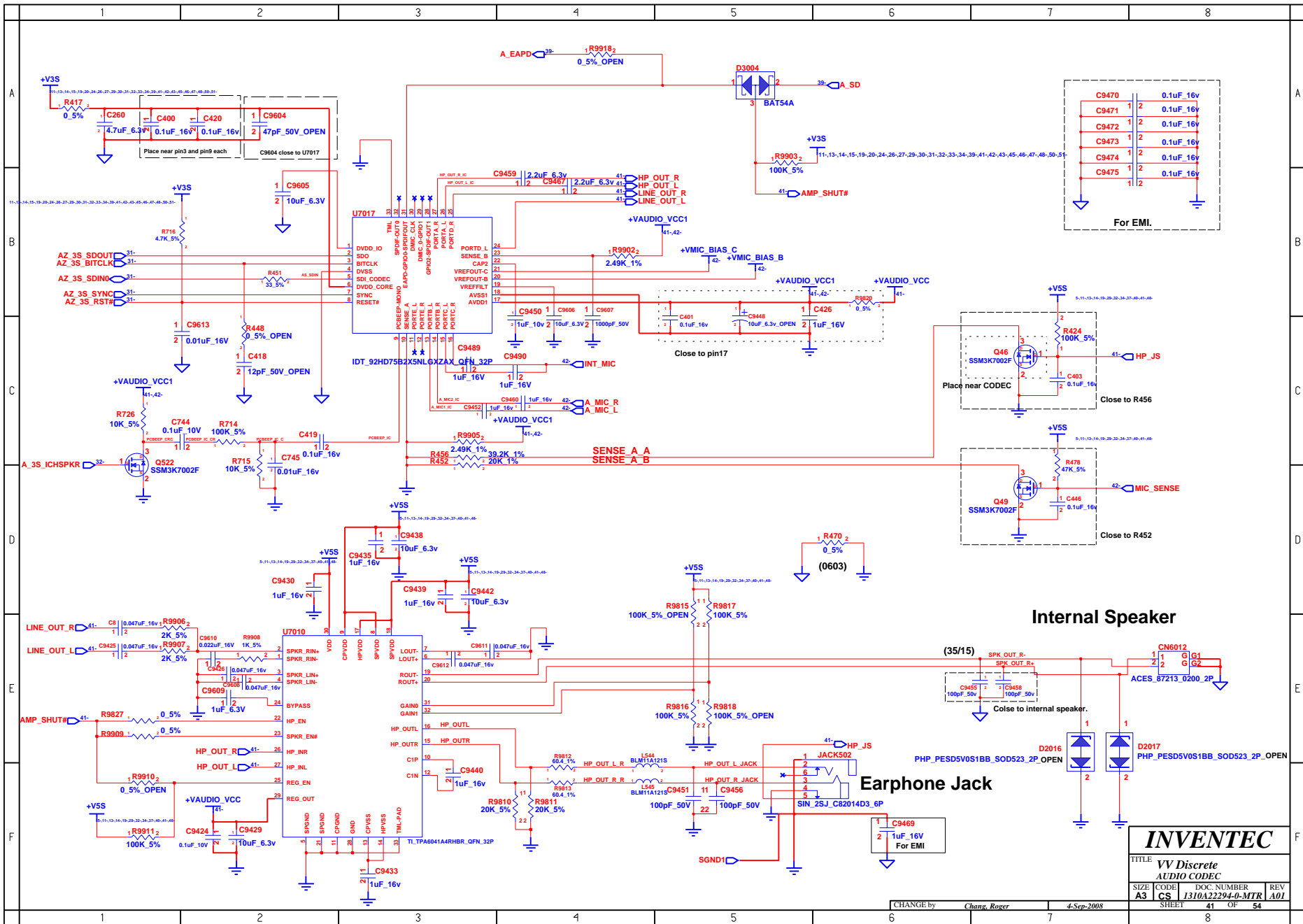
<b>INVENTEC</b>			
TITLE <b>VV Discrete KBC</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A22294-0-MTR</b>	REV <b>A01</b>
CHANGE by <b>Chang, Roger</b>		16-Nov-2008	
SHEET <b>39</b>		OF <b>54</b>	





**INVENTEC**

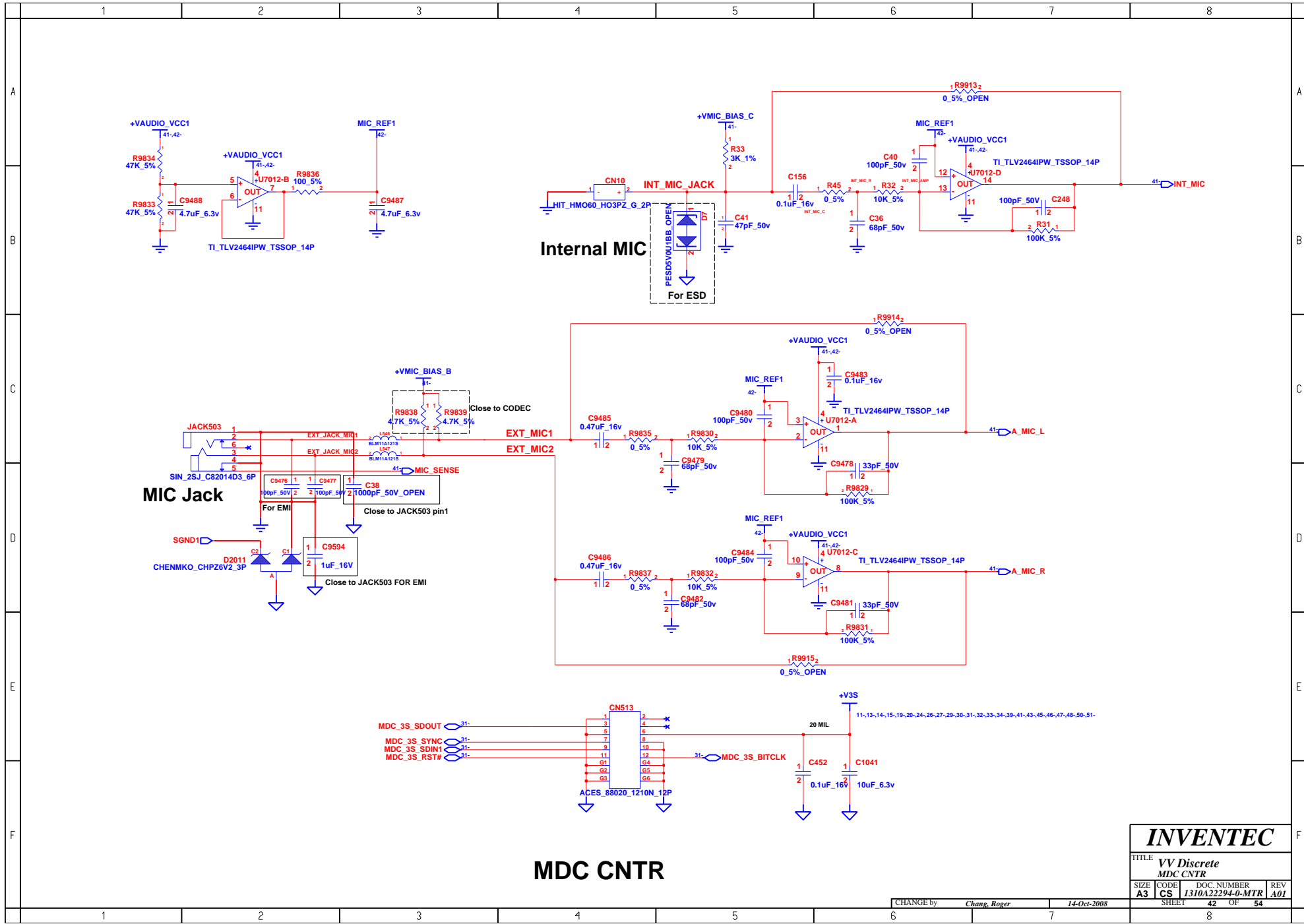
TITLE <b>VV Discrete</b>			
KB & TP CONN			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC NUMBER <b>1310A22294-0-MTR</b>	REV <b>A01</b>
CHANGE by <b>Chang, Roger</b>		DATE <b>15-Dec-2008</b>	
SHEET <b>40</b>		OF <b>54</b>	



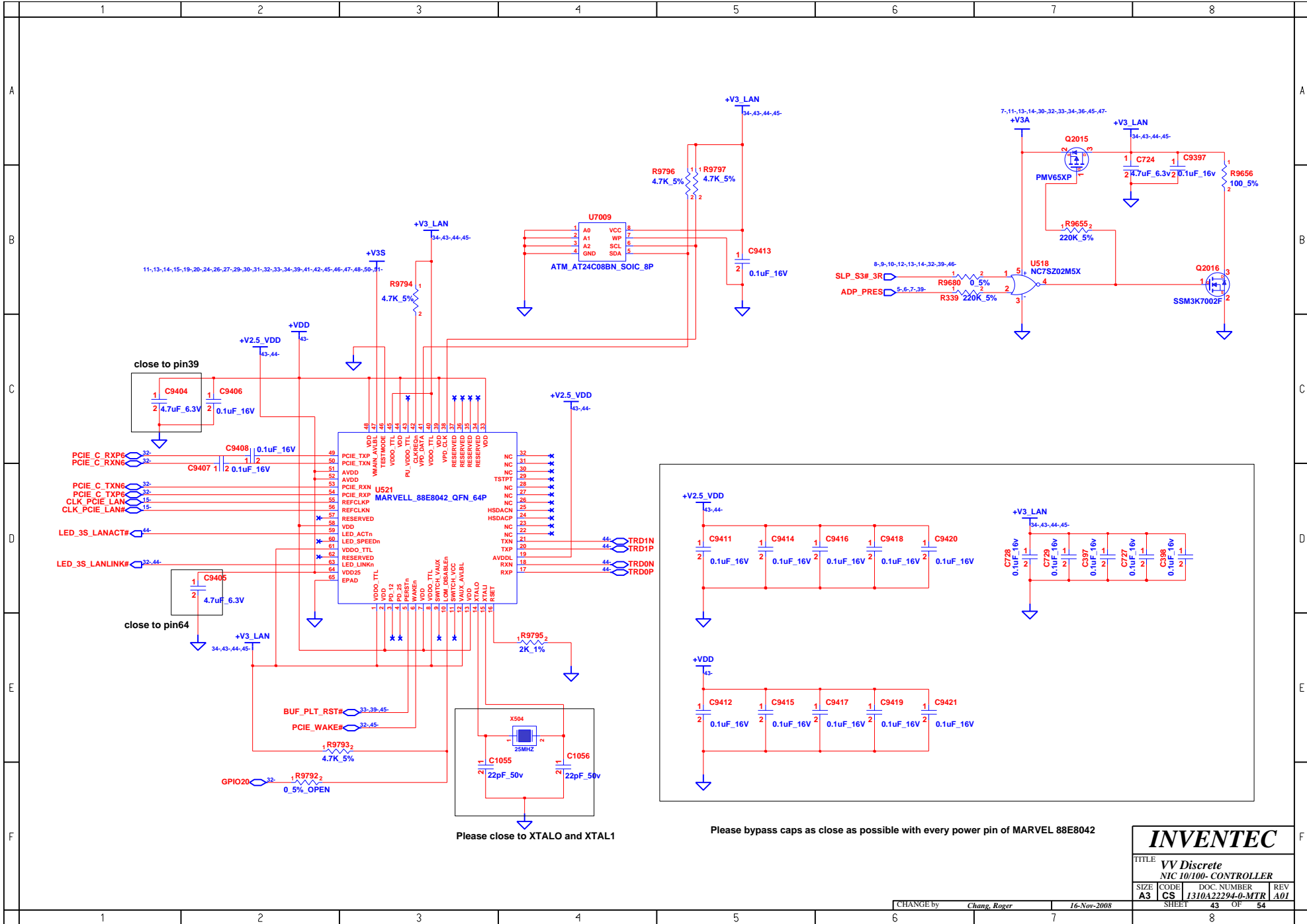
<b>INVENTEC</b>			
TITLE VV Discrete AUDIO CODEC			
SIZE A3	CODE CS	DOC. NUMBER 1310A22294-0-MTR	REV A01
SHEET 41		OF 54	

**Internal Speaker**

**Earphone Jack**



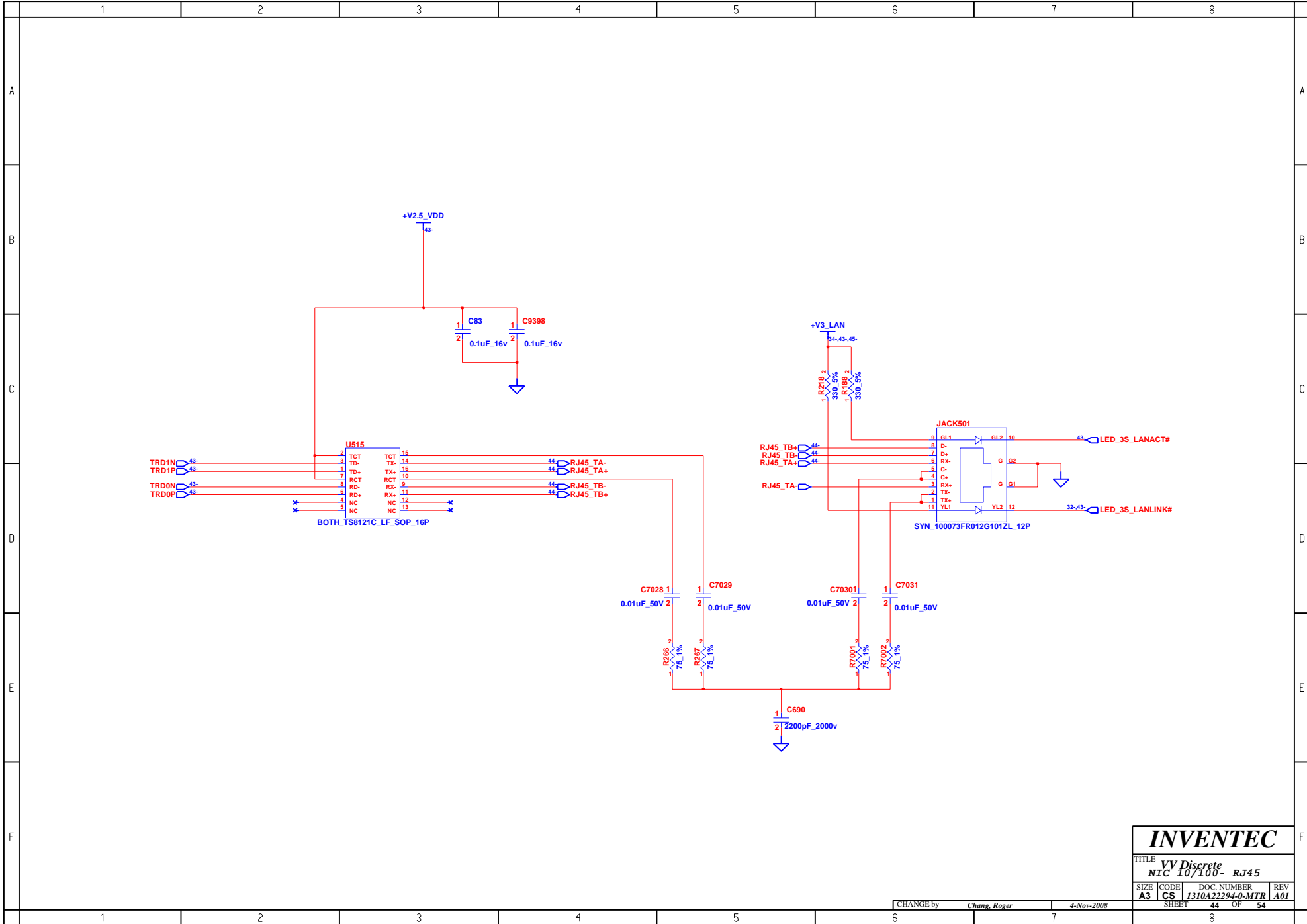
<b>INVENTEC</b>			
TITLE: VV Discrete MDC CNTR			
SIZE: A3	CODE: CS	DOC NUMBER: 1310A22294-0-MTR	REV: A01
CHANGE by: Chang, Roger		14-Oct-2008	
SHEET: 42		OF: 54	



Please close to XTALO and XTAL1

Please bypass caps as close as possible with every power pin of MARVEL 88E8042

<b>INVENTEC</b>			
TITLE: <b>VV Discrete</b>			
NIC 10/100-CONTROLLER			
SIZE: <b>A3</b>	CODE: <b>CS</b>	DOC NUMBER: <b>1310A22294-0-MTR</b>	REV: <b>A01</b>
CHANGE by: <i>Chang, Roger</i>		16-Nov-2008	
SHEET: 43		OF: 54	



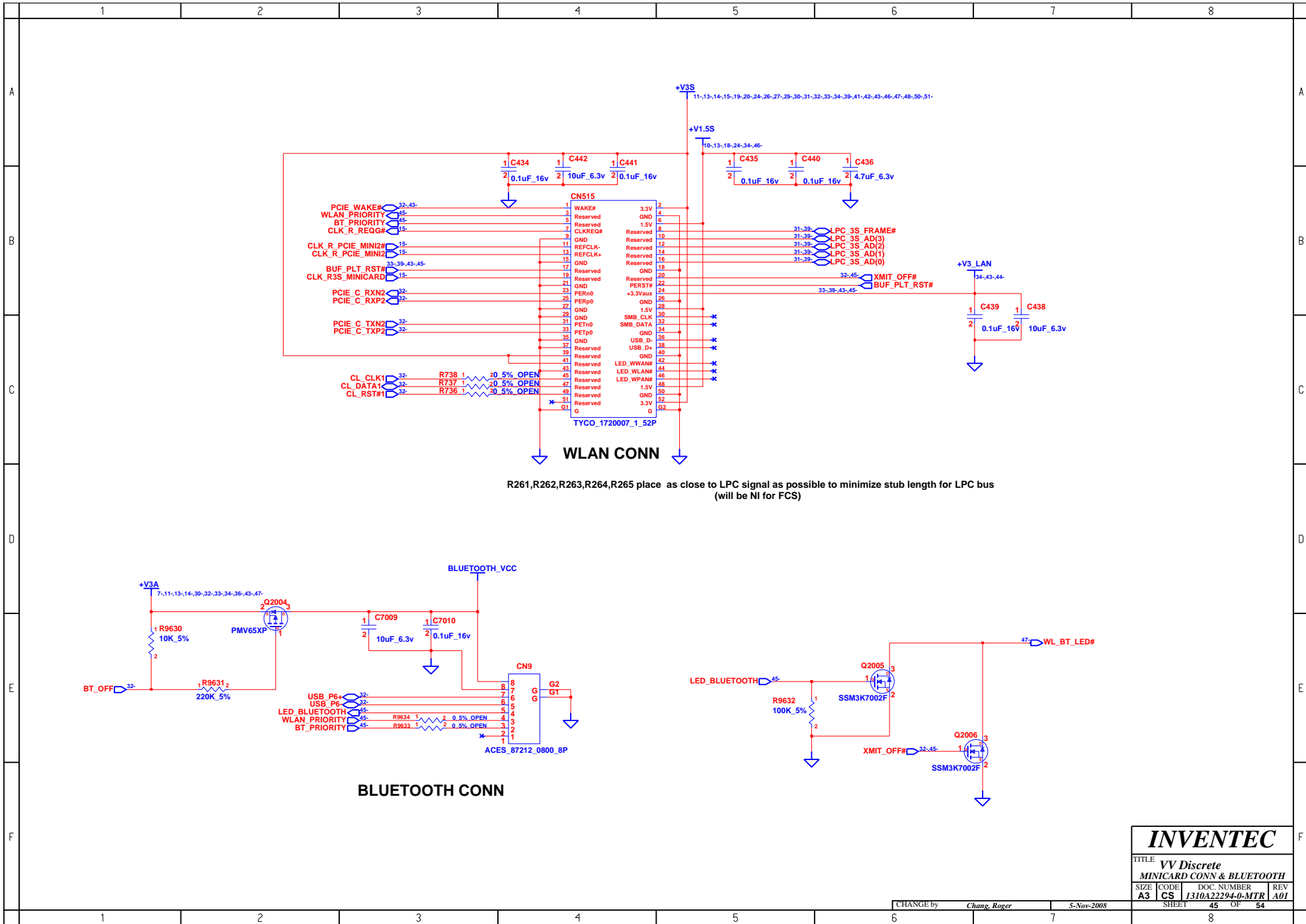
# INVENTEC

TITLE *VV Discrete*  
**NIC 10/100 - RJ45**

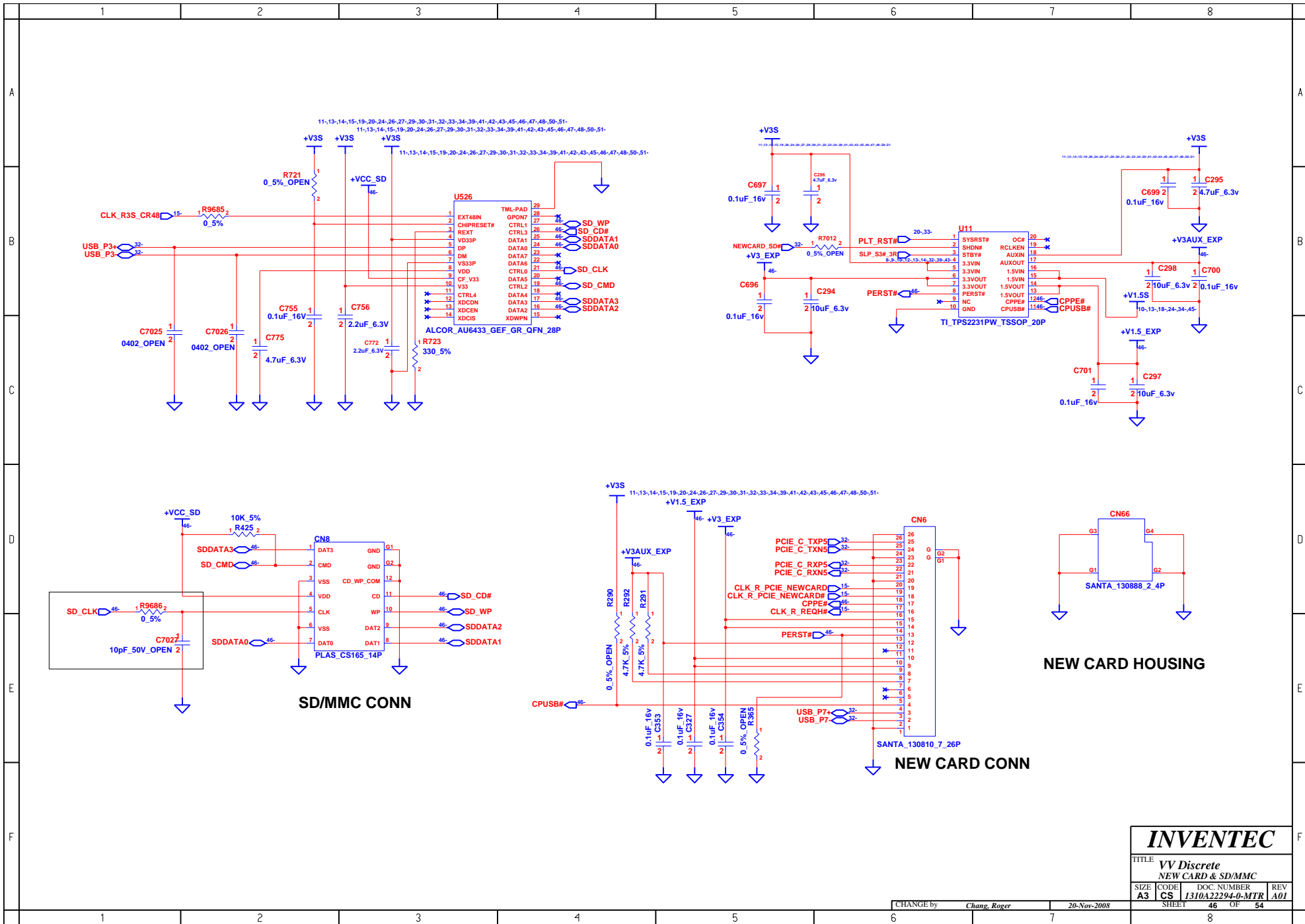
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22294-0-MTR	A01

CHANGE by *Chang, Roger* 4-Nov-2008

SHEET 44 OF 54



<b>INVENTEC</b>			
TITLE <b>VV Discrete</b>			
MINICARD CONN & BLUETOOTH			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A22294-0-MTR	A01
CHANGE by <i>Chang, Roger</i>		5-Nov-2008	SHEET 45 OF 54



NEW CARD HOUSING

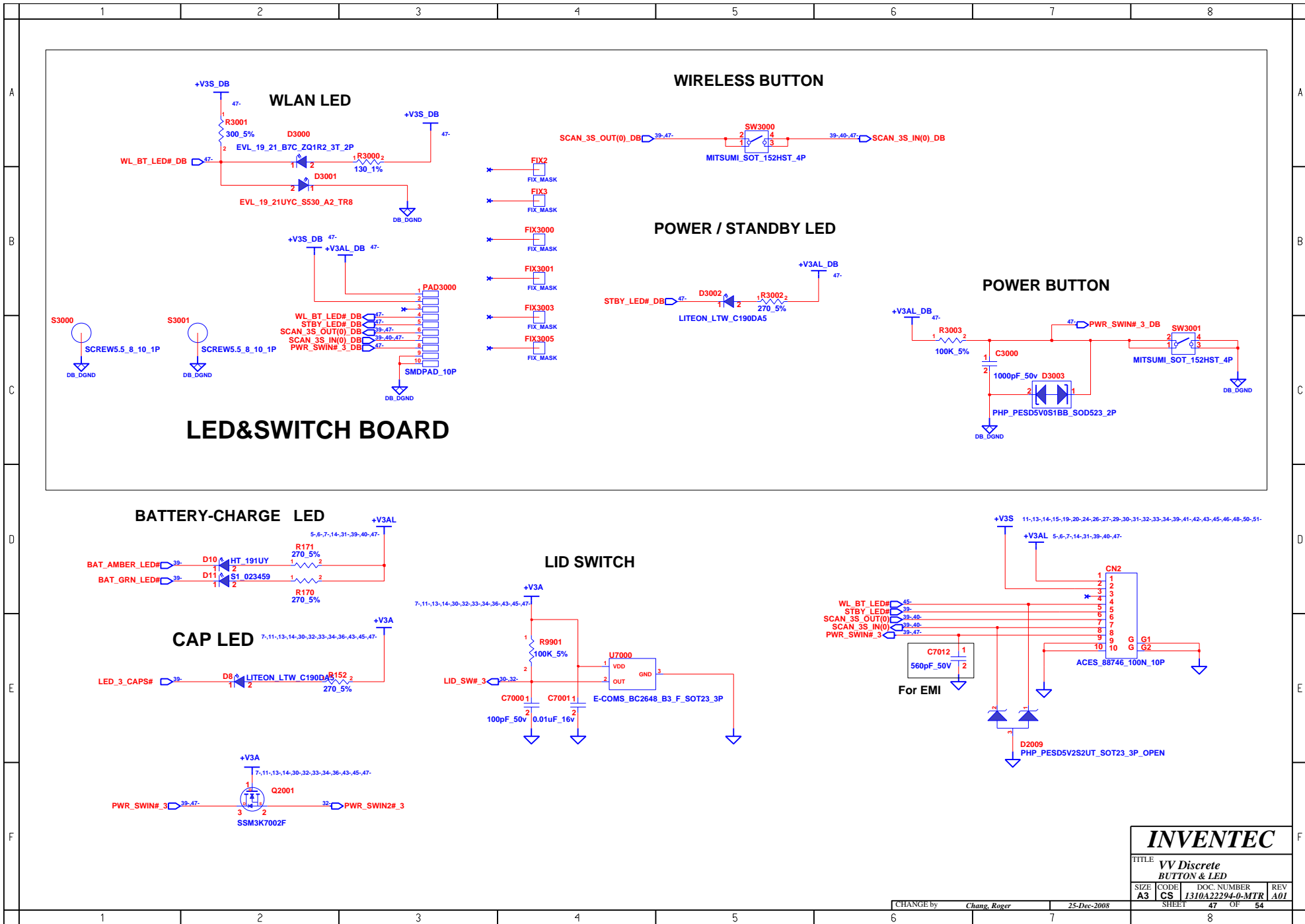
NEW CARD CONN

SD/MMC CONN

# INVENTEC

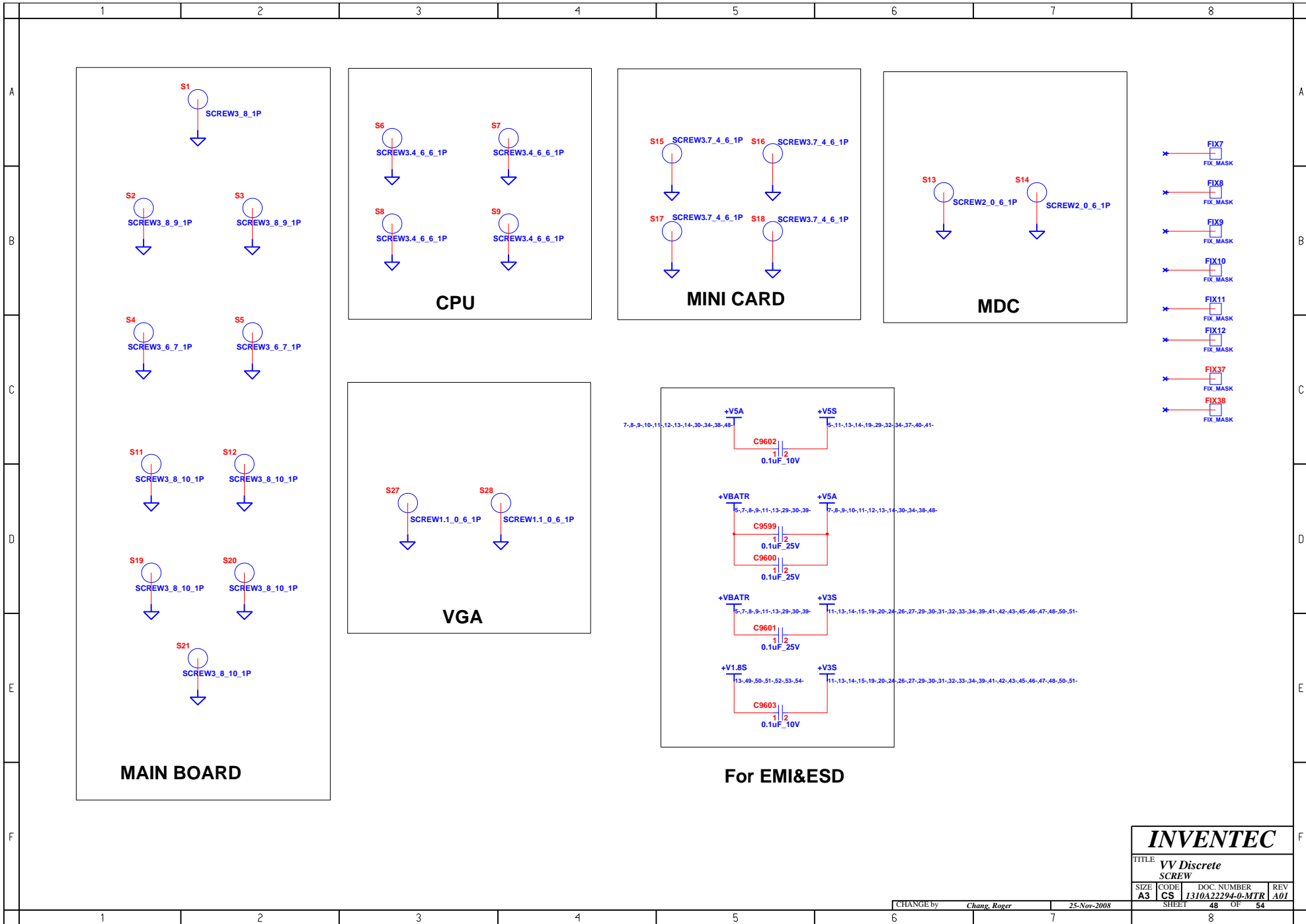
TITLE			
VV Discrete			
NEW CARD & SD/MMC			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A22294-0-MTR	A01
SHEET		OF	
46		54	

CHANGE by Chang, Roger 20-Nov-2008



<b>INVENTEC</b>			
TITLE <b>VV Discrete BUTTON &amp; LED</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC NUMBER <b>1310A22294-0-MTR</b>	REV <b>A01</b>
CHANGE by <i>Chang, Roger</i>		25-Dec-2008	
SHEET <b>47</b>		OF <b>54</b>	





<b>INVENTEC</b>			
TITLE <b>VV Discrete SCREW</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC NUMBER <b>1310A22294-0-MTR</b>	REV <b>A01</b>
CHANGE by <i>Chang, Roger</i>		25-Nov-2008	
SHEET <b>48</b>		OF <b>54</b>	

STRAPS	PIN	Description of default settings	
TX_PWRS_ENB	GPIO0	PCIe full TX output swing.	0
TX_DEEMPH_EN	GPIO1	PCIe transmitter de-emphasis enabled.	0
BIF_DEBUG_ACCESS	GPIO4	Debug signals muxed out.	0
ROMIDCFG(0:3)	GPIO[11:13,9]	Memory aperture type	X X X X

X = DESIGN DEPENDANT  
0 = DO NOT INSTALL RESISTOR  
1 = INSTALL 10K RESISTOR

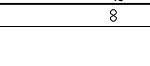
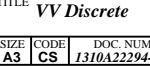
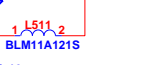
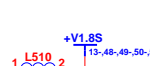
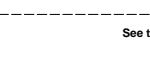
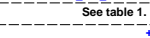
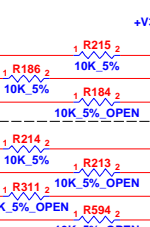
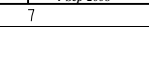
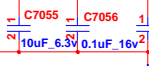
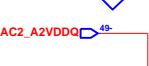
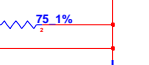
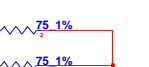
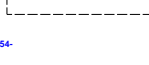
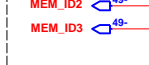
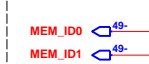
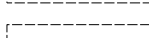
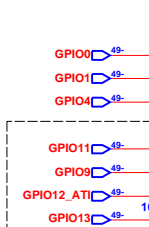
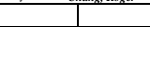
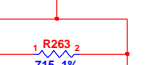
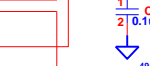
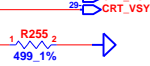
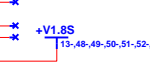
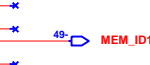
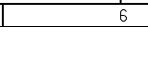
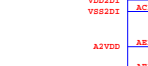
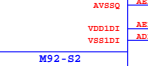
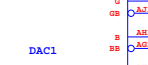
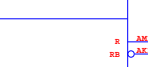
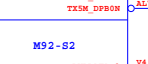
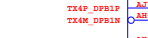
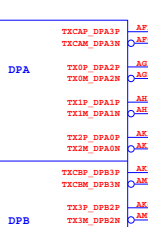
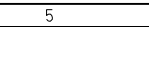
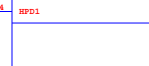
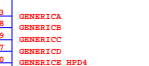
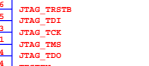
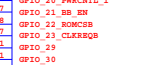
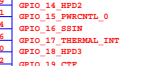
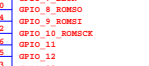
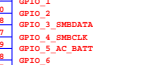
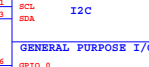
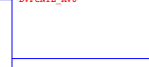
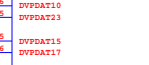
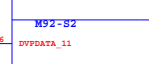
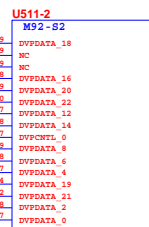
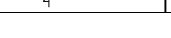
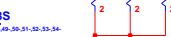
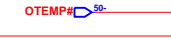
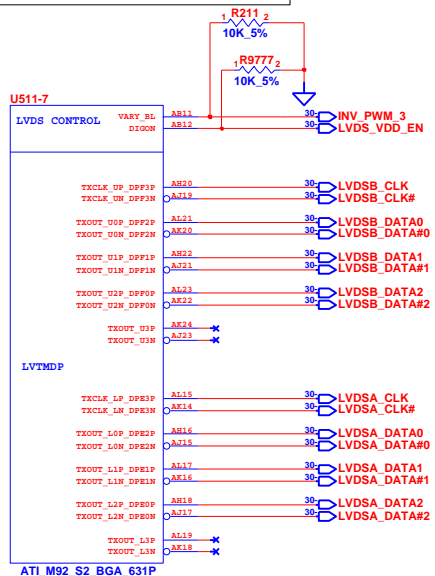
Table 1

GPIO_13	GPIO_12	GPIO_11	For DIS-GDDR2
0	0	0	128M memory aperture
0	0	1	256M memory aperture (Default)
1	1	0	reserved

POW_SW1	POW_SW0	+VDD_CORE
0	0	1.0V
0	1	0.95V
1	0	0.9V

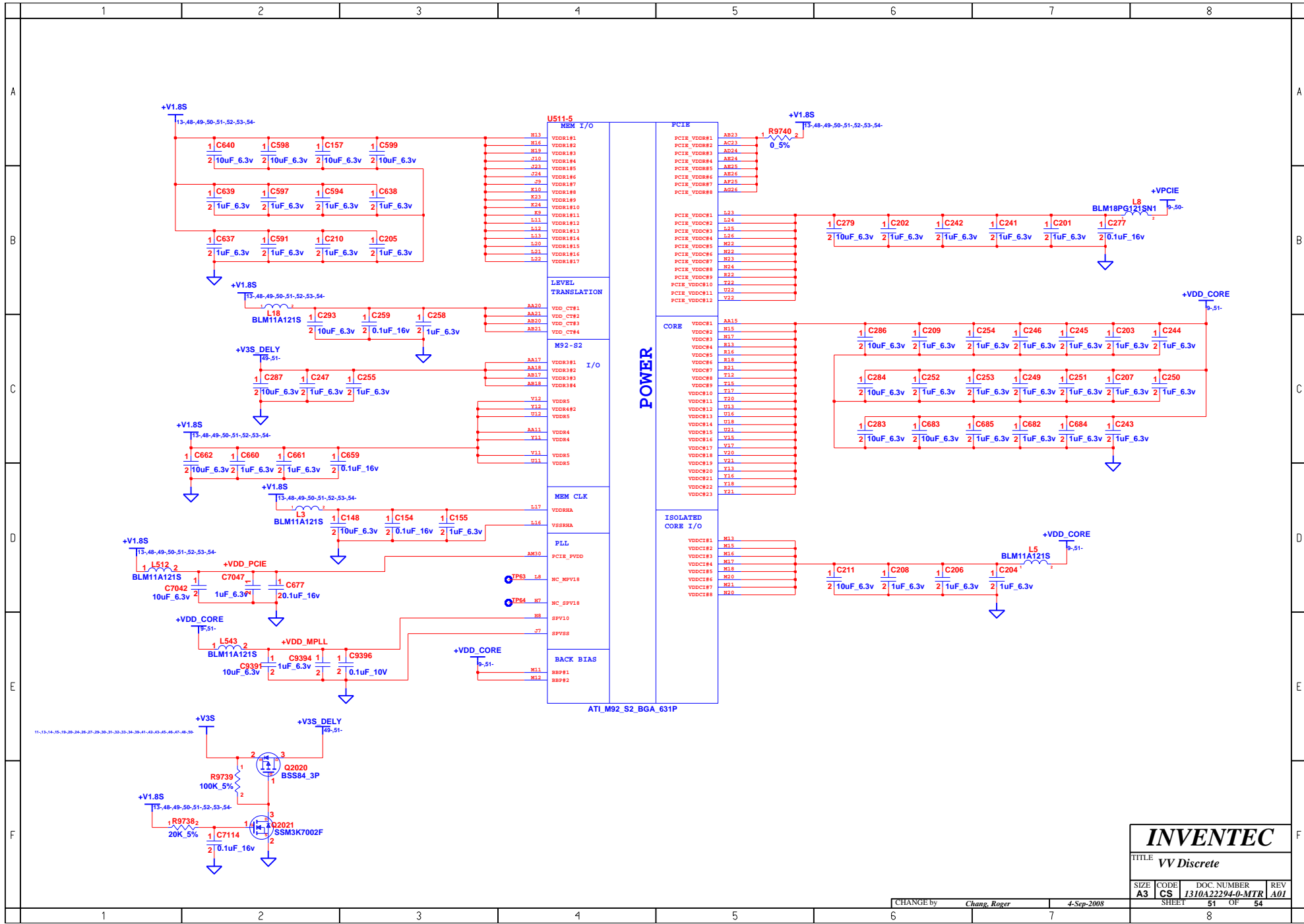
Table 2

MEM_ID3	MEM_ID2	MEM_ID1	MEM_ID0	vendor
0	1	0	1	samsung (256MB)
0	1	1	0	Qimonda(256M) (Default)
0	1	1	1	hynix (256MB)



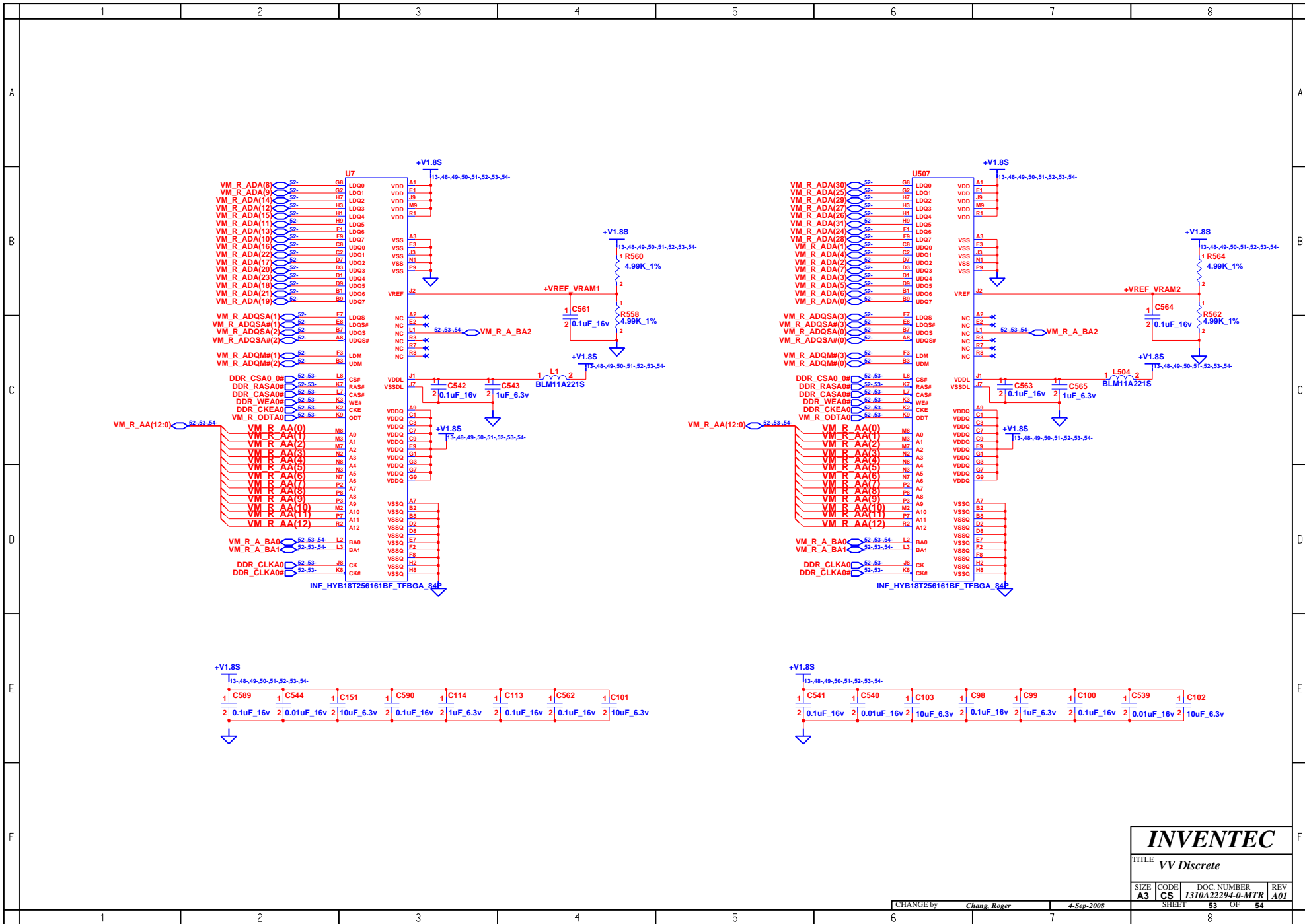
**INVENTEC**  
TITLE VV Discrete  
SIZE CODE DOC NUMBER  
A3 CS 1310A22294-0-MTR  
SHEET 49 OF 54  
REV A01





<b>INVENTEC</b>				
TITLE <i>VV Discrete</i>				
SIZE	CODE	DOC. NUMBER	REV	
A3	CS	1310A22394-0-MTR	51	A01
CHANGE by		Chang, Roger	4-Sep-2008	
SHEET		51	OF	54





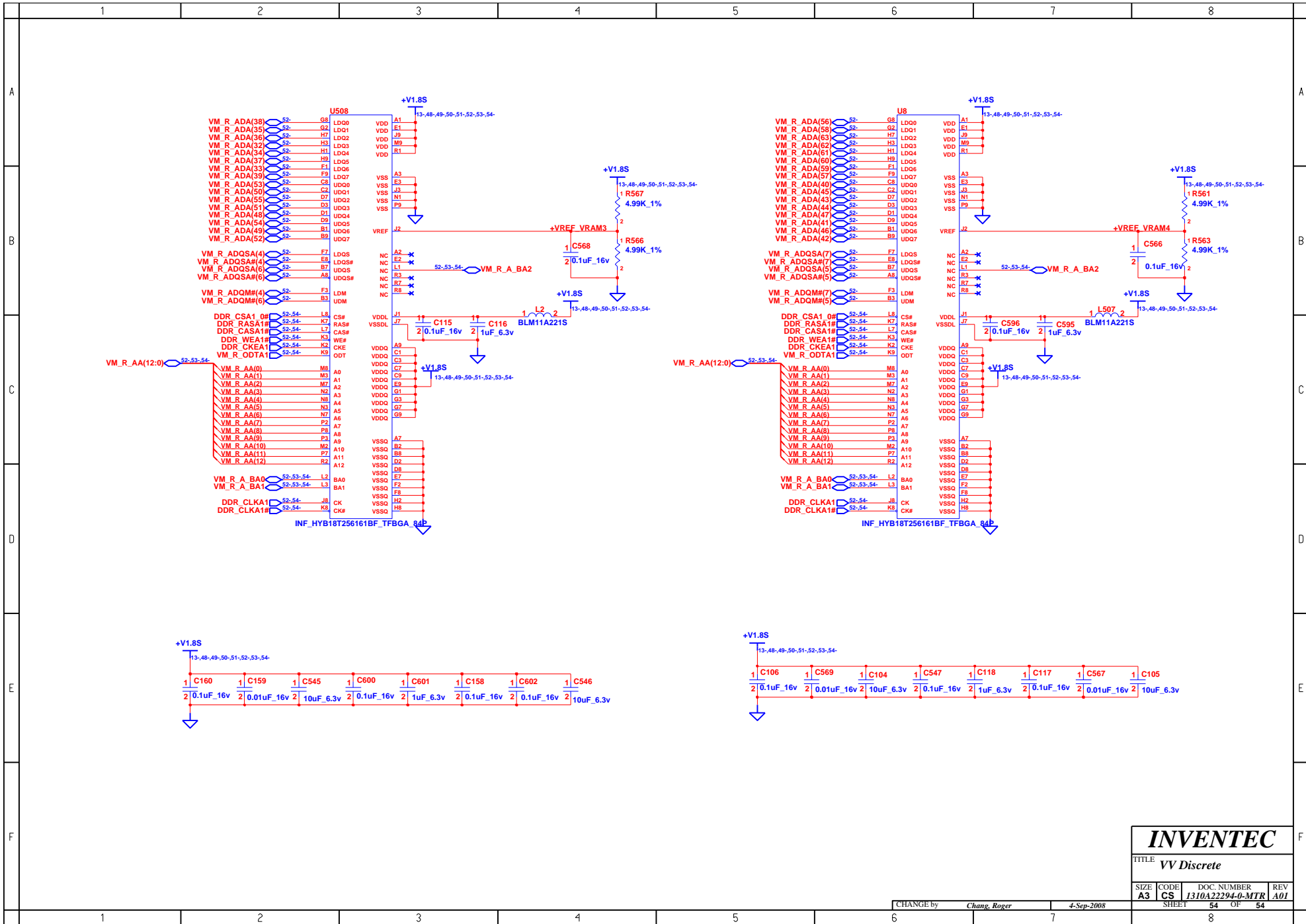
# INVENTEC

TITLE VV Discrete

SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A22294-0-MTR	A01

CHANGE by Chang, Roger 4-Sep-2008

SHEET 53 OF 54



<b>INVENTEC</b>			
TITLE <b>VV Discrete</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A22294-0-MTR</b>	REV <b>A01</b>
CHANGE by <i>Chang, Roger</i>		4-Sep-2008	
SHEET <b>54</b>		OF <b>54</b>	