

Compal Confidential

Schematics Document

AUBURNDALE/CLARKSFIELD with
Intel IBEX PEAK-M core logic

Dior DIS

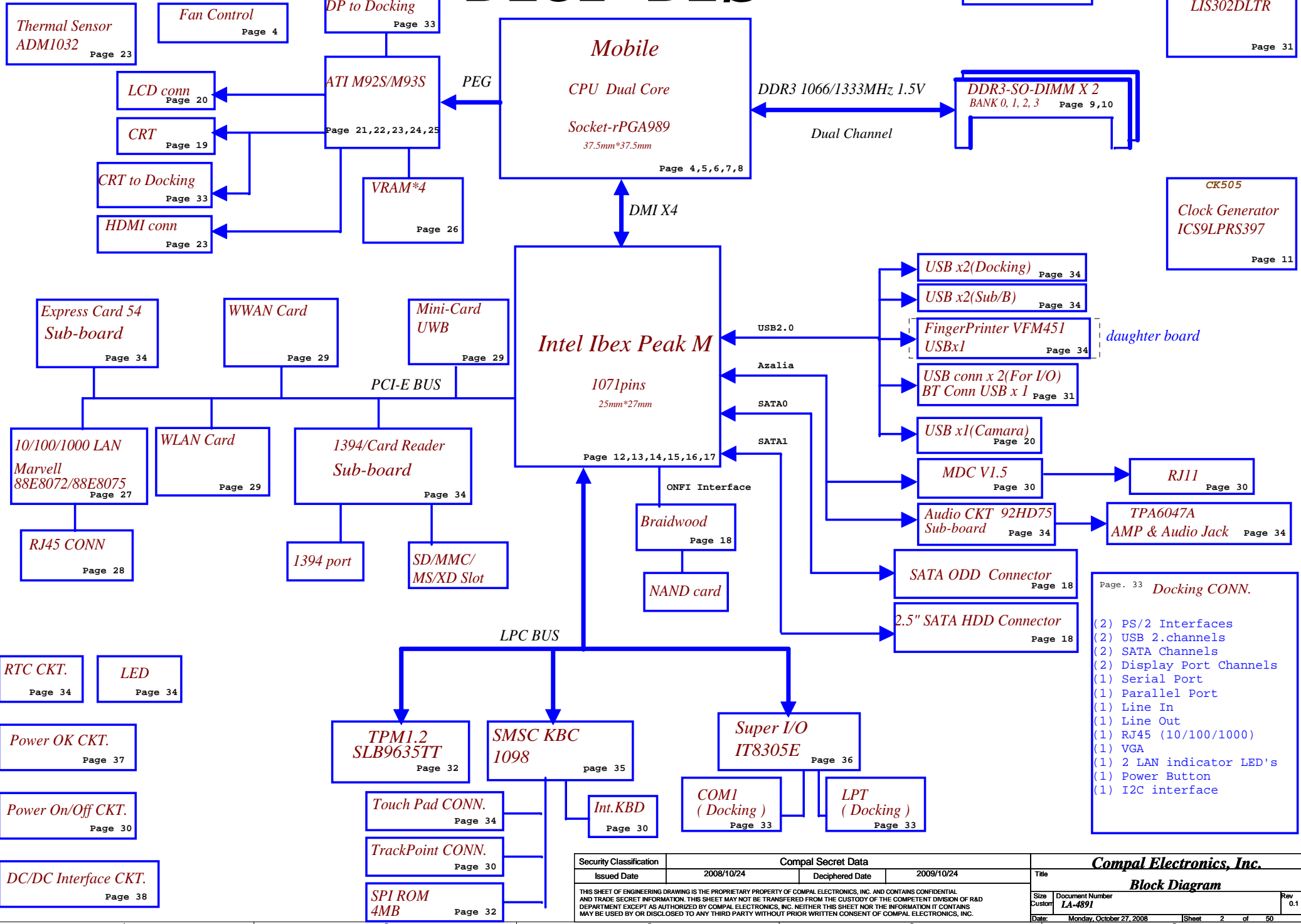
2008-10-30

REV:0.1

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	產出人員	
	產出日期	
	解密日期	

Security Classification	Compal Secret Data			Title		
Issued Date	2008/10/24	Deciphered Date	2009/10/24	Cover Sheet		
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Dior DIS

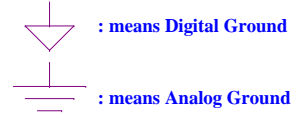


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Voltage Rails (O MEANS ON X MEANS OFF)

power plane State	+RTCVCC	+B +3VL	+5VALW +3VALW	+1.5V +0.75V	+5VS +3VS +1.5VS +NVVDD +VCCP +CPU_CORE +1.05VS +1.8VS
S0	O	O	O	O	O
S1	O	O	O	O	O
S3	O	O	O	O	X
S5 S4/AC	O	O	O	X	X
S5 S4/ Battery only	O	O	X	X	X
S5 S4/AC & Battery don't exist	O	X	X	X	X

Symbol Note :



Install below 43 level BOM structure for ver. 0.1

DEBUG@ : means just build when PCIE port 80 CARD function enable. *Remove before MP*

M92@ : Install for M92 Graphic controller

8072@ : Install for 8072 NIC controller

1098@ : Install for 1098 KBC controller

CK32@ : Install for 32 pin CLOCK GEN

Install below 45 level BOM structure for ver. 0.1

45@ : means just put it in the BOM of 45 level.

Reserve below BOM structure for ver. 0.1

@ : means just reserve , no build

CONN@ : means ME part.

M93@ : Install for M93 Graphic controller

8075@ : Install for 8075 NIC controller

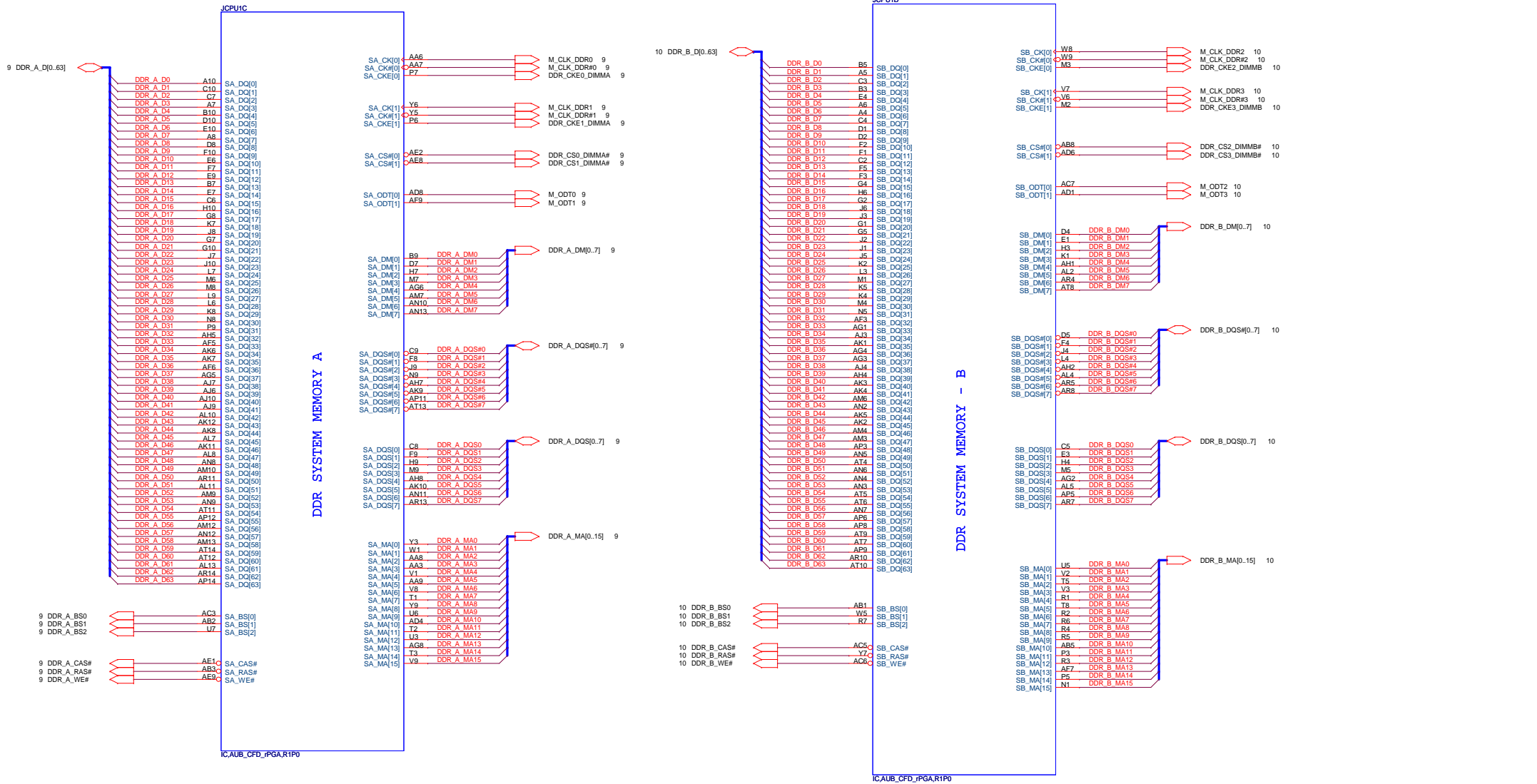
1091@ : Install for 1091 KBC controller

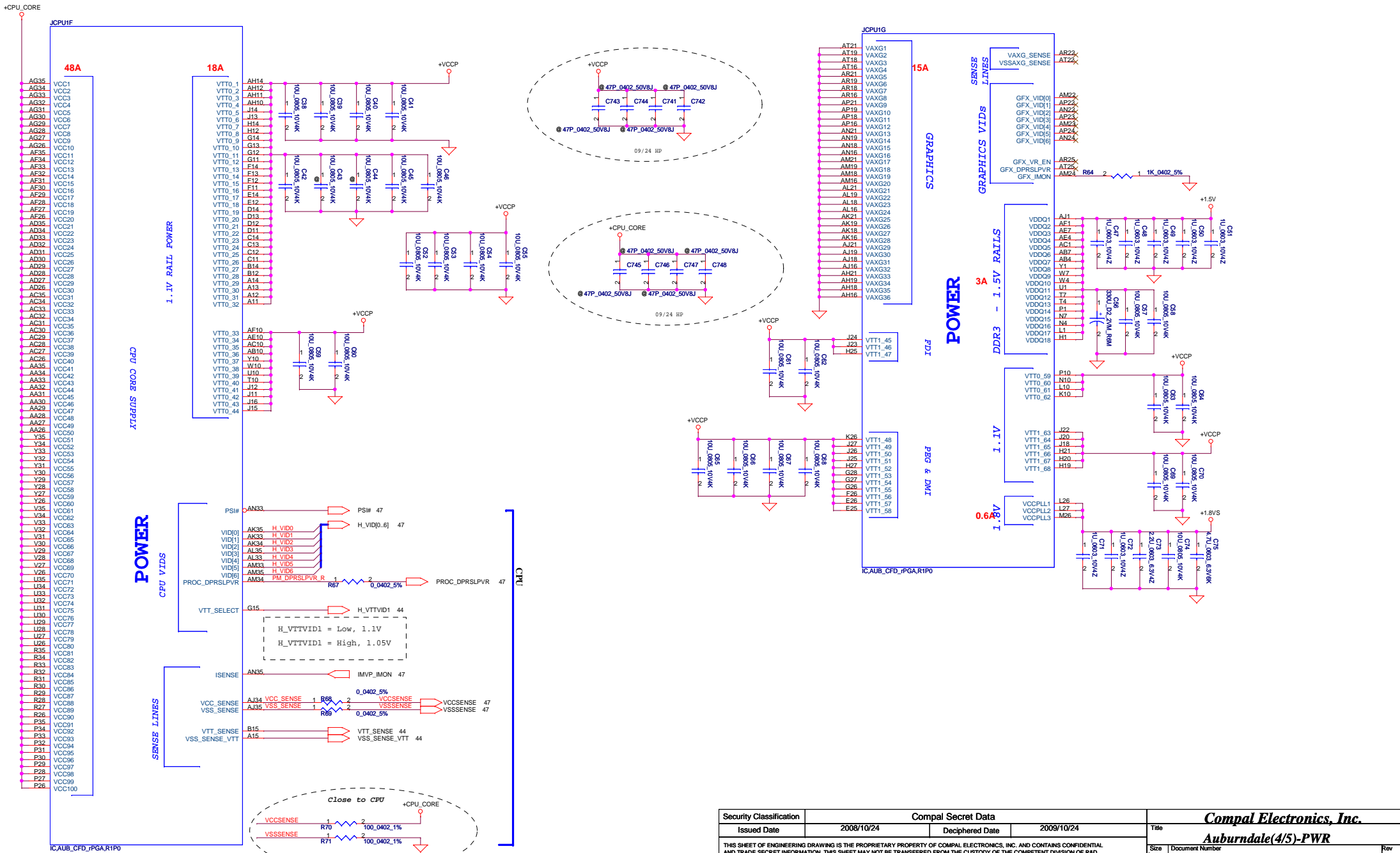
CK72@ : Install for 72 pin CLOCK GEN

SMBUS Control Table

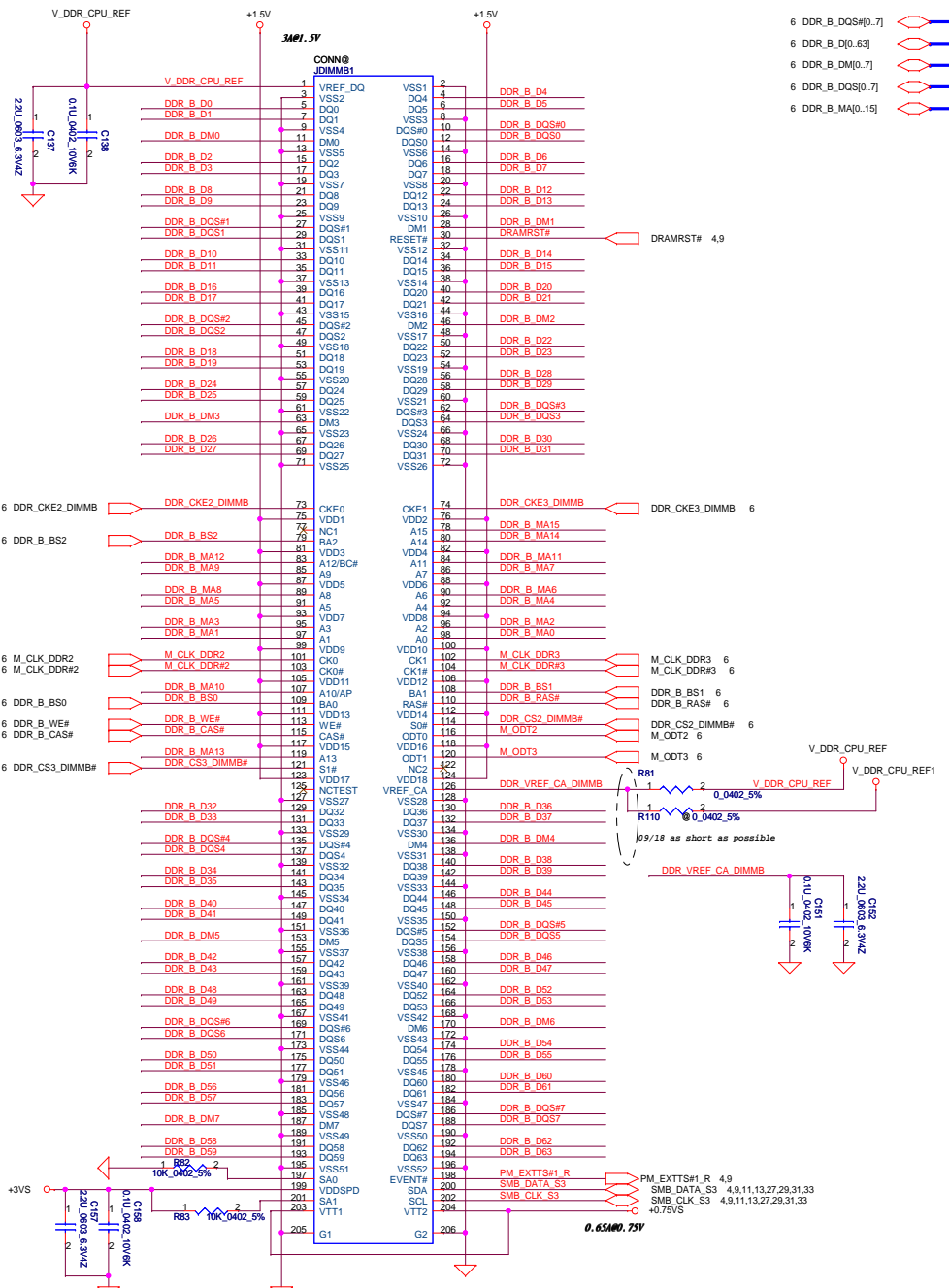
	SOURCE	BATT	XDP	SODIMM	CLK CHIP	MINI CARD	DOCK	NIC	THERMAL SENSOR	G-SENSOR
SMB_EC_CK1 SMB_EC_DAI	SMSC1098	V	X	X	X	X	X	X	X	X
SMBCLK SMBDATA	Calpella	X	V	V	V	V	V	V	X	V
SML0CLK SML0DATA	Calpella	X	X	X	X	X	X	X	X	X
SML1CLK SML1DATA	Calpella	X	X	X	X	X	X	X	V	X

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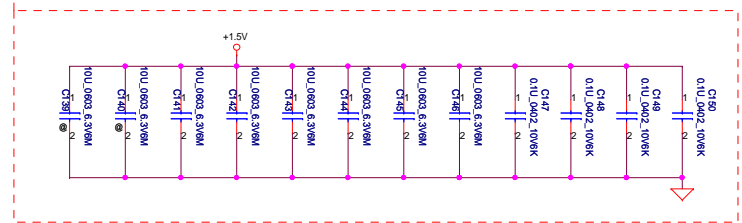


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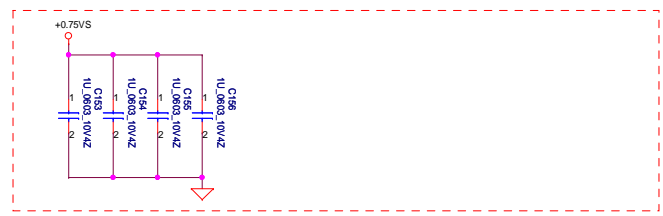


- 6 DDR_B_DQS#0..7
- 6 DDR_B_D[0..63]
- 6 DDR_B_DM[0..7]
- 6 DDR_B_DQS[0..7]
- 6 DDR_B_MA[0..15]

Layout Note:
Place near DIMM

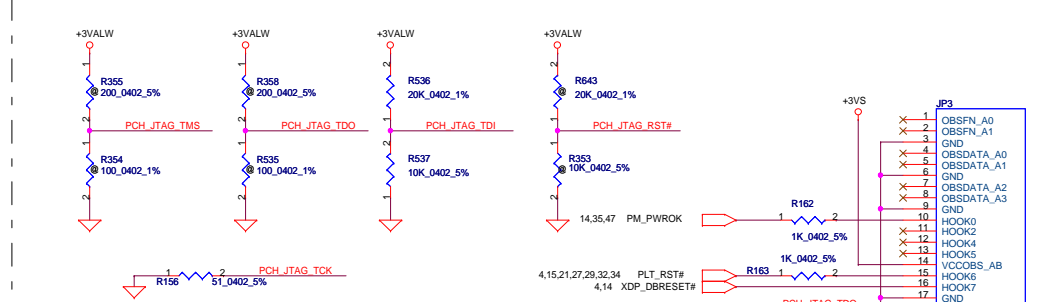
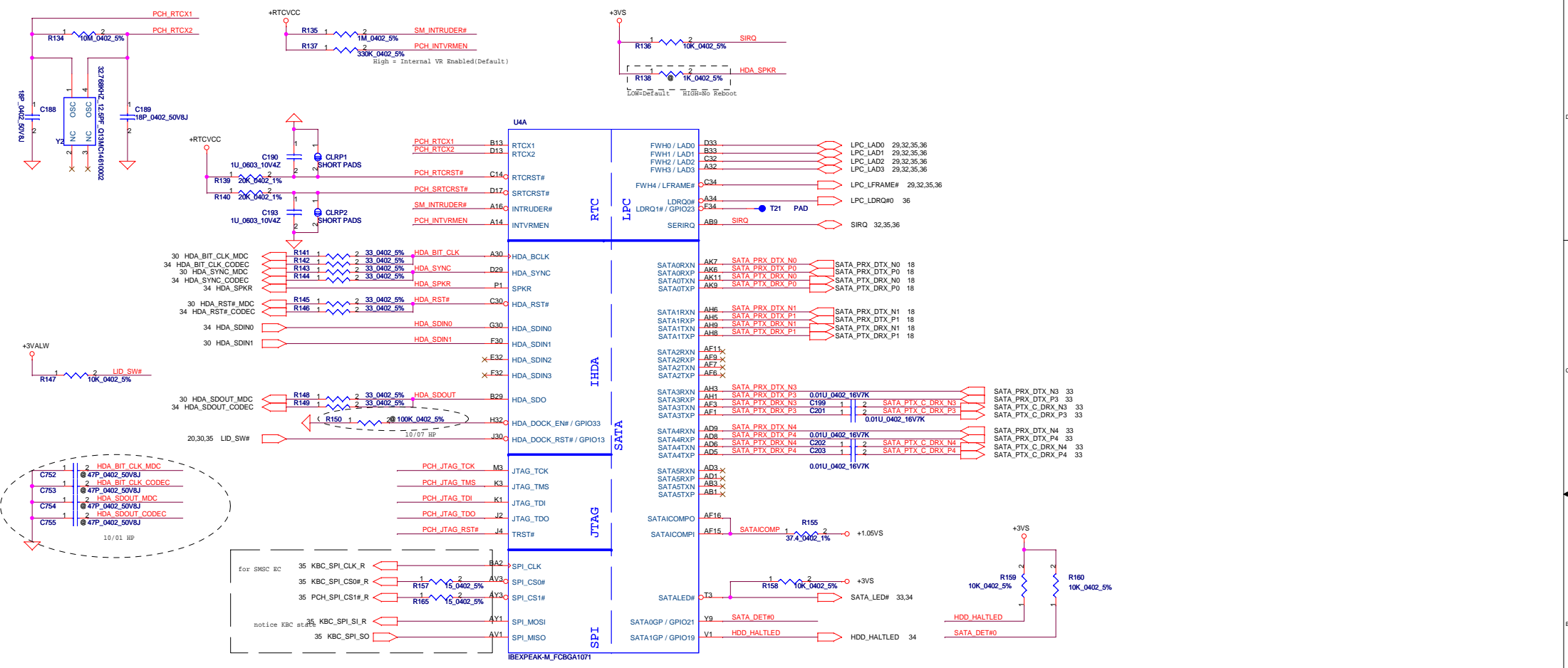


Layout Note:
Place near DIMM

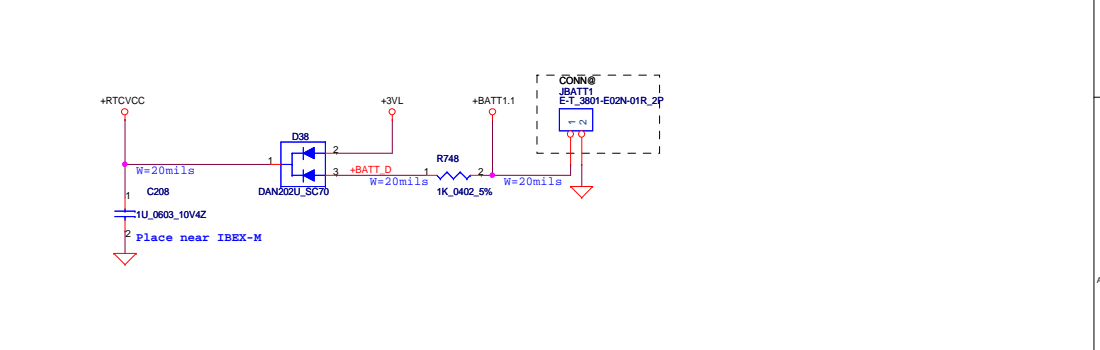


BOT SLOT

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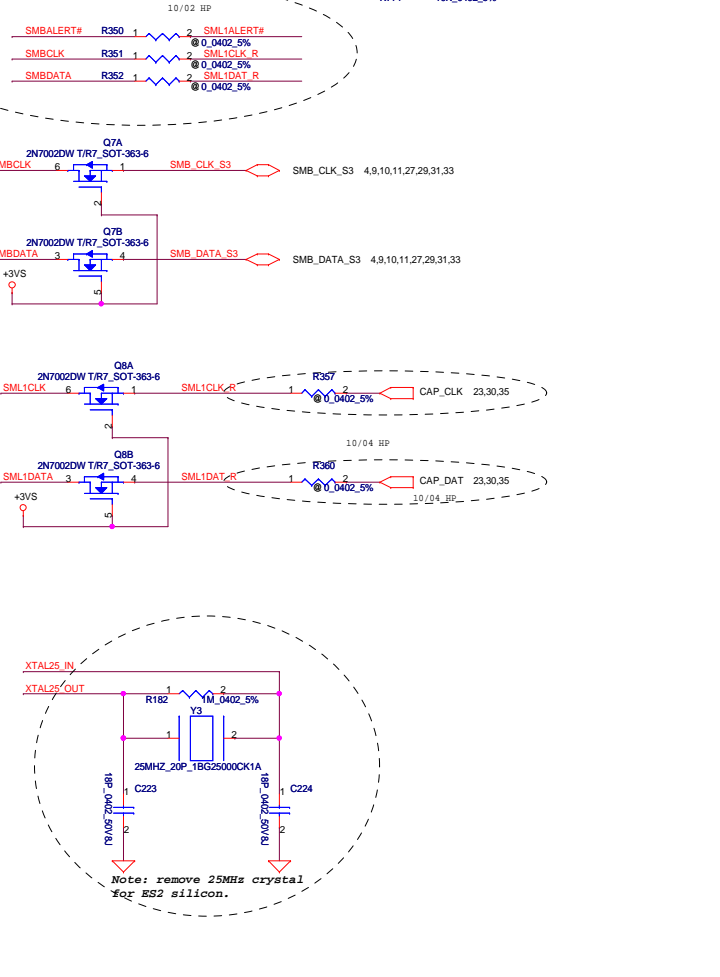
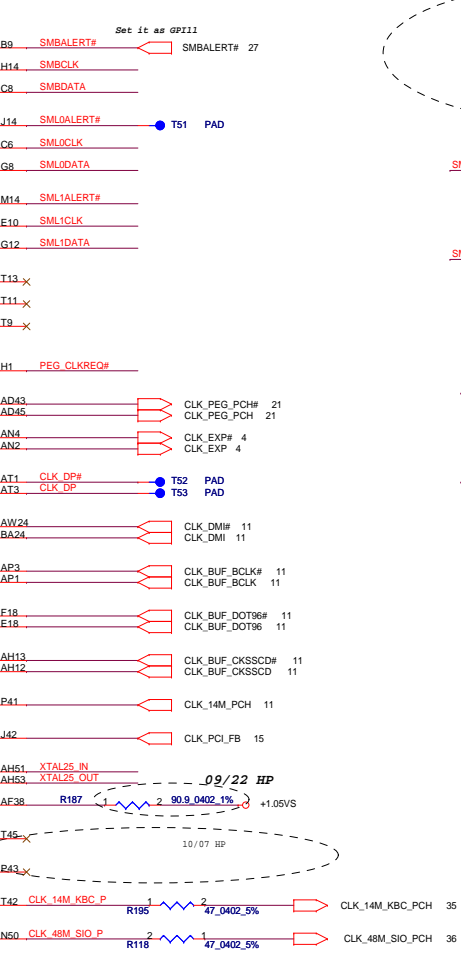
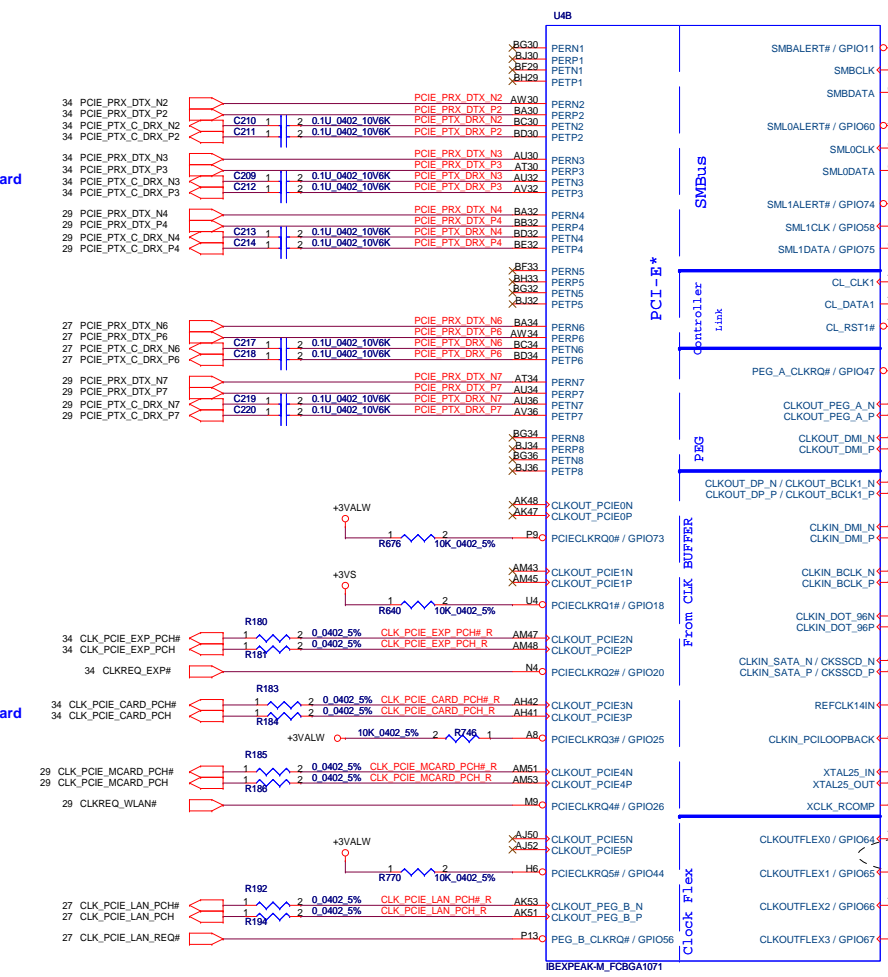


PCH Pin	RefDes	PCH JTAG Enable		PCH JTAG Disable	
		ES1	ES2	ES1	ES2
PCH_JTAG_TDO	R358	No Install	200ohm	No Install	No Install
PCH_JTAG_TMS	R355	No Install	100ohm	No Install	No Install
PCH_JTAG_TDI	R354	100ohm	100ohm	No Install	No Install
PCH_JTAG_RST#	R356	200ohm	200ohm	No Install	No Install
PCH_JTAG_TCK	R357	100ohm	100ohm	No Install	No Install
PCH_JTAG_TCK	R356	51ohm	51ohm	51ohm	51ohm
PCH_JTAG_RST#	R353	200ohm	200ohm	No Install	No Install
PCH_JTAG_RST#	R353	100ohm	100ohm	No Install	No Install

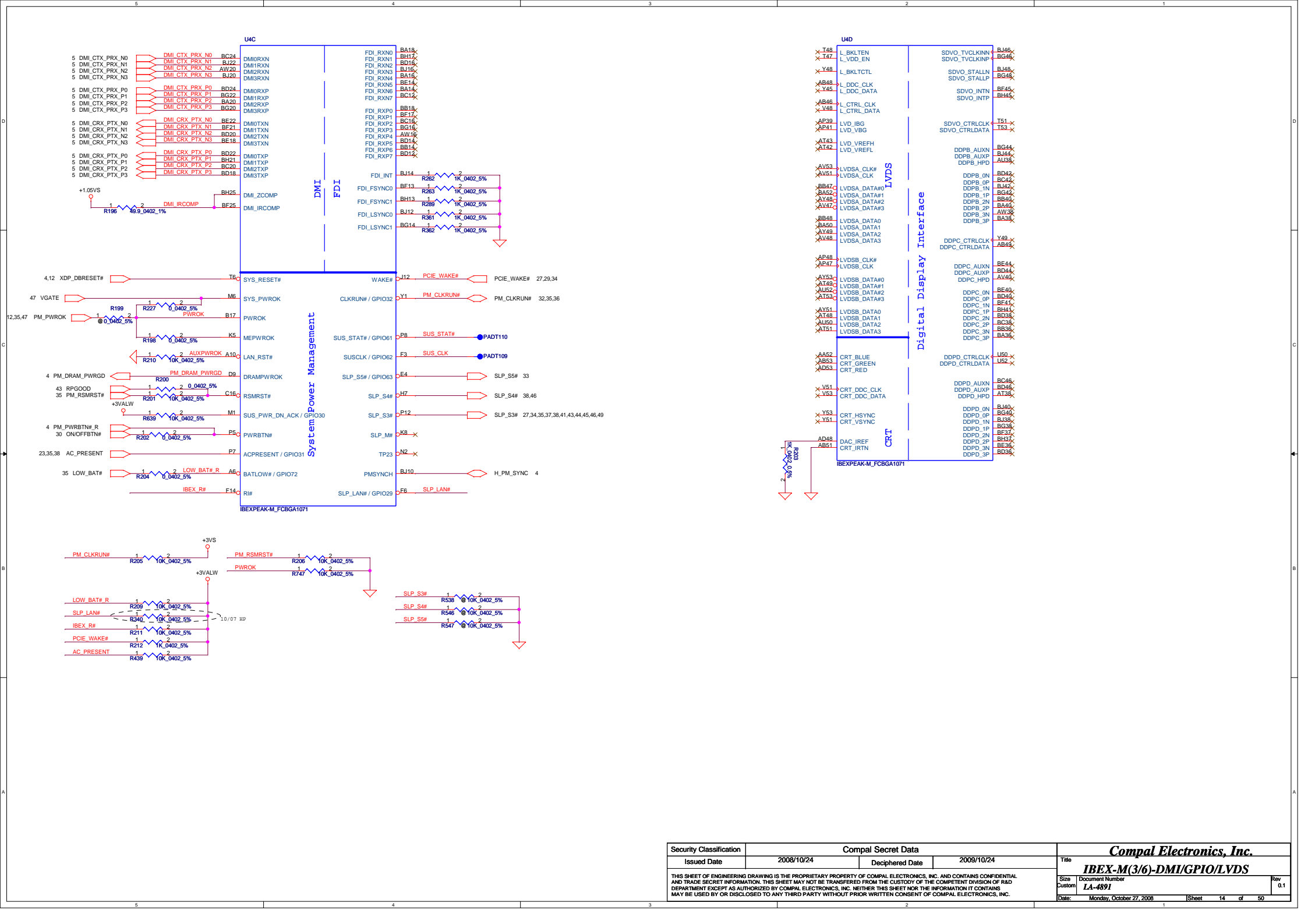


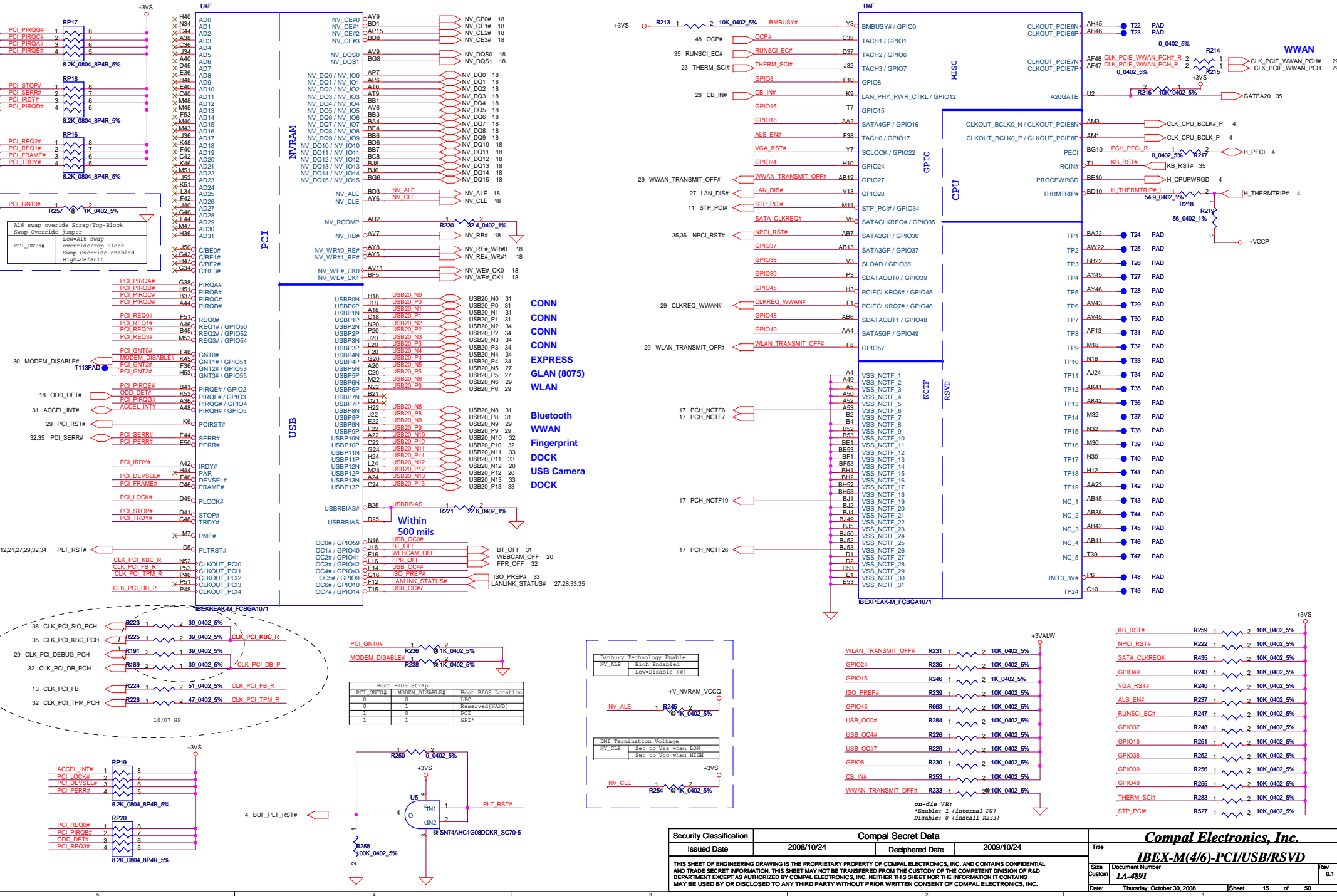
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EXP
Media Card
WLAN
NIC
WWAN
EXP
Media Card
WLAN
NIC

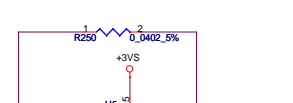


Security Classification	Compal Secret Data		Title IBEX-M(2/6)-PCI-E/SMBUS/CLK
Issued Date	2008/10/24	Deciphered Date	
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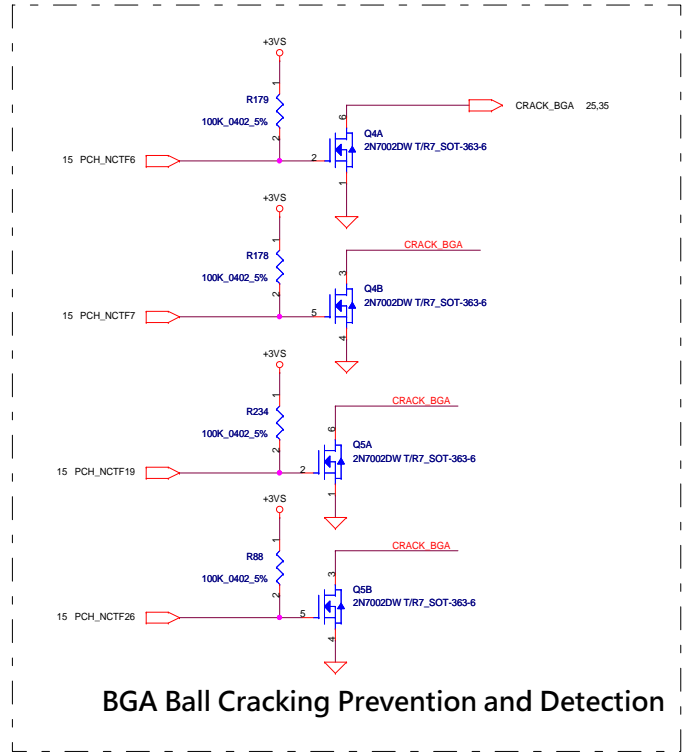
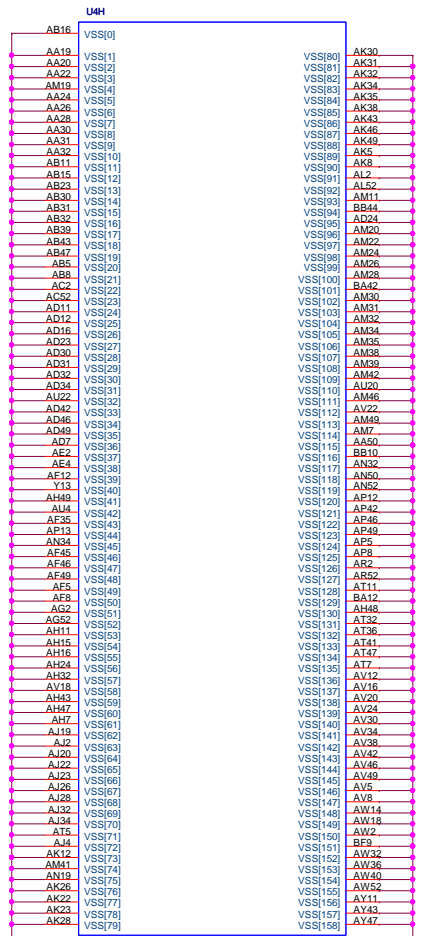
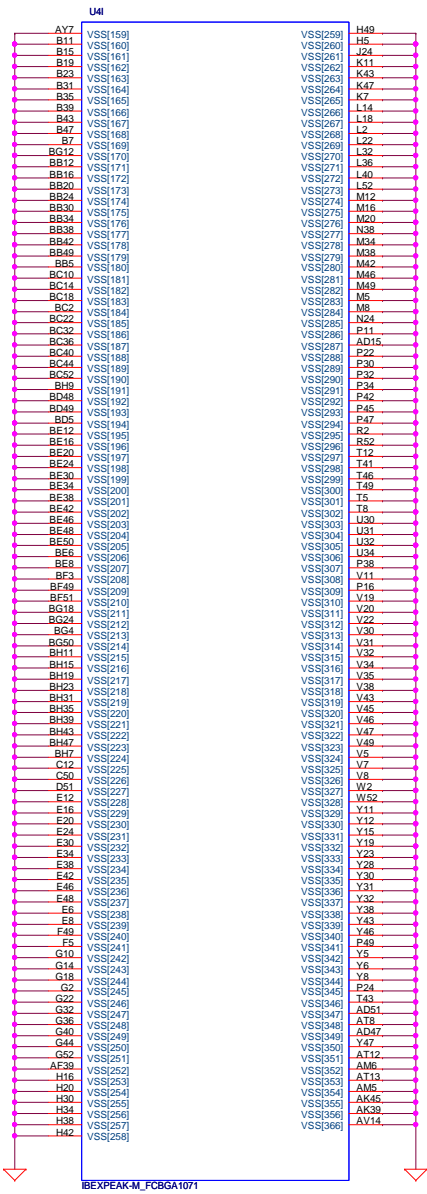
PCI_GNT0#	MODEM_DISABLE#	Boot BIOS Location
0	0	LPC
1	1	Reserved(MAN0)
1	0	PCI
1	1	SPI*



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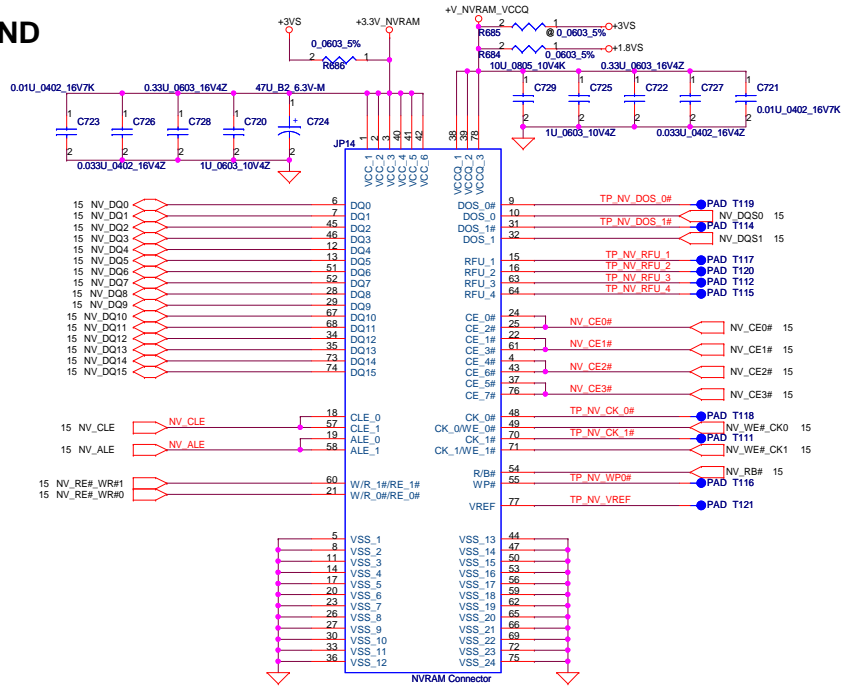
Title		Compal Electronics, Inc.	
IBEX-M(4/6)-PCI/USB/RSVD <td colspan="2">Rev 0.1</td>		Rev 0.1	
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BGA Ball Cracking Prevention and Detection

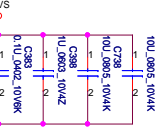
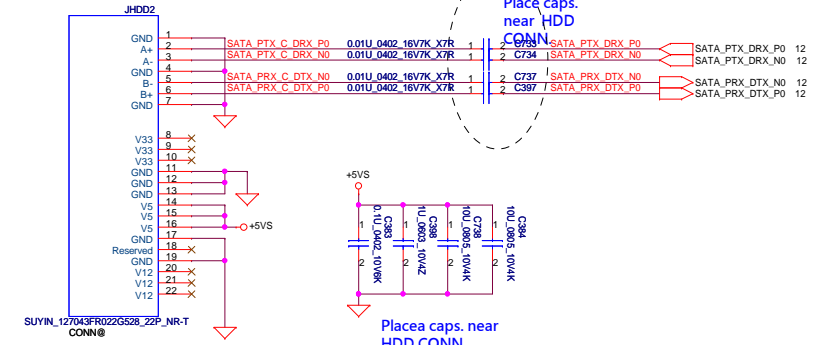
Security Classification	Compal Secret Data		Title Compal Electronics, Inc. IBEX-M(6/6)-GND
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NAND

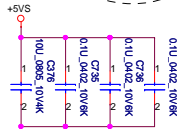
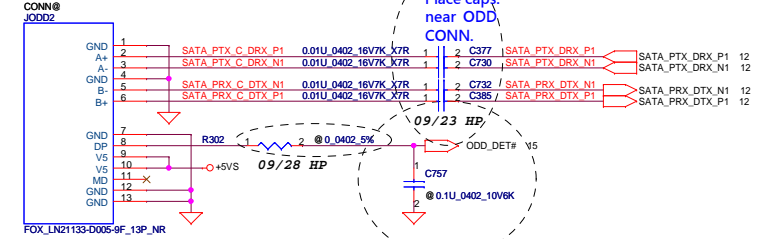


NVRAM Connector

SATA HDD CONN.



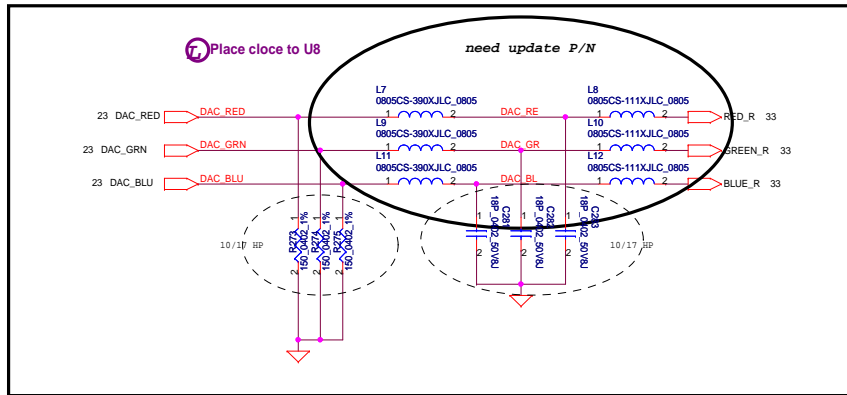
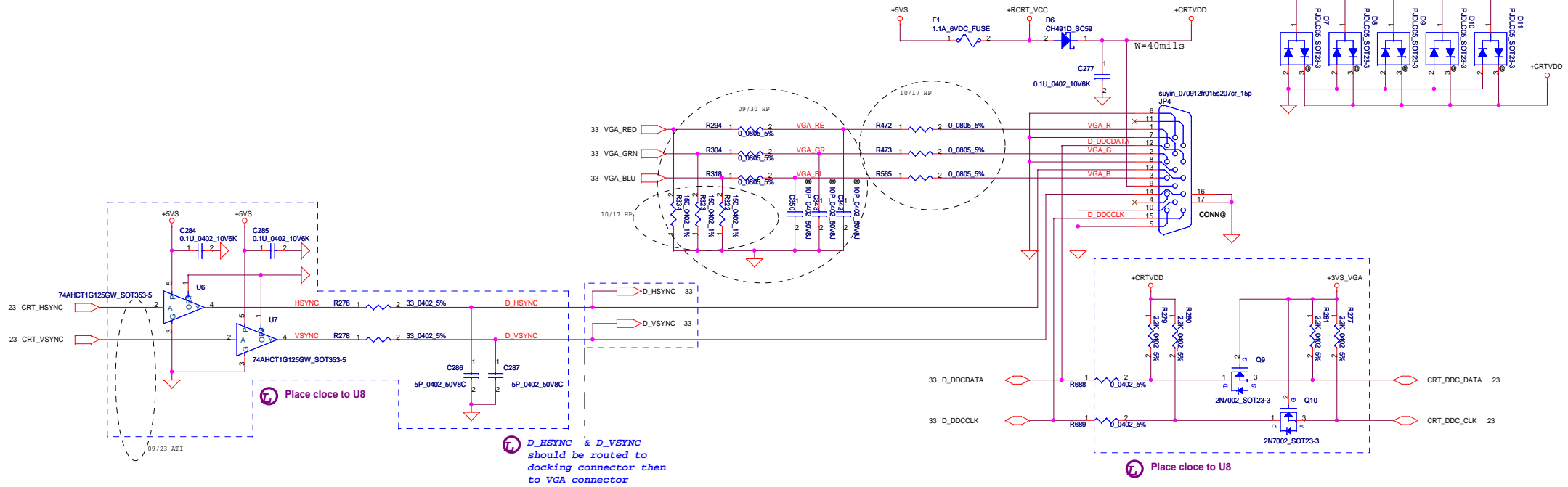
SATA ODD CONN.



Place caps. near ODD CONN.

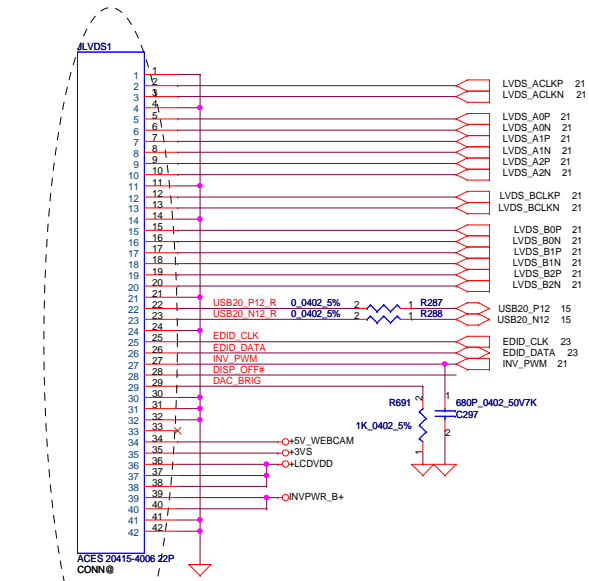
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CRT Connector



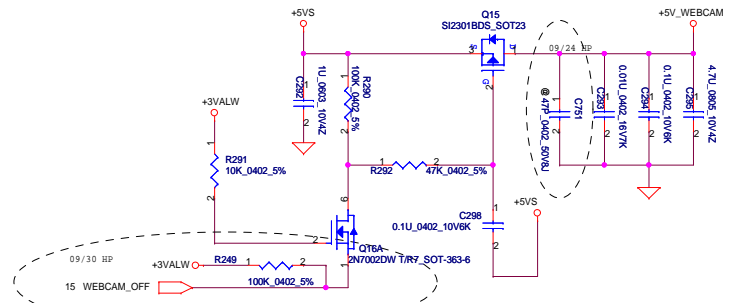
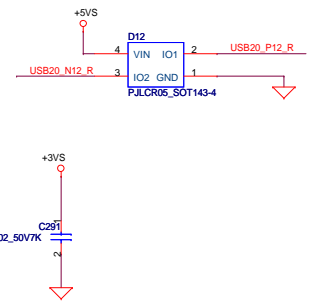
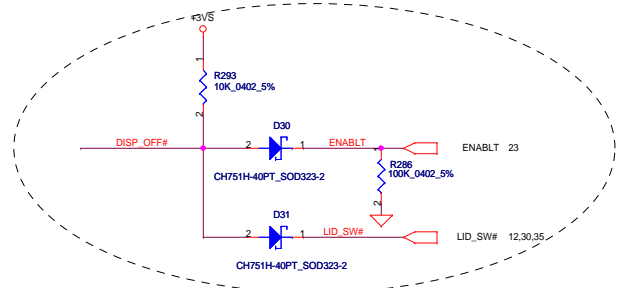
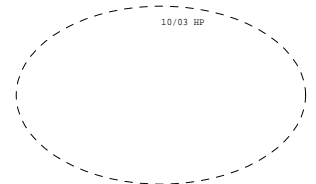
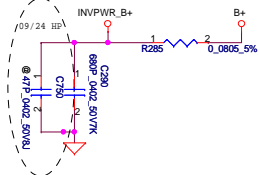
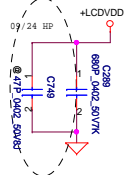
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LCD/PANEL BD. CONN.

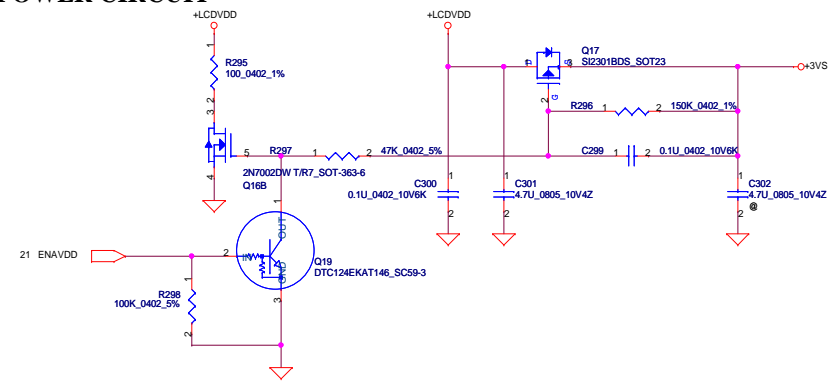


ACES 20415-4006 22P CONN®

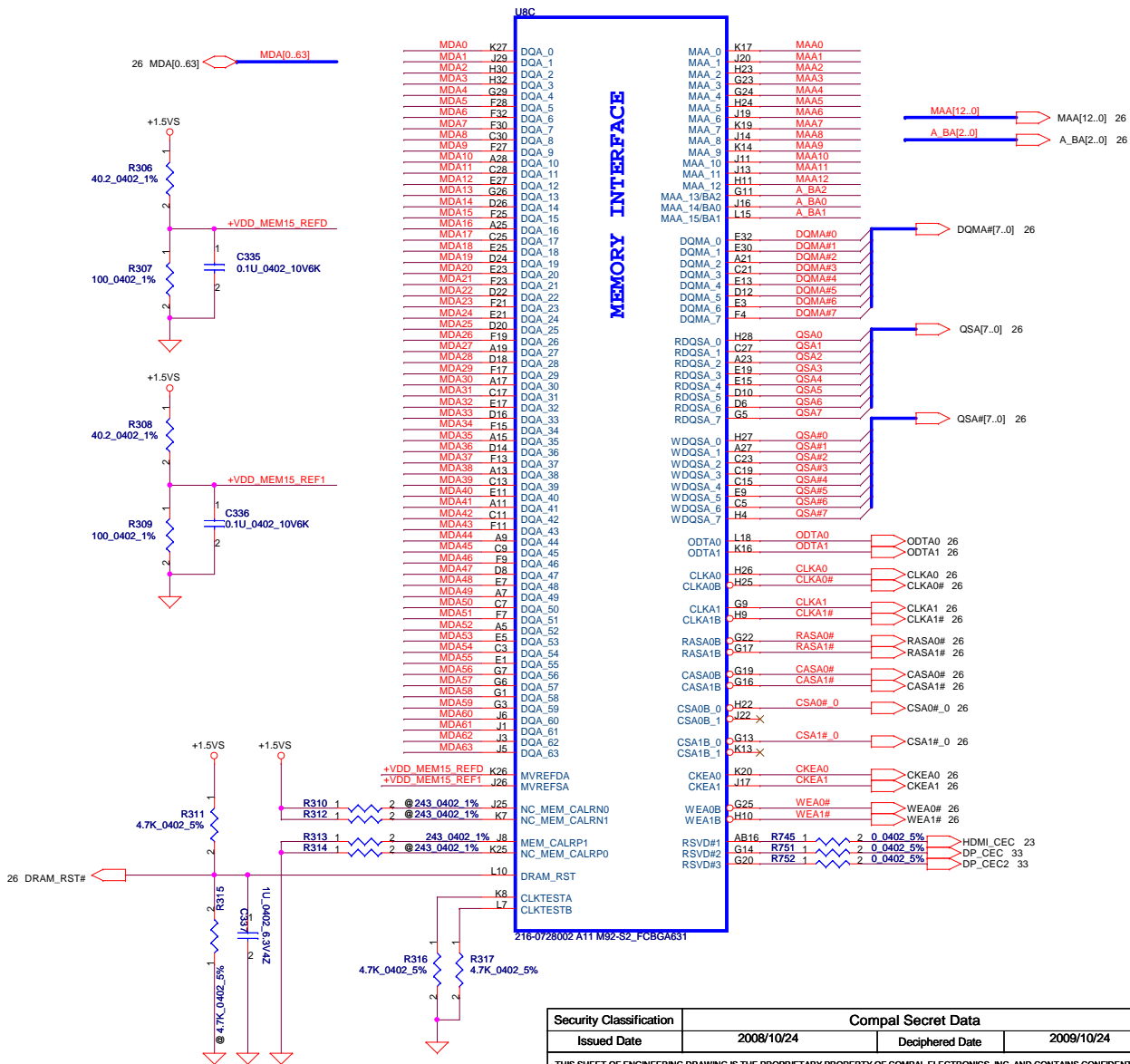
10/01 Change the footprint



LCD POWER CIRCUIT

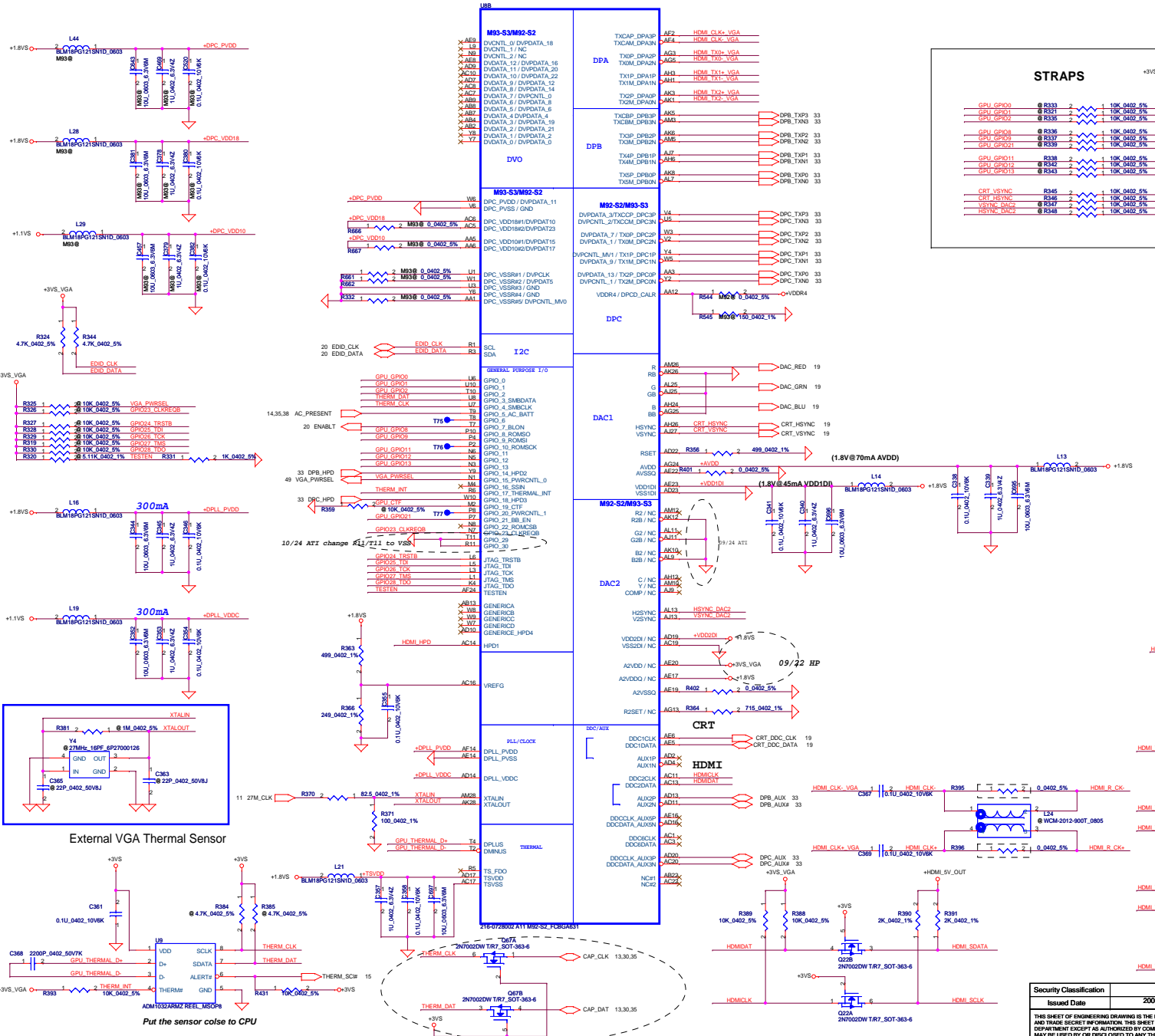


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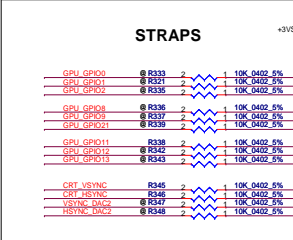
Compal Electronics, Inc.		
Memory Interface		
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CONFIGURATION STRAPS

ALLOW FOR PULLUP PADS FOR THESE STRAPS AND IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET

STRAPS	PIN	DESCRIPTION OF DEFAULT SETTINGS	RECOMMENDED SETTINGS
TX_PWRS_ENB	GPIO0	POE FULL TX OUTPUT SWING	0
TX_DEEMPH_EN	GPIO1	POE TRANSMITTER DE-EMPHASIS ENABLED	0
BIF_GENZ_EN_A	GPIO2	POE GNE2 ENABLED	0
	GPIO8		0
BIF_VGA_DS	GPIO9	VGA ENABLED	0
	GPIO21		0
BIOS_ROM_EN	GPIO_22_ROMCSB	ENABLE EXTERNAL BIOS ROM	0
ROMCFGIG(2)	GPIO[3:11]	SERIAL ROM TYPE OR MEMORY APERTURE SIZE SELECT	001
VP_DEVICE_STRAP_ENA	V2SYNC	IGNORE VIP DEVICE STRAPS	0
	H2SYNC		0
	GENERICC		0
AUD[1]	HSYNC	AUD[1] AUD[0] 0 0 No audio function 0 1 Audio for DisplayPort and HDMI if dangle is detected 1 0 Audio for DisplayPort only 1 1 Audio for both DisplayPort and HDMI	11
AUD[0]	VSYNC		0

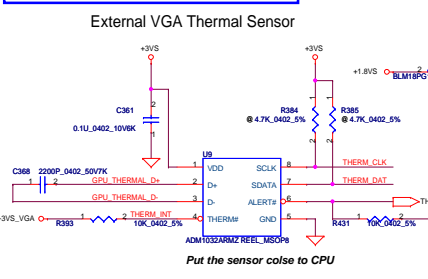
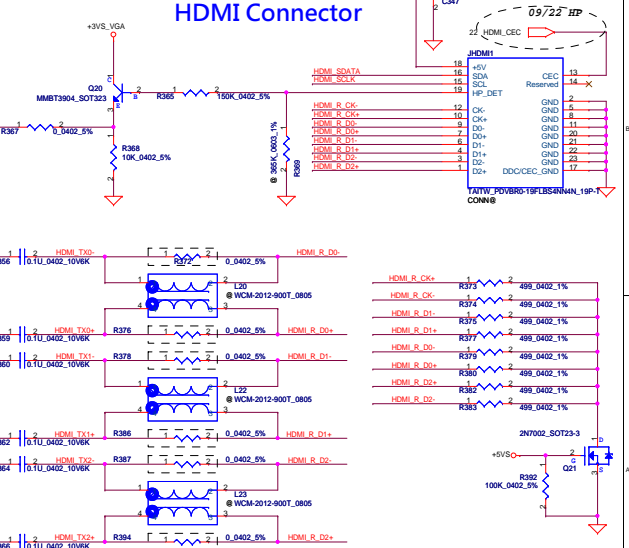


AMD RESERVED CONFIGURATION STRAPS

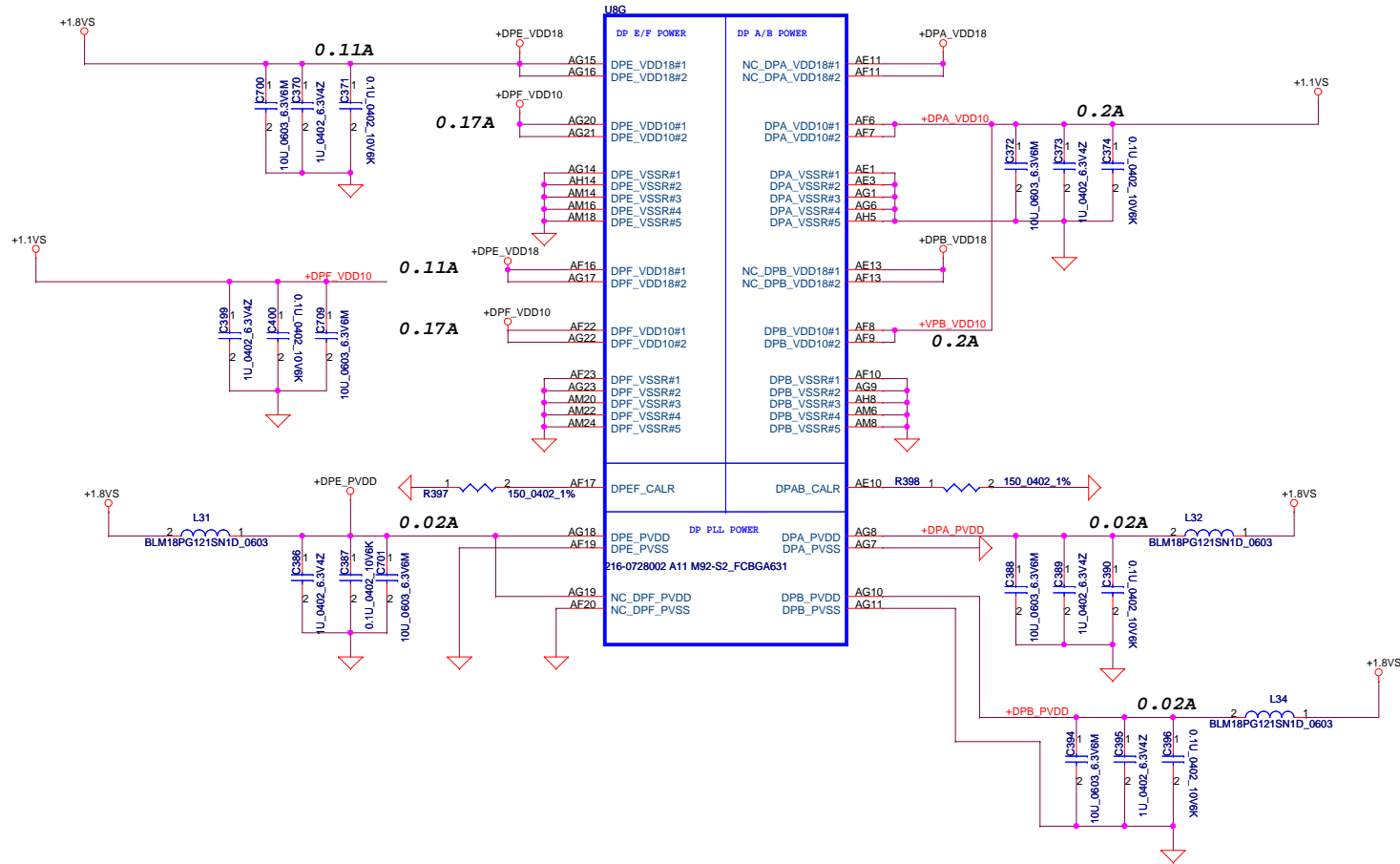
ALLOW FOR PULLUP PADS FOR THESE STRAPS AND IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET

STRAPS	DESCRIPTION OF DEFAULT SETTINGS	RECOMMENDED SETTINGS
H2SYNC	GENERICC	0

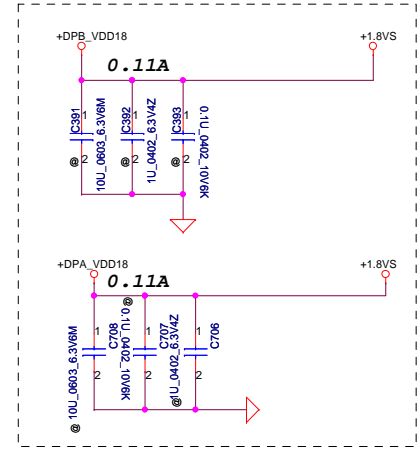
PULLUP PADS ARE NOT REQUIRED FOR THESE STRAPS BUT IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET



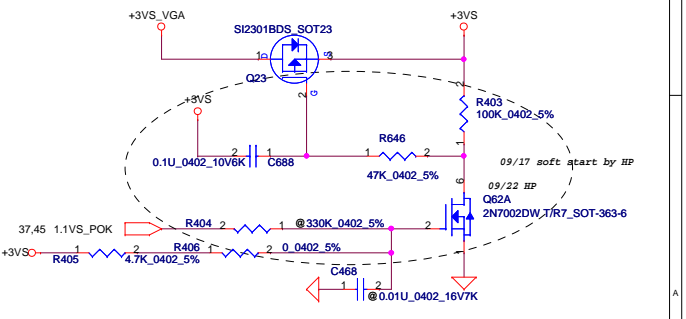
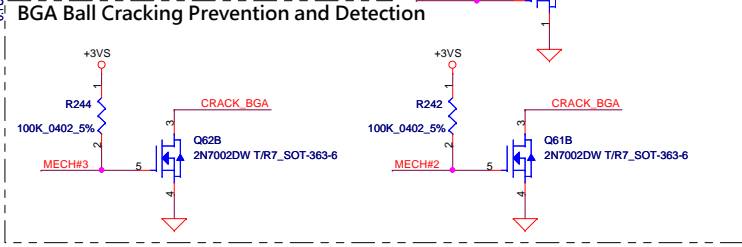
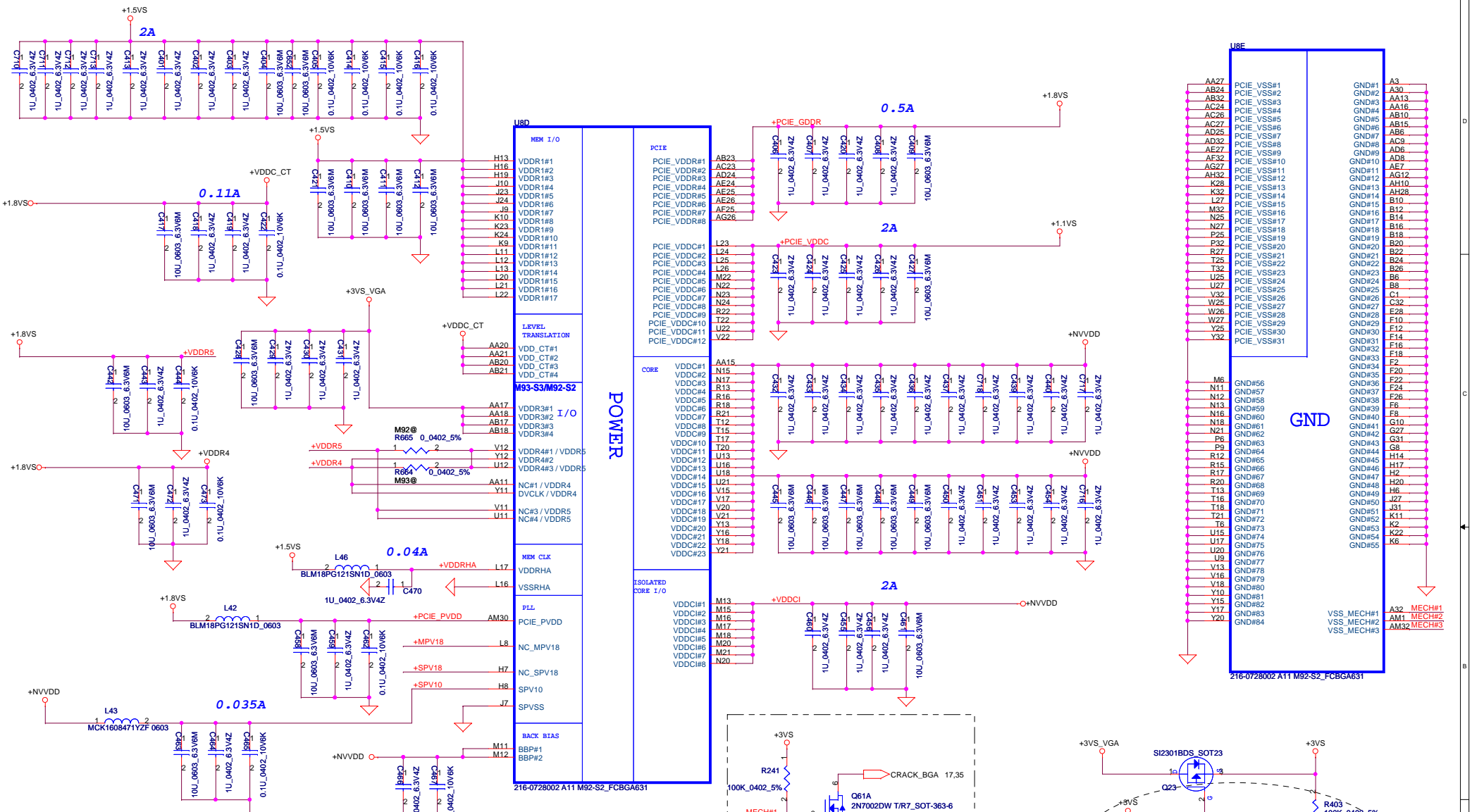
Security Classification	Compal Secret Data	
Issued Date	2009/10/24	Deciphered Date
		2009/10/24
Compal Electronics, Inc.		
HDMI, MSIC & Thermal		
Title	Document Number	Rev
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Date	Monday, October 27, 2008	Sheet 23 of 50



For Future S3 GPUs



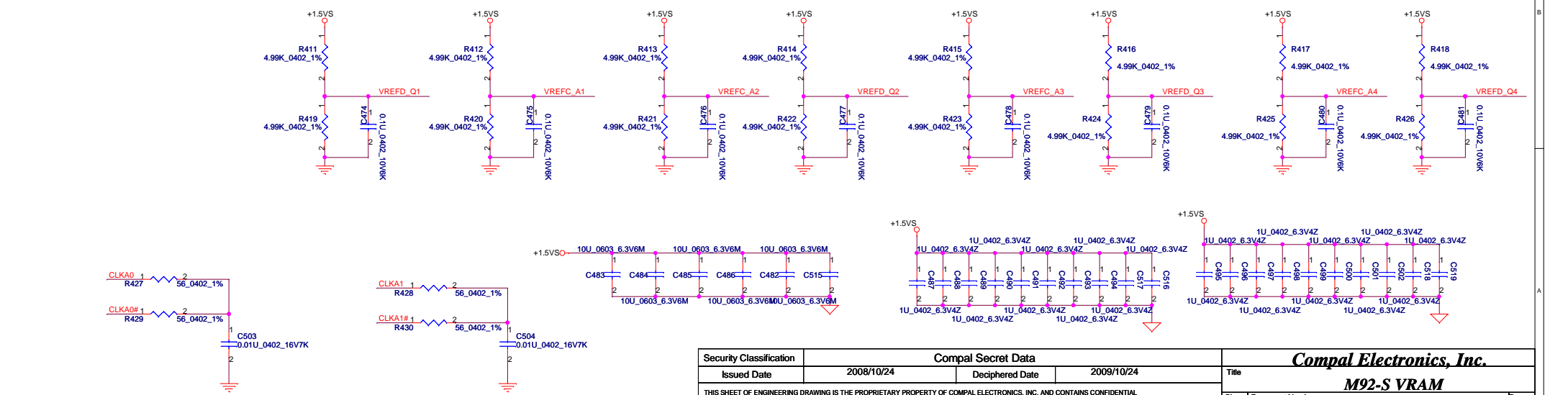
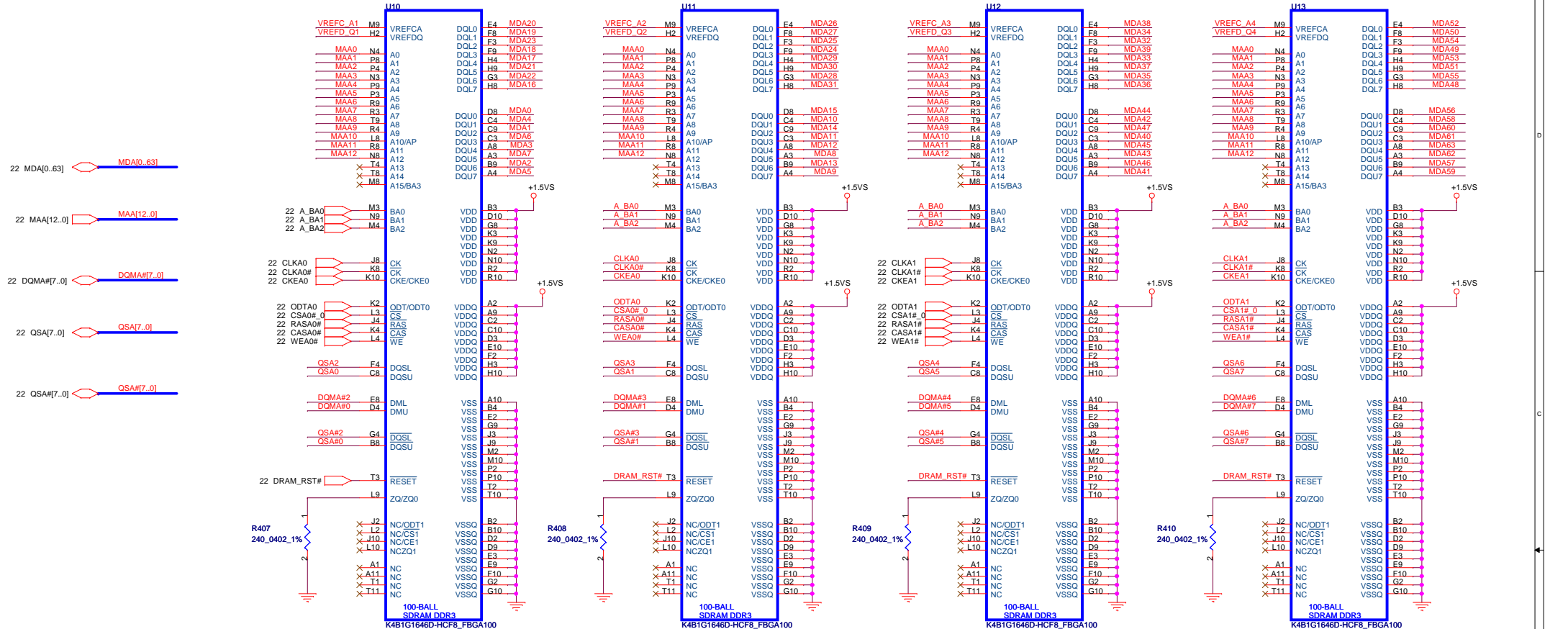
Security Classification	Compal Secret Data		<p align="center">Compal Electronics, Inc.</p> <p align="center">DPX POWER</p>	
Issued Date	2008/10/24	Deciphered Date		
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			Document Number LA-4891	Rev 0.1
Date: Monday, October 27, 2008			Sheet 24 of 50	



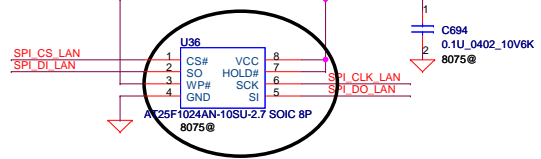
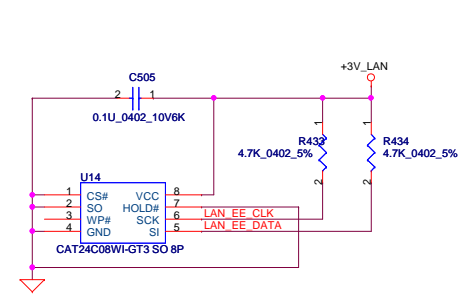
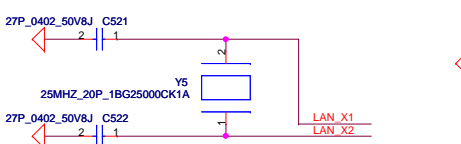
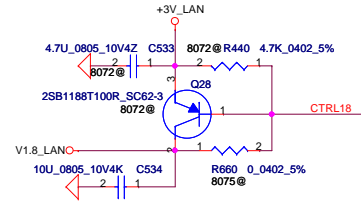
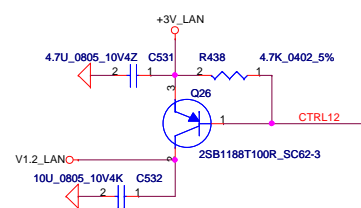
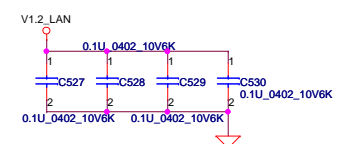
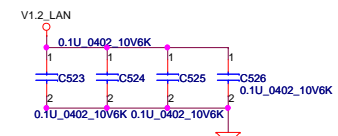
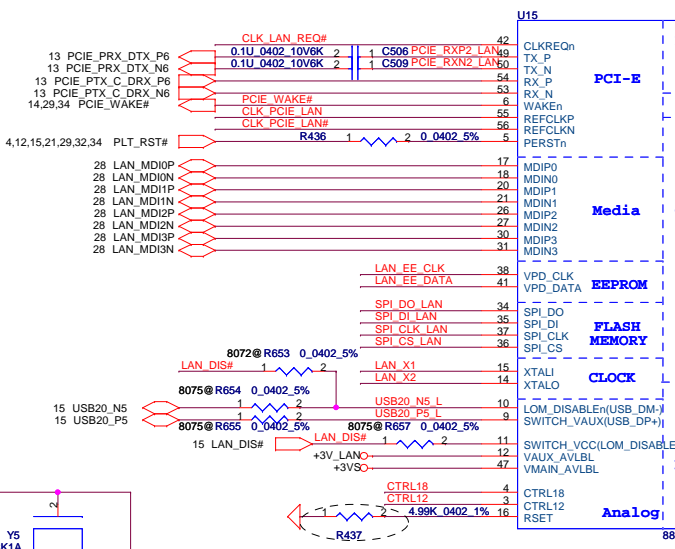
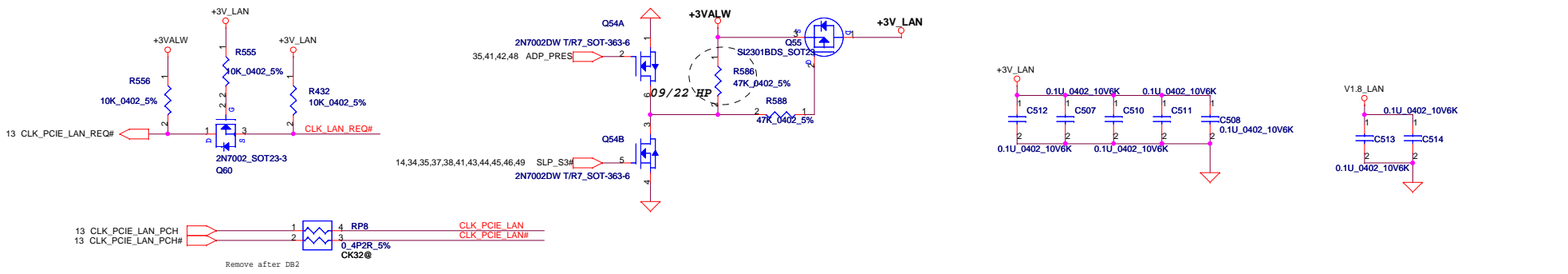
Security Classification	Compal Secret Data	
Issued Date	2008/10/24	Deciphered Date
		2009/10/24

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Compal Electronics, Inc.		
Main Power		
Size	Document Number	Rev
Custom	LA-4891	0.1
Date:	Monday, October 27, 2008	Sheet 25 of 50



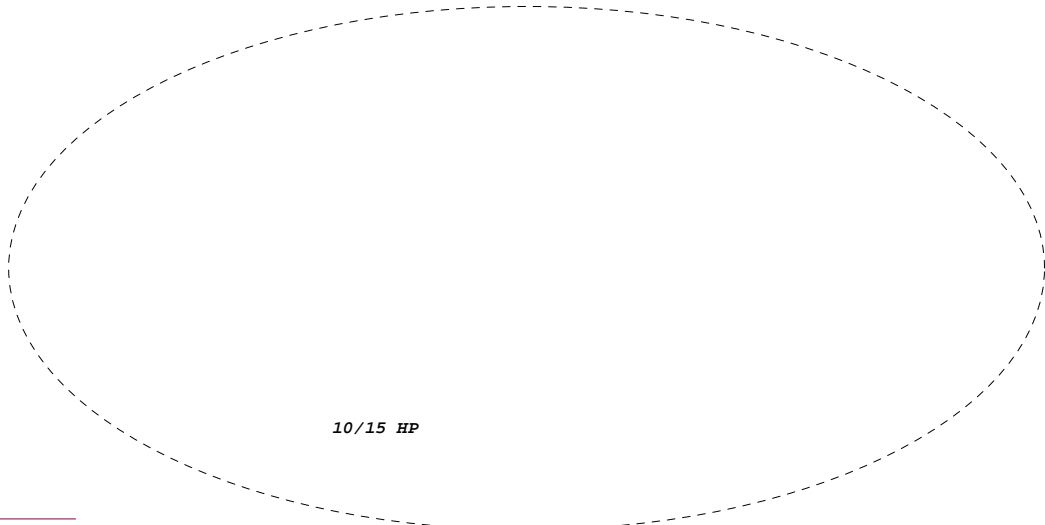
Security Classification	Compal Secret Data		Title	
Issued Date	2008/10/24	Deciphered Date	2009/10/24	2008/10/24
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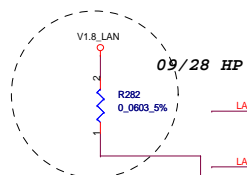
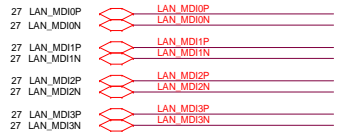
ROM size will update after check with vendor

Insert	88E8072	88E8075
	R653	R654
	C533	R655
	R440	U36
	Q28	C694
		R659
		R658
		R656
		R652
		R660

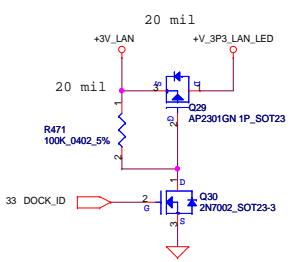
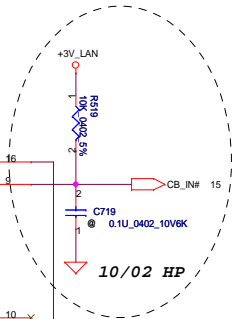
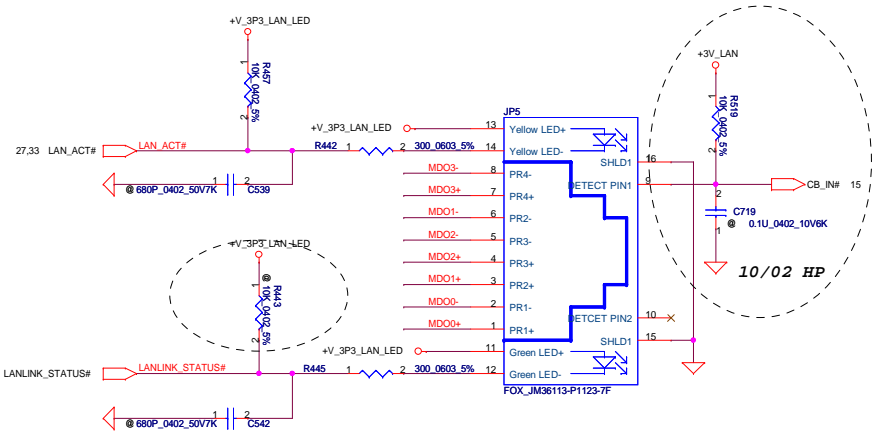
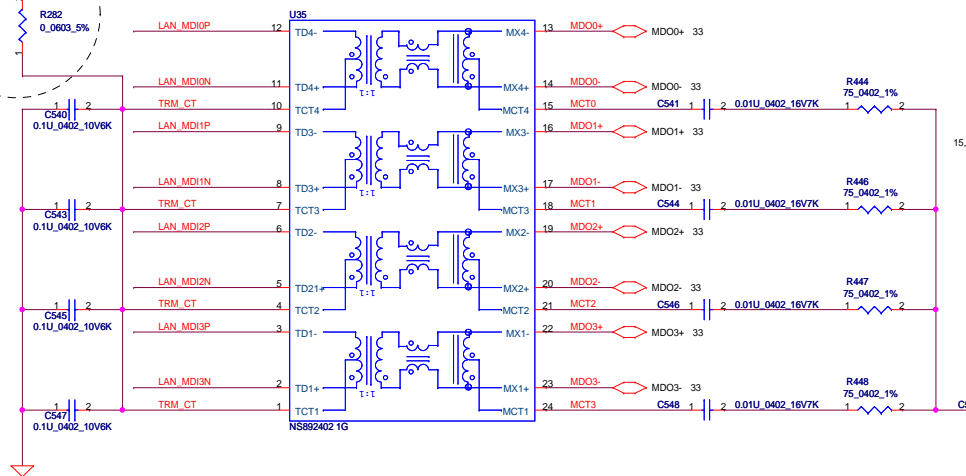
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				Date:	Monday, October 27, 2008	Sheet 27 of 50



10/15 HP

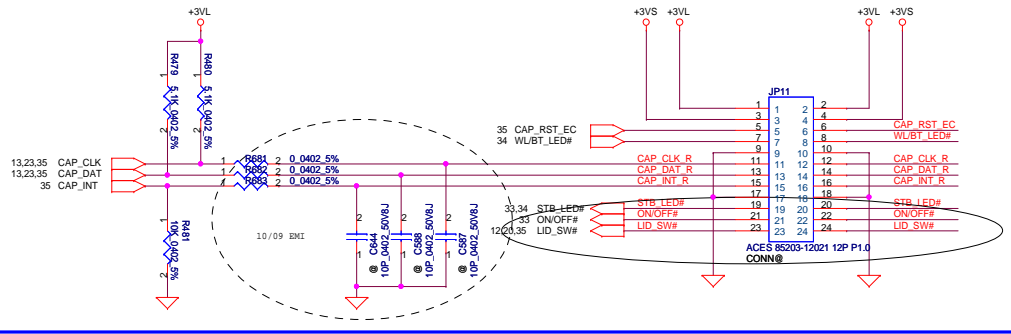


09/28 HP

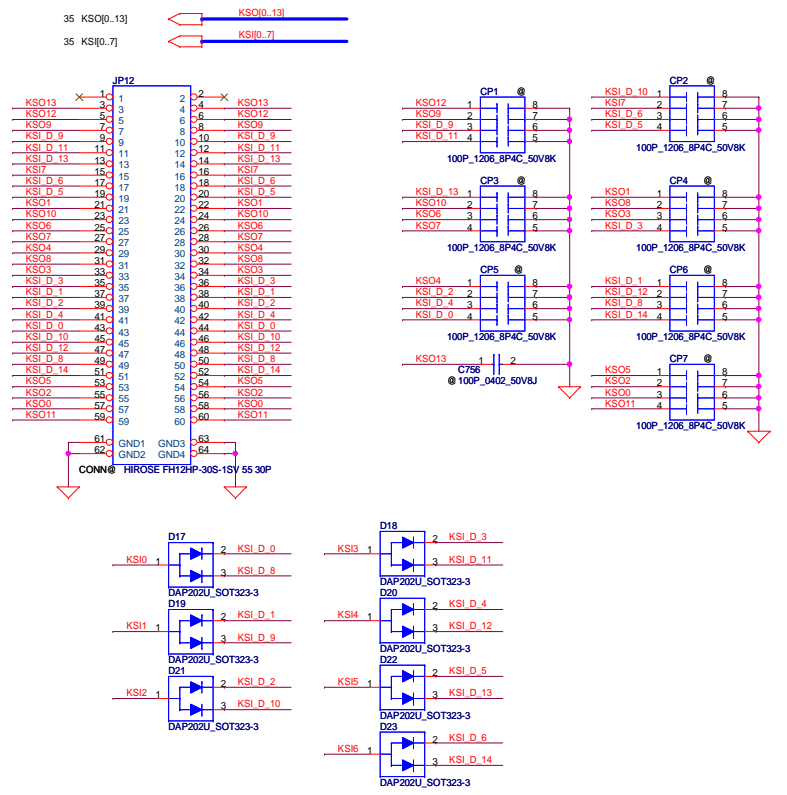


Security Classification	Compal Secret Data		Title	Compal Electronics, Inc.	
Issued Date	2008/10/24	Deciphered Date	2009/10/24	Magnetic & RJ45	
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Size	Document Number	Date		Sheet	Rev
	LA-4891	Thursday, October 30, 2008		28 of 50	0.1

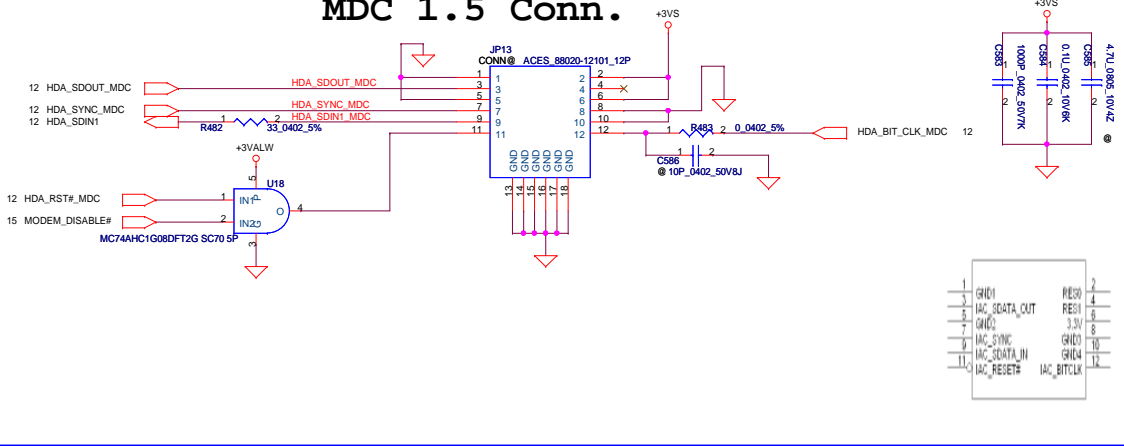
CAP SWITCH BOARD.



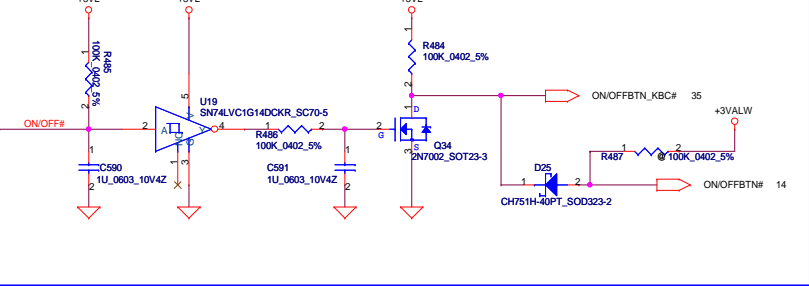
INT_KBD CONN.



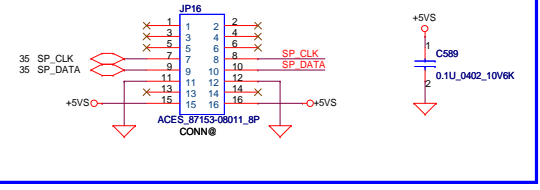
MDC 1.5 Conn.



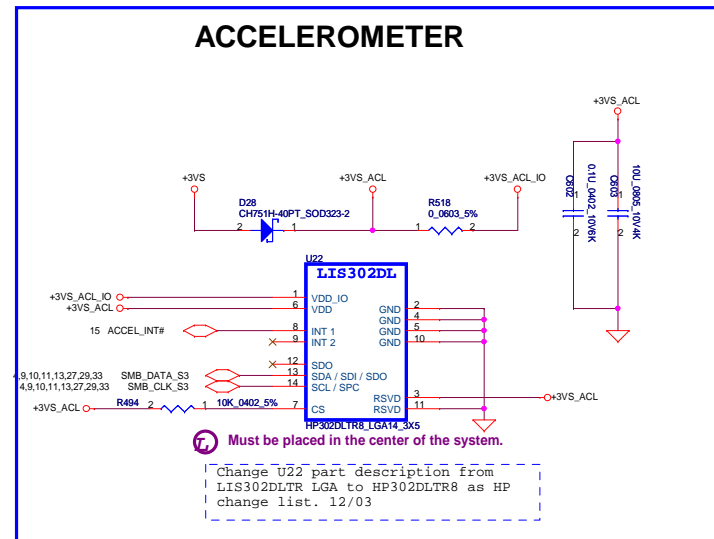
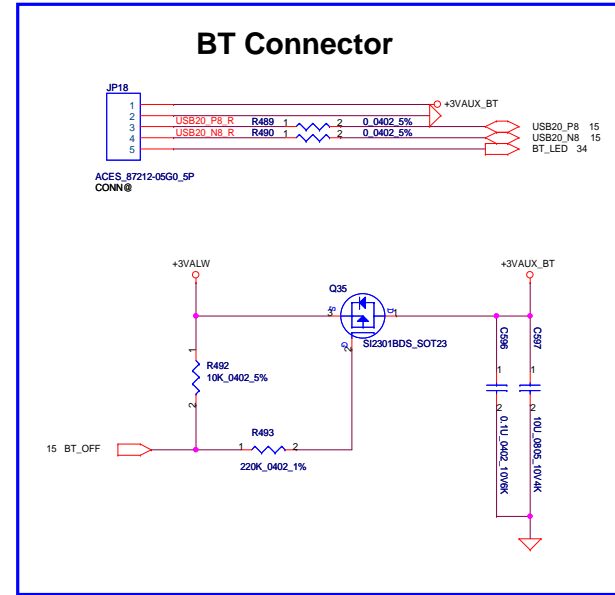
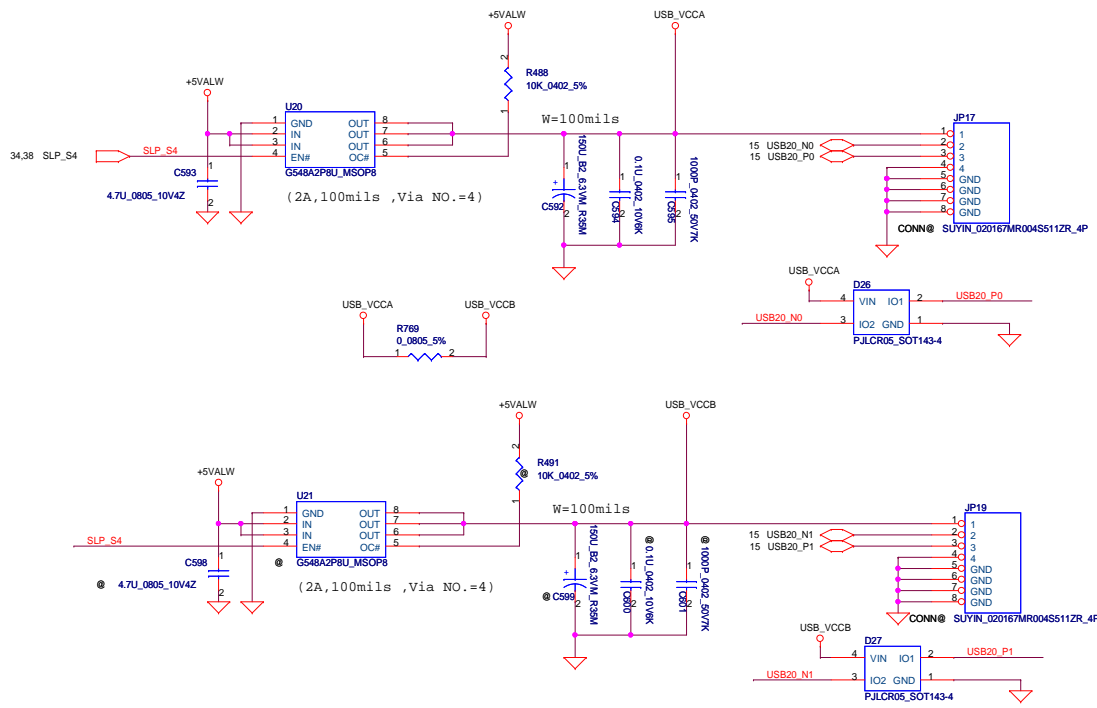
Power button



TrackPoint CONN.

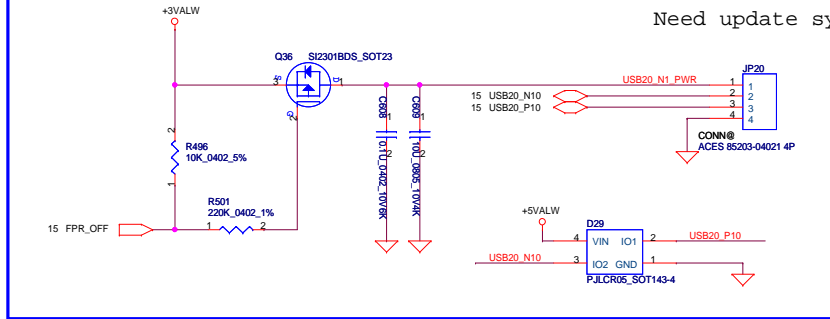


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Issued Date	2008/10/24	Deciphered Date	2009/10/24	MDC/KBD/ON OFF/LID	
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				0.1U	LA-4891
				Date:	Monday, October 27, 2008
				Sheet	30 of 50

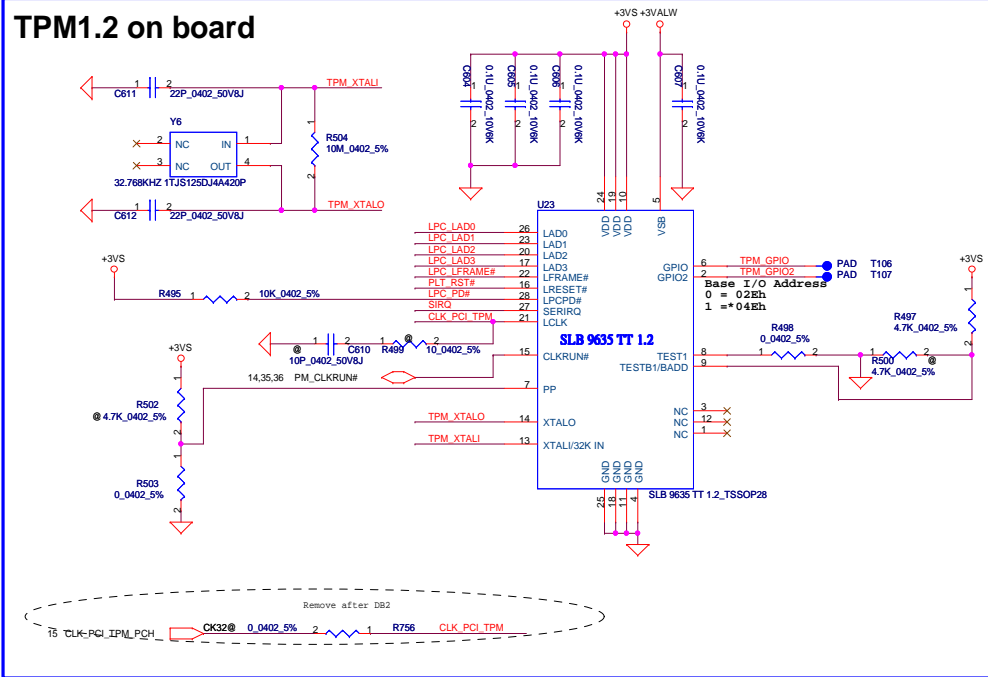


Security Classification	Compal Secret Data		Title	
Issued Date	2008/10/24	Deciphered Date	2009/10/24	USB & BT Connector & Acclerometer
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				Document Number
				LA-4891
				Rev
				0.1
				Date: Monday, October 27, 2008
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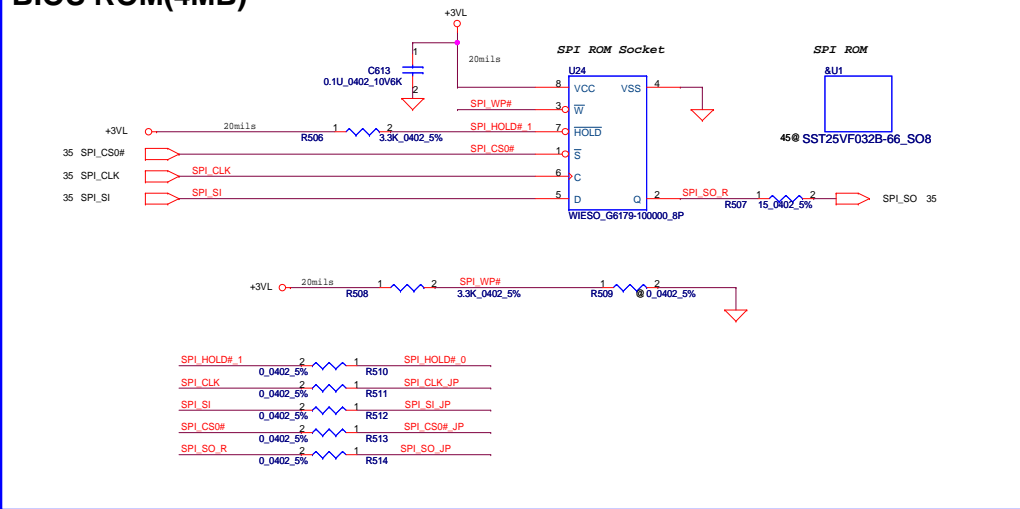
Finger printer



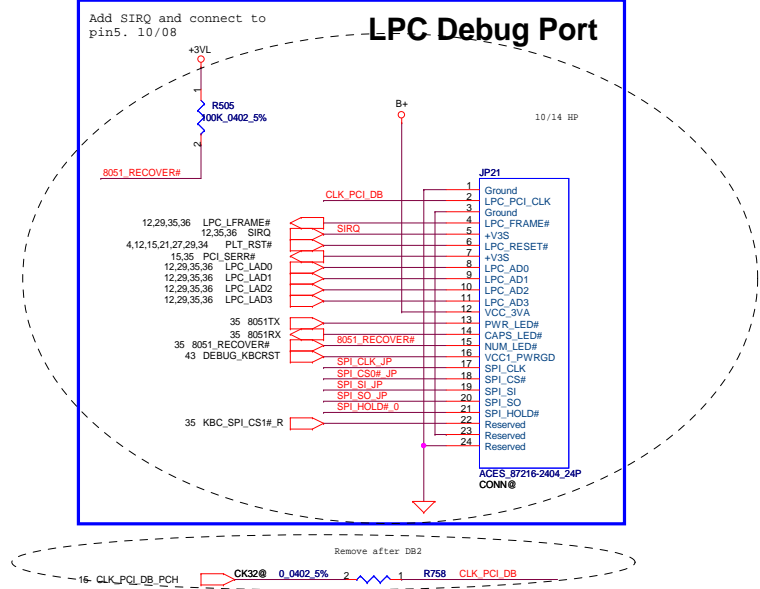
TPM1.2 on board



BIOS ROM(4MB)

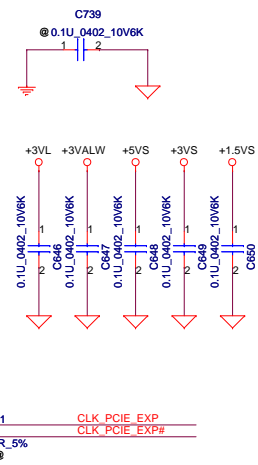
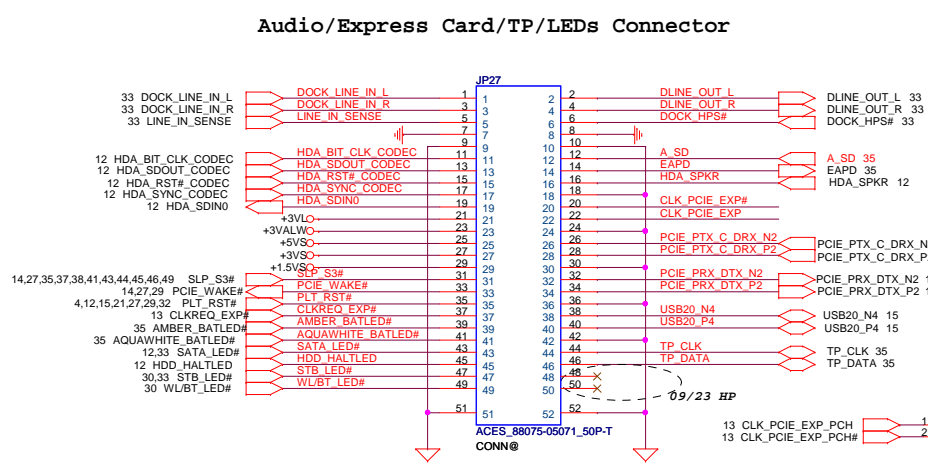


LPC Debug Port

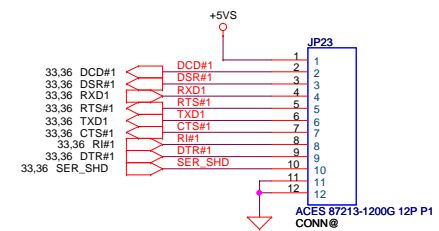


Security Classification	Compal Secret Data		Title TCG/BIOS ROM/PS2/SW LPC DEBUG
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Date: Tuesday, October 28, 2008			Sheet 32 of 50

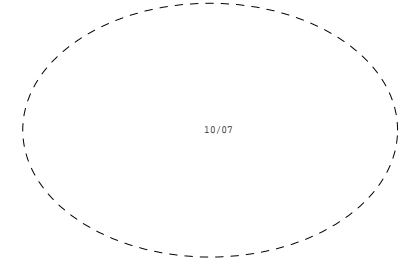
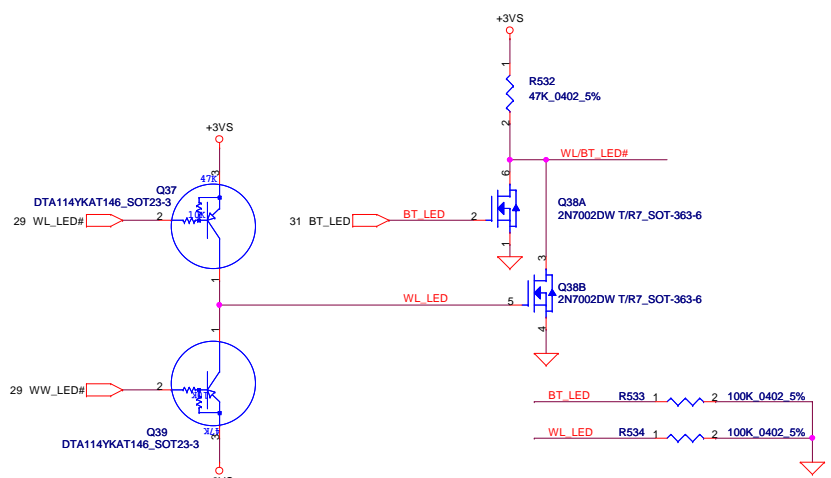
Audio/Express Card/TP/LEDs Connector



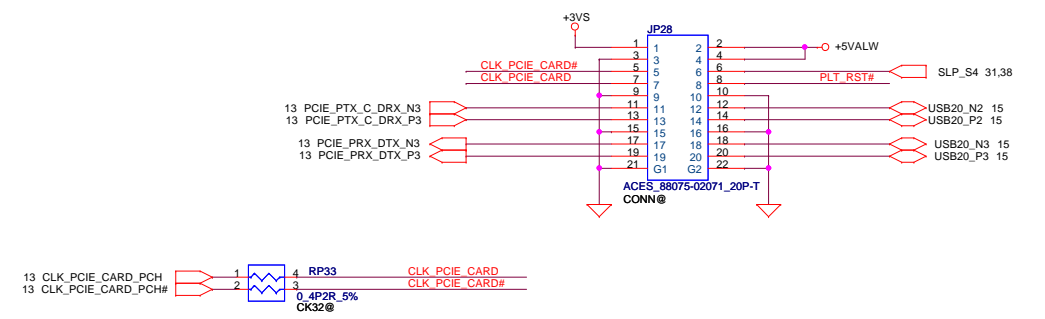
Serial Port CONN



Remove after DB2



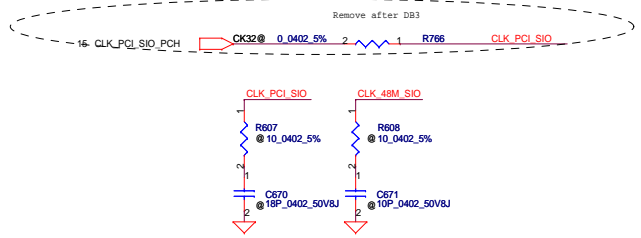
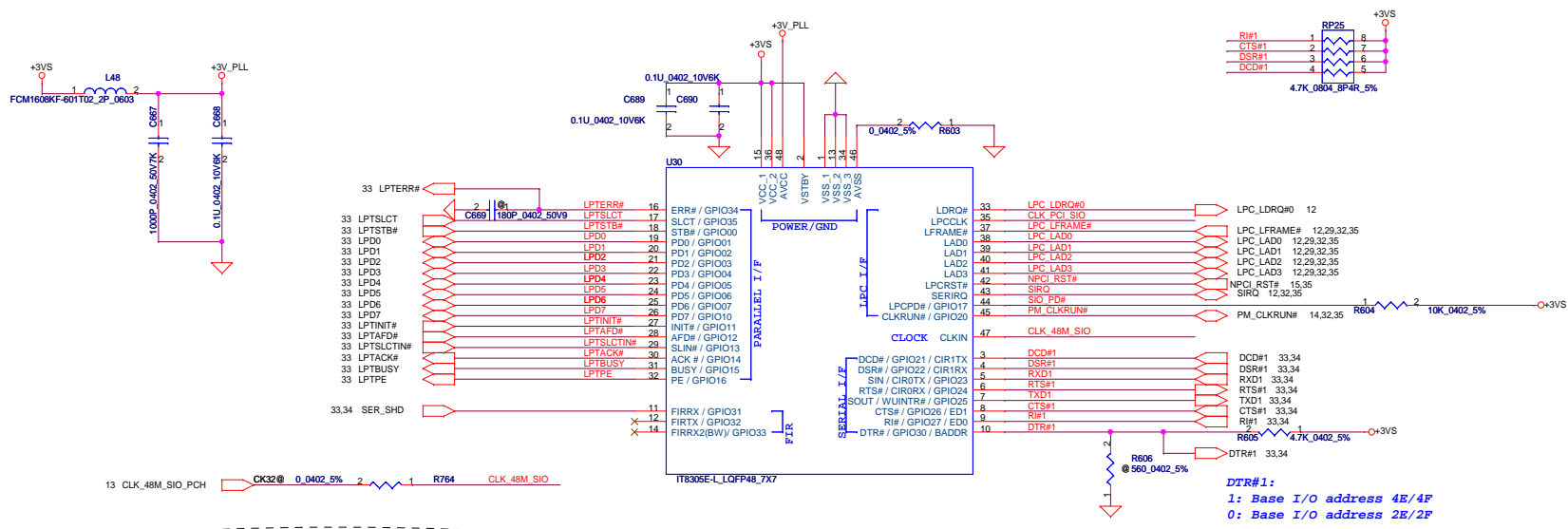
Card Reader 7 in 1 + 2 Port USB Connector



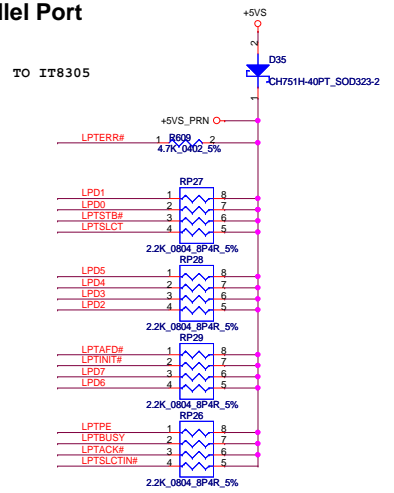
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		2009/10/24

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		CR&LEDs&PW&Audio&Exp Conn	
Size	Document Number	Rev	
Custom	LA-4891	0.1	
Date:	Monday, October 27, 2008	Sheet	34 of 50

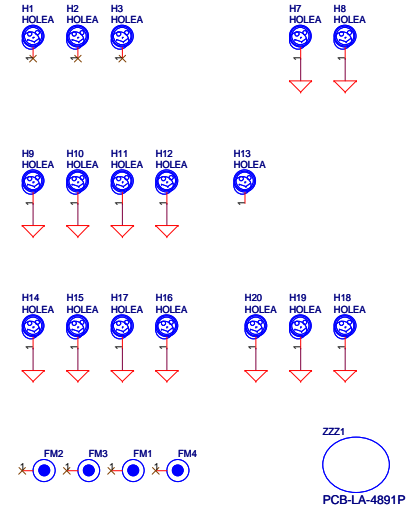
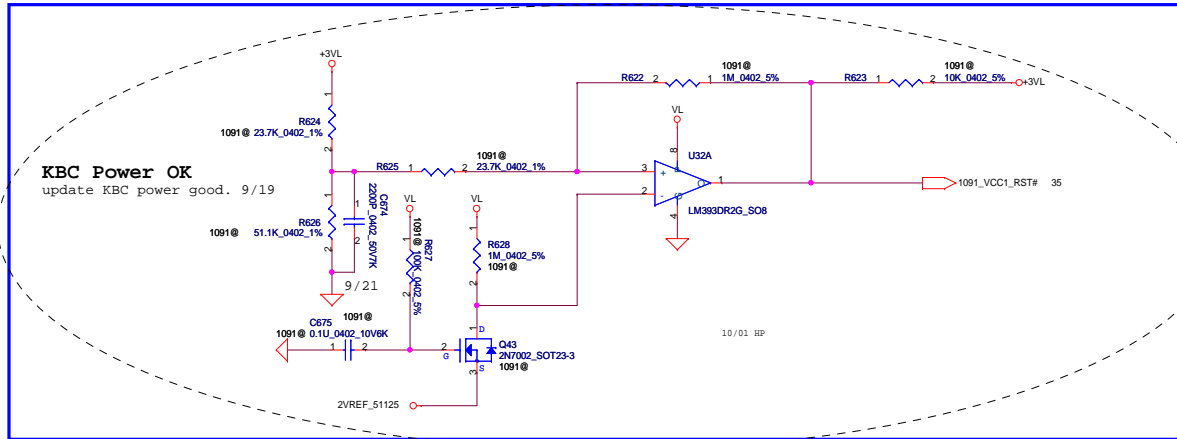
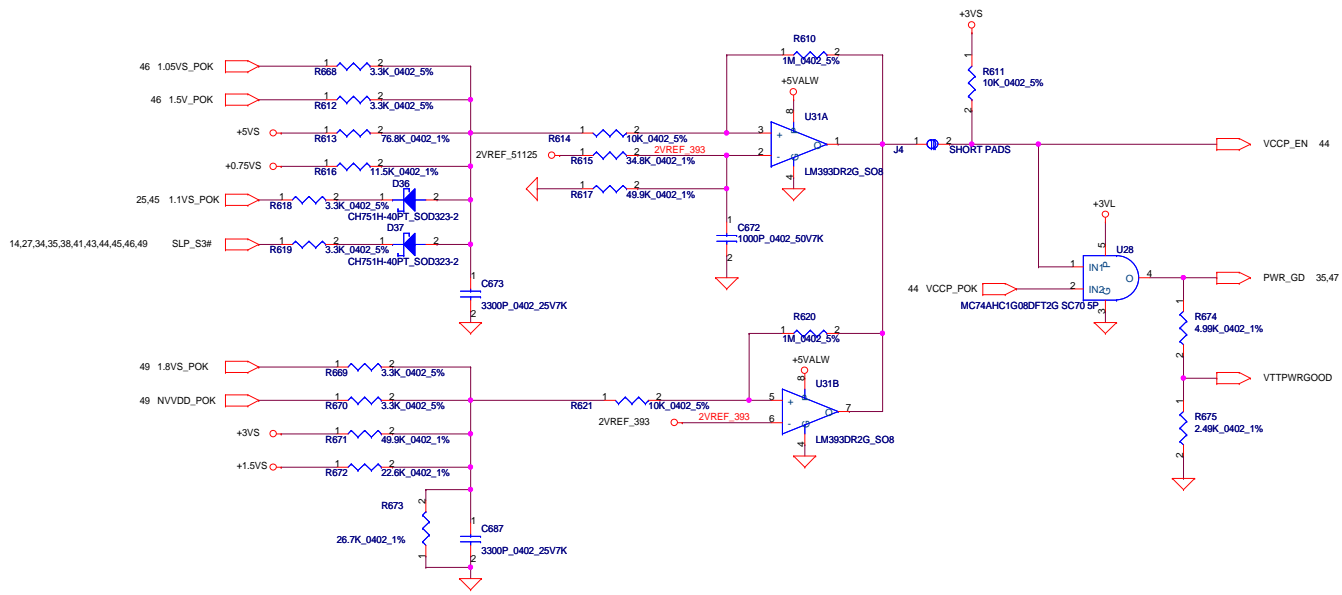
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Parallel Port



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			Sheet	36 of 50



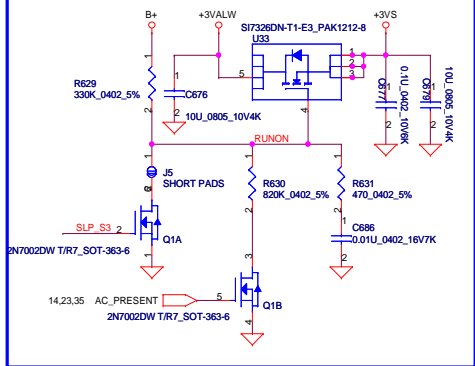
KBC Power OK
update KBC power good. 9/19

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				LA-4891	Rev 0.1
Date: Monday, October 27, 2008				Sheet	37 of 50

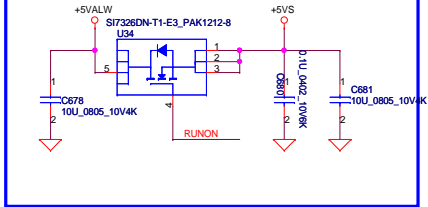
POK CKT

ZZ1
PCB-LA-4891P

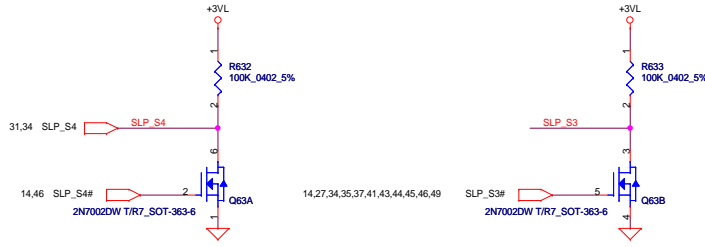
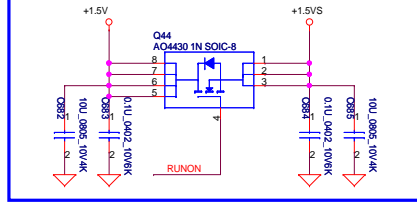
+3VALW to +3VS Transfer



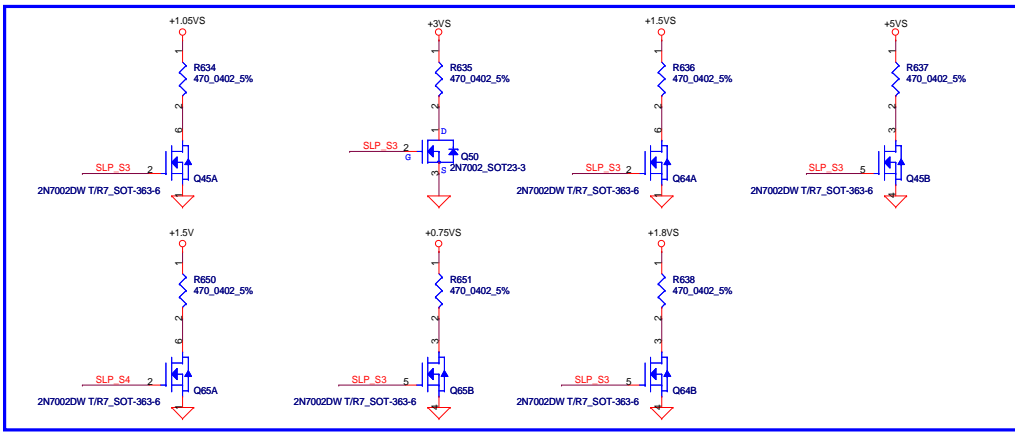
+5VALW to +5VS Transfer



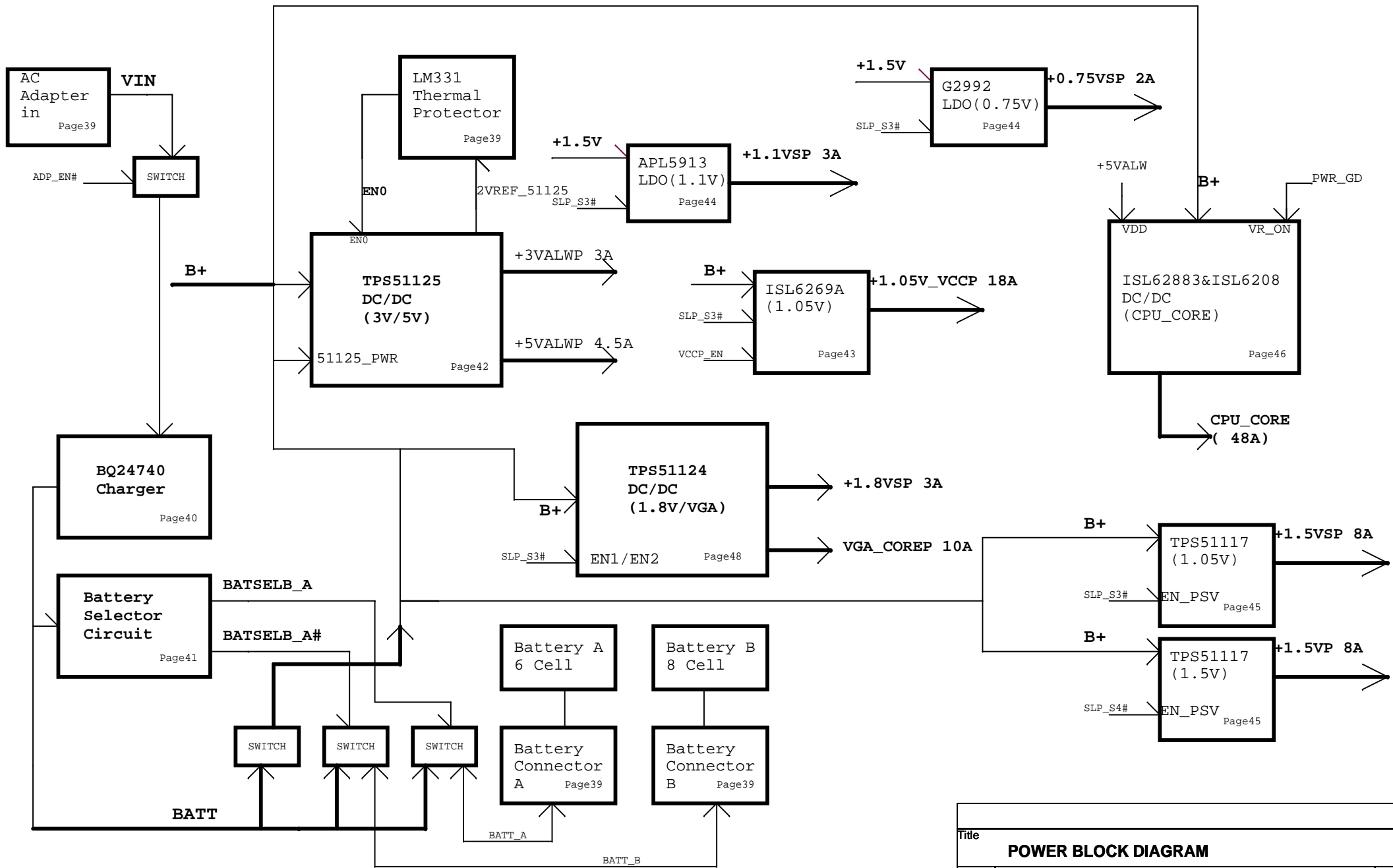
+1.5V to +1.5VS Transfer



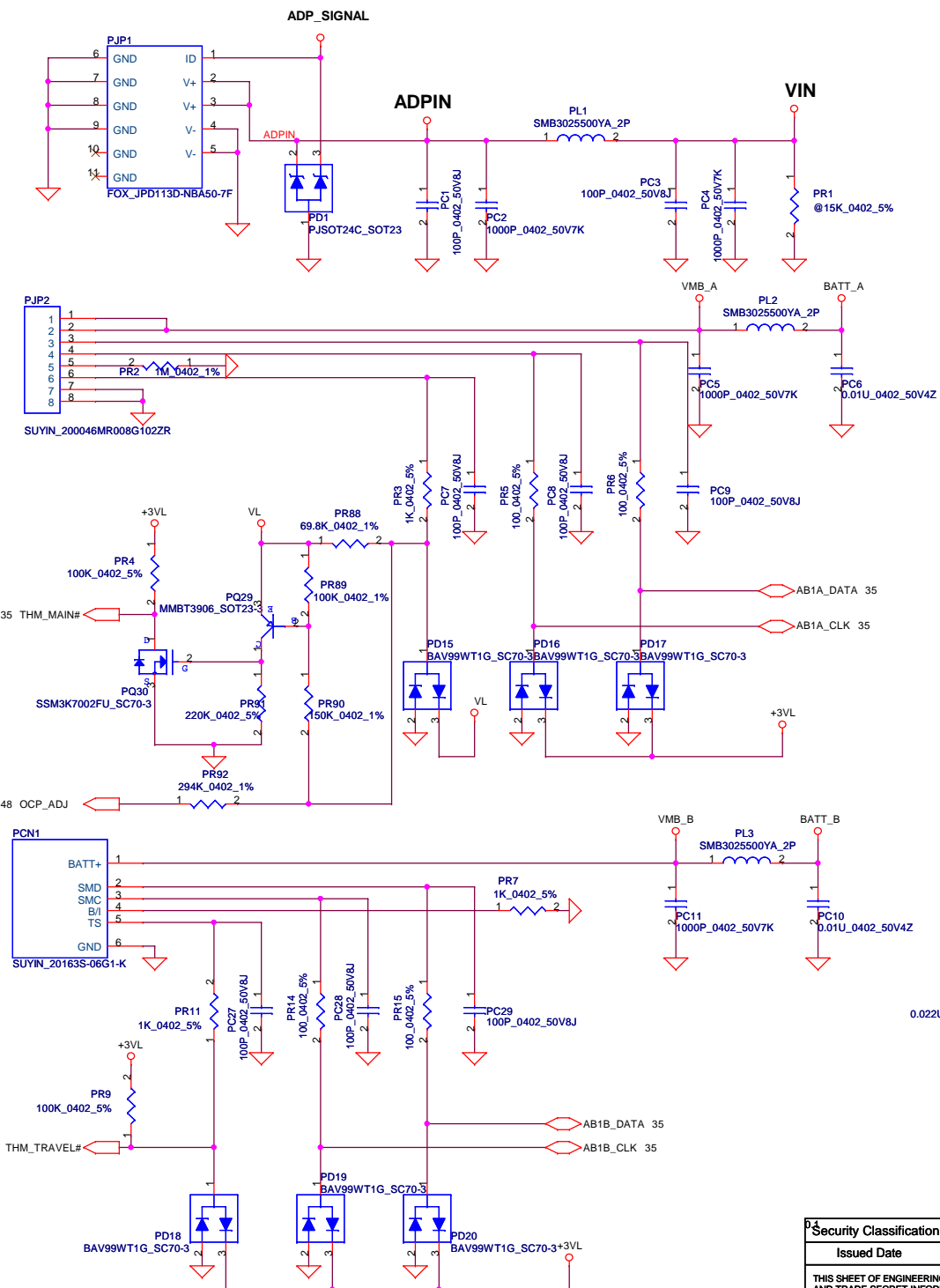
Discharge circuit-1



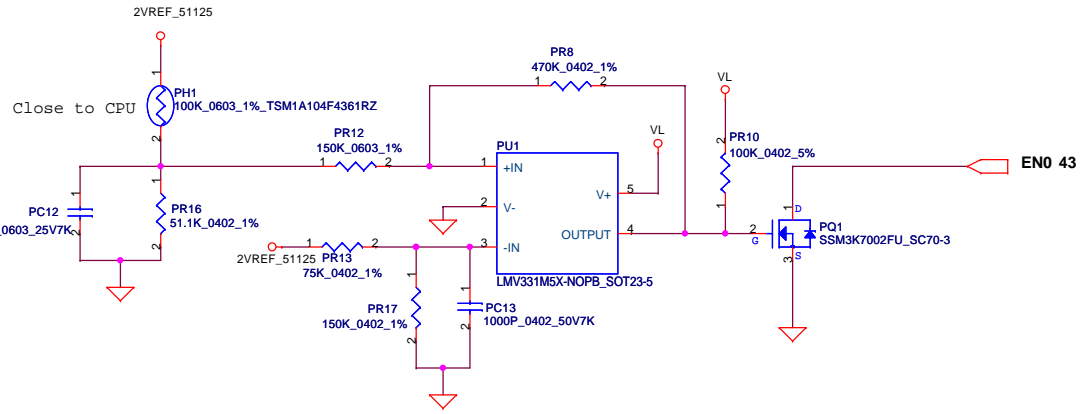
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				LA-4891
			Date:	Monday, October 27, 2008
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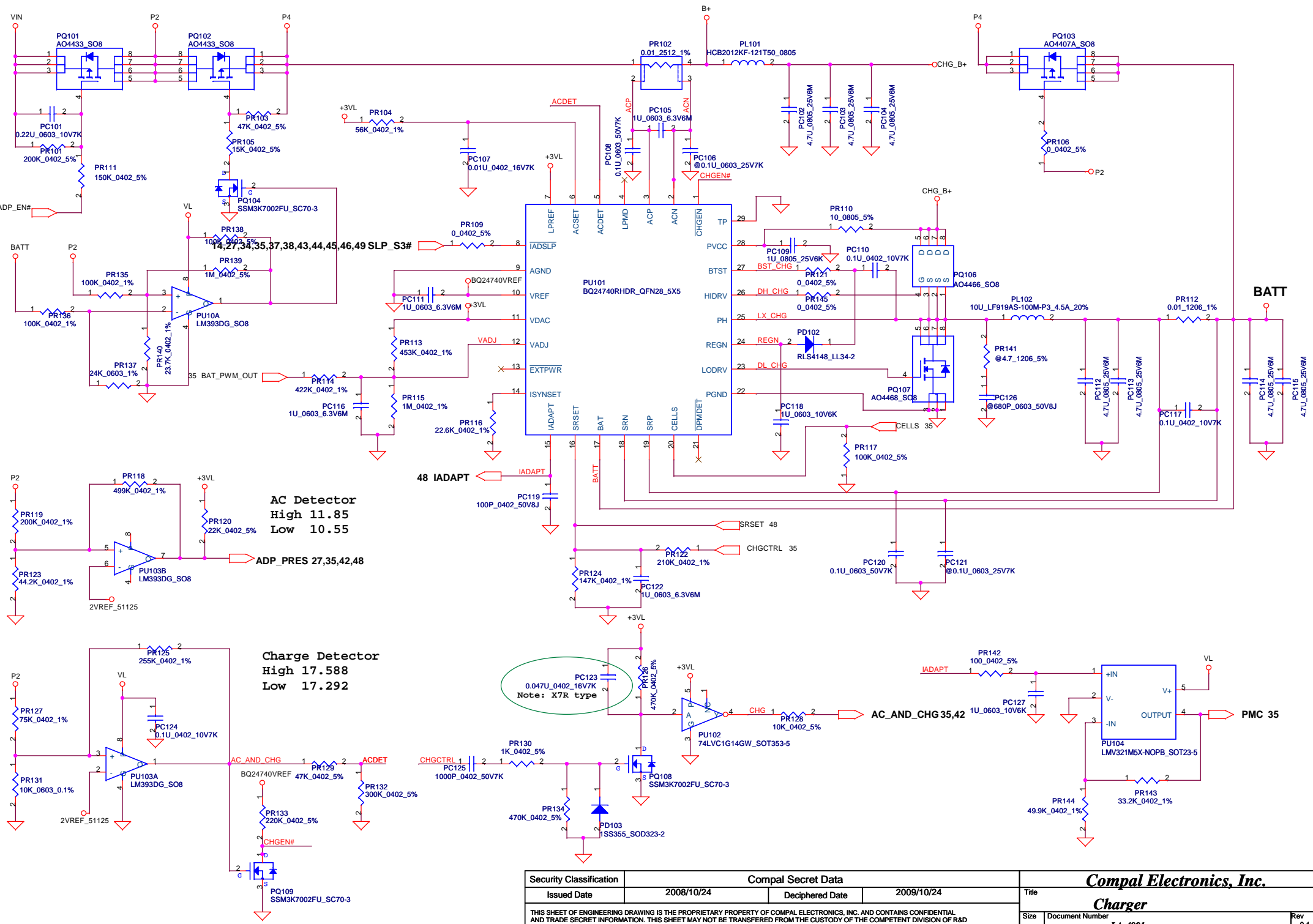
Title		
POWER BLOCK DIAGRAM		
Size	Document Number	Rev
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PH1 under CPU bottom side :
 CPU thermal protection at 90 +/- 3 degree C
 (Need to be checked)



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				Custom	LA-4891
				Date:	Monday, October 27, 2008
				Sheet	40 of 50



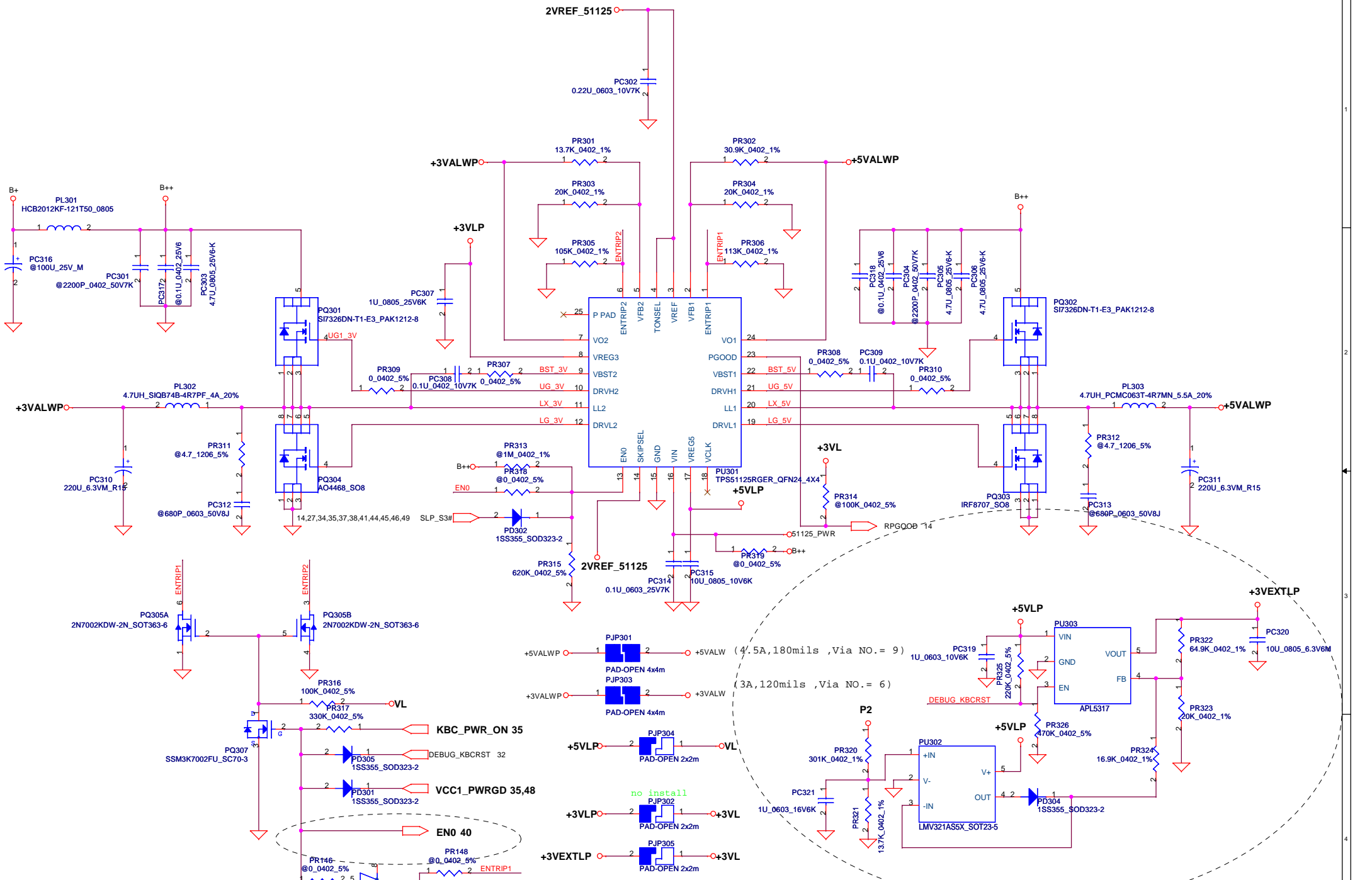
AC Detector
 High 11.85
 Low 10.55

Charge Detector
 High 17.588
 Low 17.292

0.047U_0402_16V7K
 Note: X7R type

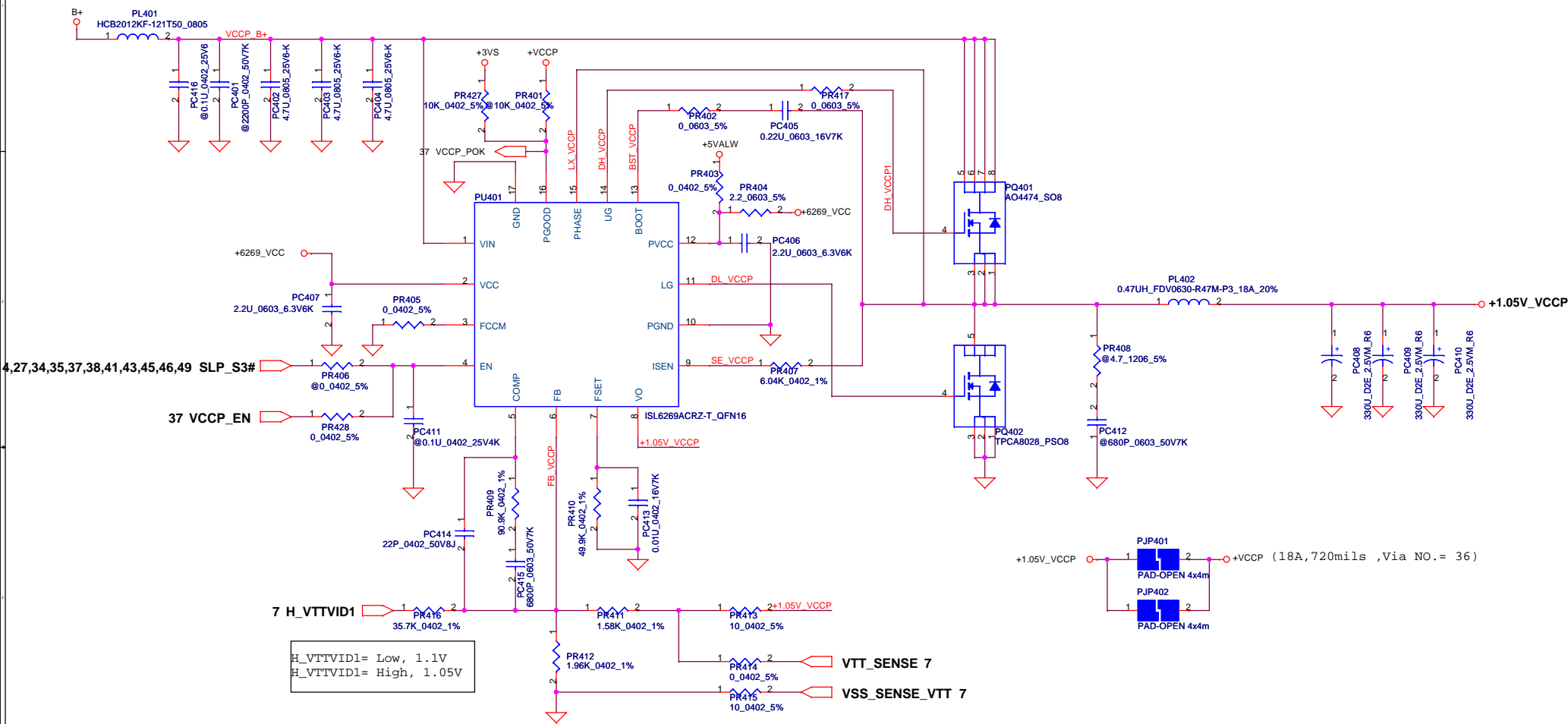
Security Classification		Compal Secret Data	
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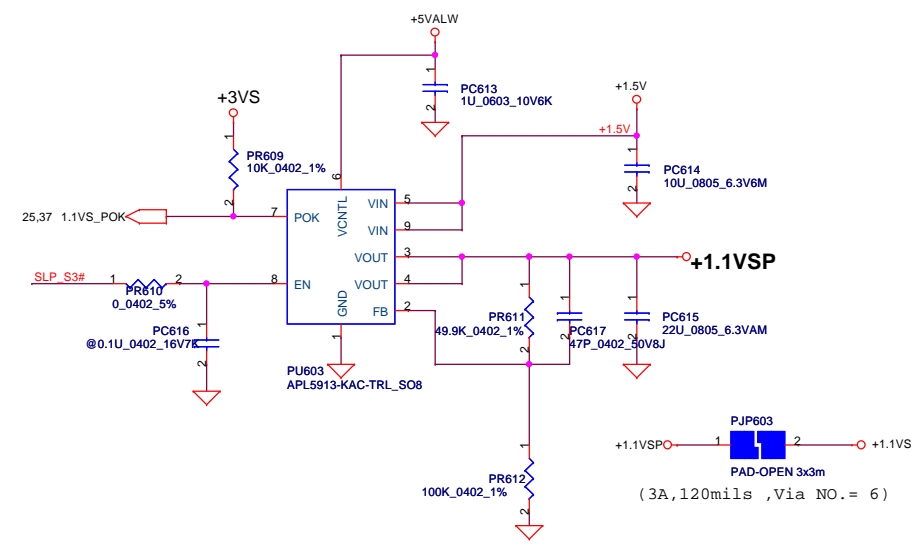
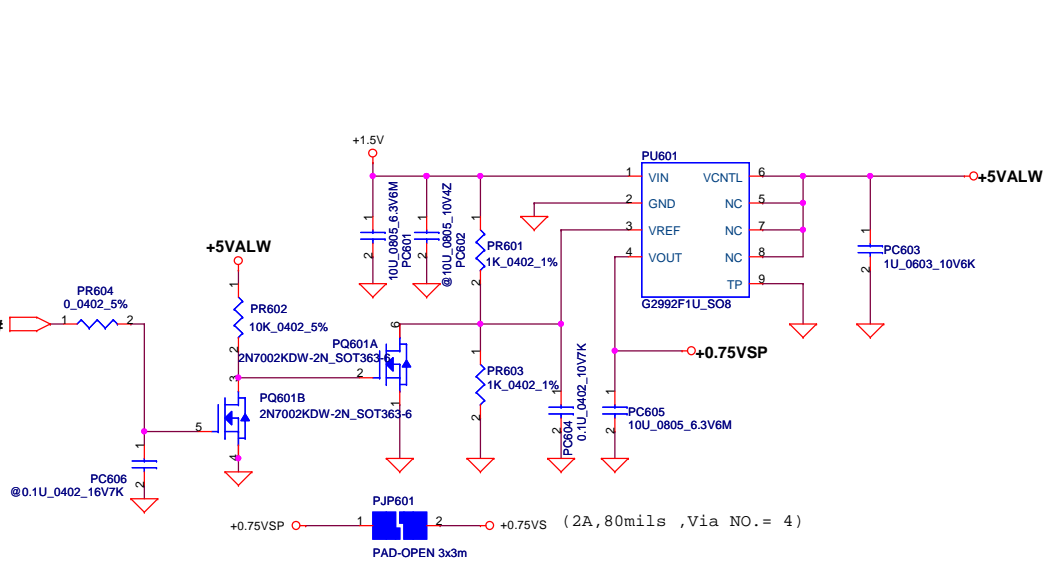
Security Classification		Compal Secret Data	
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3.3VALWP/5VALWP		
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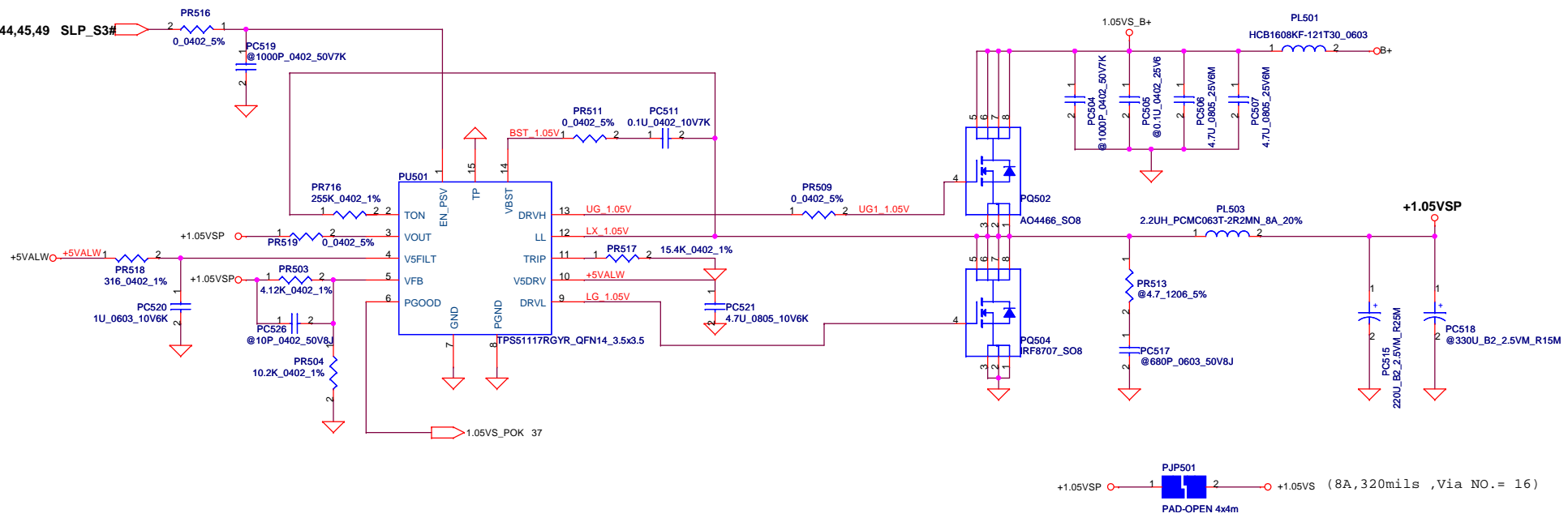
Security Classification		Compal Secret Data		Compal Electronics, Inc. 1.05V_VCCP	
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44,46,49 SLP_S3#

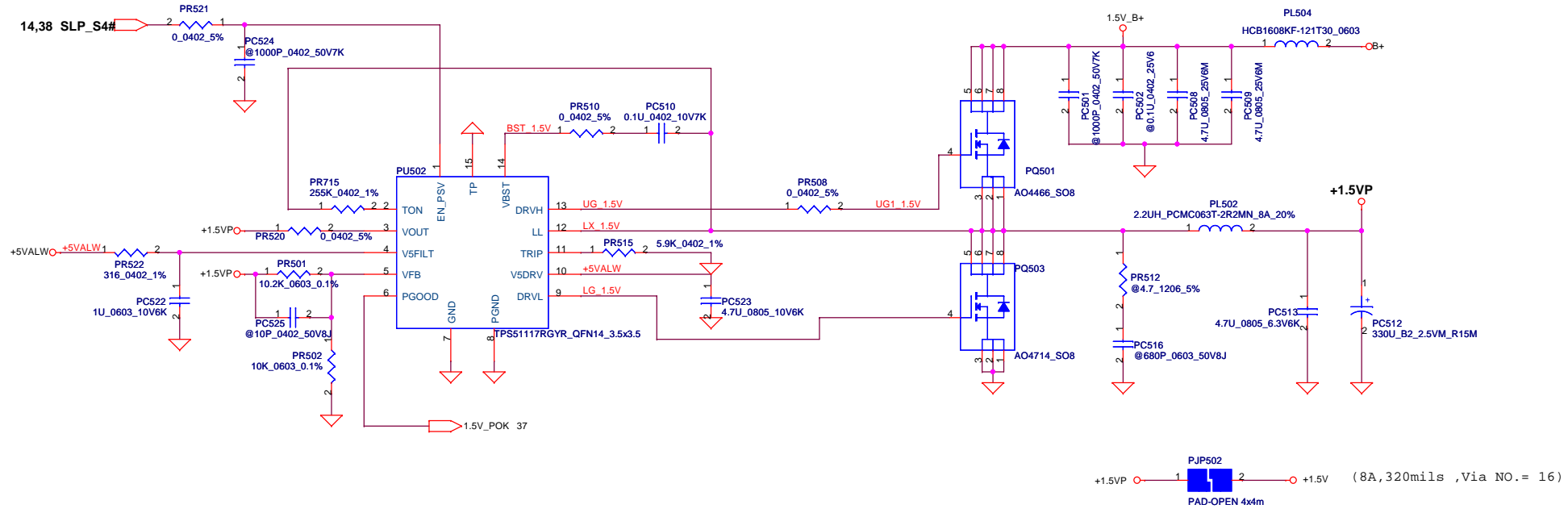


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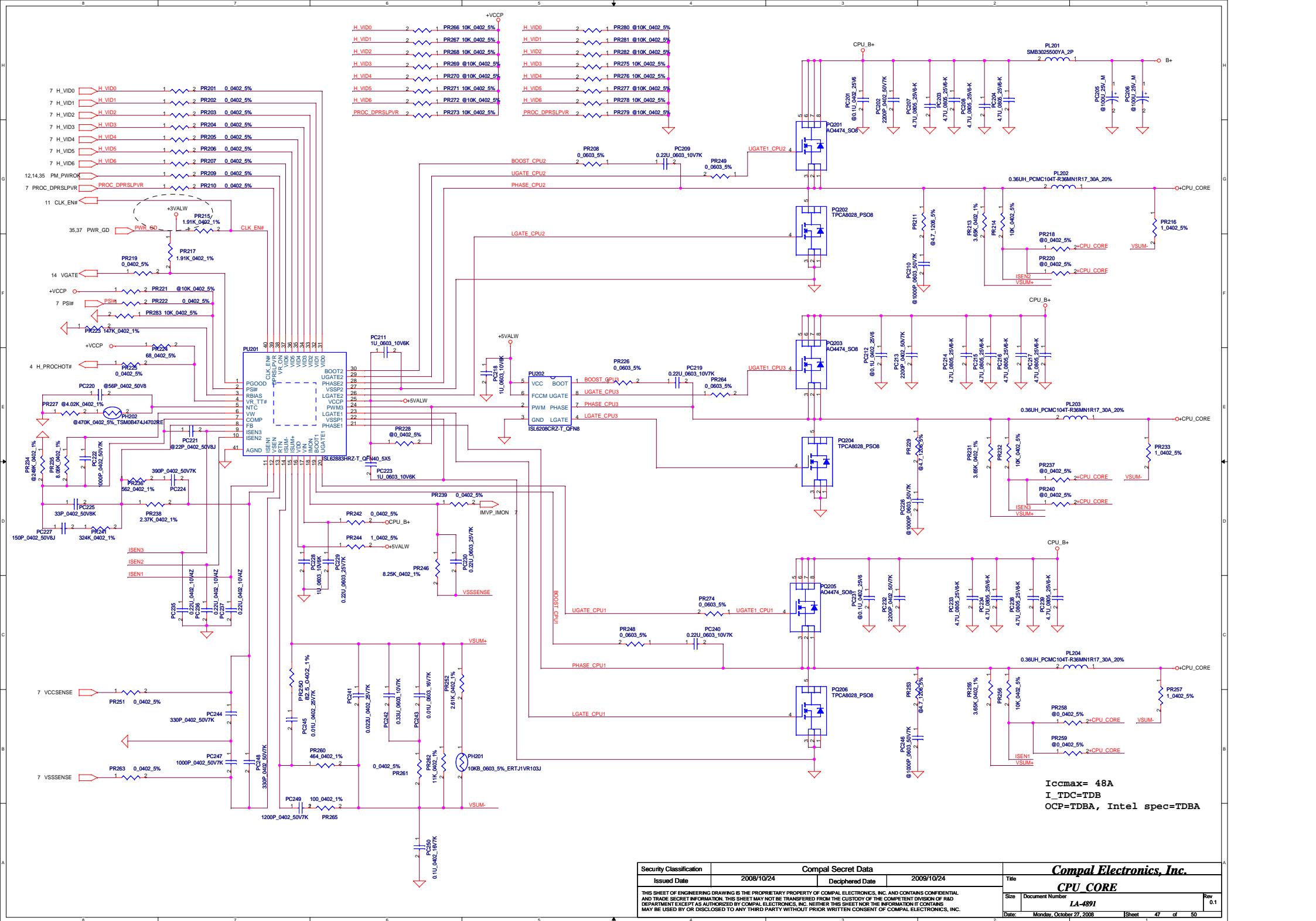
14,27,34,35,37,38,41,43,44,45,49 SLP_S3#



14,38 SLP_S4#

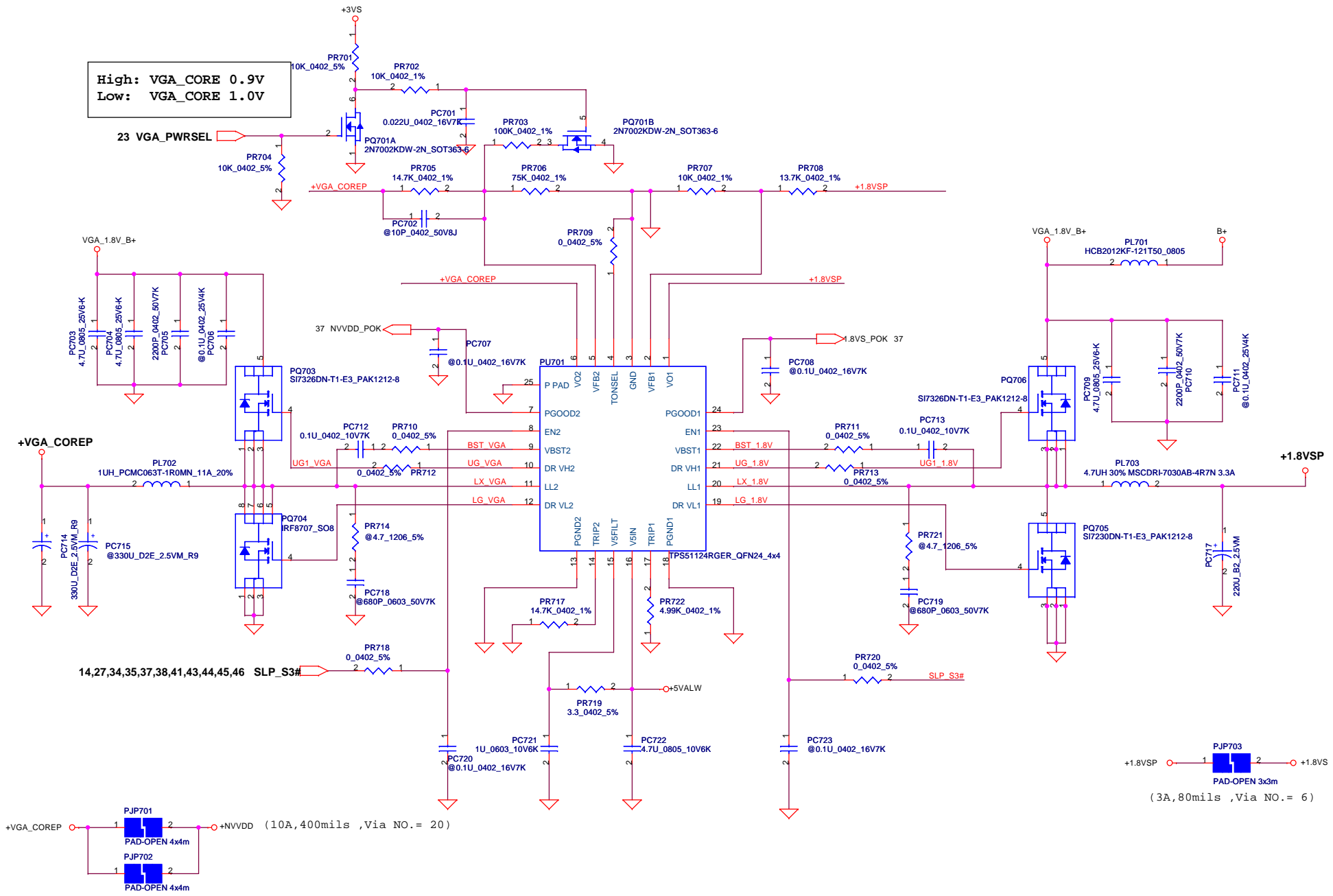


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Iccmax= 48A
 I_TDC=TDB
 OCP=TDBA, Intel spec=TDBA

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High: VGA_CORE 0.9V
Low: VGA_CORE 1.0V

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Item	Reason for change	PG#	Modify List	Date	Phase
1					
2					
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<small>DATE: Monday, October 21, 2008</small>				<small>Sheet 59 of 69</small>	