

# Compal Confidential

## Schematics Document

INTEL AUBURNDALE with IBEX core logic

### Cartier UMA

### LA-4902P

2009-12-07

REV: 1.0

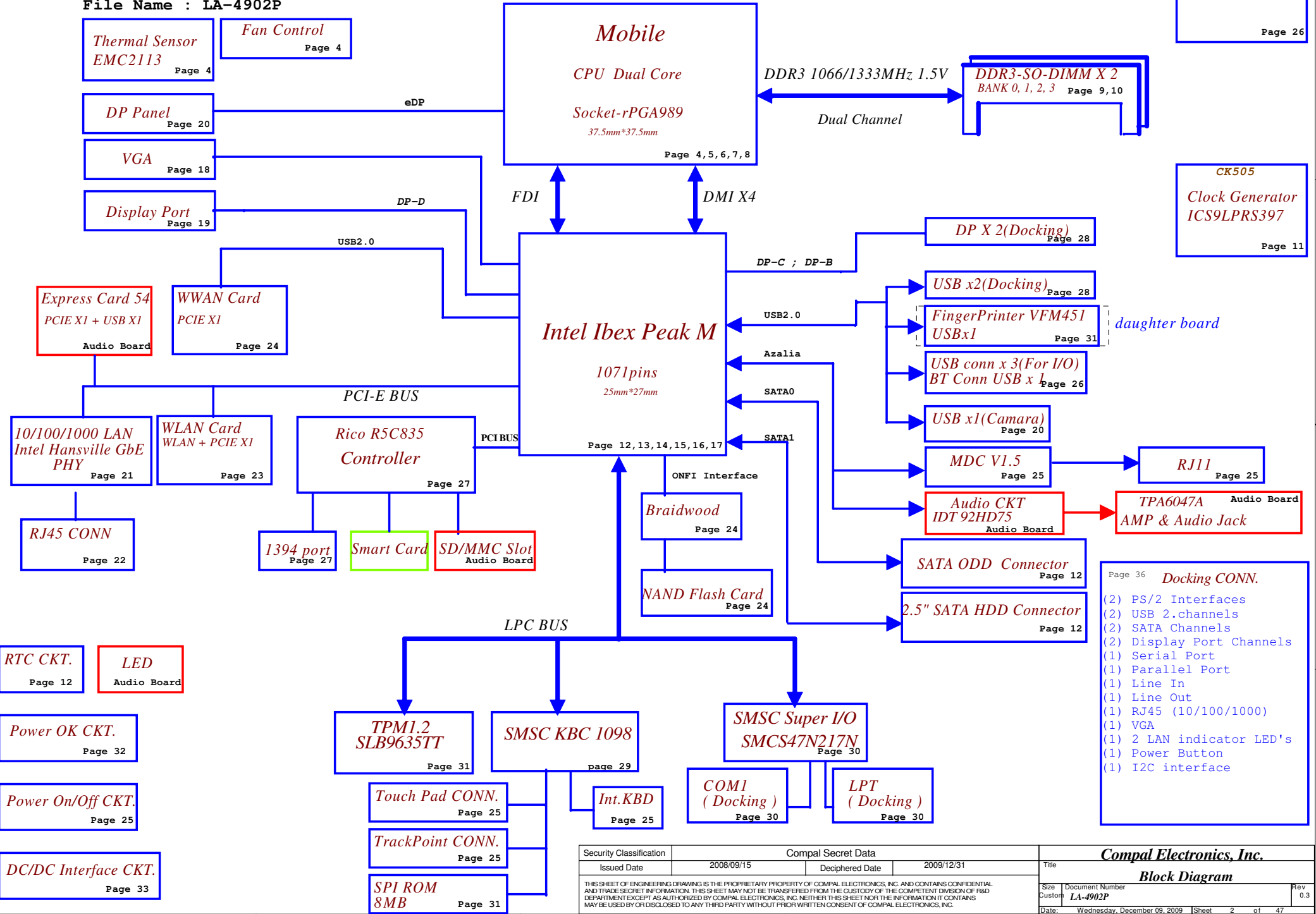
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Issued Date	2008/09/15	Deciphered Date	2009/12/31	Cover Sheet		
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# Cartier UMA

XDP Conn. Page 4

Accelerometer LIS302DLTR Page 26

File Name : LA-4902P



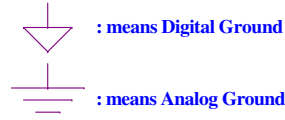
- Page 36 **Docking CONN.**
- (2) PS/2 Interfaces
  - (2) USB 2.channels
  - (2) SATA Channels
  - (2) Display Port Channels
  - (1) Serial Port
  - (1) Parallel Port
  - (1) Line In
  - (1) Line Out
  - (1) RJ45 (10/100/1000)
  - (1) VGA
  - (1) 2 LAN indicator LED's
  - (1) Power Button
  - (1) I2C interface

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**Voltage Rails** ( O MEANS ON X MEANS OFF )

power plane State	+RTCVCC	+B +3VL	+5VALW +3VALW	+3VM +1.05VM	+1.5V +0.75V	+5VS +3VS +1.5VS +VCCP +CPU_CORE +1.05VS +1.8VS
S0	O	O	O	O	O	O
S1	O	O	O	O	O	O
S3	O	O	O	O	O	X
S5 S4/AC	O	O	O	O	X	X
S5 S4/ Battery only	O	O	X	X	X	X
S5 S4/AC & Battery don't exist	O	X	X	X	X	X

**Symbol Note :**



@ : means just reserve , no build  
CONN@ : means ME part.

**Install below 45 level BOM structure for ver. 0.1**

45@ : means just put it in the BOM of 45 level.

**Install below 43 level BOM structure for ver. 0.1**

DEBUG@ : means just build when PCIE port 80 CARD function enable. *Remove before MP*

N10M@ : Install for N10M Graphic controller

1098@ : Install for 1098 KBC controller

**Reserve below BOM structure for ver. 0.1**

1091@ : Install for 1091 KBC controller

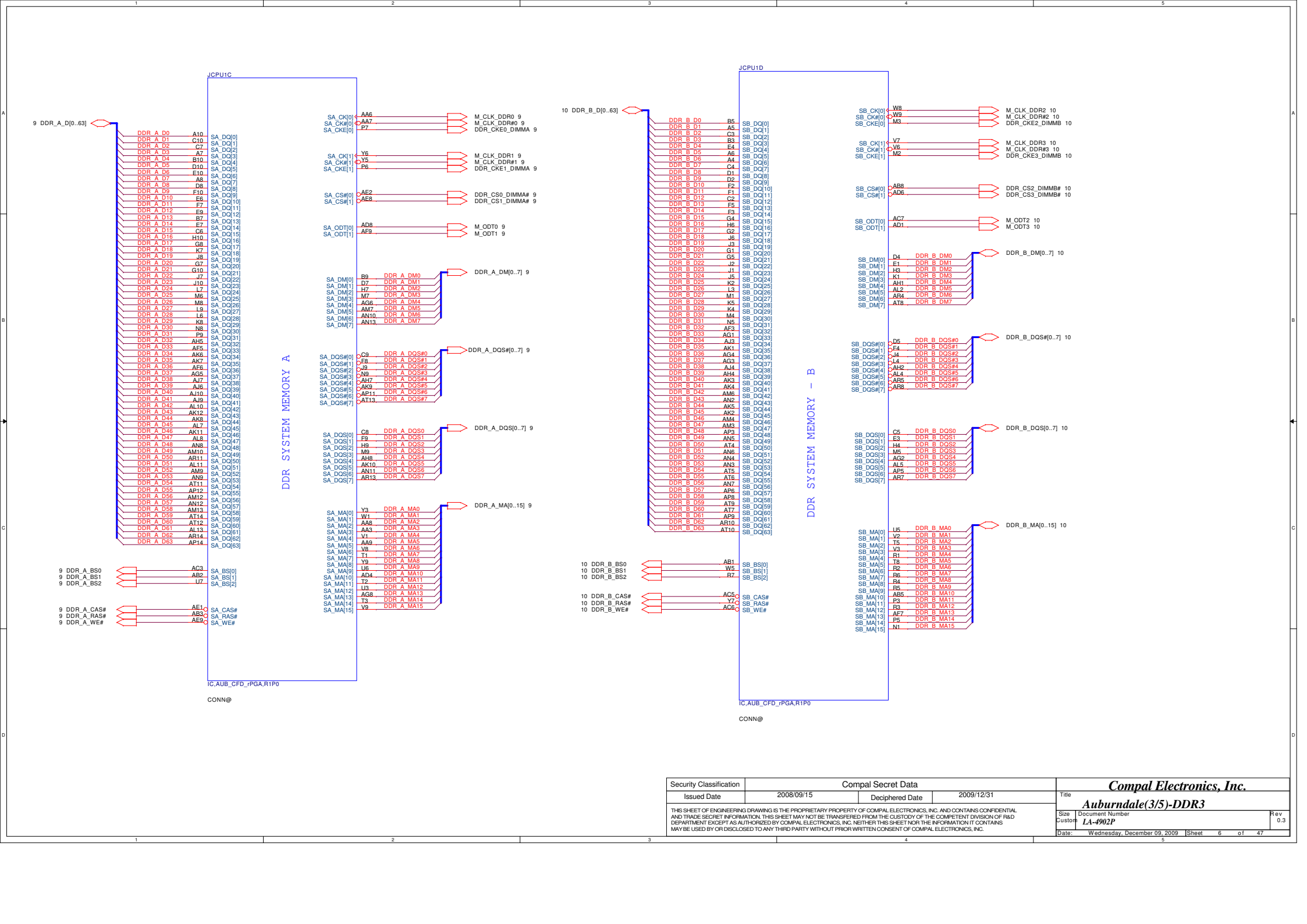
**SMBUS Control Table**

	SOURCE	BATT	XDP	SODIMM	CLK CHIP	MINI CARD	DOCK	NIC	THERMAL SENSOR	G-SENSOR
SMB_EC_CK1 SMB_EC_DA1	SMSC1098	V	X	X	X	X	X	X	X	X
SMBCLK SMBDATA	Calpella	X	V	V	V	V	V	X	X	V
SML0CLK SML0DATA	Calpella	X	X	X	X	X	X	V	X	X
SML1CLK SML1DATA	Calpella	X	X	X	X	X	X	X	V	V

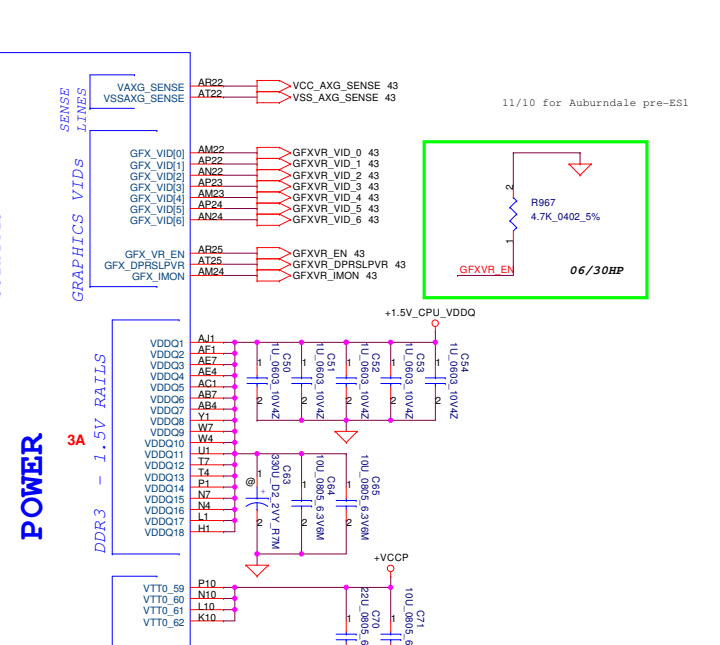
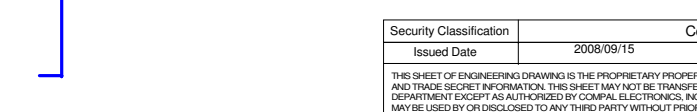
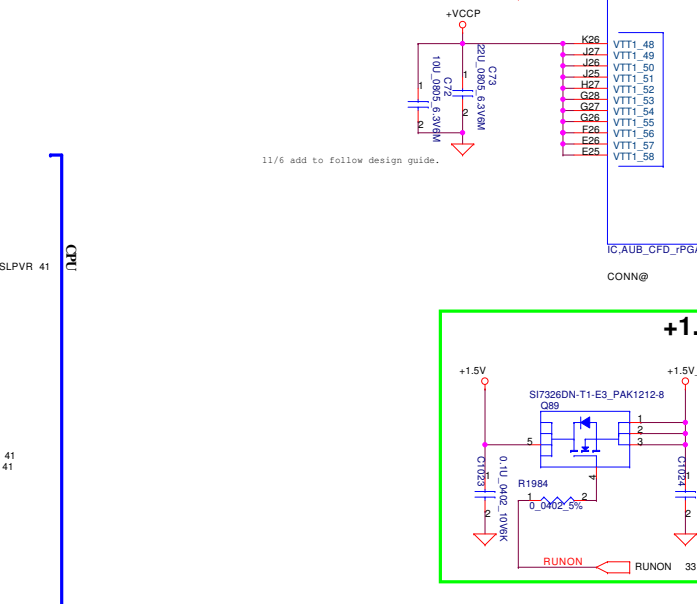
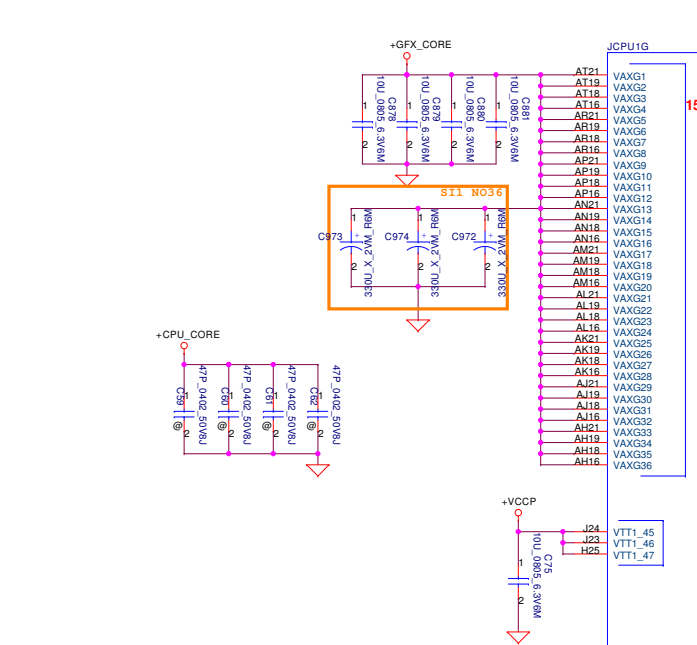
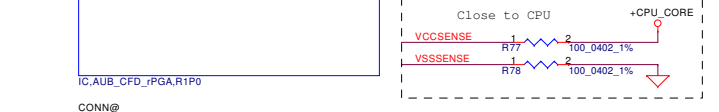
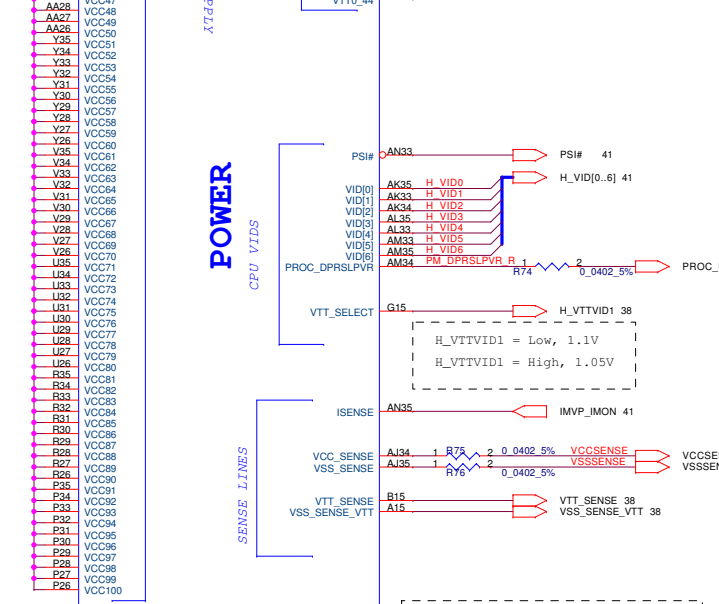
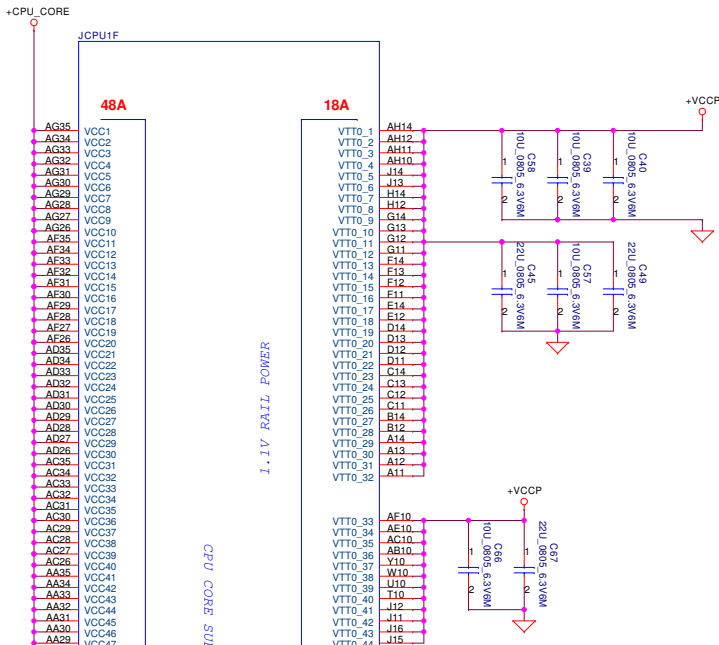
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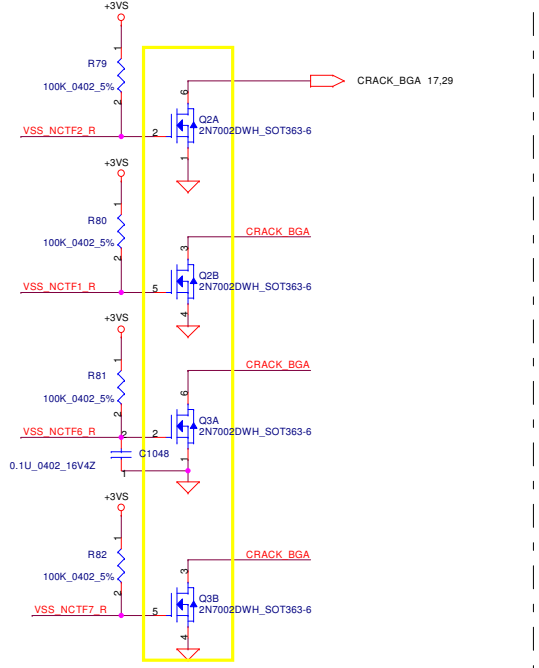
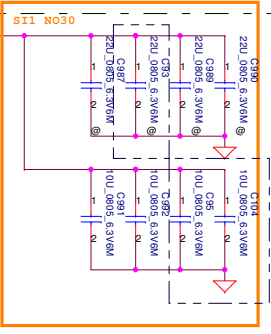
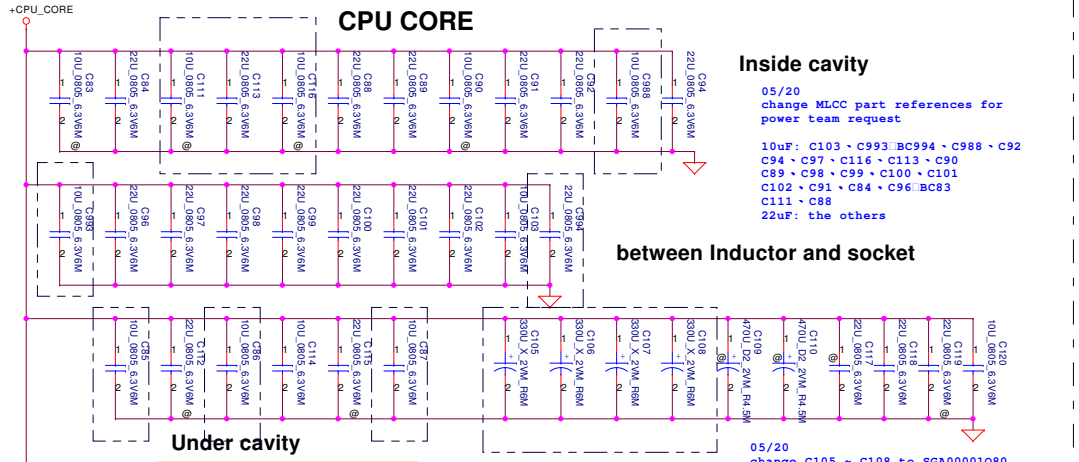
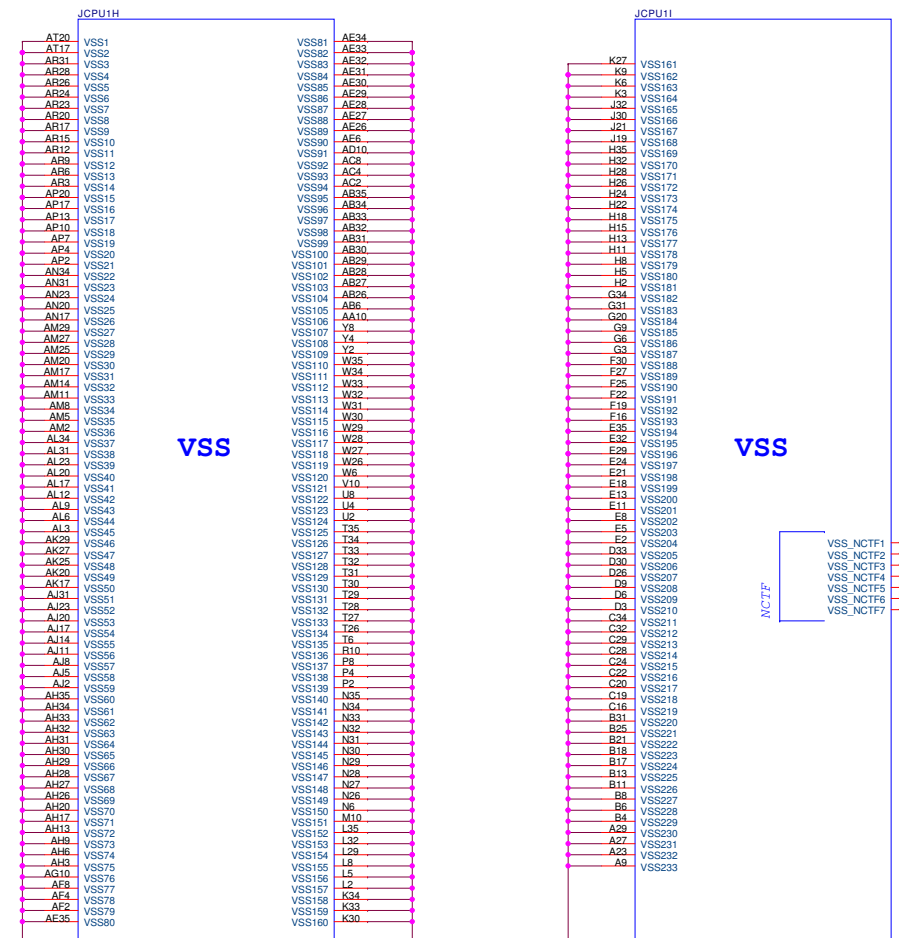




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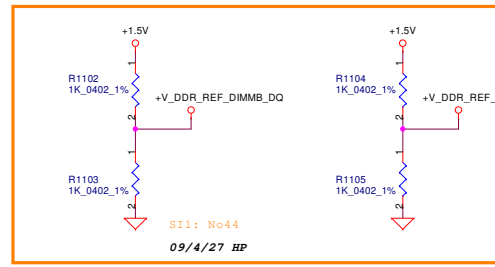
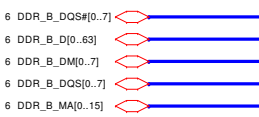
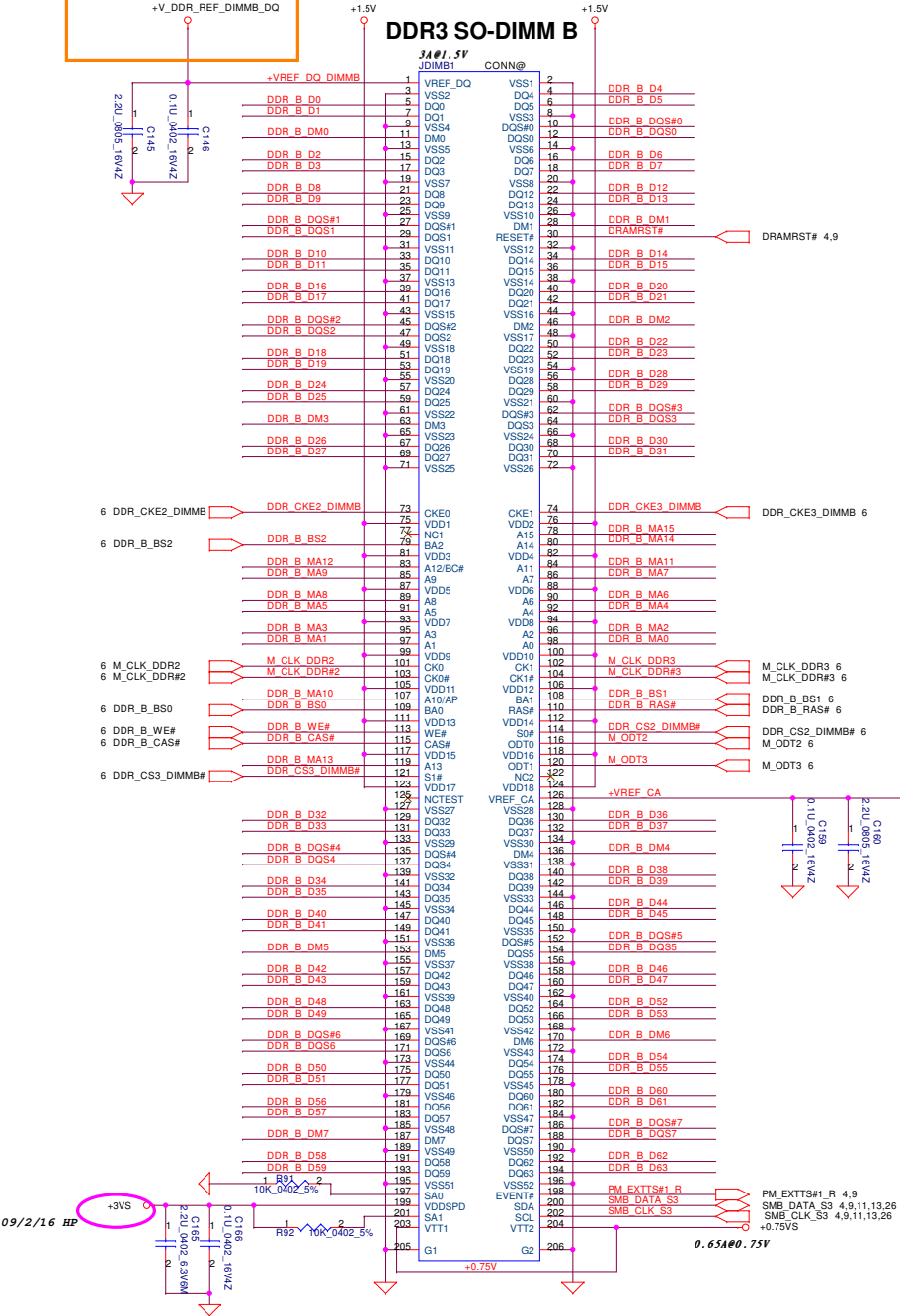
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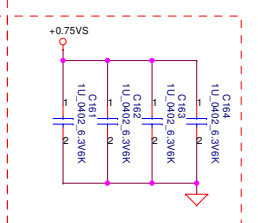
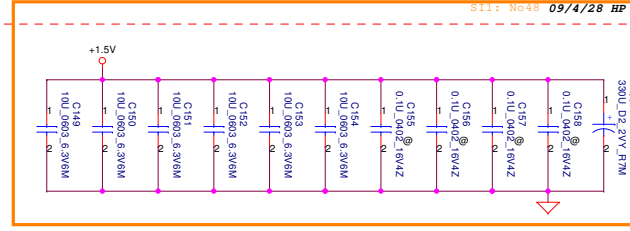
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### DDR3 SO-DIMM B



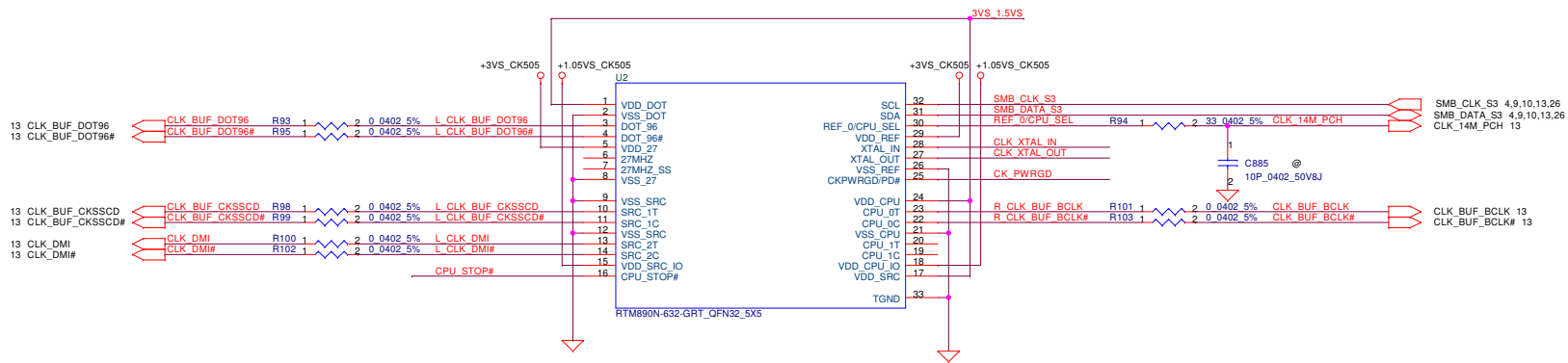
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Place near DIMMB

Layout Note:  
Place near DIMM

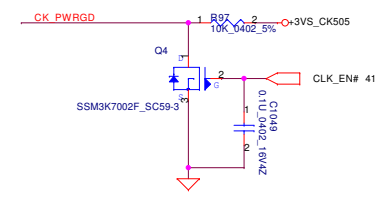
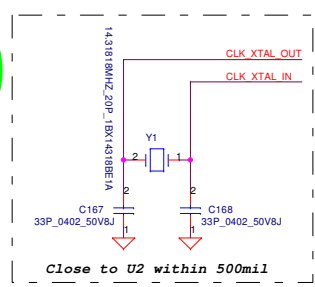
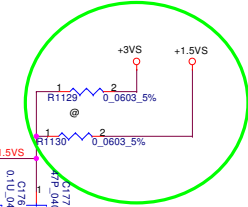
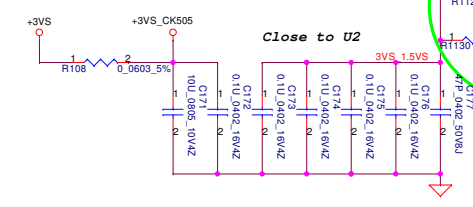
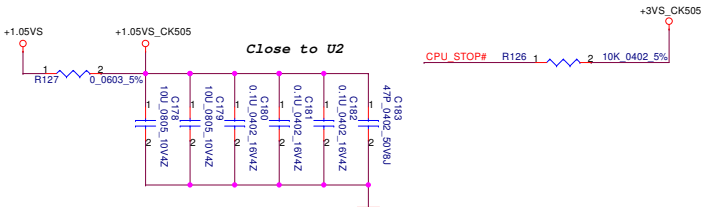


BOT

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Compal Electronics, Inc. <b>DDR3L-SODIMM SLOT2</b>			Size	Document Number
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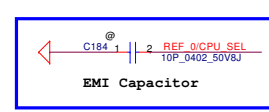


CLK Gen feature 1.5V support 6/29



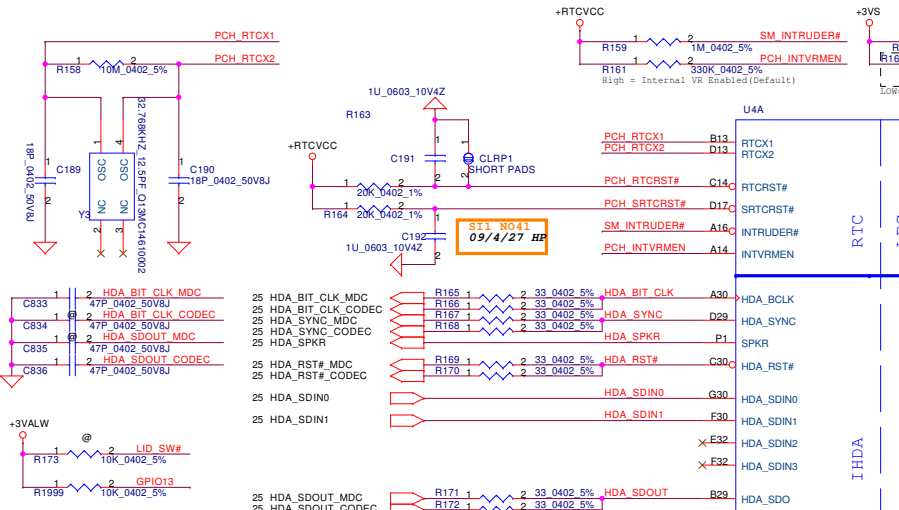
PIN 30	CPU_0	CPU_1
0 (Default)	133MHz	133MHz
1	100MHz	100MHz

09/2/5 HP

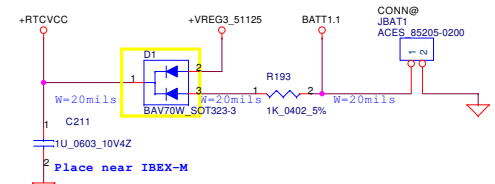


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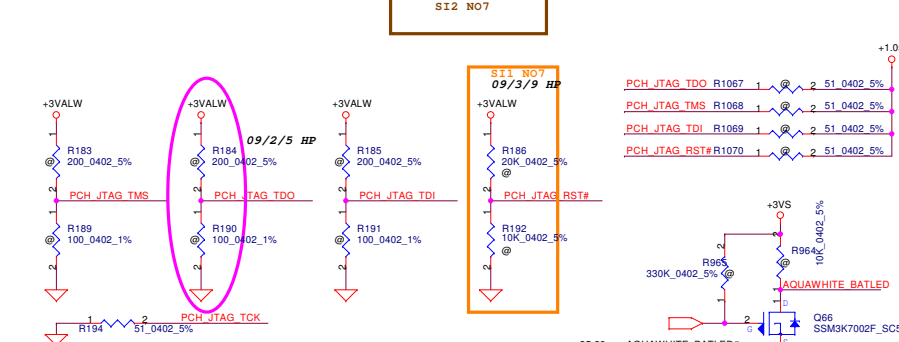
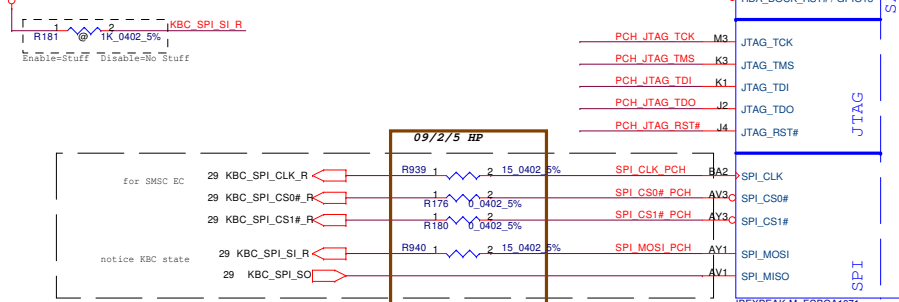
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### RTC Conn.

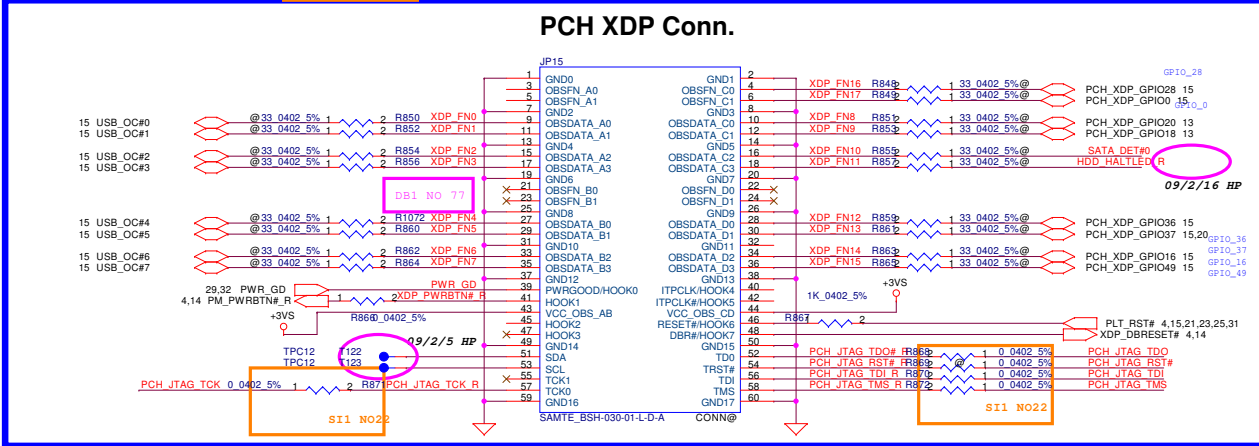


### iTPM ENABLE/DISABLE



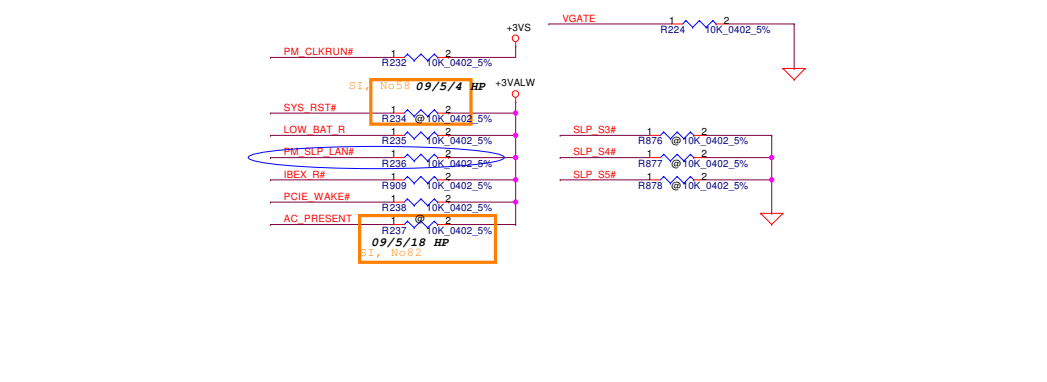
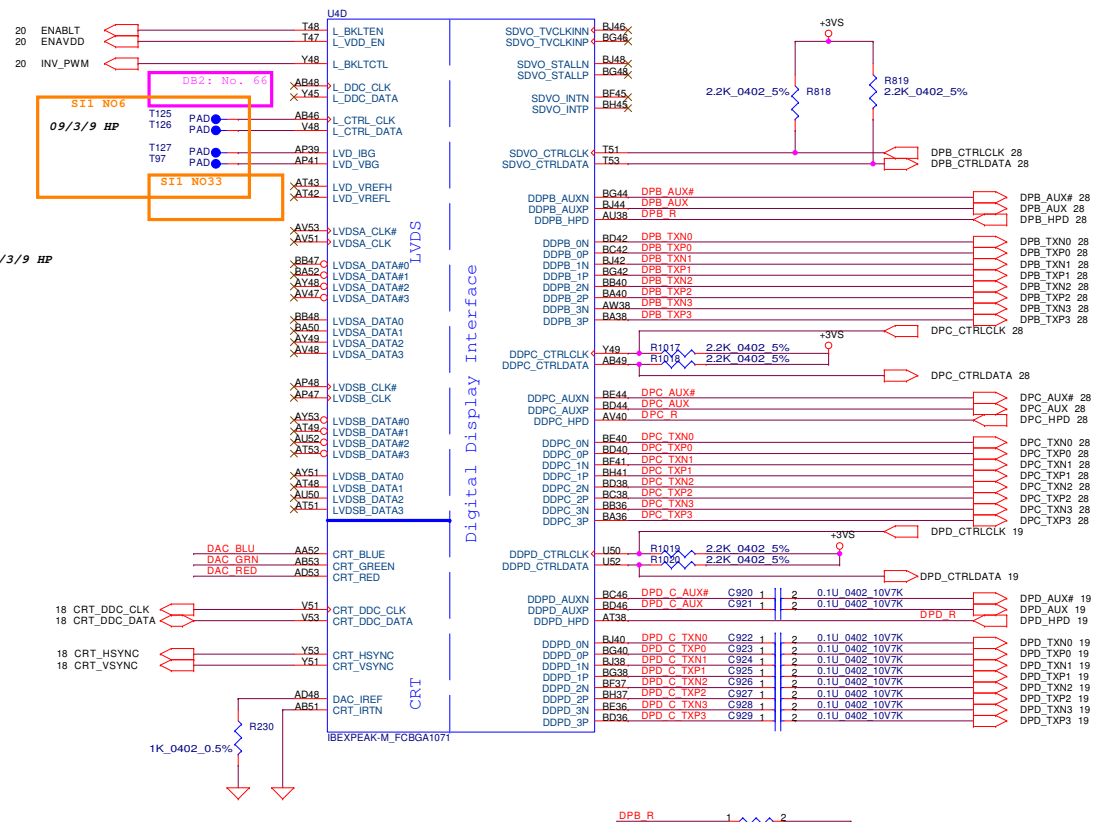
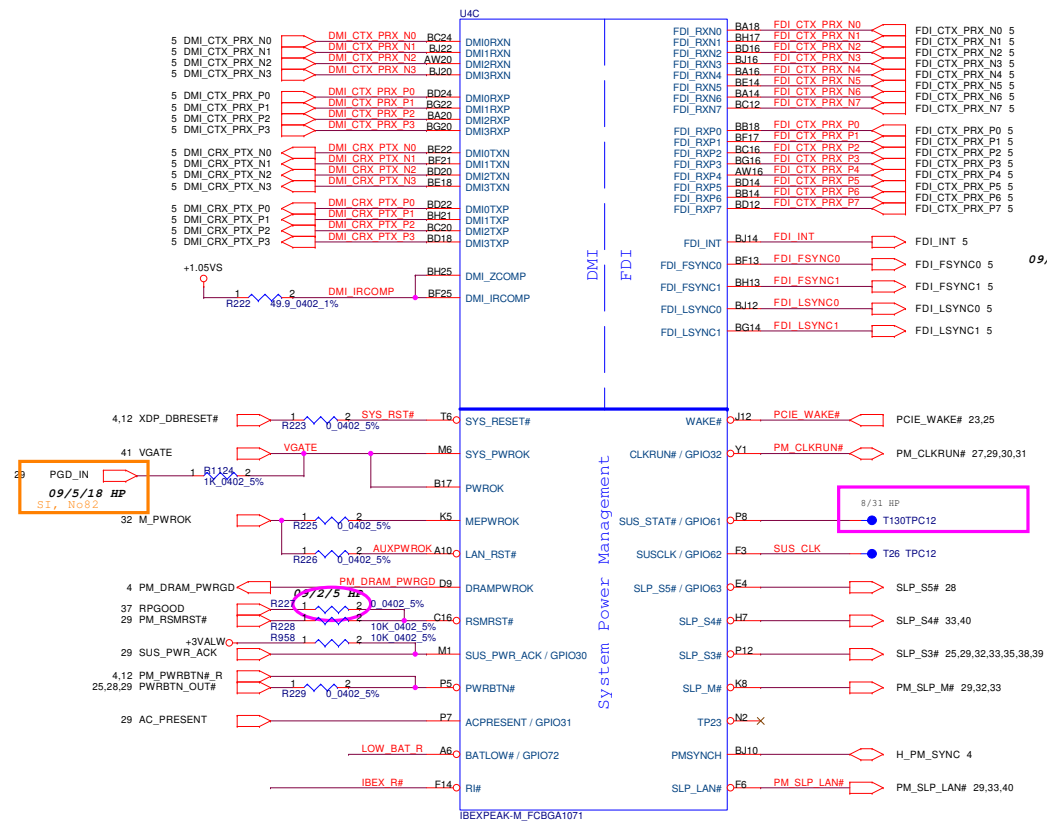
PCH Pin	Refdes	PCH JTAG Pre-Production	PCH JTAG Production
PCH_JTAG_TDO	R184	No Install	200ohm
PCH_JTAG_TMS	R189	No Install	100ohm
PCH_JTAG_TDI	R190	No Install	100ohm
PCH_JTAG_TCK	R194	No Install	100ohm
PCH_JTAG_RST#	R192	No Install	100ohm

GPIO33	iAMT Enable /Disable
Hi	Disable
Lo	Enable Default

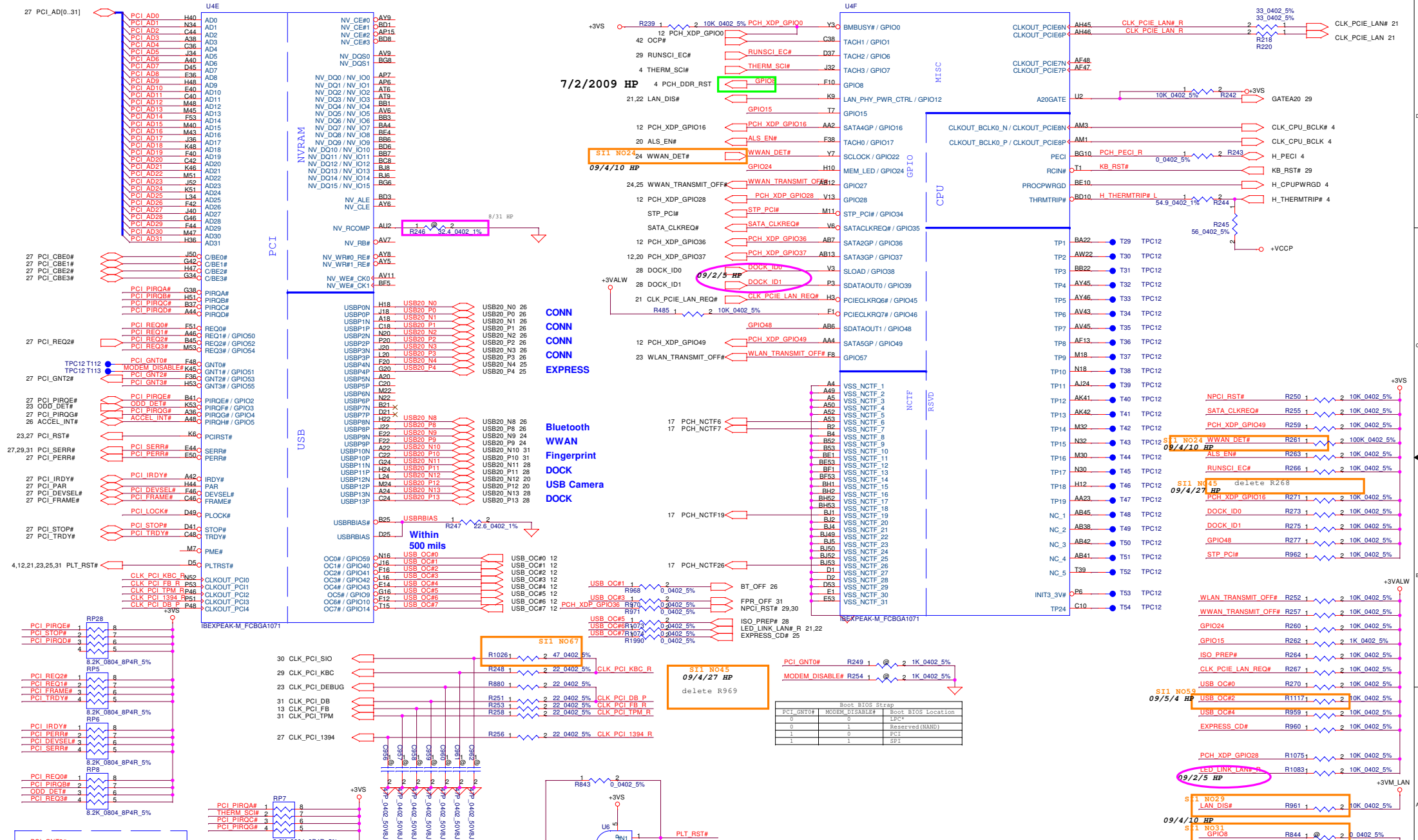


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7/2/2009 HP

09/4/10 HP

09/2/5 HP

09/4/27 HP

09/5/4 HP

09/2/5 HP

09/4/27 HP

09/5/4 HP

09/2/5 HP

09/4/10 HP

09/2/5 HP

09/4/10 HP

09/2/5 HP

09/4/10 HP

09/2/5 HP

09/4/10 HP

09/2/5 HP

09/4/10 HP

09/2/5 HP

09/4/10 HP

09/2/5 HP

09/4/10 HP

09/2/5 HP

09/4/10 HP

09/2/5 HP

09/4/10 HP

09/2/5 HP

Boot BIOS Strap	PCIE_GNT0#	MODEM_DISABLE#	Boot BIOS Location
0	0	0	LPC
1	0	1	Reserved (NAND)
0	1	0	PCI
1	1	1	SPI

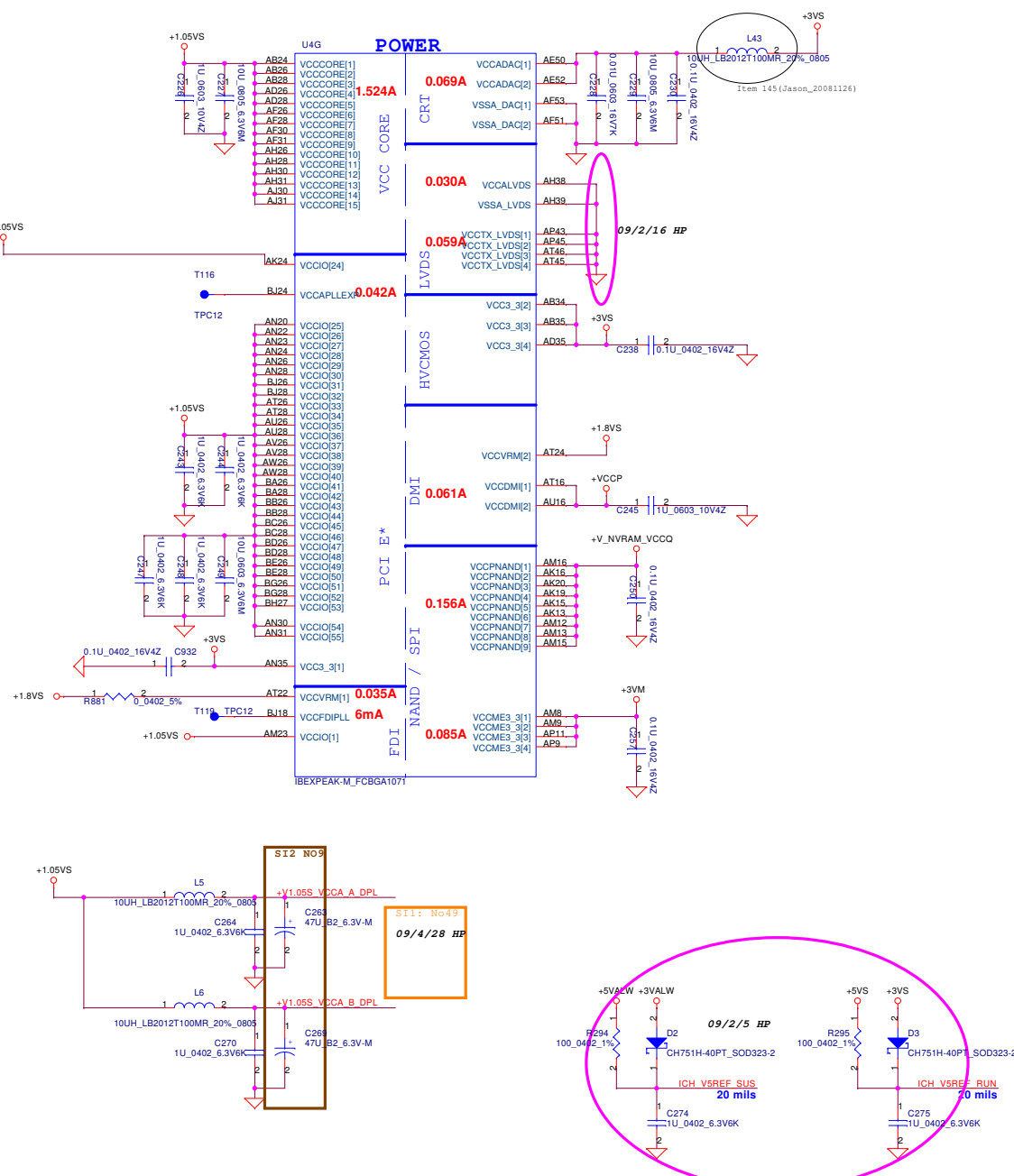
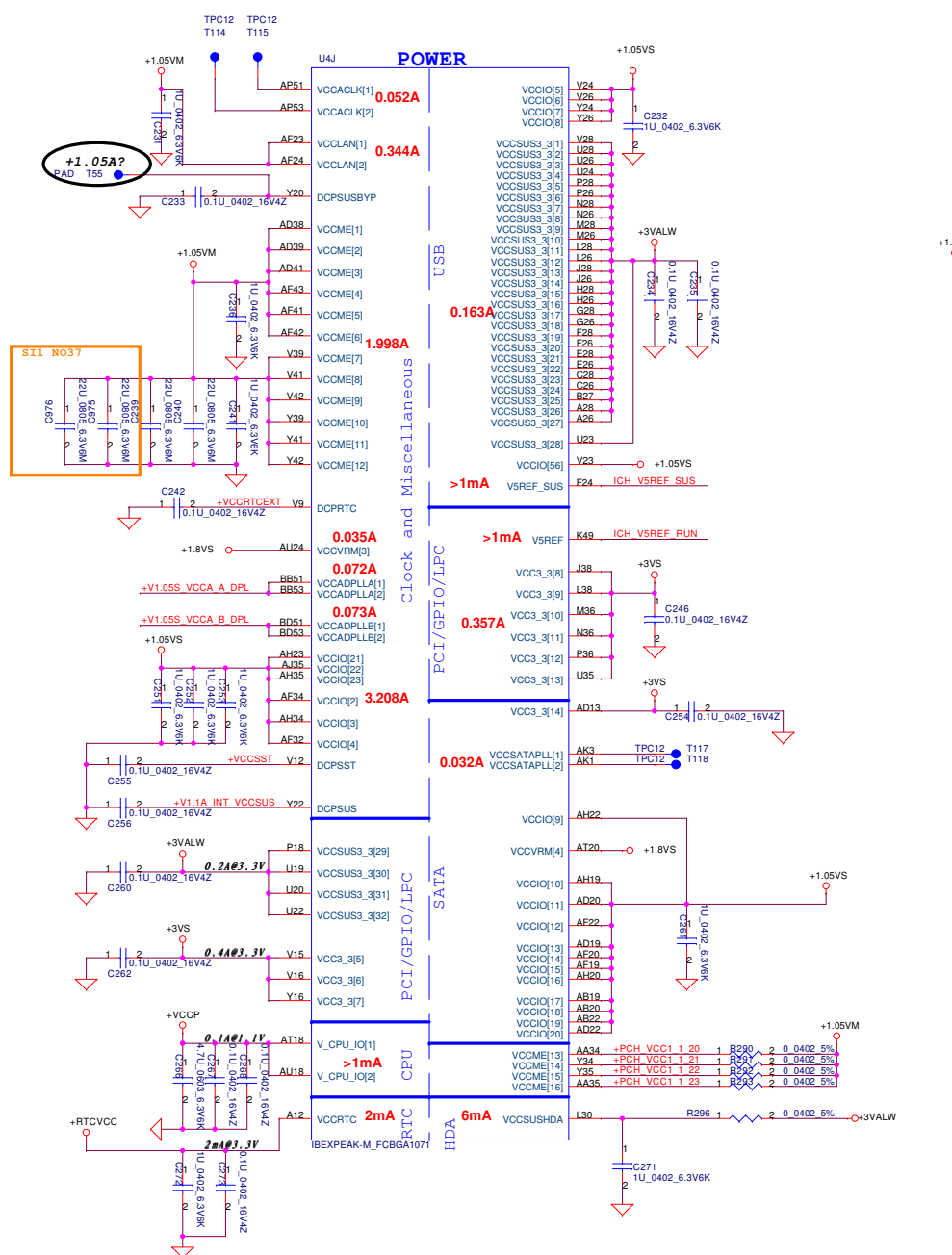
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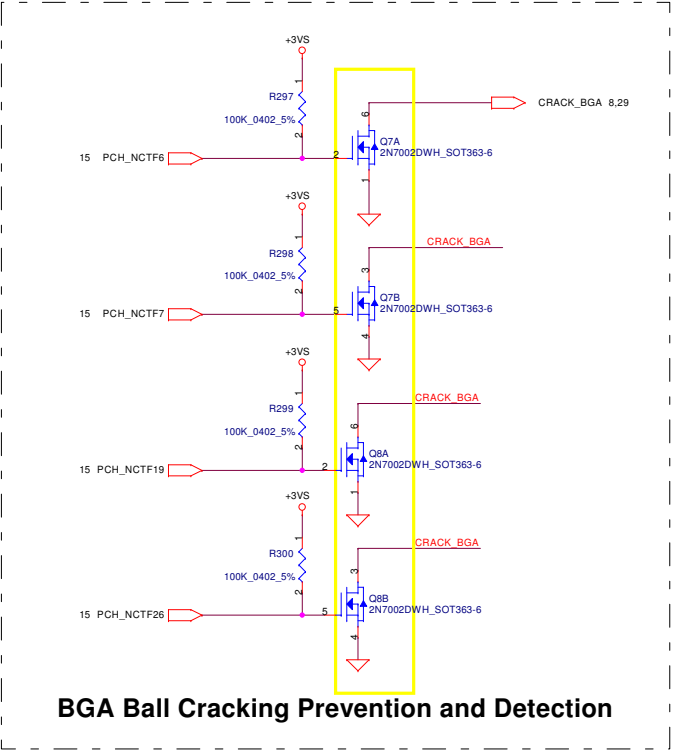
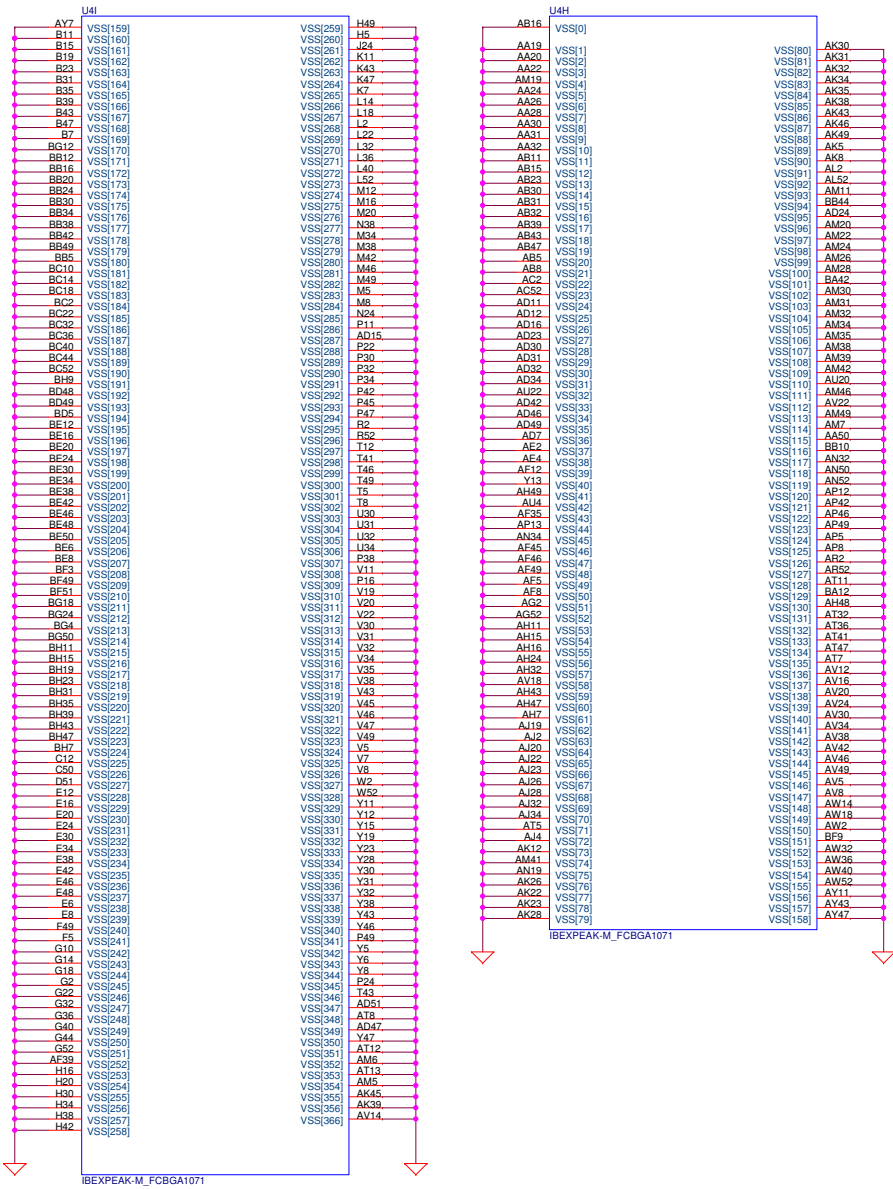
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**IBEX-M(4/6)-PCI/USB/RSVD**  
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 Size: 11.0 x 17.0  
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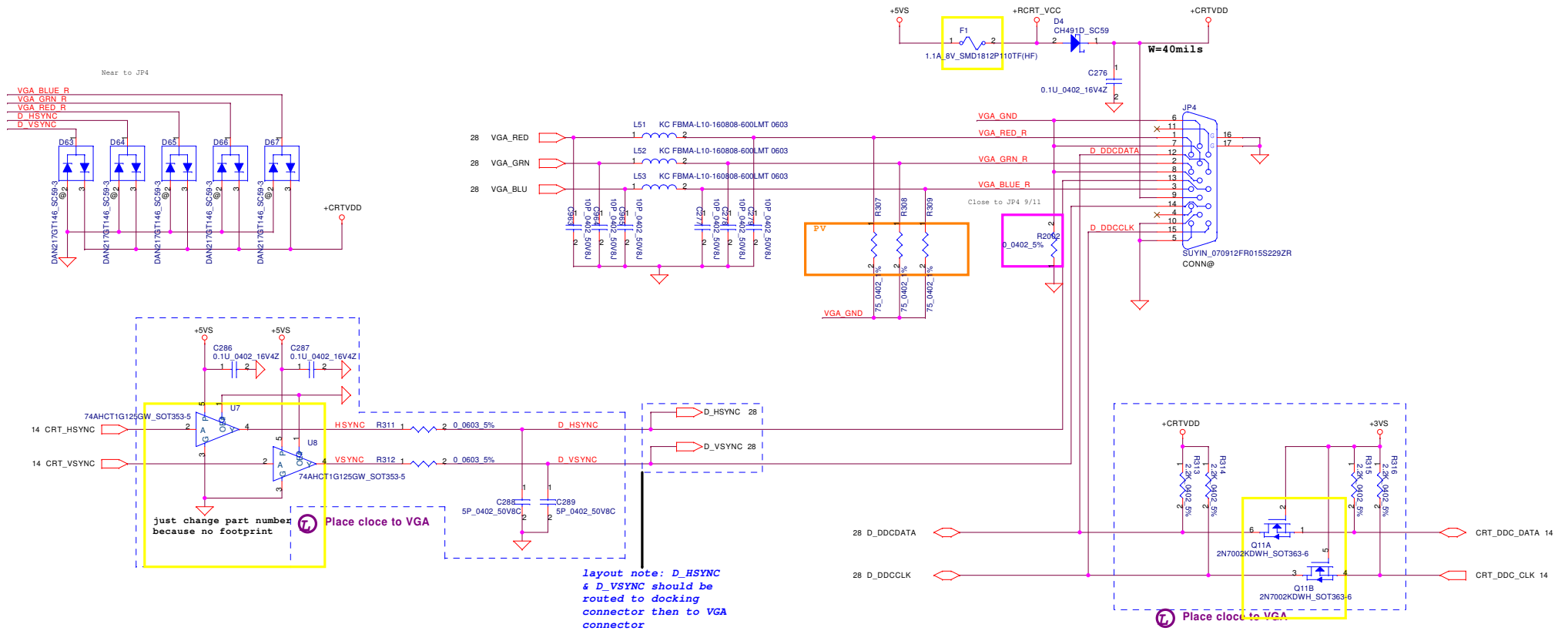


**BGA Ball Cracking Prevention and Detection**

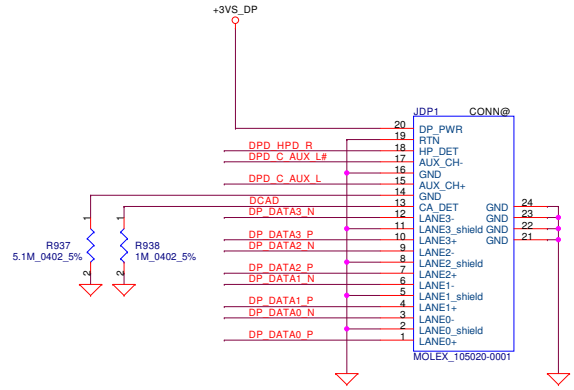
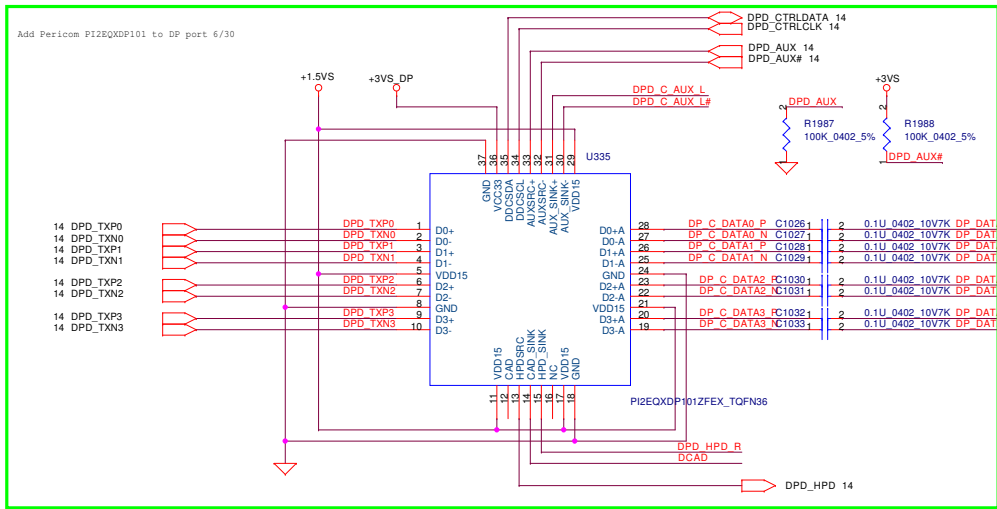
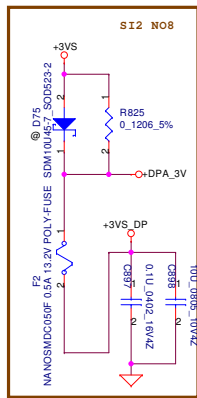
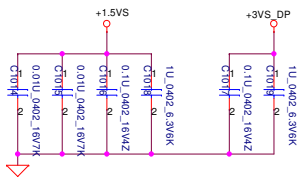
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# CRT Connector



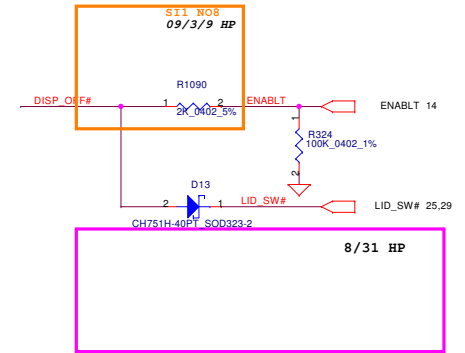
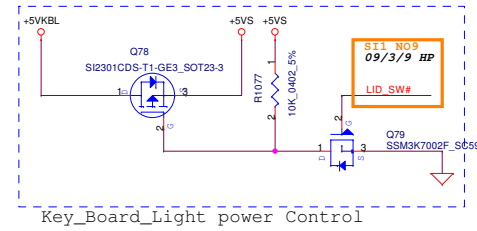
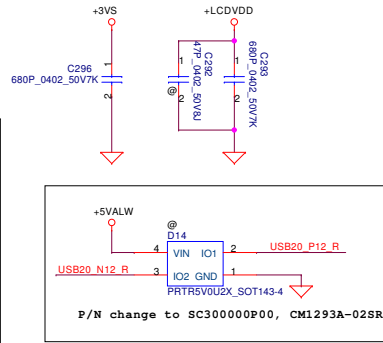
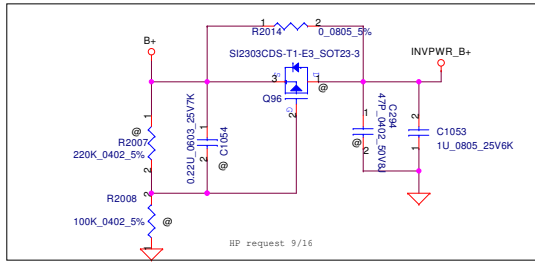
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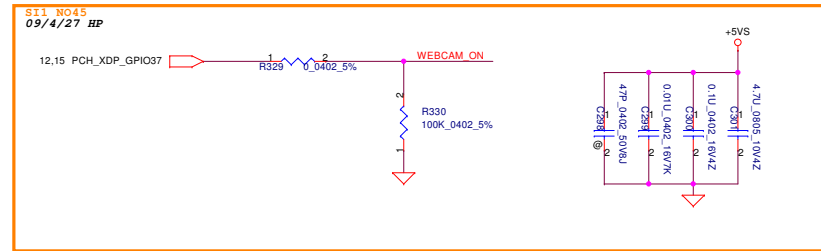
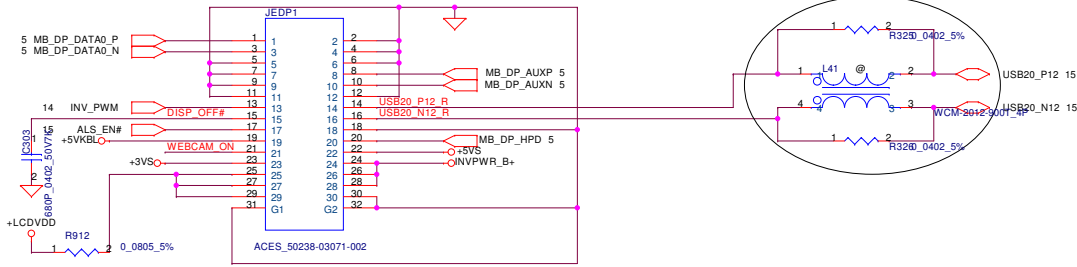
Change connecting eDP to PCH DP 11/24

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	LA-4902P			
Date:	Wednesday, December 03, 2009	Sheet	19	of 47

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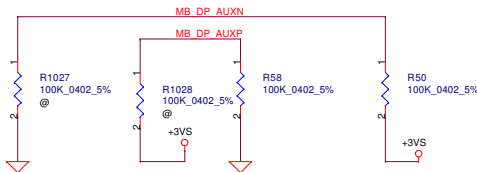
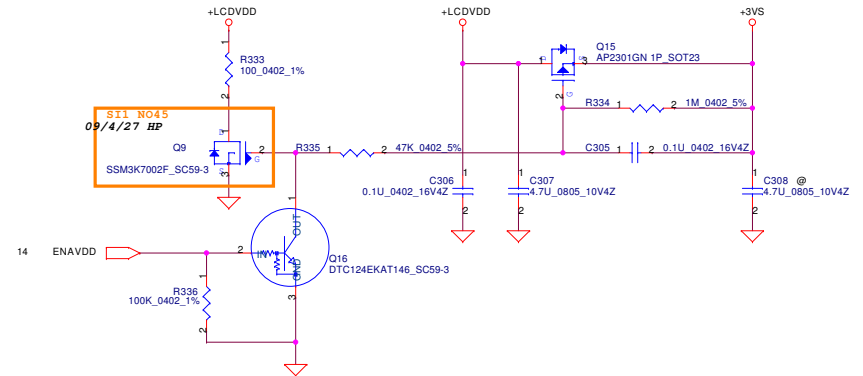


**LCD/PANEL BD. CONN.**

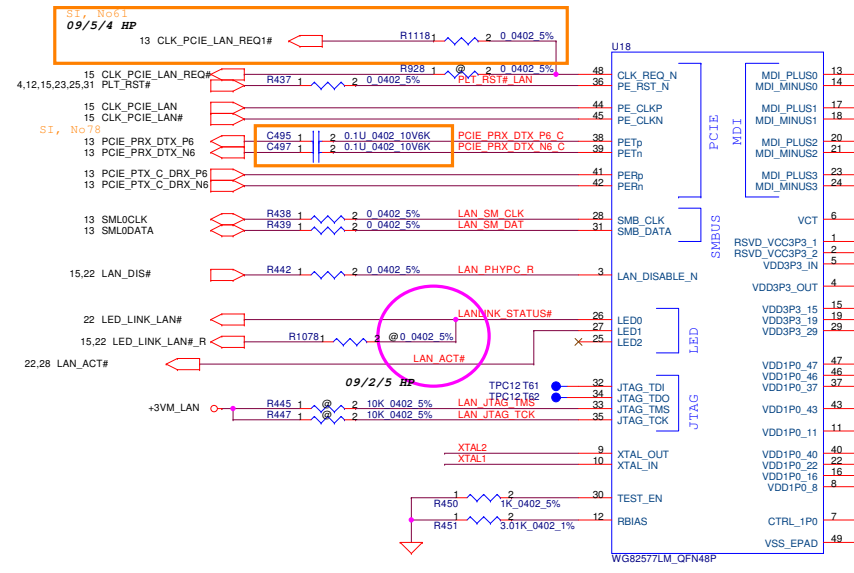
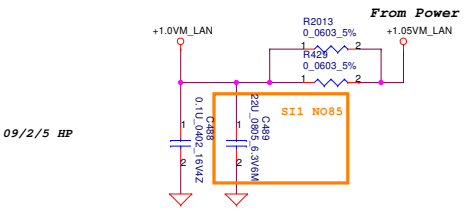
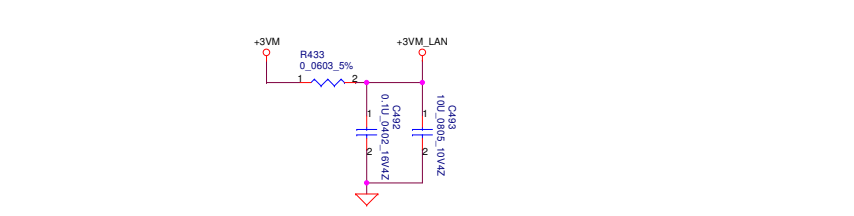


Change eDP LCD connector to 30pin for Coaxial cable 9/13

**LCD POWER CIRCUIT**



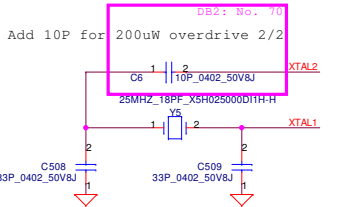
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				LA-4902P
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				Sheet 20 of 47



SI, No:61  
09/5/4 HP

SI, No:75

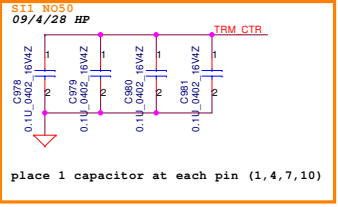
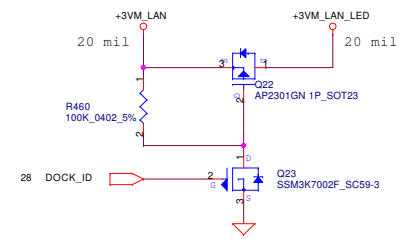
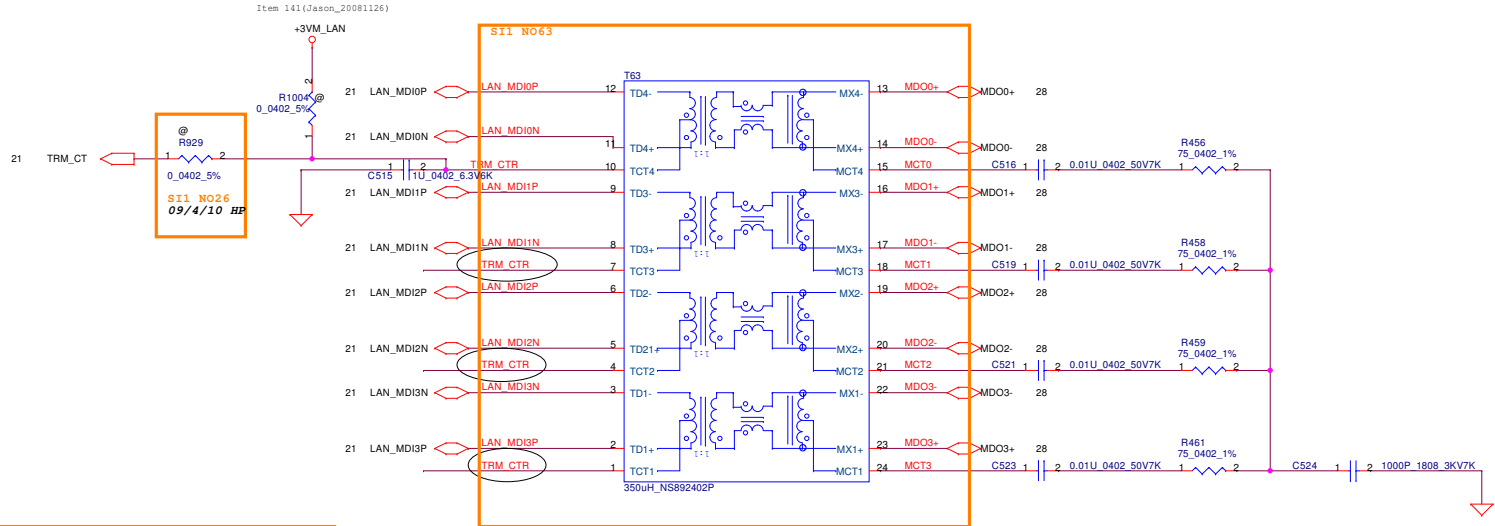
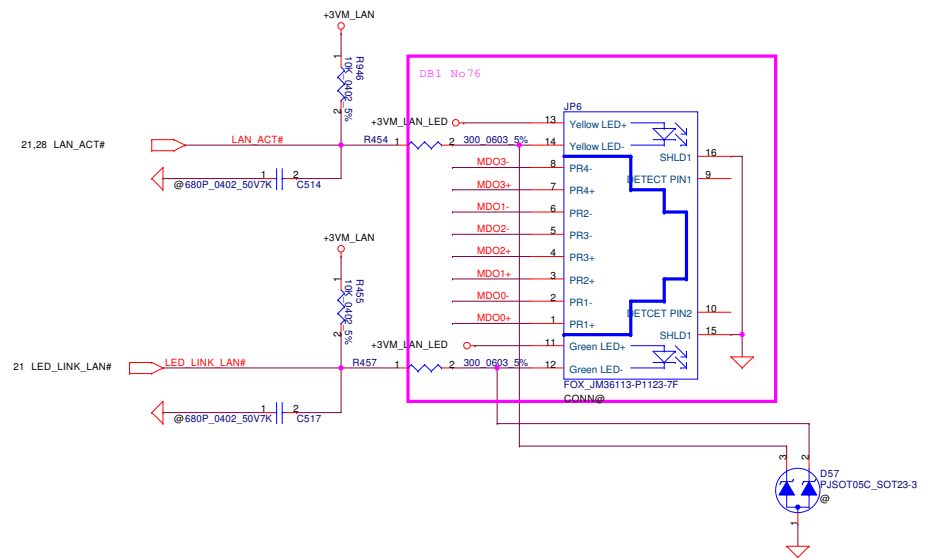
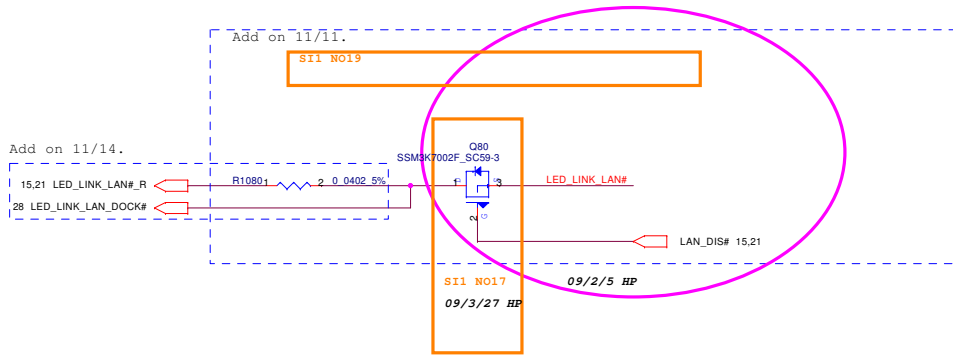
09/2/5 HP



09/2/5 HP

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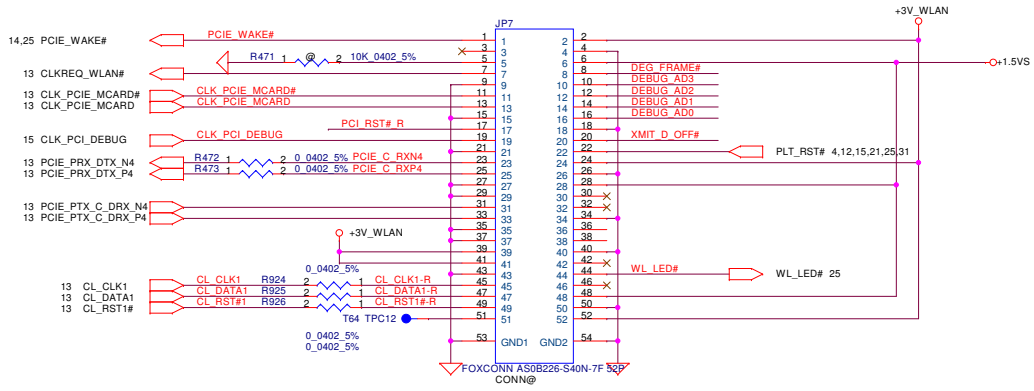
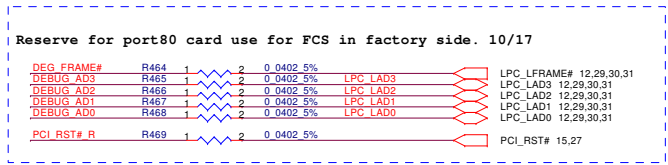
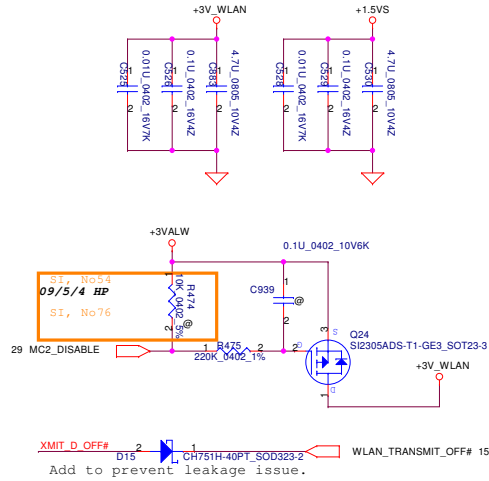
hexair@hotmail.com



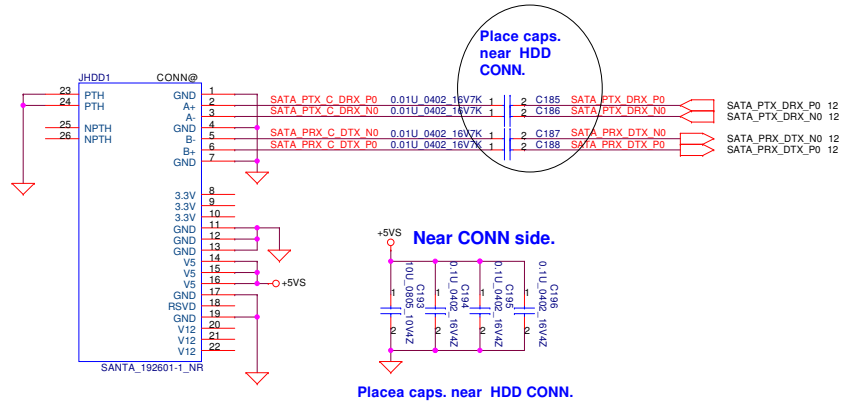
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Issued Date	2008/09/15	Deciphered Date		2009/12/31	<b>Magnetic &amp; RJ45</b>
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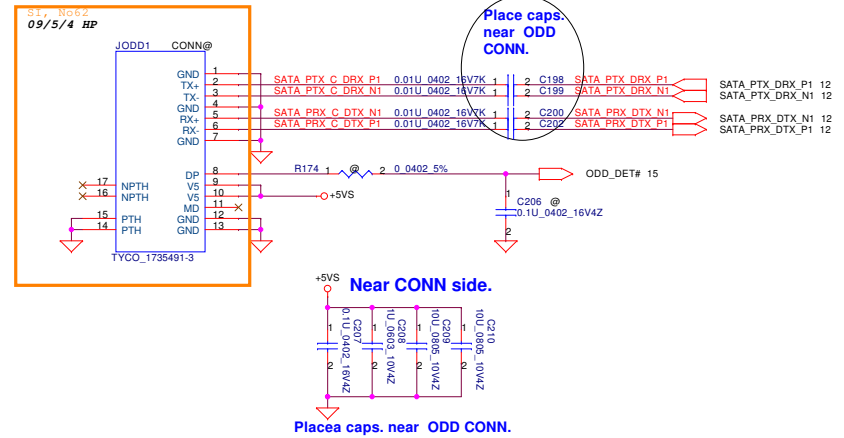
# WLAN (Half mini Card)



## SATA HDD CONN.



## SATA ODD CONN.

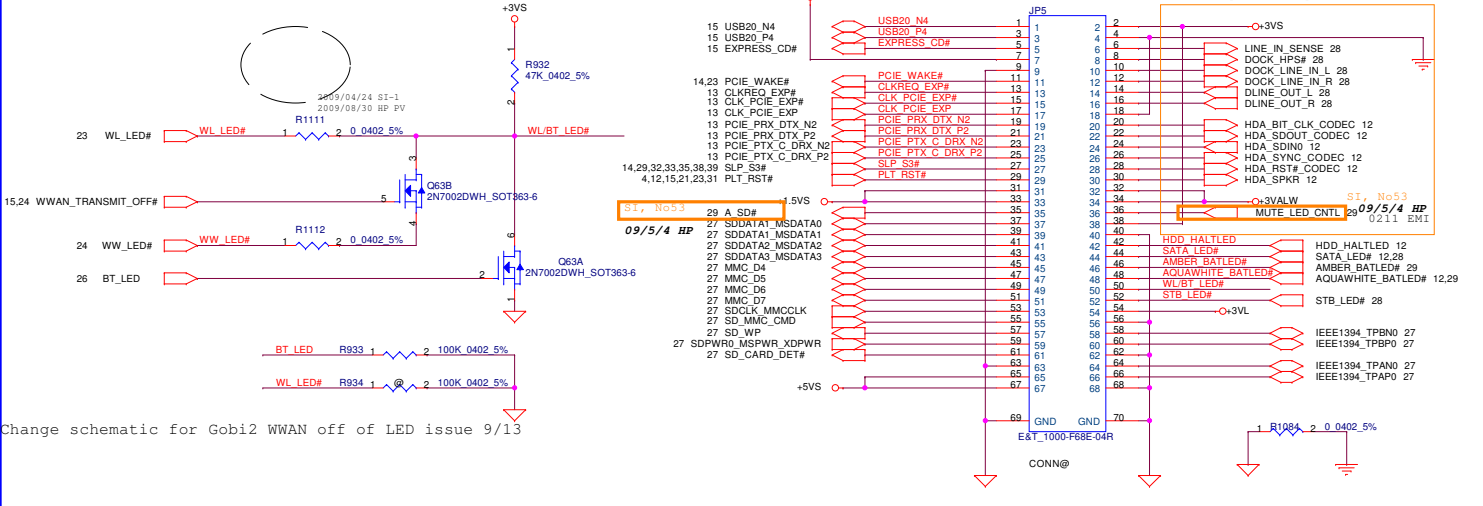


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Size	Document Number	Rev	Date: Wednesday, December 09, 2009 Sheet 23 of 47		
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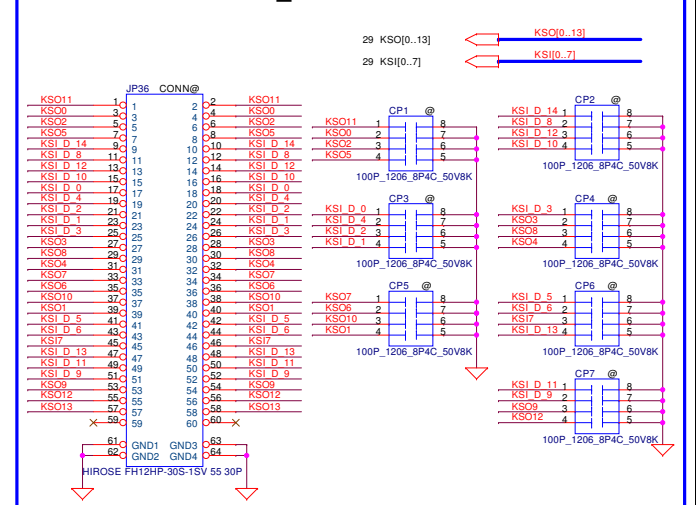
hexair@hotmail.com



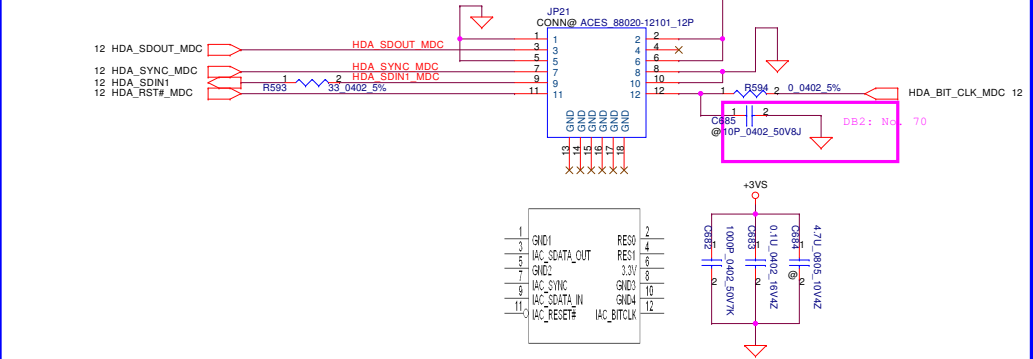
### AUDIO BOARD CONNECTOR (MALE)



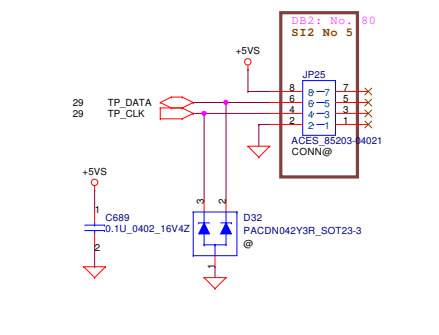
### INT\_KBD CONN.



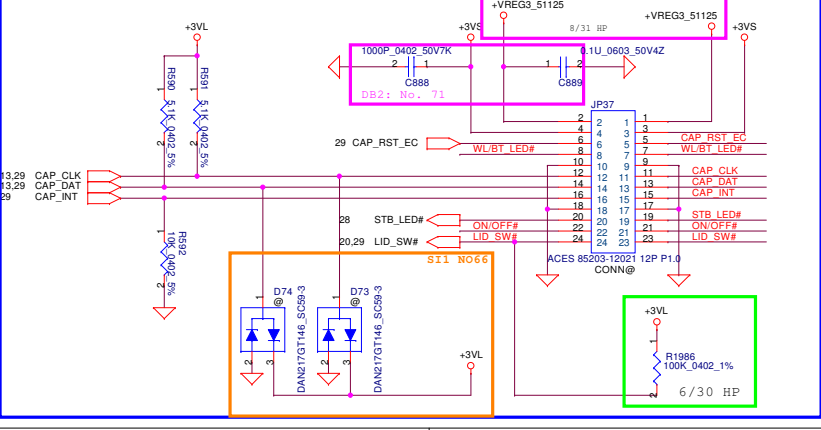
### MDC 1.5 Conn.



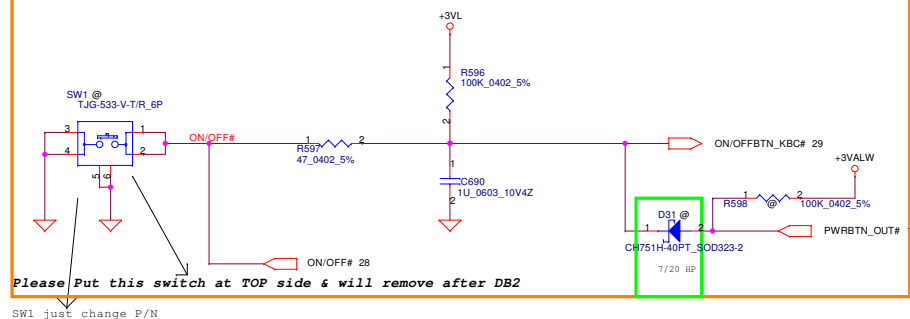
### T/P BOARD.



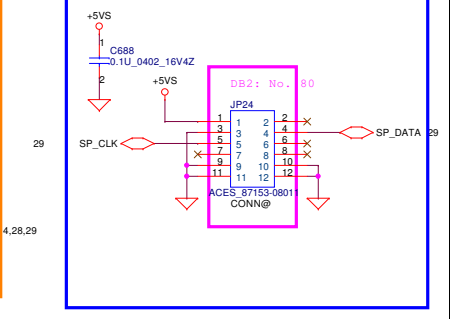
### CAP SWITCH BOARD



### Power Button

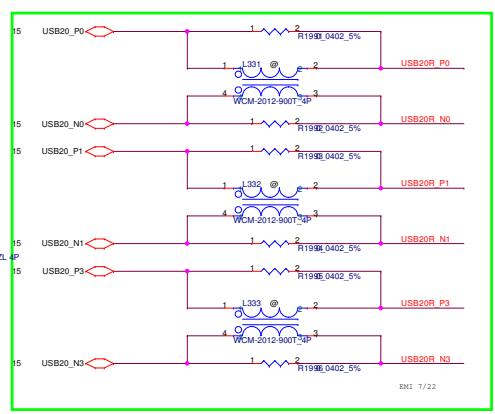
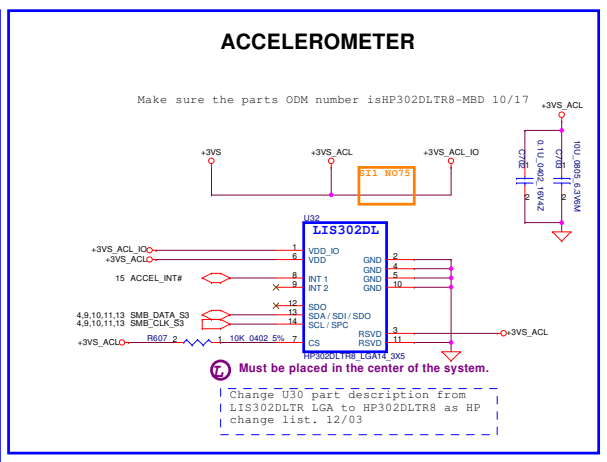
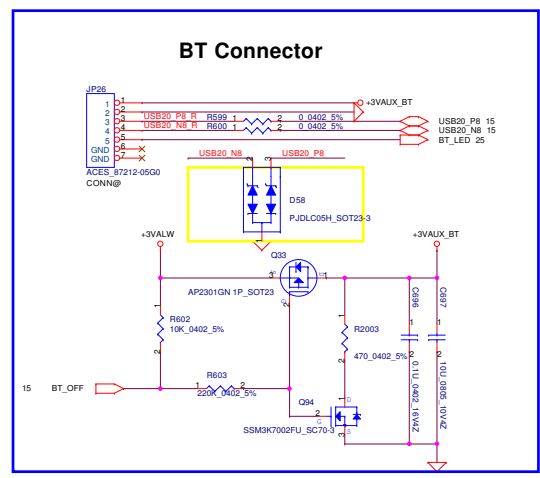
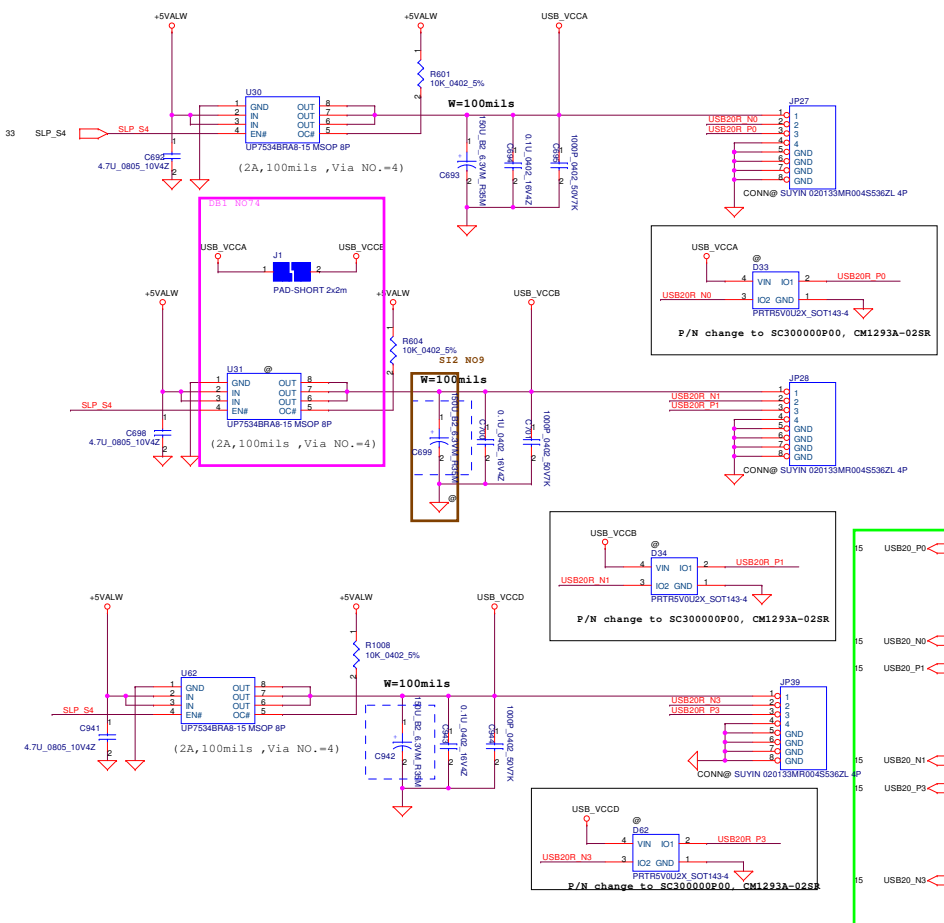


### TrackPoint CONN.

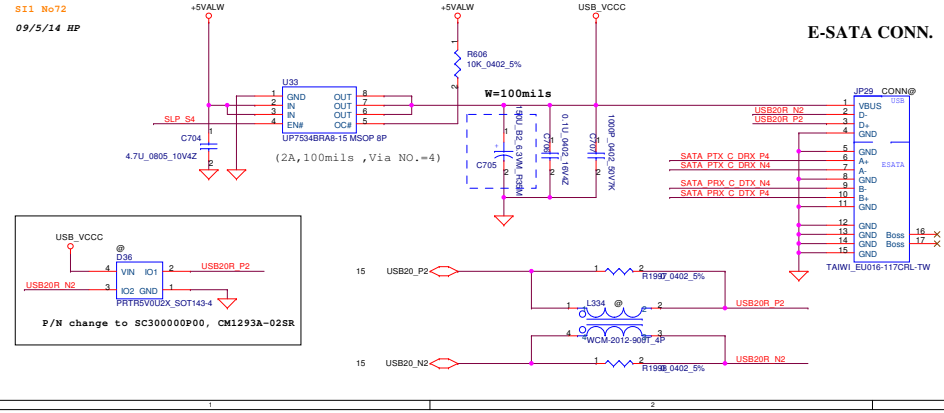


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Size	Document Number	Rev	Date	Page	of
LA-492P	LA-492P	0.3	Wednesday, December 09, 2009	25	of 47

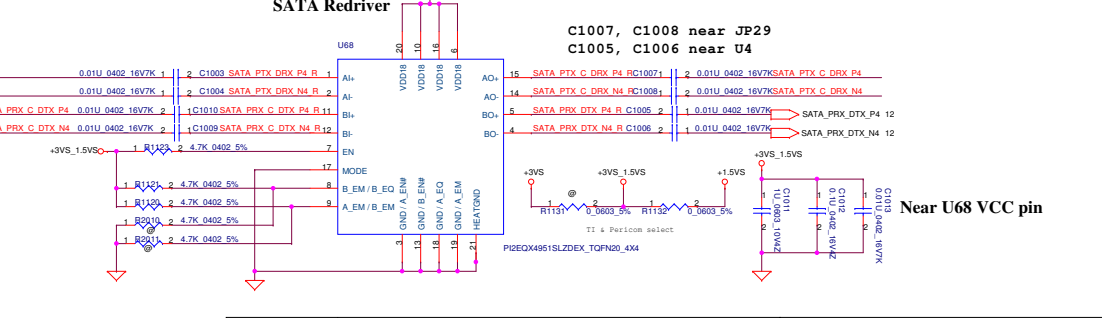
hexair@hotmail.com



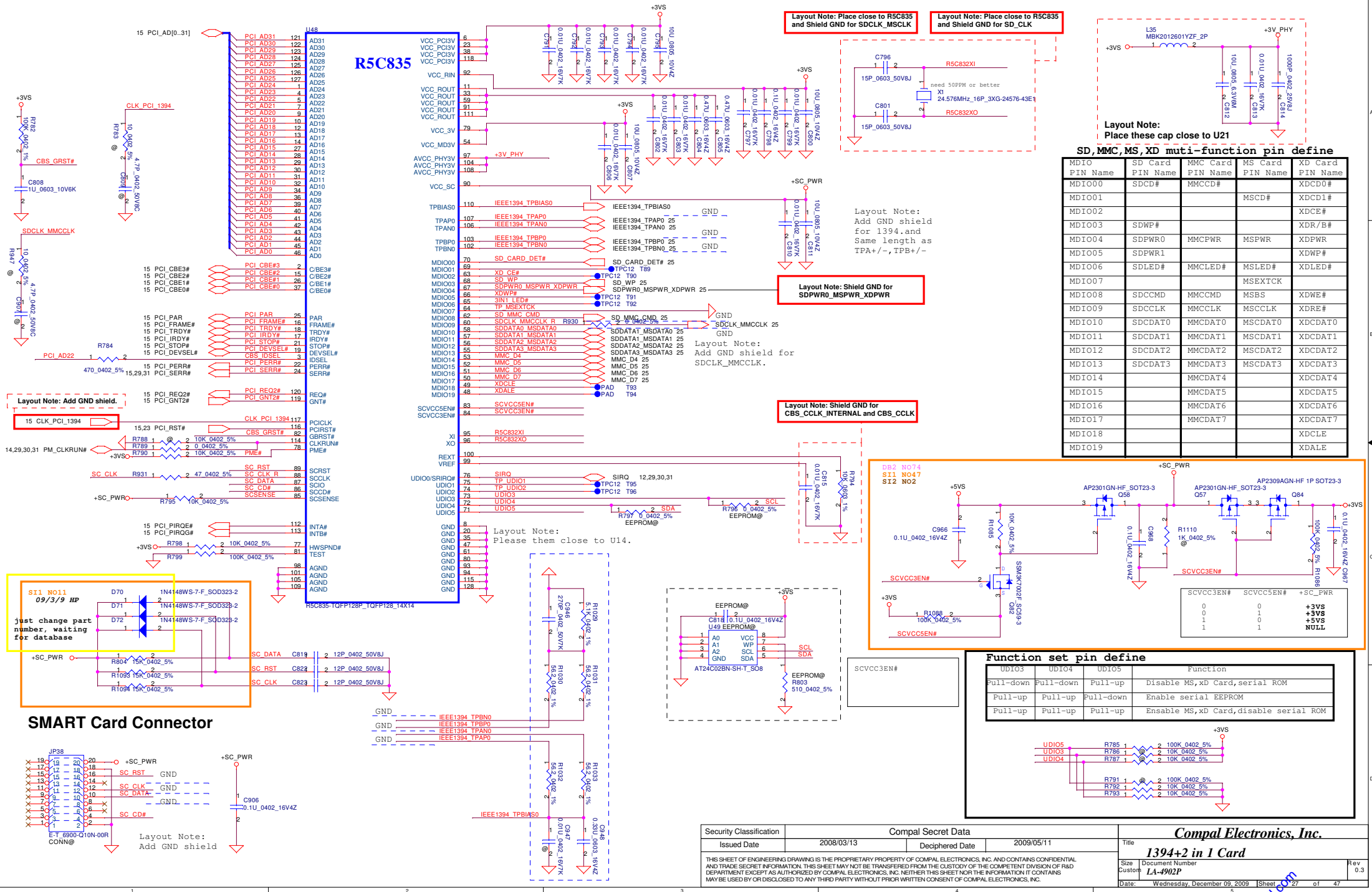
### ESATA function



### Change TI to Pericom PI3EQX4951ST\_PEND And add 1.5 power rail option 6/30

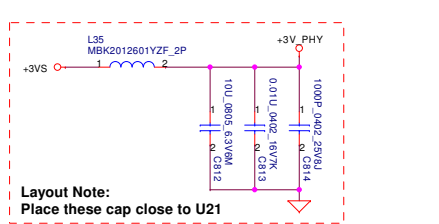


Security Classification	Compal Secret Data		Title <b>USB &amp; BT Connector &amp; Accelerometer</b>
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Layout Note: Place close to R5C835 and Shield GND for SDCLK\_MMCLK

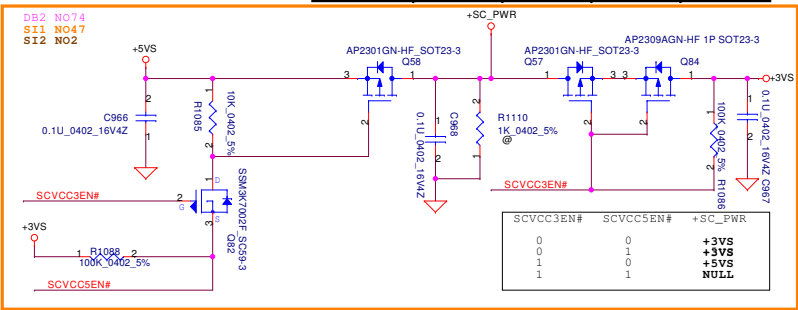
Layout Note: Place close to R5C835 and Shield GND for SD\_CLK



Layout Note: Place these cap close to U21

Layout Note: Shield GND for SDPWR0\_MSPWR\_XDPWR

Layout Note: Shield GND for CBS\_CCLK\_INTERNAL and CBS\_CCLK



SCVCC3EN#	SCVCC5EN#	+SC_PWR
0	0	+3VS
0	1	+3VS
1	0	+5VS
1	1	NULL

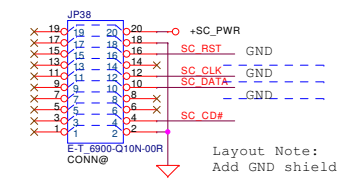
**Function set pin define**

UDI03	UDI04	UDI05	Function
Pull-down	Pull-down	Pull-up	Disable MS, xD Card, serial ROM
Pull-up	Pull-up	Pull-down	Enable serial EEPROM
Pull-up	Pull-up	Pull-up	Enable MS, xD Card, disable serial ROM

Layout Note: Add GND shield.

Layout Note: Please them close to U14.

**SMART Card Connector**



Layout Note: Add GND shield

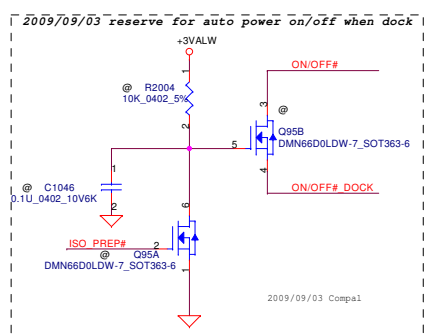
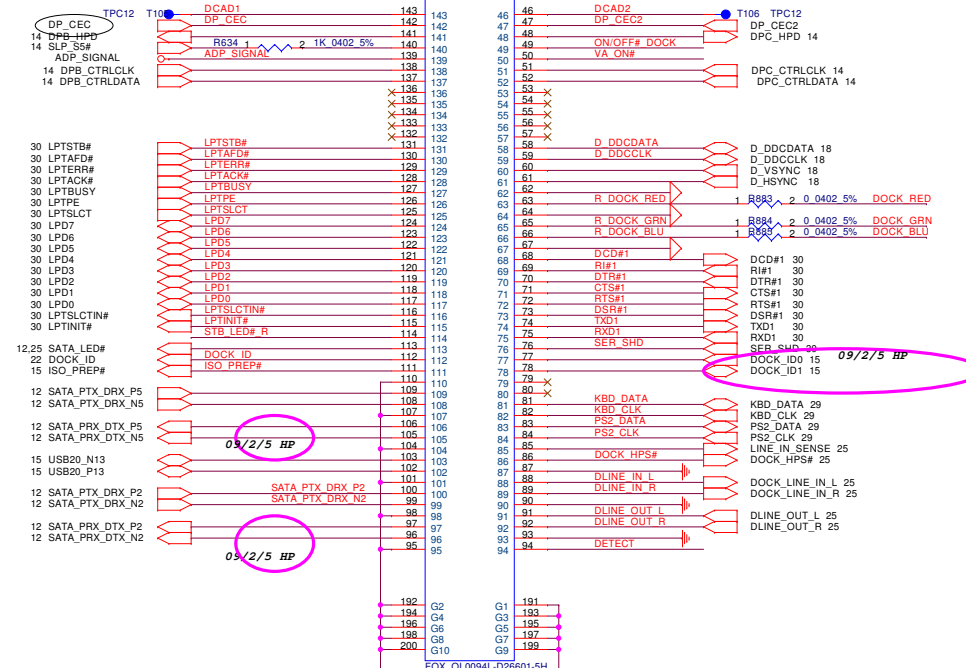
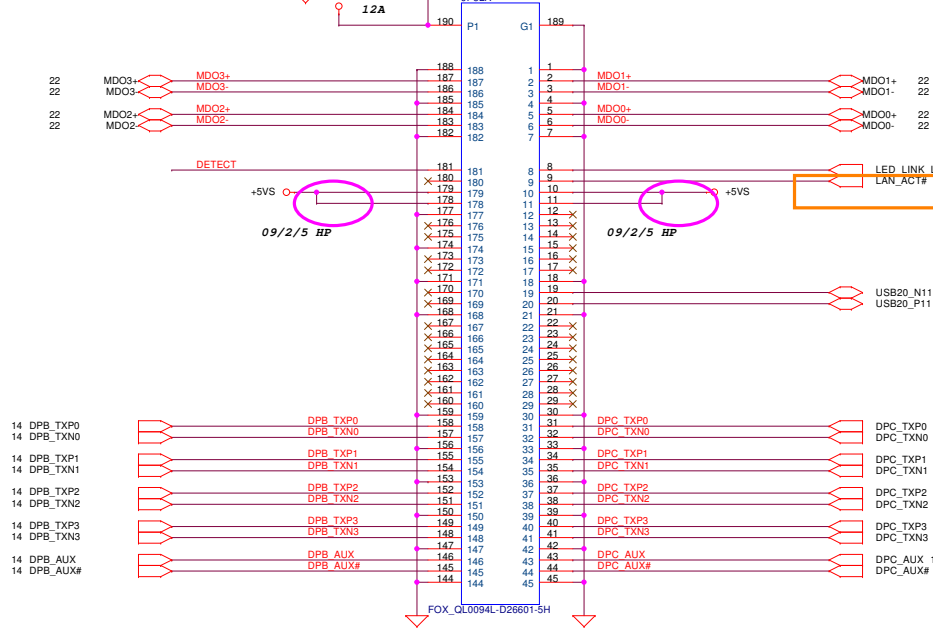
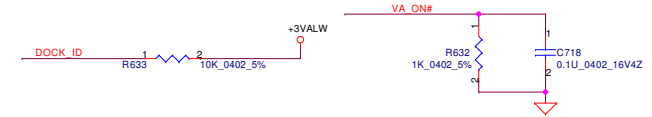
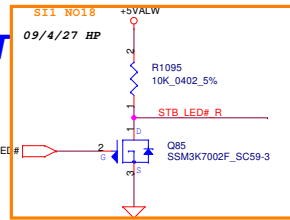
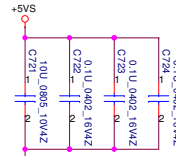
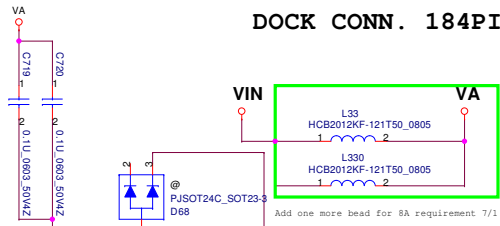
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Date:	Wednesday, December 09, 2009	Sheet	27 of 47

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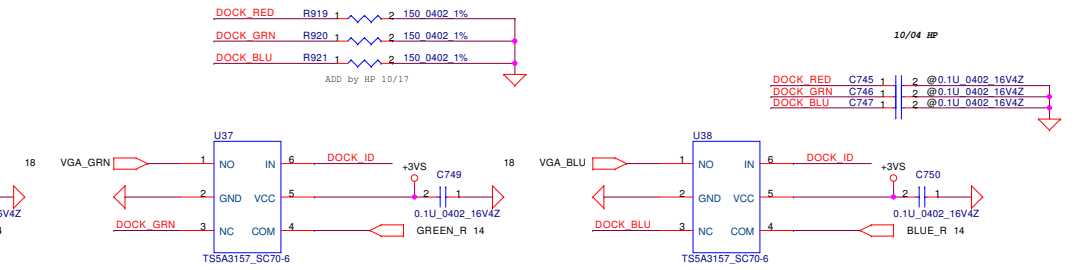
DOCK CONN. 184PIN

DOCKING CONNECT

- (1) PCI Express x1 channels
- (2) PS/2 Interfaces
- (3) USB 2.0 channels
- (4) SATA Channels
- (5) Display Port Channels
- (1) Serial Port
- (1) Parallel Port
- (1) Line In
- (1) Line Out
- (1) K145 (10/100/1000)
- (1) VGA
- (1) 2 LAN indicator LED's
- (1) Power Button
- (1) I2C interface

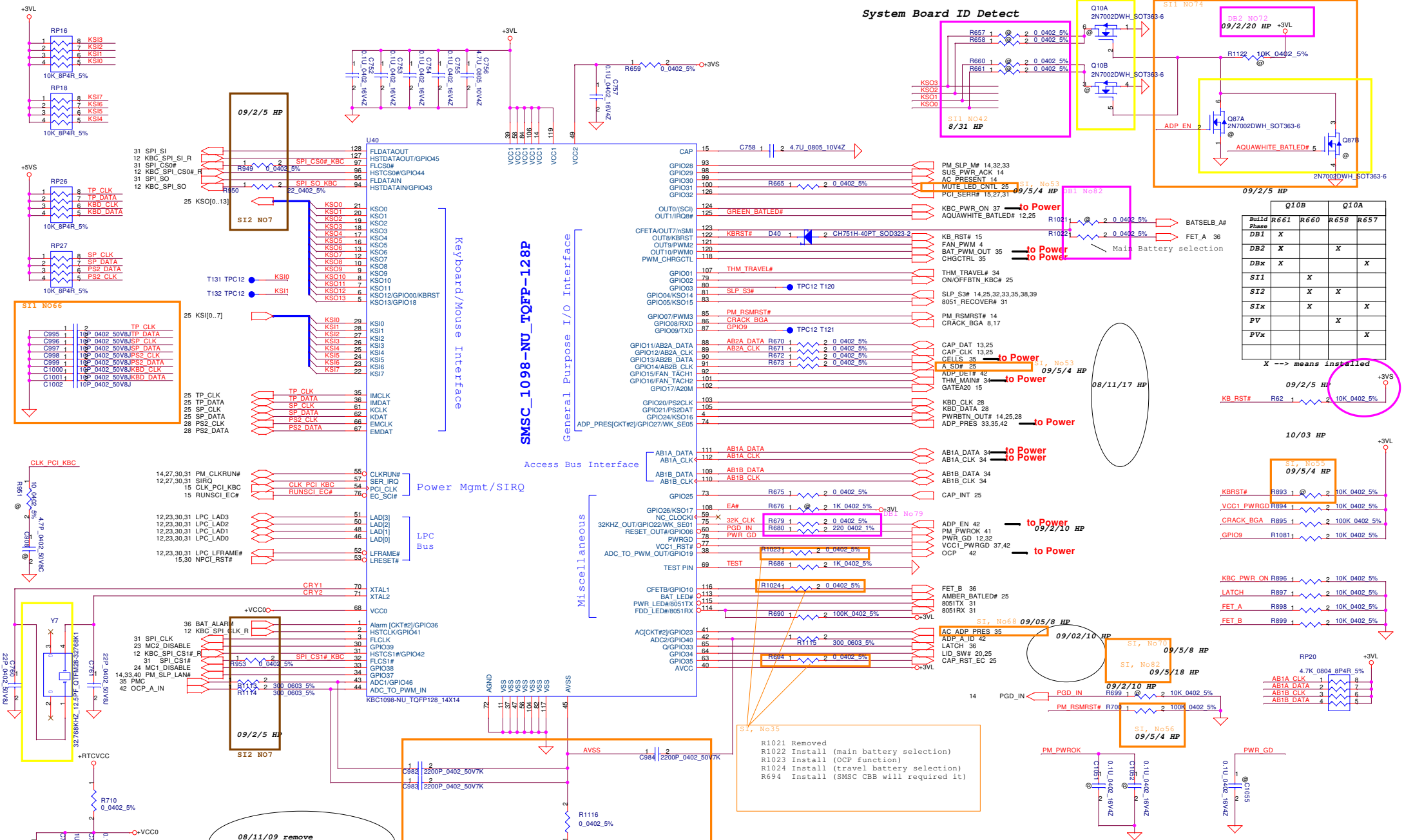


	IN	NC<-->COM	NO<-->COM
L	ON		OFF
H	OFF		ON

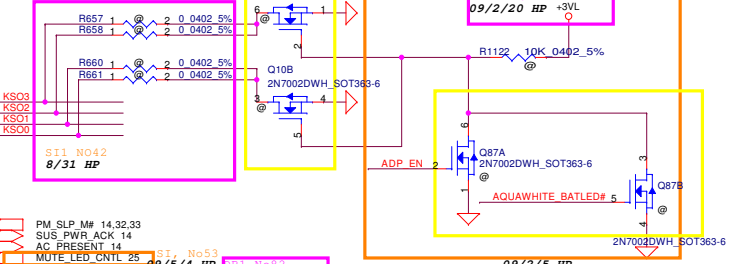


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**System Board ID Detect**



Build Phase	R661	R660	R658	R657
DB1	X			
DB2	X		X	
DBx	X			X
SI1		X		
SI2		X	X	
SIx		X		X
PV			X	
PVx				X

X --> means installed

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Size	Document Number
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Date	Rev
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08/11/09 remove all options of 1091

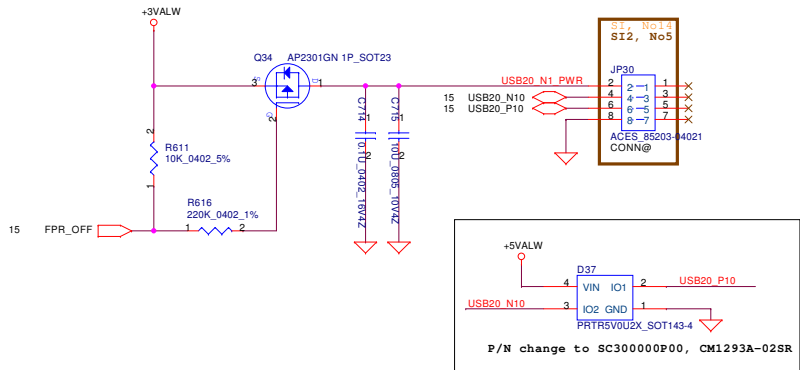
SI1, No57  
09/5/4 HP  
add R113-R116 and C982-C984, these should be placed at pins nearby (except R116)

hexair@hotmail.com

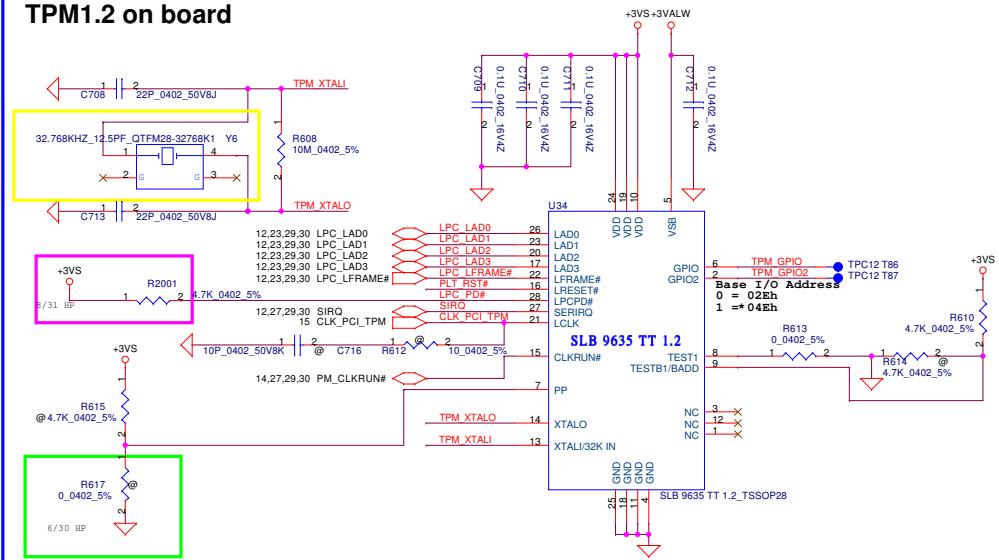




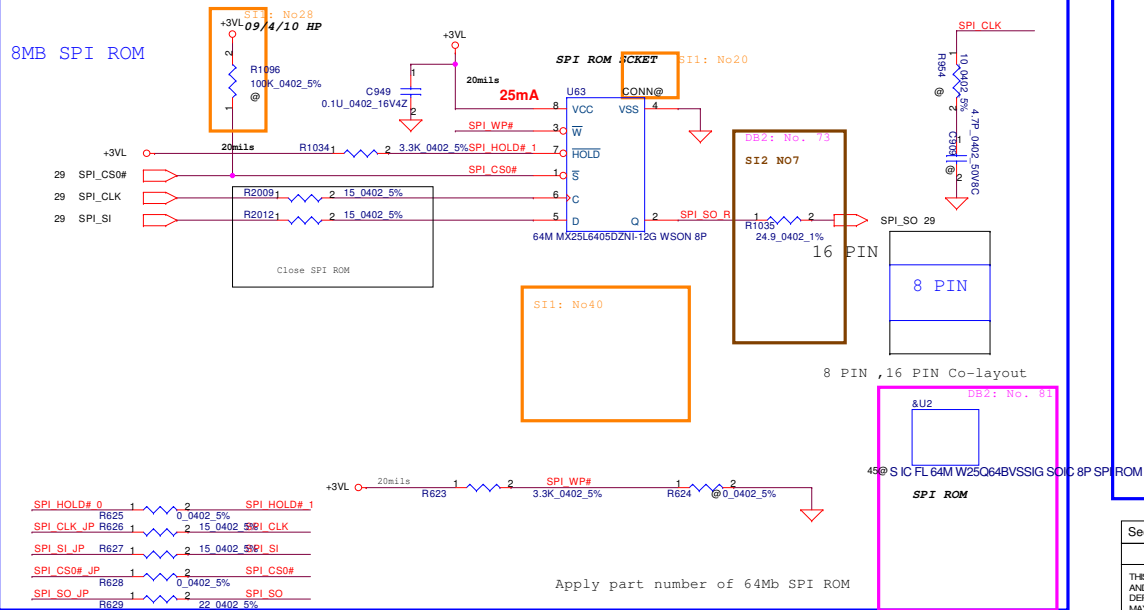
# Finger Printer



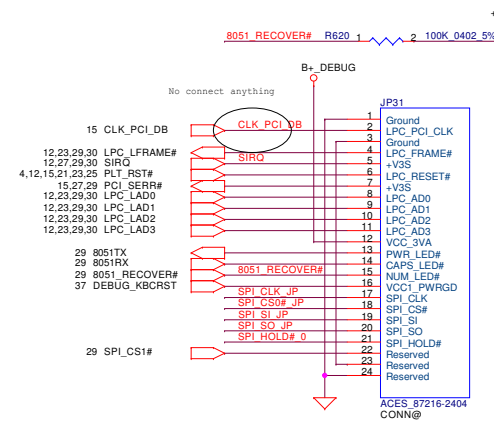
# TPM1.2 on board



# BIOS ROM(8MB)

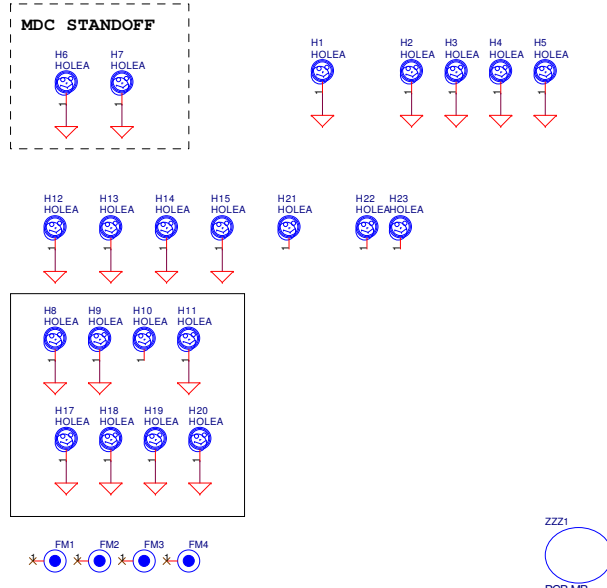
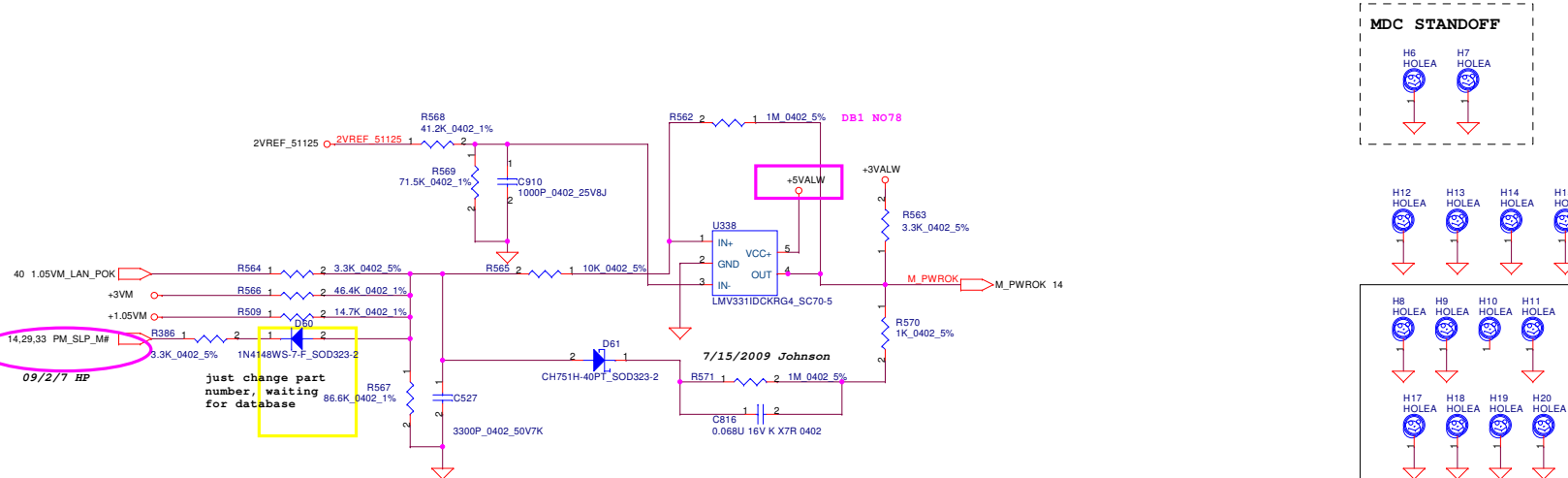
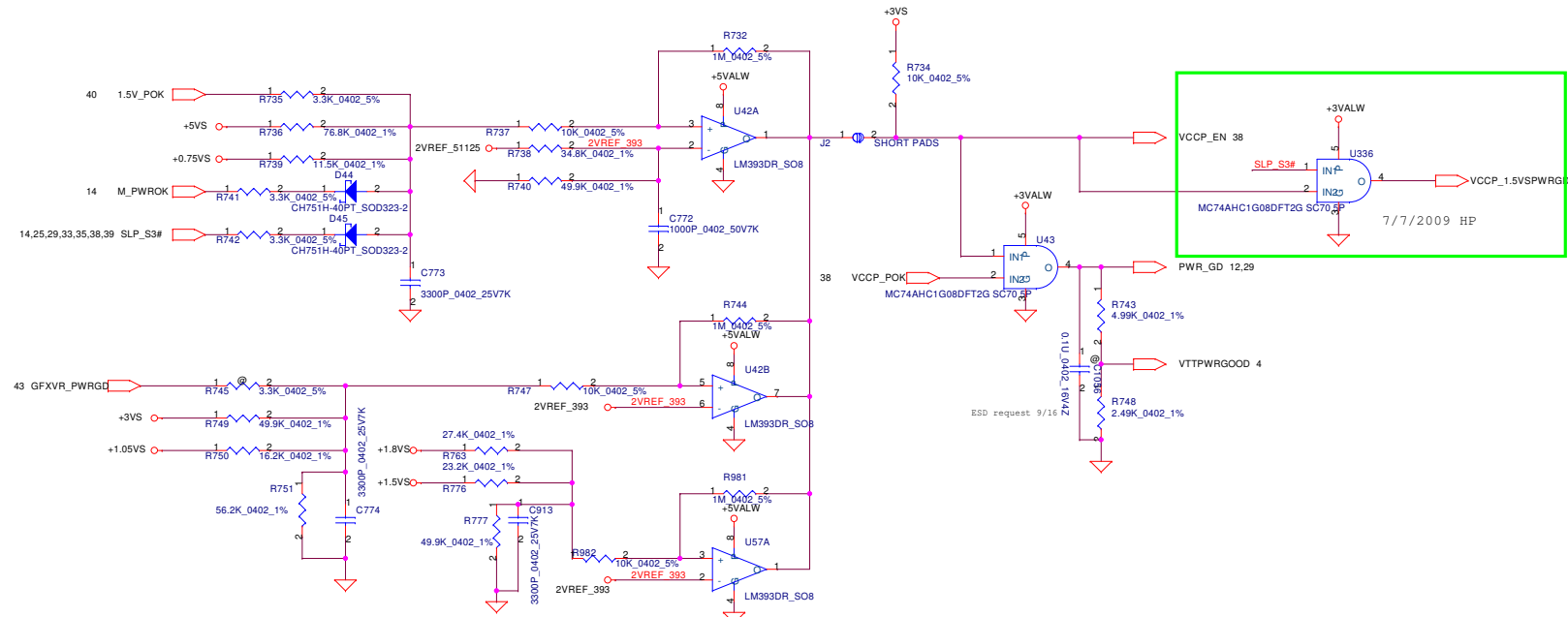


# LPC Debug Port



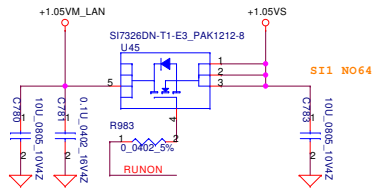
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			Size   Document Number <b>LA-492P</b>
			Rev <b>0.3</b>
			Date: Wednesday, December 09, 2009   Sheet 31 of 47

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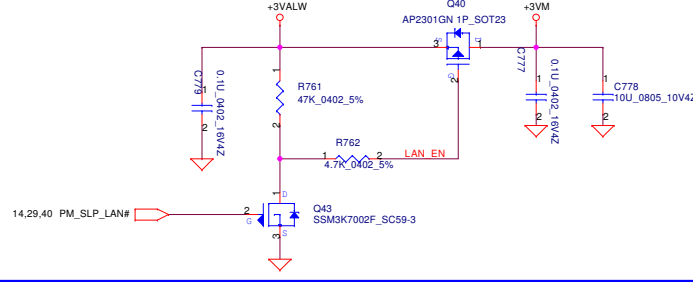


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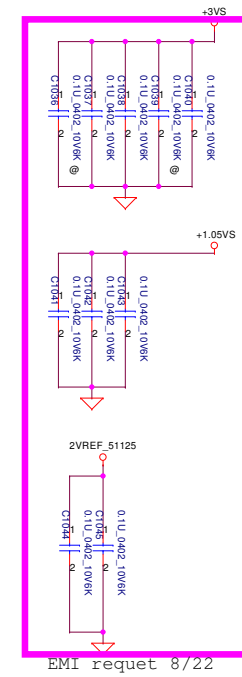
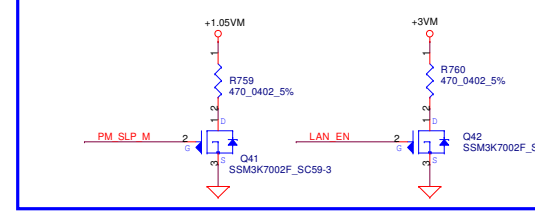
### +1.05VM\_LAN to +1.05VS Transfer



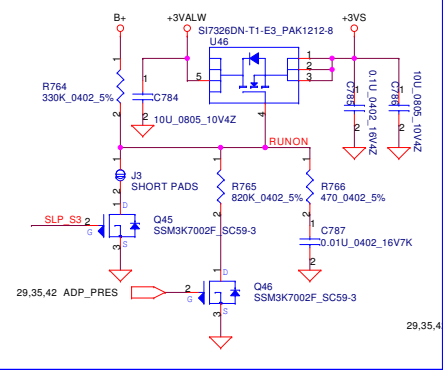
### +3VALW to +3VM Transfer



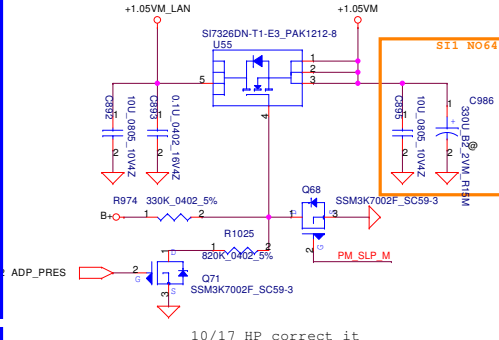
### Discharge circuit-2 for V-M



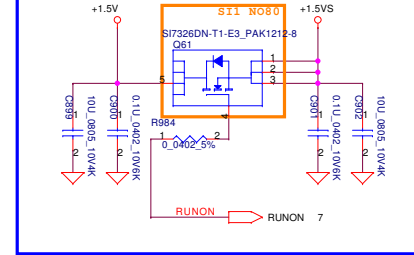
### +3VALW to +3VS Transfer



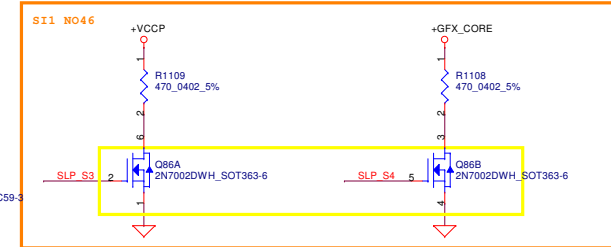
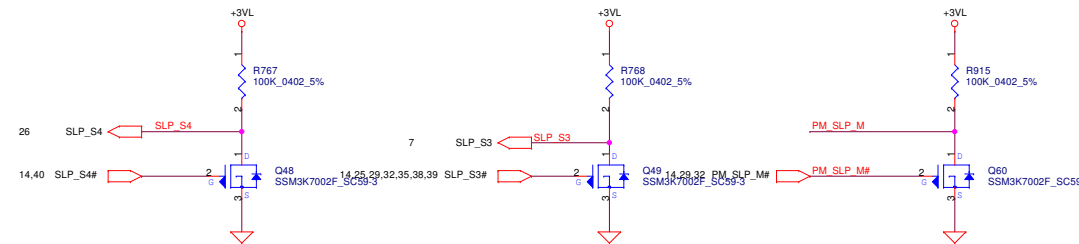
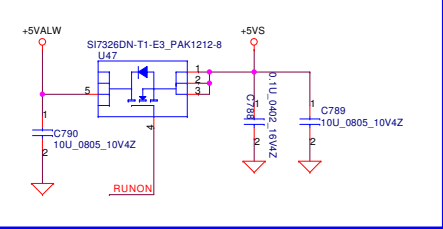
### +1.05VM\_LAN to +1.05VM Transfer

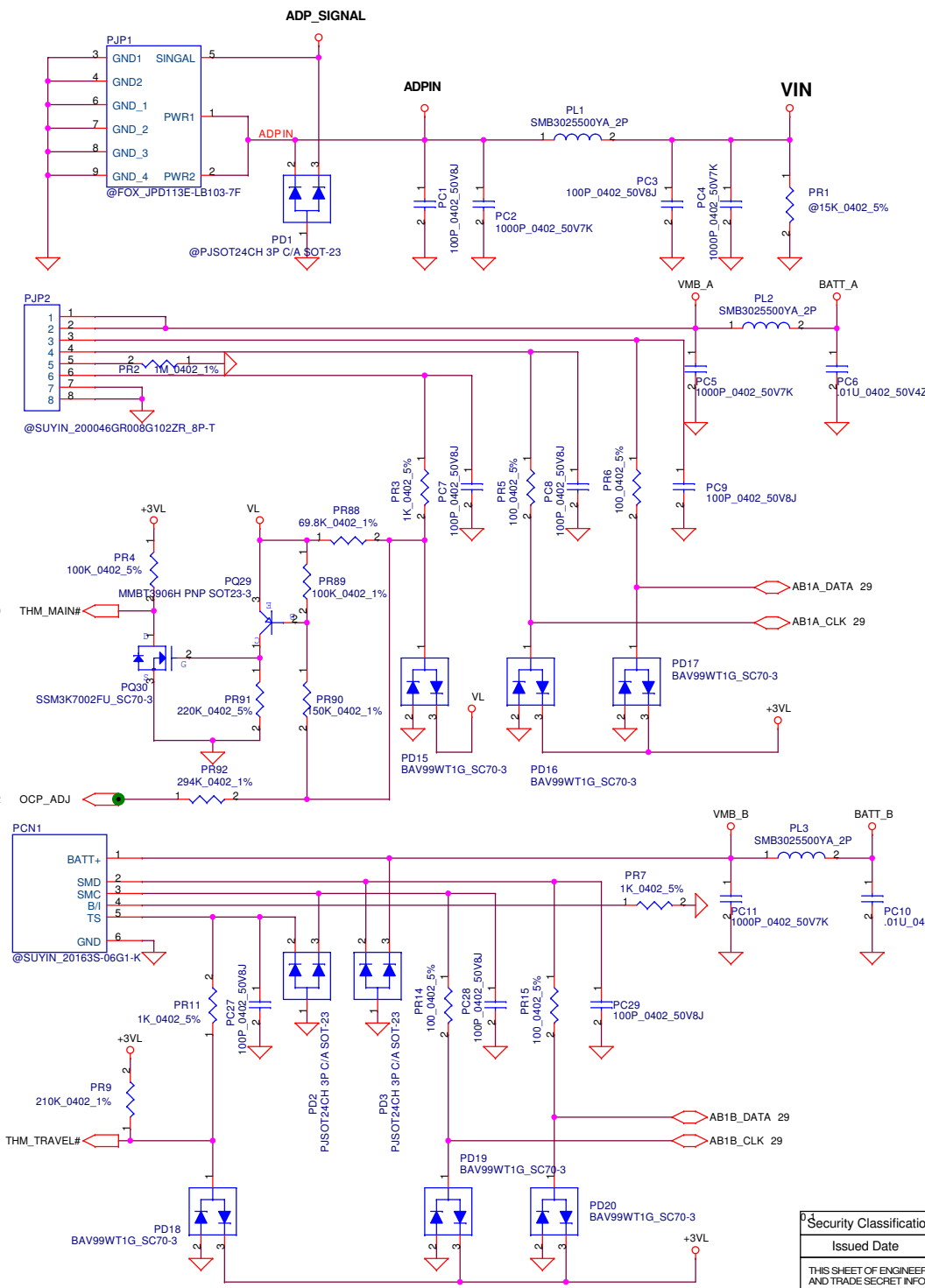


### +1.5V to +1.5VS Transfer

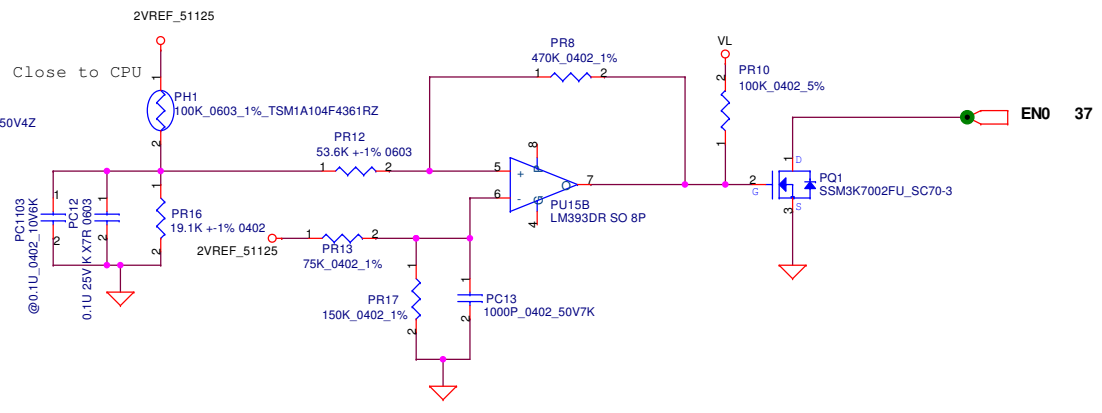


### +5VALW to +5VS Transfer

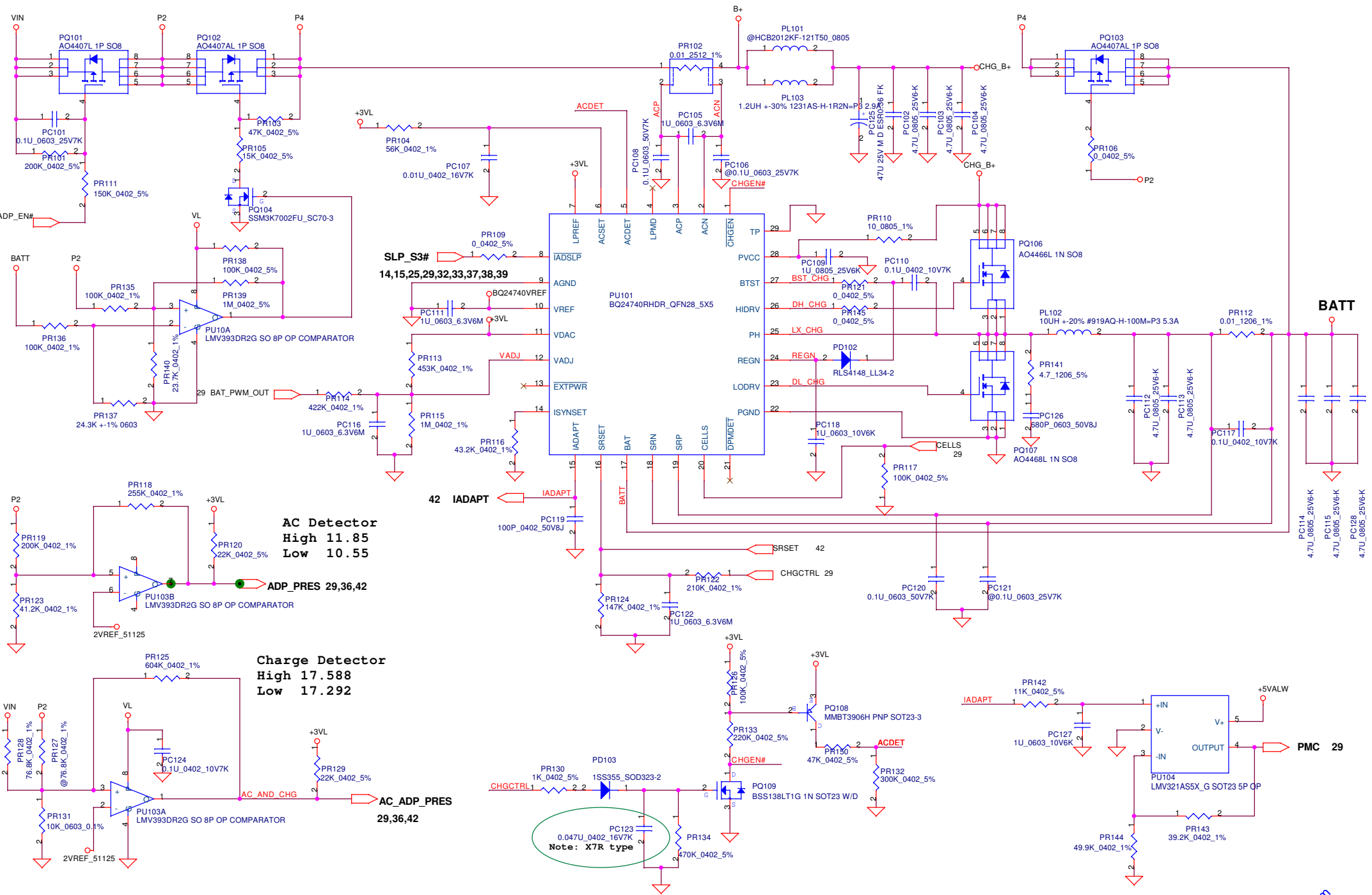




**PH1 under CPU bottom side :**  
 CPU thermal protection at 90 +/-3 degree C  
 (Need to be checked)



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**AC Detector**  
 High 11.85  
 Low 10.55

**Charge Detector**  
 High 17.588  
 Low 17.292

**SLP\_S3#**  
 14,15,25,29,32,33,37,38,39

**42 IADAPT**

**AC\_ADAPRES**  
 29,36,42

**BATT**

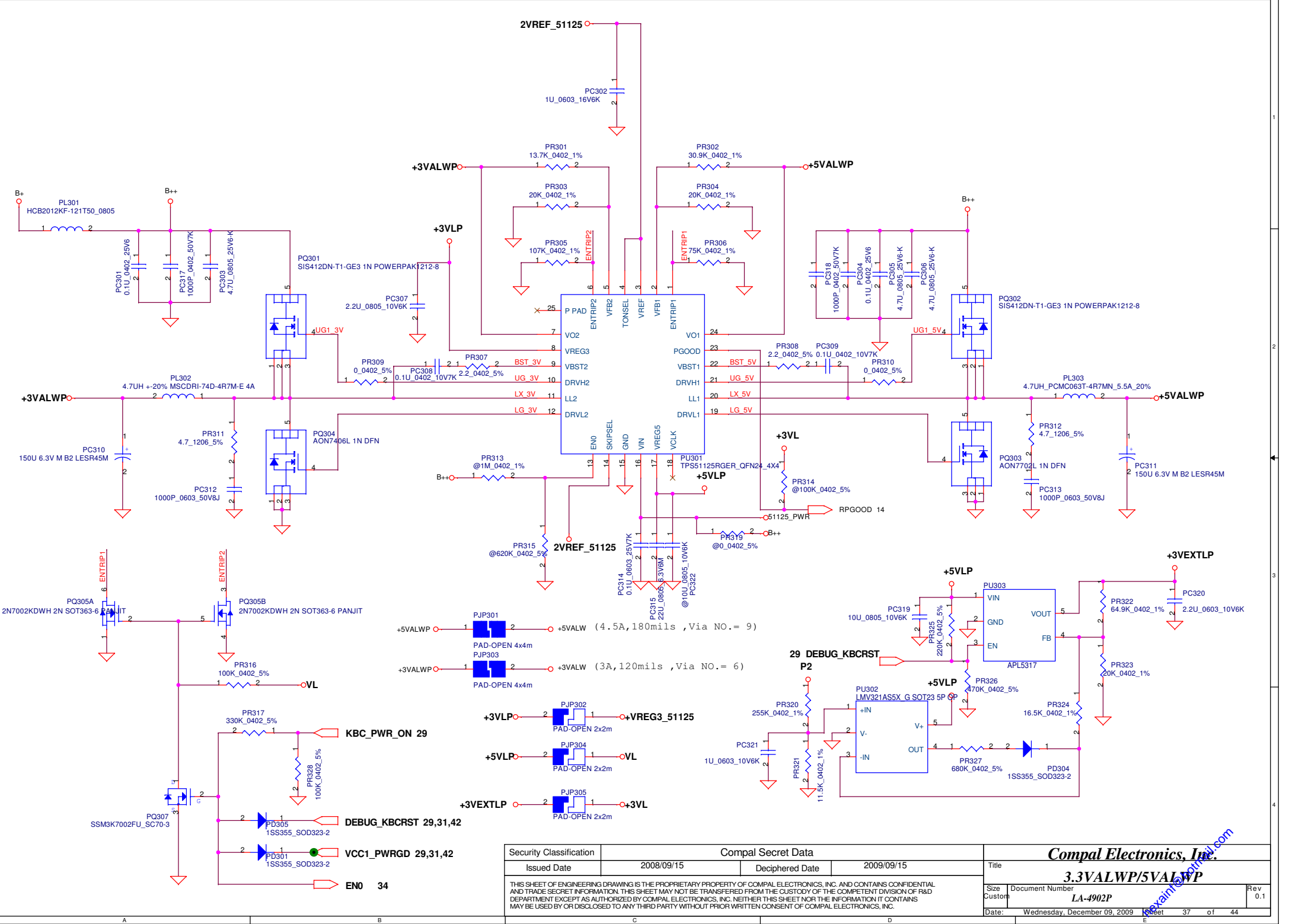
**PMC 29**

PC123  
 0.047U\_0402\_16V7K  
 Note: X7R type

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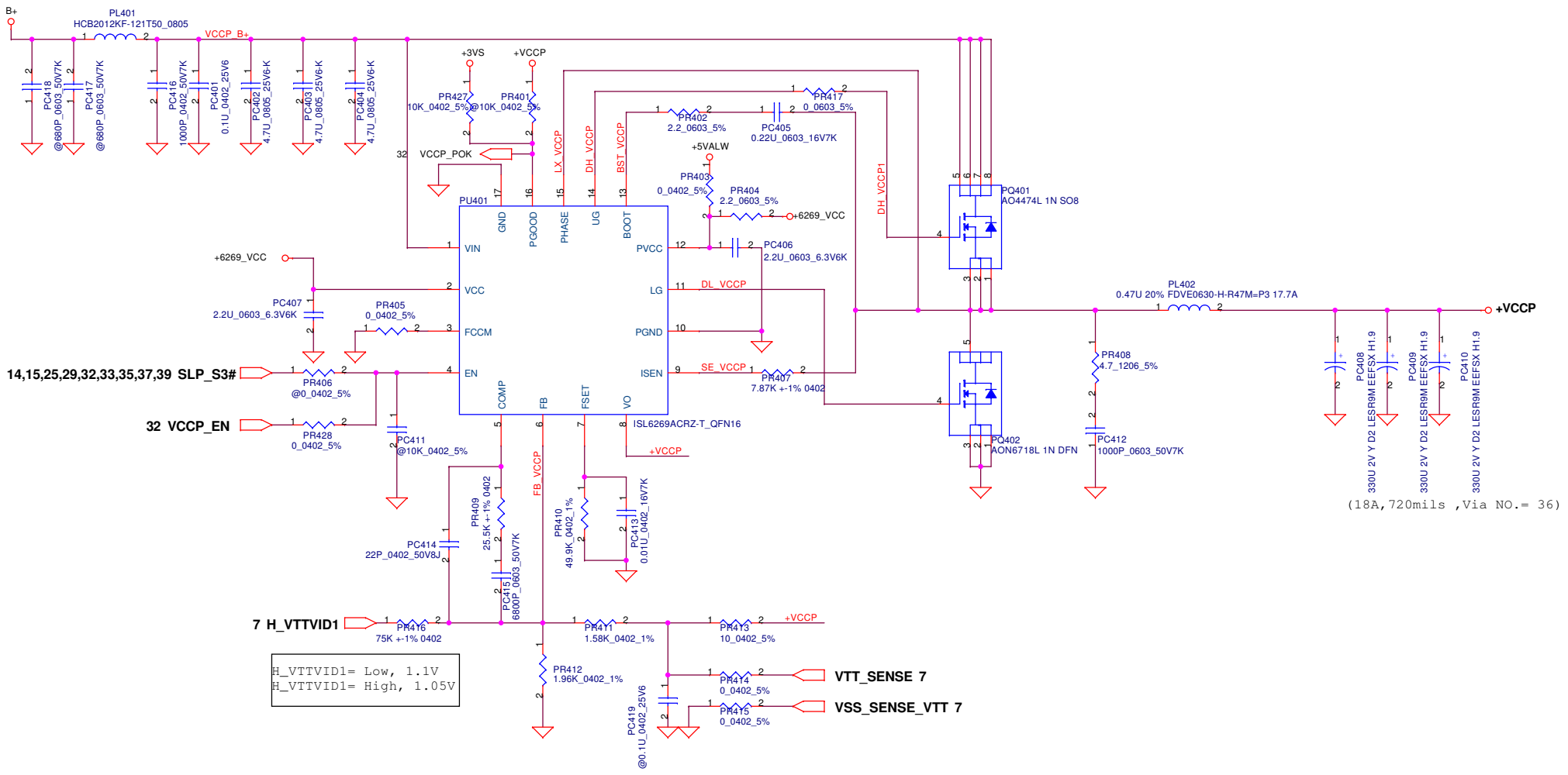




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<b>3.3VALWP/5VALWP</b>		
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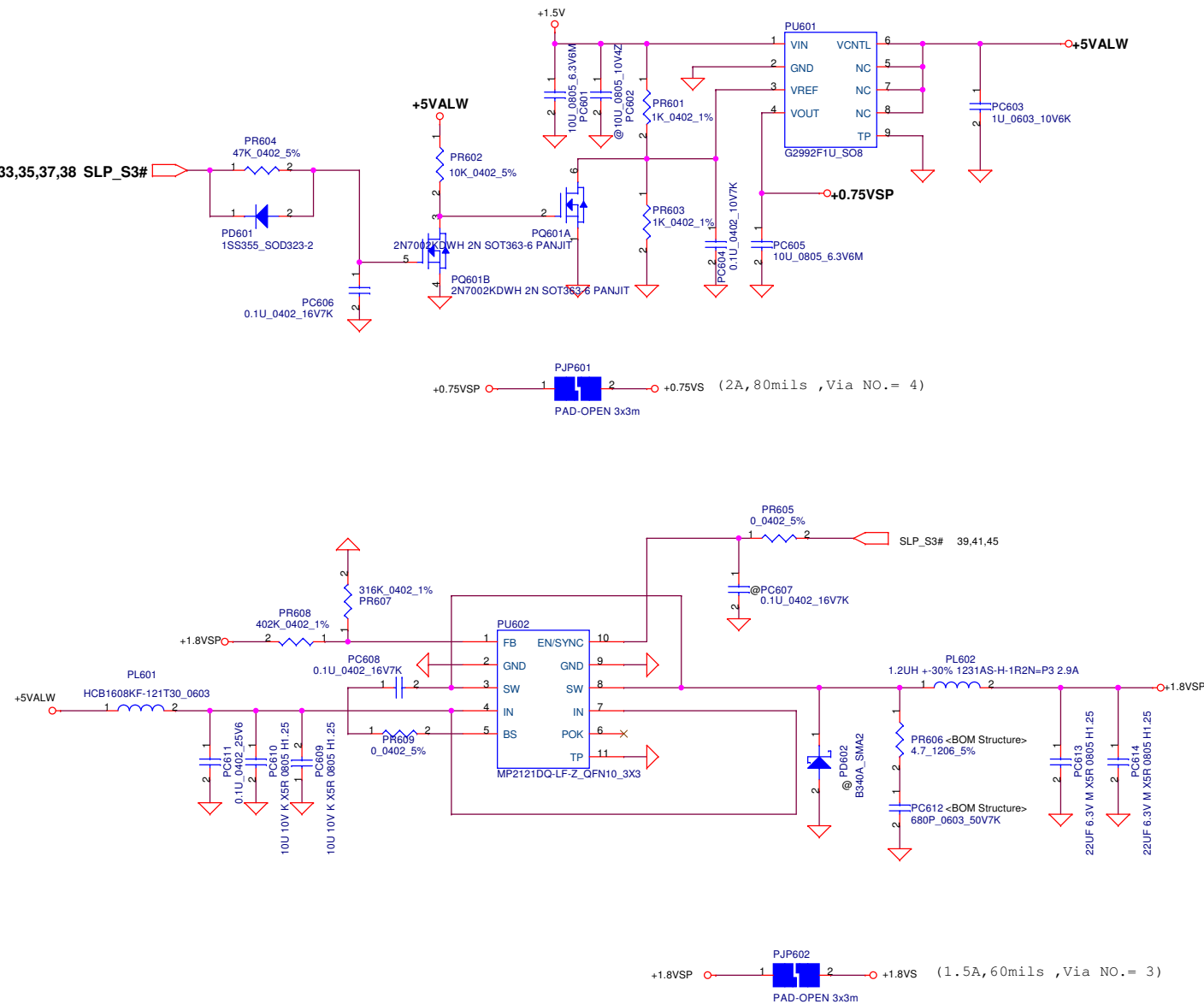


(18A, 720mils ,Via NO.= 36)

H\_VTTVID1= Low, 1.1V  
H\_VTTVID1= High, 1.05V

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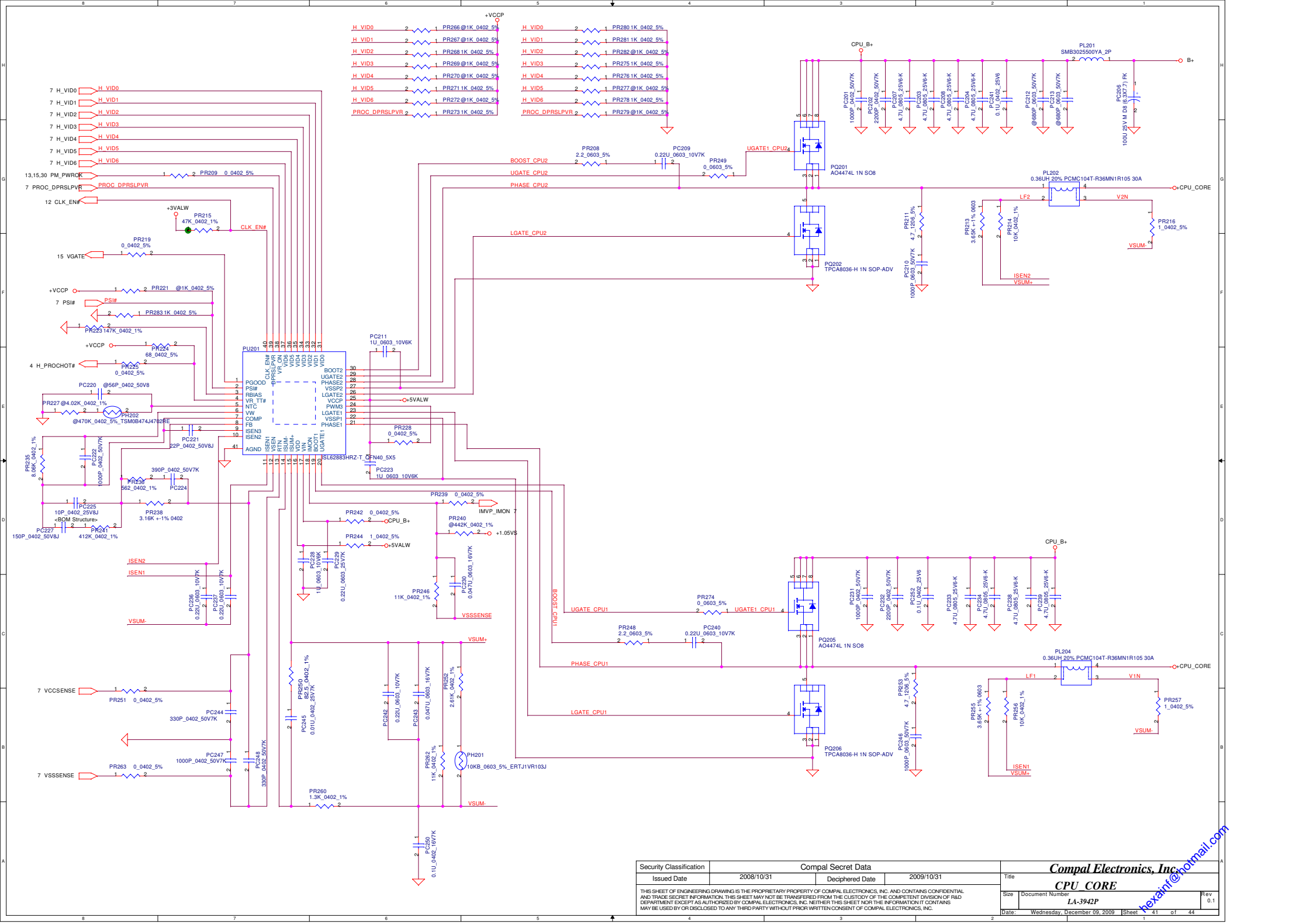
14,15,25,29,32,33,35,37,38 SLP\_S3#



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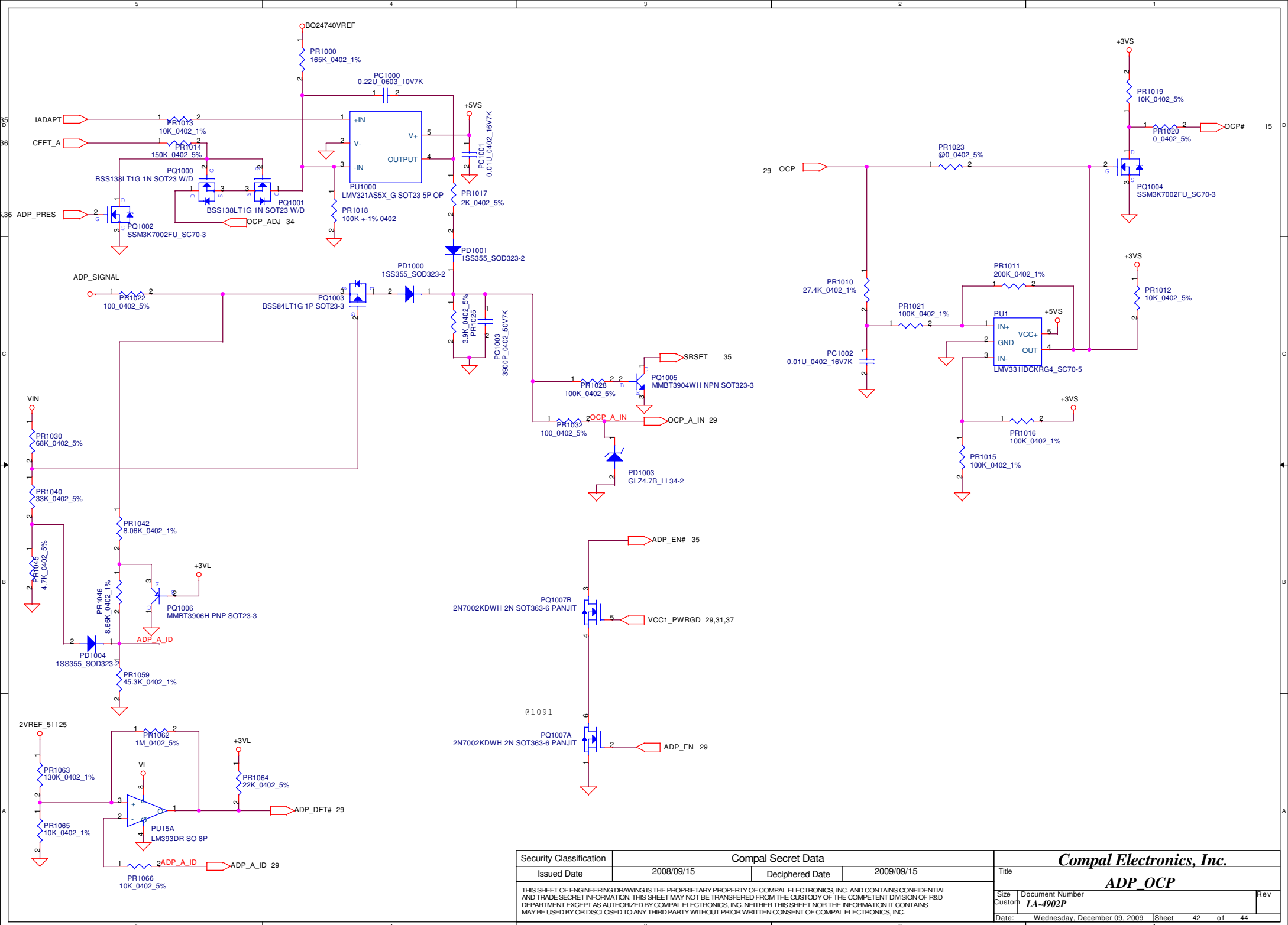
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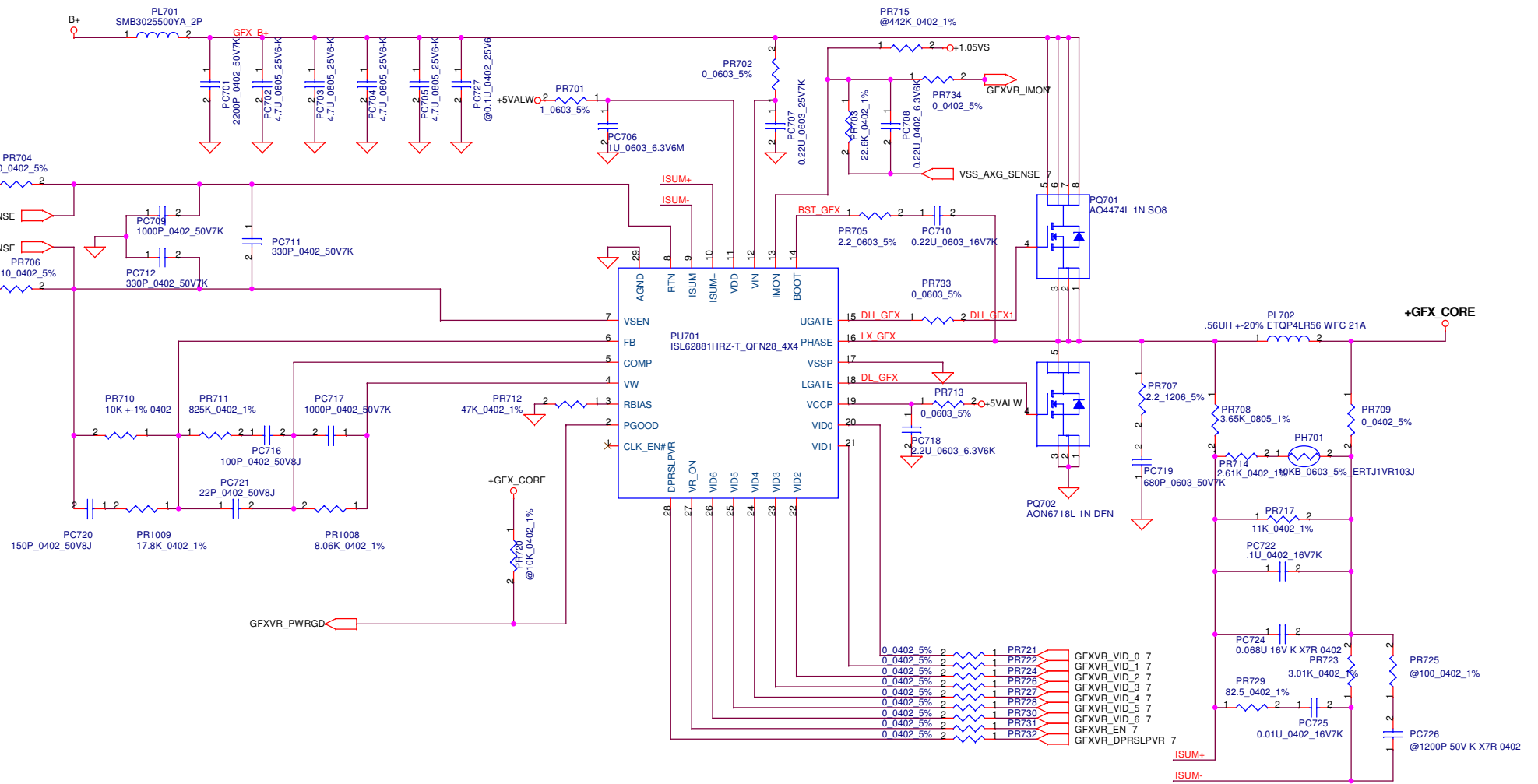


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 CPU CORE



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No1. P21, LED0 and LED1 nets reversed

No2. P26, for HP item 84, R294,R465,C274 value change

No3. P20, due to JEDP1 42pin to 30 pin, redefining the signals, please remember to change footprint, symbol, part number.

No4. P20, JEDP1 change footprint and value

No5. P14,15, delete ECH LVDS signals and USB\_5 for LVDS\_CAMERA

No6. P21, LED0 and LED1 change back.

No7. P21, HDD/ODD footprint modified.

No8. P20, add 2 pins on JEDP1 by myself, different from database part

No9. P20, use real JEDP1 from database

No10. P12, for HP item 69, delete SMB\_DATA\_S3, SMB\_CLK\_S3 and add 2 test points

No11. P14, for HP item 78, install R277

No12. P29, for HP item 82, KB\_RST# pull high to +3VS

No13. P4, for HP item 85, unistall R997 and del R40

No14. P4, for HP item 97, delete R34,R36,R37,R46,R49 and change name of XDP\_TDI & XDP\_TDO, and short XDP\_TDI\_M to XDP\_TDO\_M

No15. P4, for HP item 98, add a series R between pin3 and 5 on U54

No16. P21, for HP item 99, delete C184-C487, C945, R427, R428, R963, Q18

No17. P22, for HP item 100, delete Q80A

No18. P22, for HP item 101, source and drain of Q80B are swapped and change to a single 2N7002

No20. P12&P29, for HP item 103, change R948, R949, R952, R953, R939, R176, R180, R940 to 0ohm and R950 to 33ohm

No21. P28, for HP item 104, Connect +5VS to JP32 pins 11, 178, 179.

No22. P11, for HP item 105, R141 is connected to +1.05VS

No23. P22, for HP item 106, delete Q70B and Q70C. Replace Q70B with a single 7002

No24. P28, for HP item 107, Delete R635-R638 and short the signals

No25. P28, for HP item 108, Delete C203-C205 and short the signals

No26. P21, for HP item 110, LED0 and LED1 nets reversed again

No27. P22, for HP item 111, Control signal for Q80B\_GATE should be LAN\_DIS#

No28. P22, for HP item 112, NO INSTALL R967 for ESI silicon

No29. P12, for HP item 113, INSTALL R847 and change to 1Kohm. Connect R847.1 to Q66.1 and remove the GND connection at R847.1.

No30. P12, for HP item 80, install R184 and R190

No31. P15 & P28, for HP item 79, GPIO38 and GPIO39 on U4 connect DOCK\_ID0 and DOCK\_ID1 to the docking connector pins 77 and 78

No32. P15, for HP item 60, delete R283

No33. P29, for item 66, change K503 to K503, change 10K to 0 Ohm, change the Table, add a NOR gate

No34. for No 18, change Q80 Source and Drain pin back.

No35. P32, for HP item 115, change PM\_SLP\_LAN# to PM\_SLP\_M# at R386-1

No36. P24, for HP item 116, circuits TecuPeration because of canceling item 106

No37. P19, for HP item 117, swap DP\_CTRLDATA and DP\_CTRLCLK, AUX connects to CLK and AUX# connects to DATA, add isolation nFET in series with Q74A and Q74B.

No38. P15, add 7 47P.0402 but "0" at every clock of PCI

No39. P12, change RTCVCC source from +VREG5\_51125 instead of +3VL

No40. ESD change: (1) 0: D63-D67, D14, D57, D32, D68, D33, D34, D36, D62, D62, D37

No41. ESD change: (2) change P/N: D14, D57, D32, D68, D33, D34, D36, D62, D37

No42. ESD change: (3) affect layout: D14, D33, D34, D36, D62, D37

No43. modify C962 GND disconnection and R70 to GND

No44. modify HP part number, please search "change HF P/N" to know which parts changed.

No45. for Load BOM problems, change some parts as below:  
 (1) add CONN#: JCPU1, JP5  
 (2) add P/N for dual 2N7002: Q2,Q3,Q7,Q8,Q81  
 (3) change P/N: R570, C6, C829, R43, R44, R47

No46. for DRC check,  
 (1) P23, delete dummy net of JODD1 pin16, 17  
 (2) P28, add intersheet symbol at SMB\_CLK\_S3 and SMB\_DATA\_S3  
 (3) P21, add a TP at U18.7: LAN\_CTRL\_18  
 (4) P14, delete a dummy net N19910781  
 (5) P28, change JP32 pin DCAD net name to DCAD1

No47. for parts forbidden:  
 (1) C829 change to SE026104KN0 (2) R800 change to SD028100380  
 (3) D68 change to SCA00000E000 (4) C818 change to 0402 SE070104280

No48. EMI concern:  
 (1) install C833, C836, C956  
 (2) P25, JP25 pin definition changes.  
 (3) R931 to 47 ohm  
 (4) P18, modify CRT circuits: add L and C, change R places, install C

No49. P29, for HP item 123, Change R680 to 100 ohms, and unistall R699

No50. P29, delete R886, R887 and relative circuits

No51. P29, delete R892 for EBITCON

No52. P23, change JODD1 pin16, 17 type to avoid from useless net names

No53. HF parts link database: D1

No54. HF parts link database:  
 (1) Q78 link SB00000H500  
 (2) D16, D63-D67 link SC2AN217020  
 (3) D1 link SC2N202U000  
 (4) D23-D29 did not link SC2P202U000, just revise manually  
 (5) Q57 & Q58 link SB000007H10  
 (6) C263 & C269 link SGA202211D0  
 (7) lots of 2N7002(Q4, Q23, Q32,Q41,Q42,Q43,Q45,Q46,48,49,50,51, 52,53,54,55,56,60,65,66,68,71,76,79,80) link SB000009080  
 (8) T63 link SP050002I10  
 (7) U42A, U42B, U44A, U57A link SA003930080

No55. combine power schematics 0212

No56. P29, for HP item 122, Connect D42-2 to VCORE\_GP (not PM\_PWROK) TEST. change U42,U44,U57 value and footprint LM393DG\_S08

**before netin**  
 No57. change U42,U44,U57 link another SA003930080

**2/16**

No58. P5, for HP item 126, R60 and R61 should be NO INSTALL.

No59. P12, for HP item 127, Connect R57.1 to HDD\_HALTLED instead of HDD\_HALTLED

No60. P9,10, for HP item 128, Connect JDIM1A.199 and JDIM1B.199 to 3VS as Intel reference board

No61. P16, for HP item 131, Based on spec, VccTX\_LVDS and VCCA\_LVD to GND.

No62. P19, for HP item 136, install Q76 and no install R1055 as there must be isolation

No63. P13, for HP item 138, Change Gate of Q77 to +3VALW

No64. change Pb-free  
 (1)R1058 to SD028100180,  
 (2)R1059,1060,1062,1063,1064 to SD028100280  
 (3)R615, 1061 to SD028470180  
 (4)RP31, RP33 to SD309100280  
 (5)RP29, 30, 32,34,35 to SD309470180 --> footprint should keep original  
 (5)C953 to SE053475280  
 (6)C950, 951, 952 to SE070104280  
 change HF  
 (1) SW1 to SN100000W10

No65. separate GND signals  
 (1) P28 and P25, add GNDA and resistors,

No66. P20, change JEDP1 to 24 pin connector, delete LANE[1:3] and EDID, as well as U4 relative signals.

No67. P31, change SPI ROM back to DB1 design, but mount 8pin, unmount 16pin

No68. P19, change misunderstand name:DPD\_C\_AUX/DPD\_C\_AUX# to DPD\_C\_AUX\_1/DPD\_C\_AUX\_L#

**2/19**

No69. P5, delete MB\_DP\_DATA[1:3] N/P for JEDP pin cutting

No70. C6 and C685 change to SE071100J80 because of Jason's request( vendor doesn't have the original 25V part)

No71. P25, install C888,C889

**2/20**

No72. for HP item 66, P29, U66.5 should be connected to 3VL so that KBC can read board before boot and apply necessary fixes.

No73. for HP item 103, P31, R1035 should be 0ohm

No74. P27, change SC\_PWR circuits for unsurely current

**2/23**

No74. P26, unistall U31 and add J1 for cost down

No75. change some test point footprint to IPC12: T61,T62,T1,T55,T97,T22, and P14 lots of points

**2/24**

No76. P22, JP6 symbol error, modified!

No77. P12, add a net name XDP\_FN4

**2/25**

No78. P32, change U44.8 to +5VALW for HP request

No79. P29, change R680=220 ohms

combine power schematics

No80. P25,  
 (1) JP24: redefine the singals of the pins,  
 (2) JP25: reverse pin definition

No81.  
 (1)U4 change PN to SA00002KV30 for ES2  
 (2)P31, &U1, &U2 change to SA000037A00

**3/6**

No82.  
 (1)P29, Firmwave said unmount R1021 and mount R1022  
 (2)P4, delete R998 <BOM structure>, otherwise BOM will be error

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No1. for HP item 5, P32, change U44A to U57B and delete U44  
 No2. for HP item 6, P12&24, connect JP12.17 to U4A.F34 and add 10K pull-up to +3VS for Braidwood detection.  
 No3. for HP item 7, P12, change PC12 debug ports to CF64 - pin 28, CF65 - pin 30, CF610 - pin 22, and CF611 pin 24  
 No4. for HP item 11, P20, reverse Q9A  
 No5. for HP item 12, P13, Change all express clock series terminations from 33ohm to 0ohm, R208, R209, R211, R212, R41 and R59 should be 0ohm  
 No6. for HP item 13, P12, install R337, R338, R339 to disable I/Os (DGL1.0 did not see this)  
 No7. for HP item 14, P12, unmount R186 and R192 because latest PCH EDS shows that PCH XDP UTAG\_RST# is now NO CONNECT on PCH  
 No8. for HP item 16, P20, Change D12 to 2Kohm resistor and remove R322  
 No9. for HP item 17, P20, Change Q79.2 to L1D\_SW#  
 No10. for HP item 4, P22, source and drain on Q80B should be swapped  
 No11. for HP item 22, P27, add ESD Diodes and pullups for the SCCLK, SCIO, SCRST lines on the SC socket  
 No12. P14, add RGB name:DAC\_RED\_R,DAC\_GRN\_R,DAC\_BLU\_R  
 No13. P29, R593 part number: "space" delete  
 No14. P31, reverse JP30 pin definition  
 No15. P4, change R1082 from 0 ohm to 10K  
 No16. P18, change R307, R308, R309, R301, R302, R303 from 75ohm to 150ohm  
 No17. P18, change R307, R308, R309, R301, R302, R303 from 75ohm to 150ohm  
 No18. P28, add a reversing circuit for STB\_LED# to fix the LED bug  
 No19. P21,P28, delete redundant net LAN\_ACT#\_DOCK and change it to LAN\_ACT#  
 No20. P31, revise BIOS connector to CONN@ and unmount &U1  
 No21. P9, revise "M1@" to "M1@"  
 No22. P12, revise 0ohm(R868,R869,R870,R871,R872,R1071) from SD034000080 to SD028000080  
 No23. P25, modify Power button circuits  
 No24. for HP item, P15 and 2# GPIO -->WWAN\_DET# and pull high 100K  
 No25. for HP item, P15, Scruff R51 for SYS\_SHDN# and add these 3 to my schematics as C972-974  
 No26. for HP item, P22, VCI pin U18.6 should be NO CONNECT. R929 uninstalled  
 No27. for HP item, P910, Remove M18 for UMA and make it always installed. UMA will only use M1  
 No28. for HP item, P31, reserve 100Kohm pull up to 3V1 on U63.1  
 No29. for HP item, P15, LAN\_DIS# should pull up to +3VM LAN instead of 3VALW  
 No30. for HP item, P4, Change R14/R15 to 1K1K/R18 per DGL1.52  
 No31. for HP item, P14, add NO INSTALL 0ohm to GND on GPIO6 on PCH and remove PULL UP to 3V1LW because PCH has an integrated pull up.  
 No32. for HP item, P13, For UMA:  
 NO INSTALL: R210, Y4, C222  
 INSTALL 0ohm resistor in C223  
 No33. P14, revise for HP item 13 not mentioned, delete LVD\_VREFH and LVD\_VREFL to GND  
 No34. for HP item, P19, Reserve 0.1uF on DDC\_EN and DP\_EN for concern about noise.  
 No35. P29, R1021 Removed  
 R1022 Install (main battery selection)  
 R1023 Install (OCP function)  
 R1024 Install (travel battery selection)  
 R694 Install (SMSC CBB will required it)  
 No36. P29, R1021 Removed  
 R1022 Install (main battery selection)  
 R1023 Install (OCP function)  
 R1024 Install (travel battery selection)  
 R694 Install (SMSC CBB will required it)  
 No37. for HP item, P16, Add 2x22uF for VCCME (on PCH)  
 No38. revise the footprints of T113, 122, 123, 124 from TPC to TPC12  
 No39. for HP item, P4, P14, delete R337, R820 and R821, and add 3 test points.  
 No40. for HP item, P4, install R997 and uninstalled R44 to change FAN power  
 No41. revised for HP item, P28, change name STB\_LED#\_R  
 No42. P31, delete U64 and &U1  
 No43. for HP item 50, P12, delete CLR\_P2  
 No44. for HP item 51, P29, change system ID by installing R660 and uninstalling the others  
 No45. for HP item 56, P28, change caps to 150uF and 22uF, delete the others.  
 No46. for HP item 46, P9.10, Install a new voltage divider for VREF\_CA that is different from VREF\_DQ divider  
 No47. for HP item 61, P15,20, delete R969, R330 R328, C297 R327 R329 Q14 C304 and Q9A, R268, delete WEBCAM\_OFF circuits and add WEBCAM\_ON circuits.  
 No48. P33, add +VCCP and +GFX\_CORE discharge circuits  
 No49. P27, change Smart Card circuits  
 No50. for HP item 48, P9,10,  
 (1) add 1 uninstalled 300uF on DIMMB +1.5V,  
 (2) add 2 10uF on +0.75VS,  
 (3) delete 4 uninstalled 10uF on +1.5V  
 (4) uninstalled 8 0.1uF on +1.5V  
 No51. for HP item 49, P16, delete R289  
 No52. for HP item 52, P22, TRM\_CRT: add 4 0.1uF  
 No53. for HP item 56, P28, change caps to 150uF and 22uF, delete the others.  
 No54. for HP item 60, P18, simplify WLAN/WWAN/BT LED circuits. uninstalled Q62 and Q64  
 No55. for HP item 61, P20, modify WEBCAM again  
 No56. P22, swap again  
 No57. for HP item 62, P25 & 29,  
 Change A\_SD to A\_SD# on U40.91U (GPIO14 of KBC).  
 Change A\_SD to A\_SD# on JP5.35 (Audio board connector).  
 Change EAPD to MUTE\_LED\_CNTL on U40.100U (GPIO31 of KBC).  
 Change EAPD to MUTE\_LED\_CNTL on JP5.36 (Audio board connector).  
 No58. for HP item 64, P23, uninstalled R474. The concern is leakage when system is off.  
 No59. for HP item 65, P29, uninstalled pull-up on KBRST# (R893) as it is not needed.  
 No60. for HP item 66, P29, change KBRST# from 10K to 100K to reduce current.  
 No61. for HP item 67, P29, for ADC small input filters, add R1113-R1116 and C982-C984  
 No62. for HP item 68, P18, uninstalled R234 for wrong power rail.  
 No63. for HP item 70, P18, add 10K pull-up to USB\_C0#2 as we are not using it.  
 No64. for HP item 71, P12&24, change net name from BRAID\_DET to NAND\_DET#  
 No65. for HP item 72, P13&21, add CLK\_PCIE\_LAN\_REQ1# connected U18.4# to U4B.U4  
 No66. P23, change JODD1, link database  
 No67. P23, JODD1 pin 16,17 change passive  
 No68. P22, swap T63 MDI +/- signals  
 No69. for HP item 15, P33, add 330uF to each of +1.05VS and +1.05VM  
 No70. P22, swap again  
 No71. for HP item 15, P33, change 330uF to smaller package because of lack of space, and delete C782, C894  
 No72. P27, change R1086 to 100K, R1085 to 10K, delete R1110 like DIS  
 No73. PCH PN: SA00002KV60; LAN PN: SA00002M040  
 No74. P27, change R1086 to 100K, R1085 to 10K, delete R1110 like DIS  
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 No104. P27, change R1086 to 100K, R1085 to 10K, delete R1110 like DIS  
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 No304. P27, change R1086 to 100K, R1085 to 10K, delete R1110 like DIS  
 No305. PCH PN: SA00002KV60; LAN PN: SA00002M040  
 No306. P27, change R1086 to 100K, R1085 to 10K, delete R1110 like DIS  
 No307. PCH PN: SA00002KV60; LAN PN: SA00002M040  
 No308. P27, change R108

5/20 No1. change schematics parts of Q24 and Q70, the same P/N SB923050020

5/25 No2. P27, change R1086 to 100K, R1085 to 10K, delete R1110 like DIS  
No3. PCH PN: SA00002KV60; LAN PN: SA00002M040

6/2 No4. P12, change reference name: JBATT1 --> JBAT1  
No5. P25 & P31, JP25 and JP30 are reversed(H and V) because of footprint silkscreen problem, remember not to change routing, just change ME pin1

No6. P12, P29, P31, add net names of SPI signals  
No7. P12,29,31, change 24.9ohm for SMSC request:R939,R940,R950,R948,R952,R1035

6/18 No8. P19, add 1 fuse on DP power

6/22 No9. P19, as per Johnson's request, for cost down  
(1) uninstall C123, C699  
(2) change C552 from 150uF to 100uF +0.1uF\*2 (not ok)  
(3) change C263 and C269 to 100uF (not ok)

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hexrain@hotmail.com