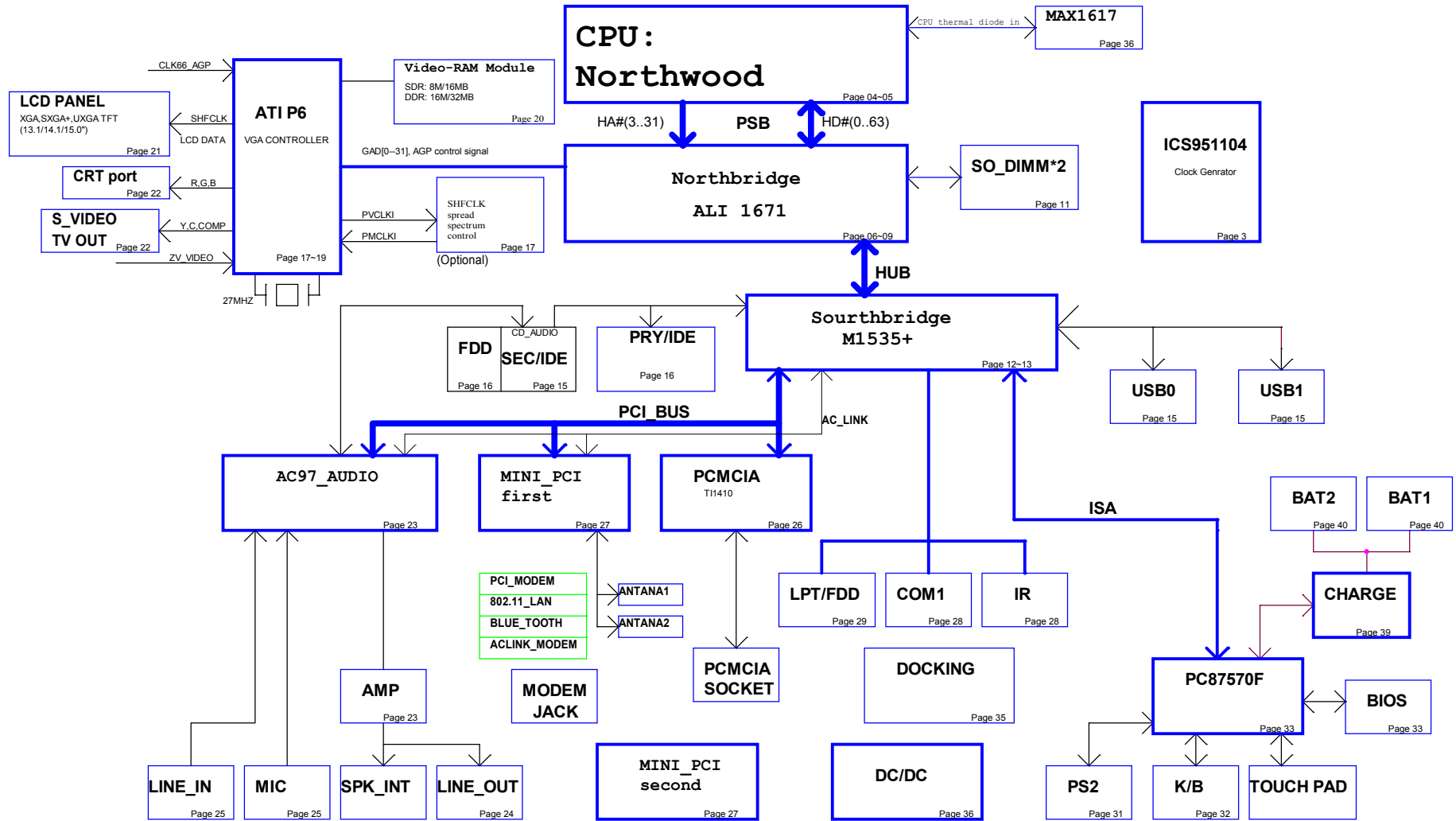
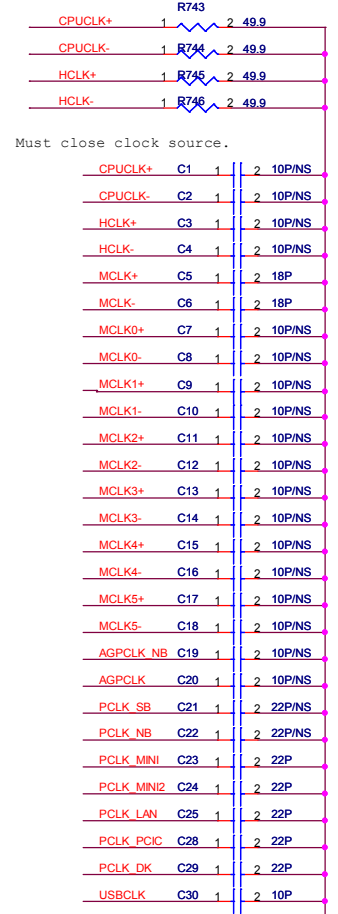
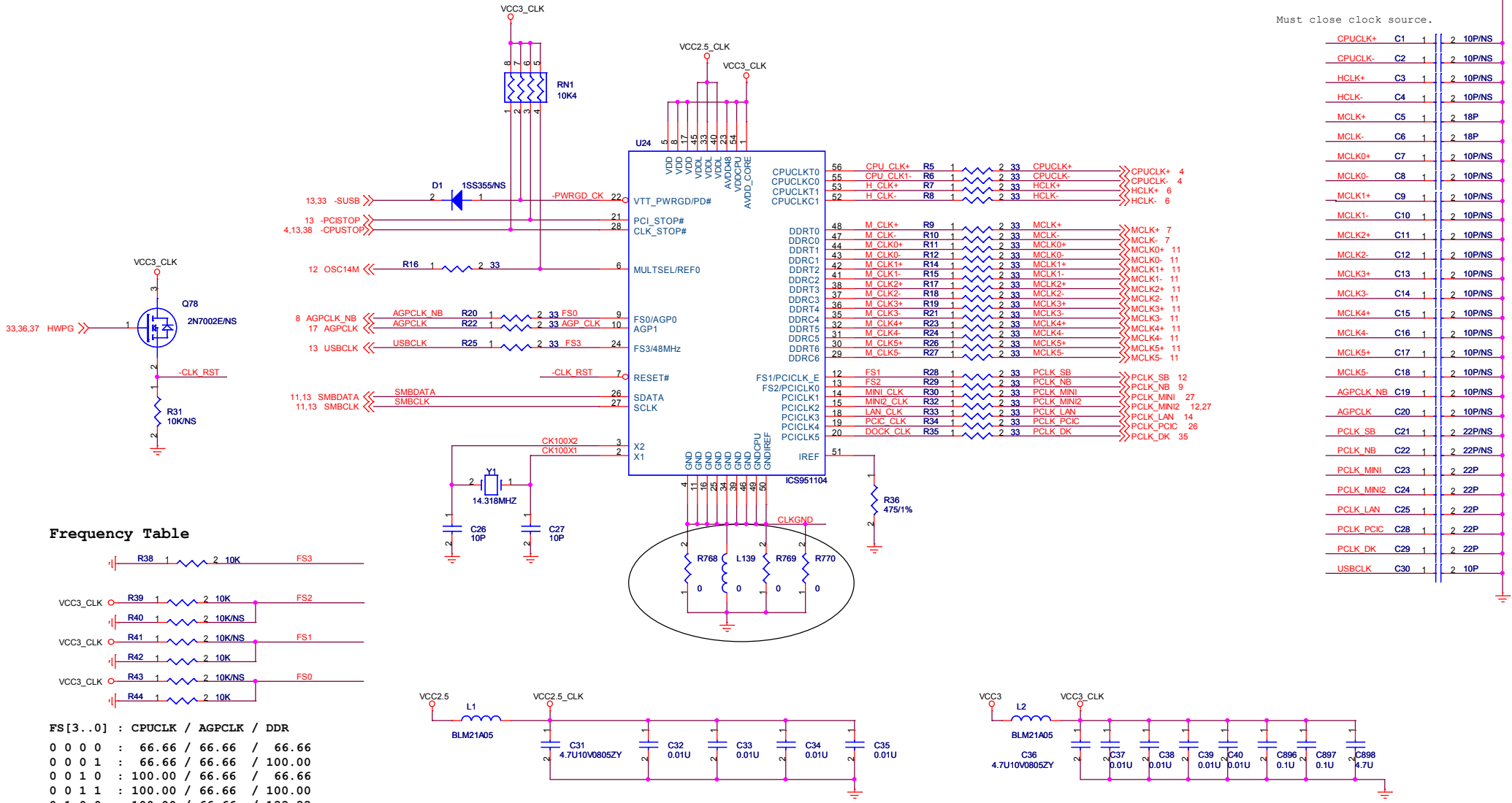


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Pg#	Description	DNI LIST	Pg#	Description	DNI LIST
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2	PAGE INDEX		28	COM/IR interface, LID_SW	
3	Clock Generator		29	LPT/FDD interface	
4	NORTHWOOD CPU(HOST BUS)-1		30	87570 IO	
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6	North Bridge ALi 1671 -2 (HOST)		32	Keyboard CONN., Ring indicator , RF CKT	
7	North Bridge ALi 1671 -1 (DRAM)		33	87570	
8	North Bridge ALi 1671 -3 (AGP)		34	Upper, Volume/B interface, ITP CONN.Thermal	
9	North Bridga ALi 1671 -4 (PCI)		35	FULL DOCK	
10	Ali M1671 Configuration and DDR Pull-up resistors		36	DC/DC 3V/5V	
11	DDR SODIMMx2		37	DC/DC 2.5/1.8/1.5VSUS, 1.25/1.2V	
12	South Bridge ALi M1535+ -1 (PCI, ISA)		38	DC/DC VCORE	
13	South Bridge ALi M1535+ -2 (USB, IDE, FDD)		39	BATTERY Charger	
14	LAN interface RTL8100B		40	BATTERY Selector	
15	Primary IDE (HDD), USBx2		41	Hole	
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17	ATI P6 (HOST, VIDEO O/P) -1				
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20	Video DDR SDRAM				
21	LCD interface				
22	S Video/Composite, CRT				
23	AC97 Codec ALC201A				
24	AMP/Line out				
25	Line in/Mic in/CD audio in				
26	TI1410 Card Bus I/F				

RT6.0





Frequency Table

FS3	10K
VCC3_CLK	20K
VCC3_CLK	20K/NS
VCC3_CLK	20K
VCC3_CLK	20K/NS
VCC3_CLK	20K

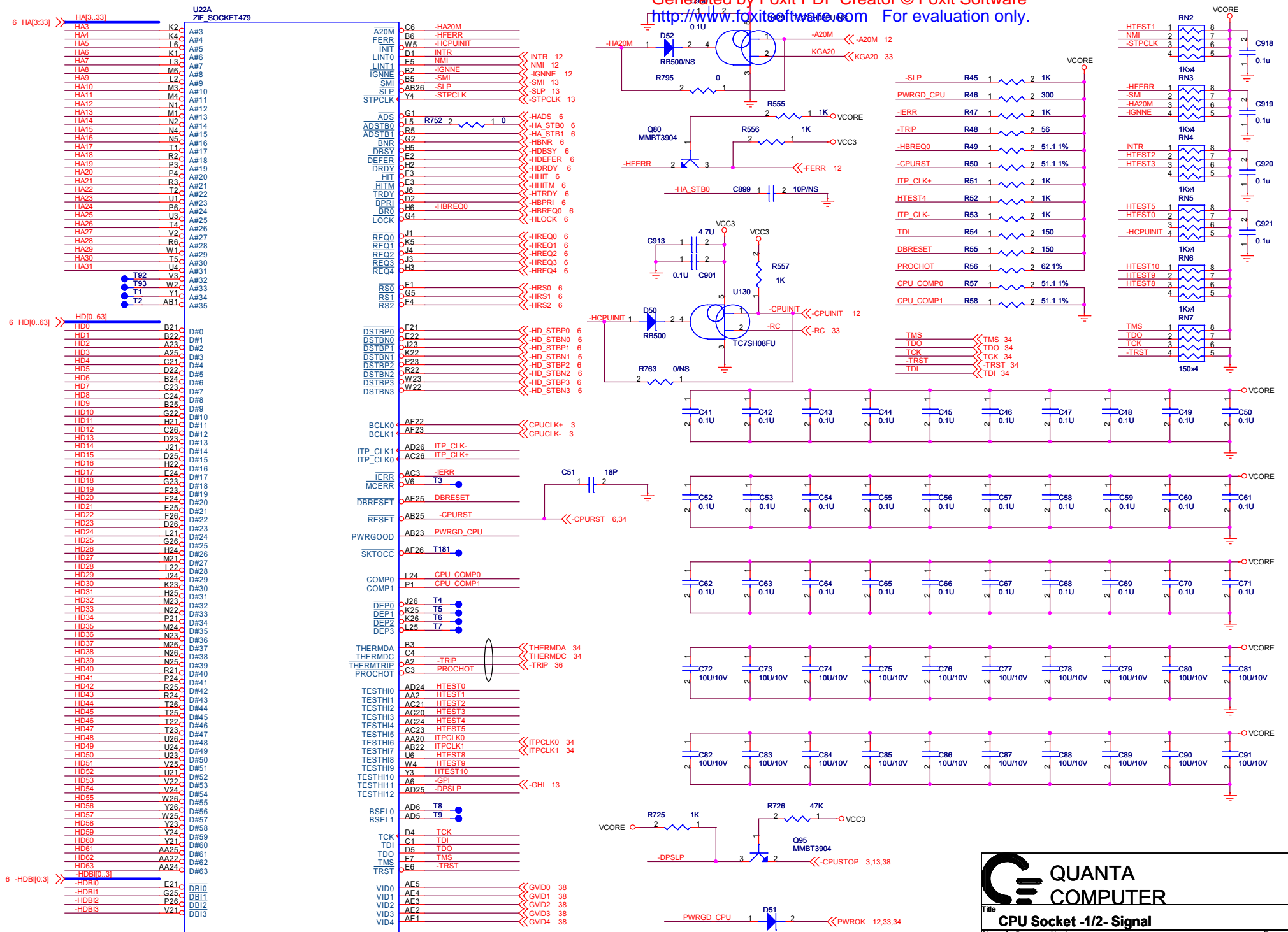
FS [3..0]	CPUCLK	AGPCLK	DDR
0 0 0 0	66.66	66.66	66.66
0 0 0 1	66.66	66.66	100.00
0 0 1 0	100.00	66.66	66.66
0 0 1 1	100.00	66.66	100.00
0 1 0 0	100.00	66.66	133.33

QUANTA COMPUTER

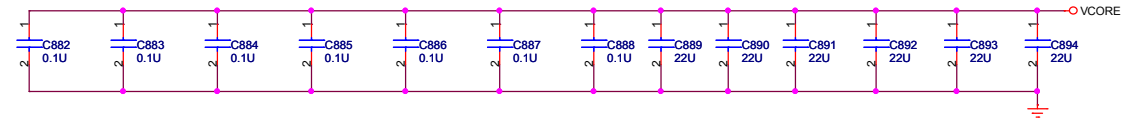
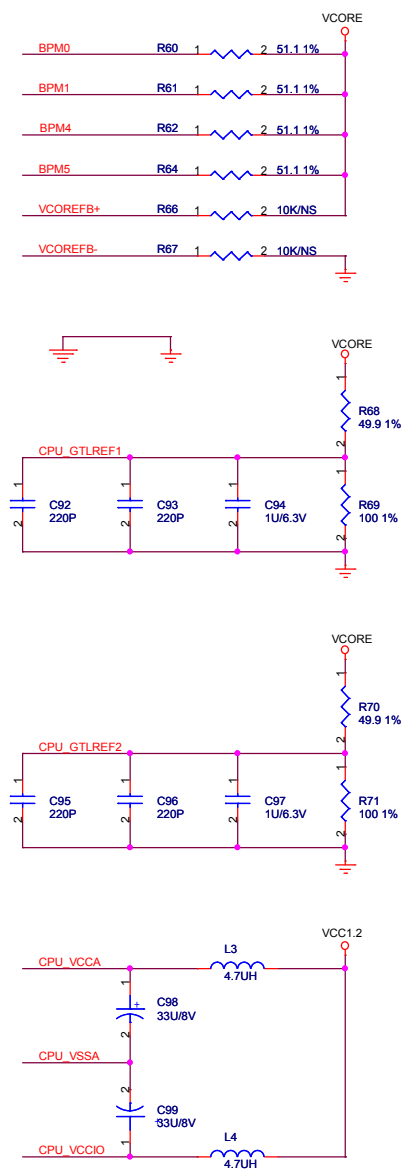
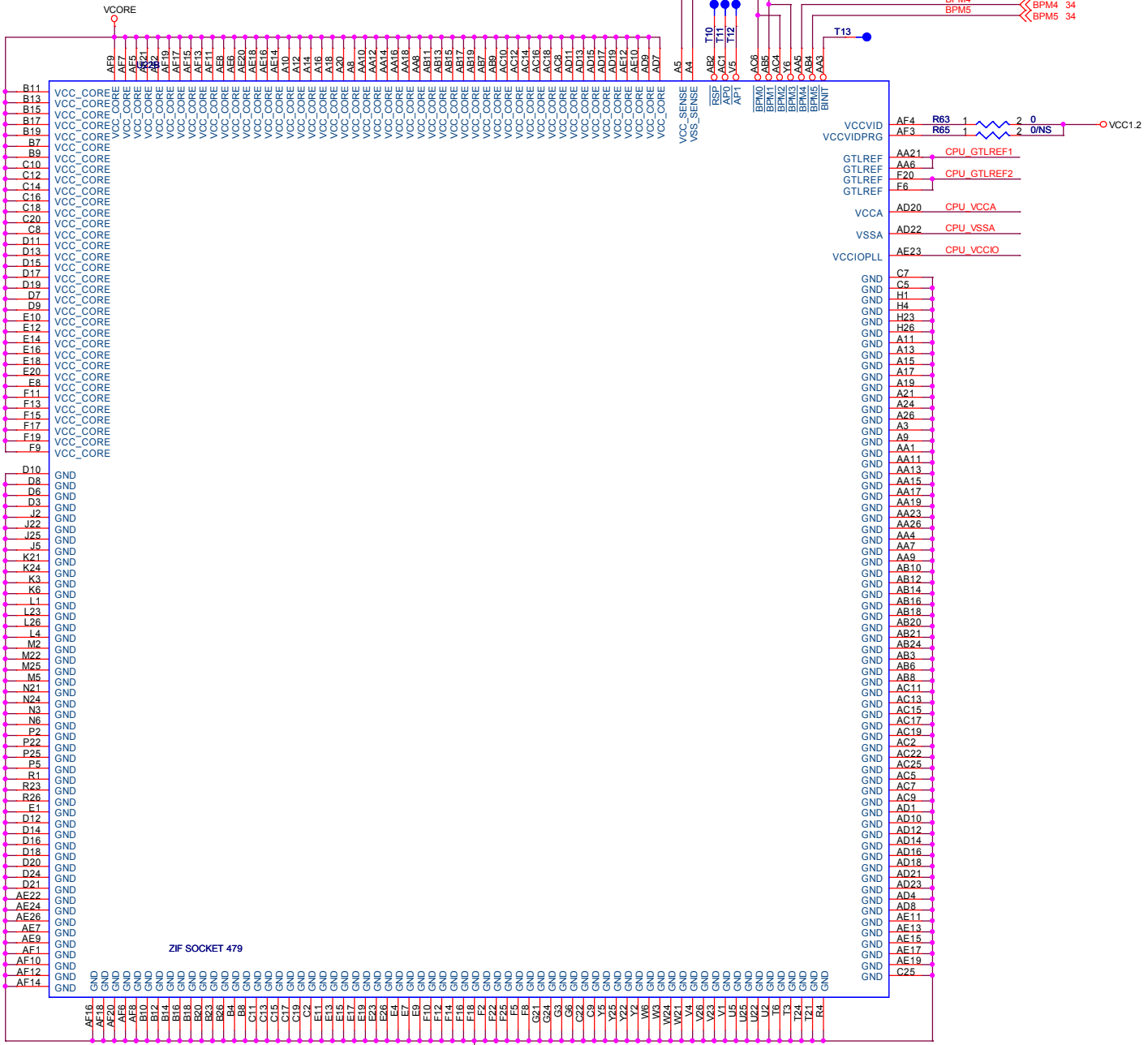
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Date: Tuesday, January 18, 2005	Sheet 4 of 41

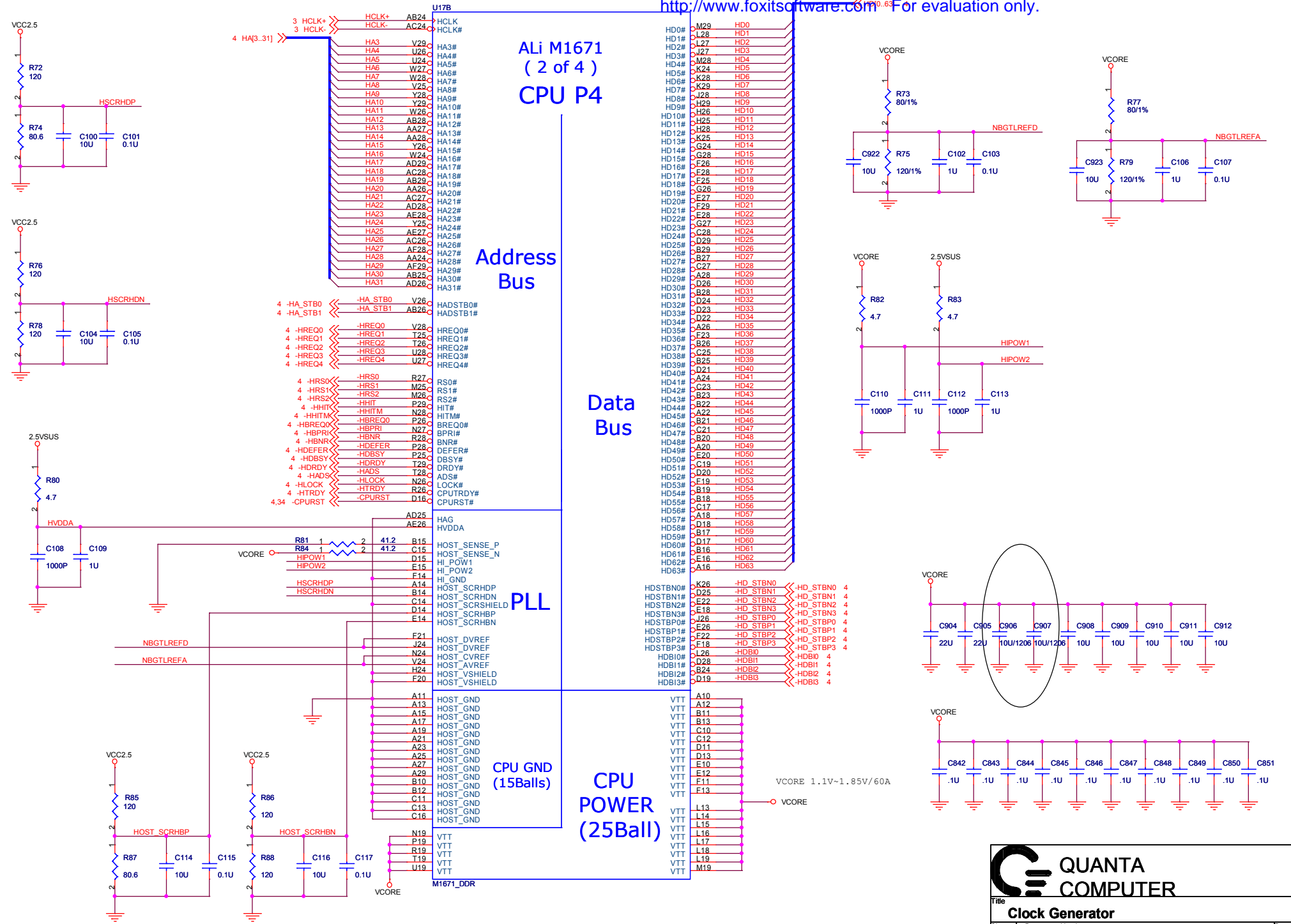


QUANTA COMPUTER

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Size	Document Number	Rev
Custom	NORTHWOOD - 2	A

Date: Tuesday, January 18, 2005 Sheet 5 of 41

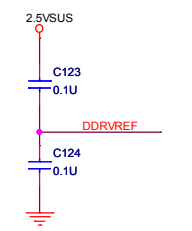
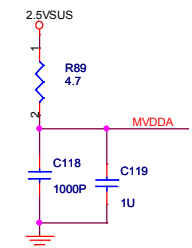
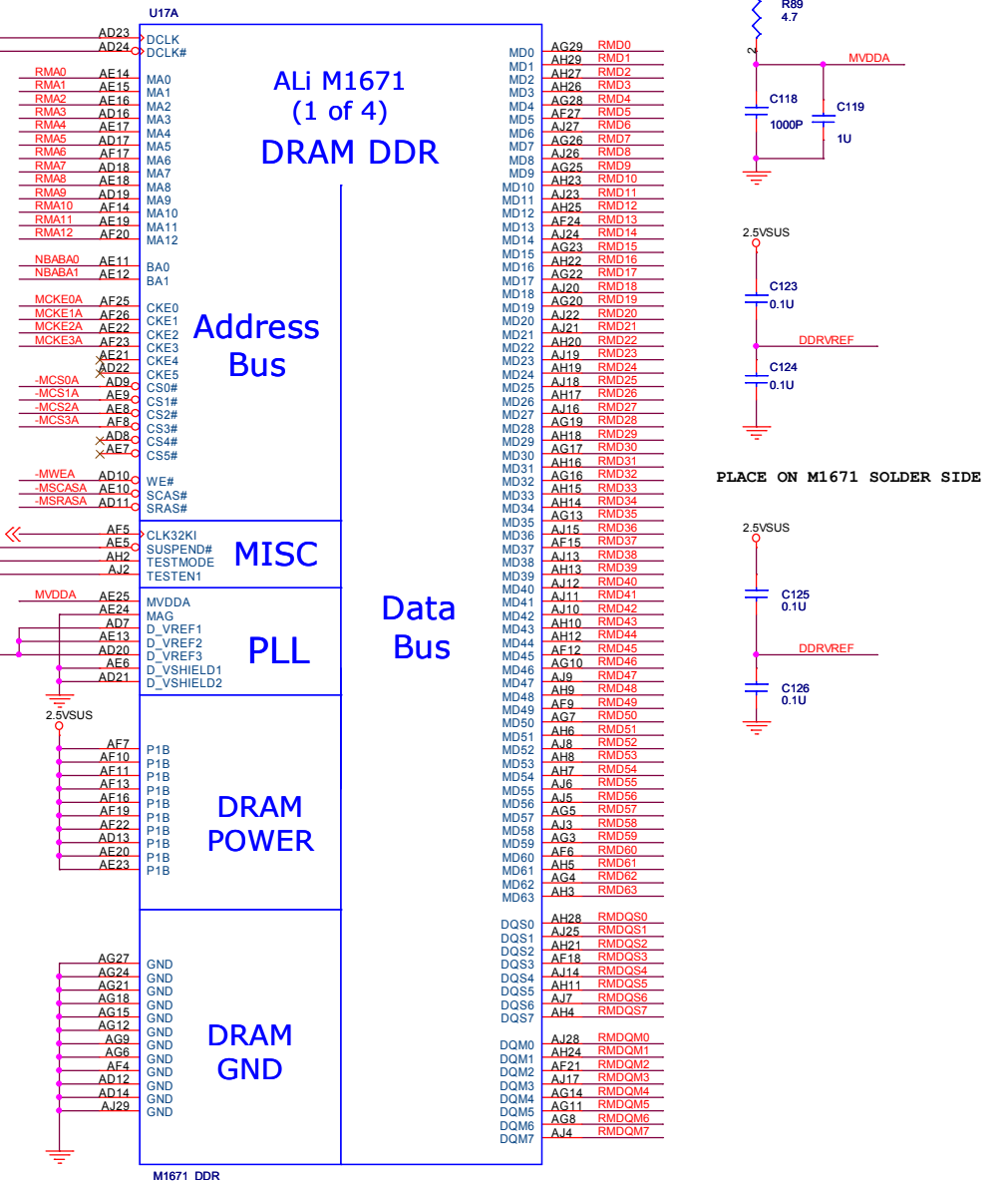
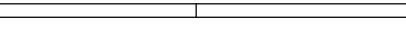
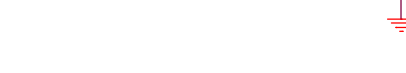
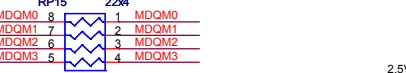
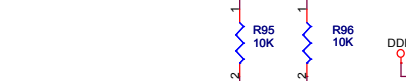
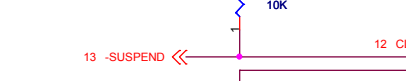
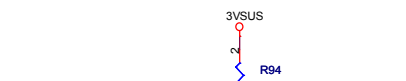
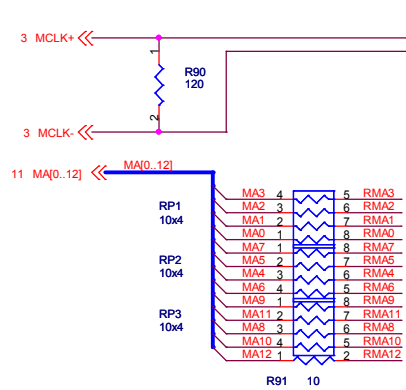
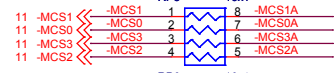
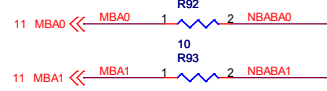
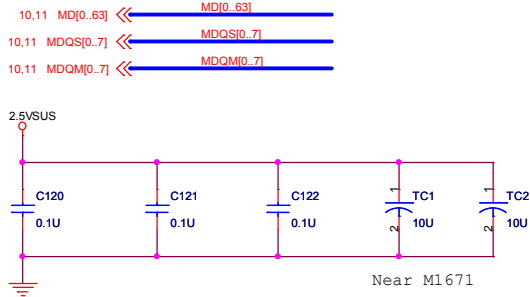


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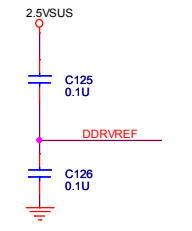
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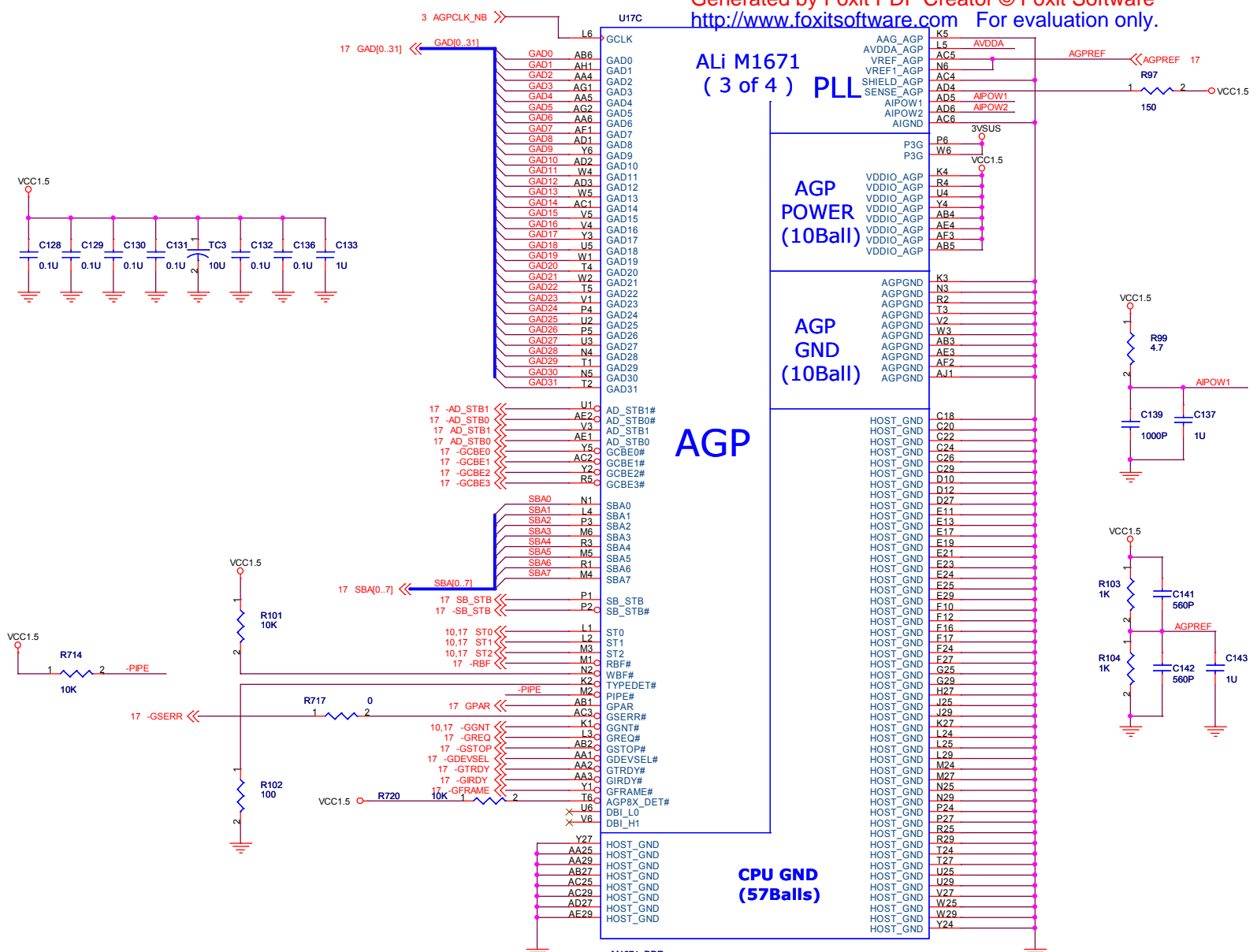
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PLACE ON M1671 SOLDER SIDE

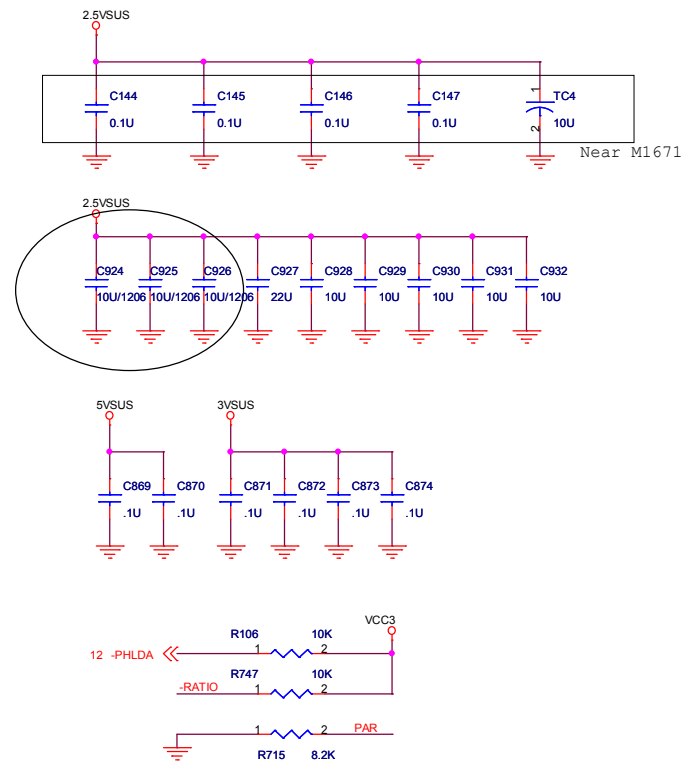
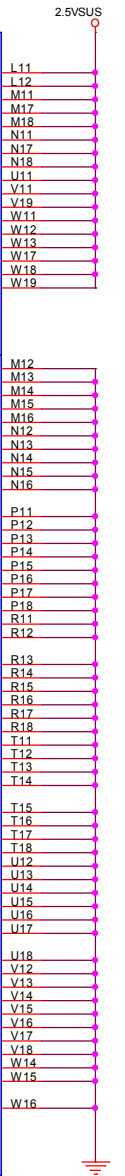
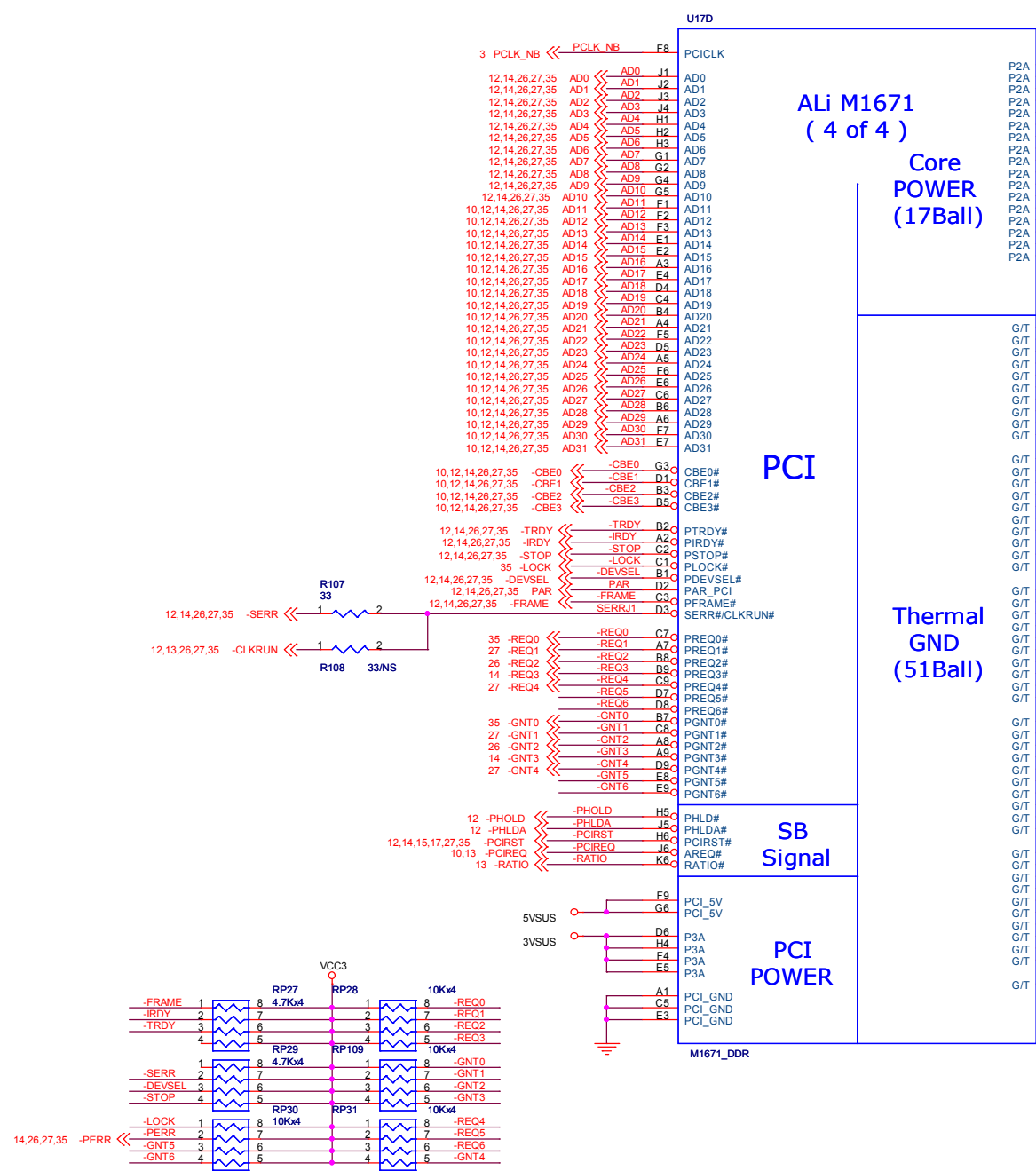




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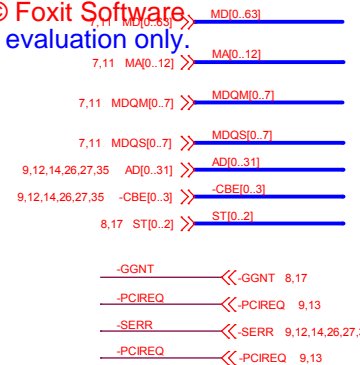
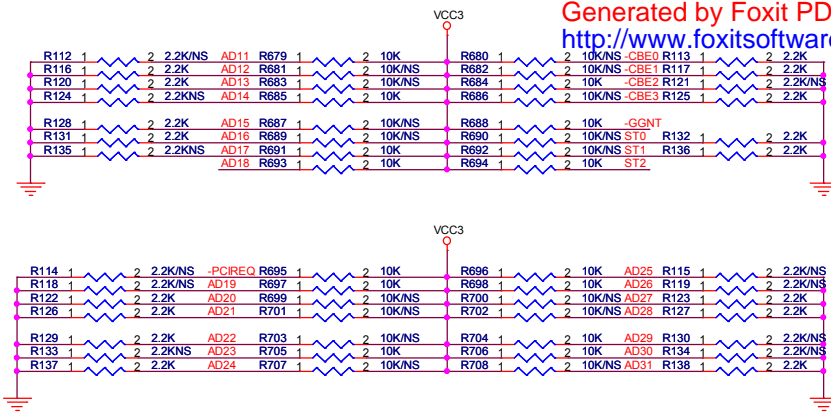
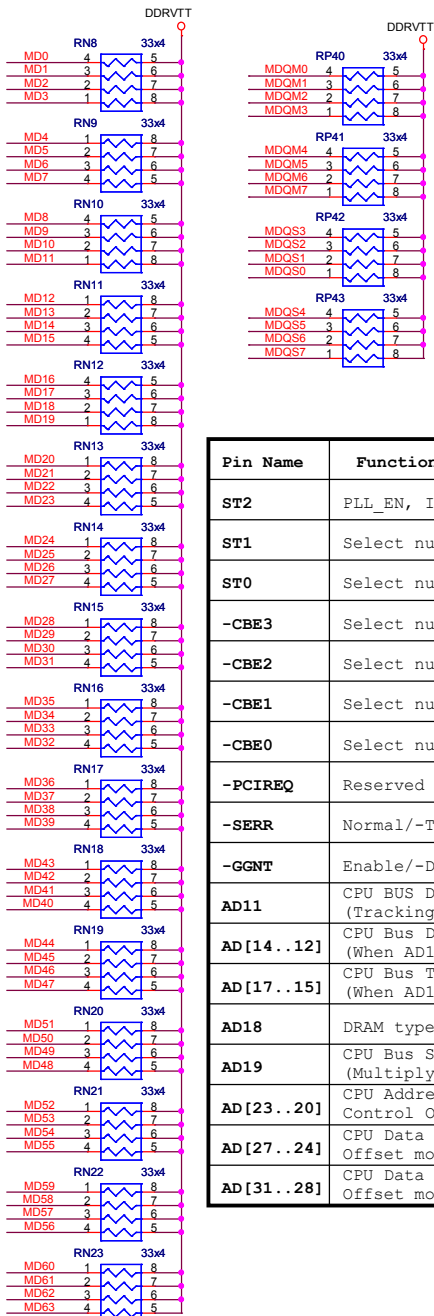
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Size: Custom	Document Number: M1671-AGP	Rev: A
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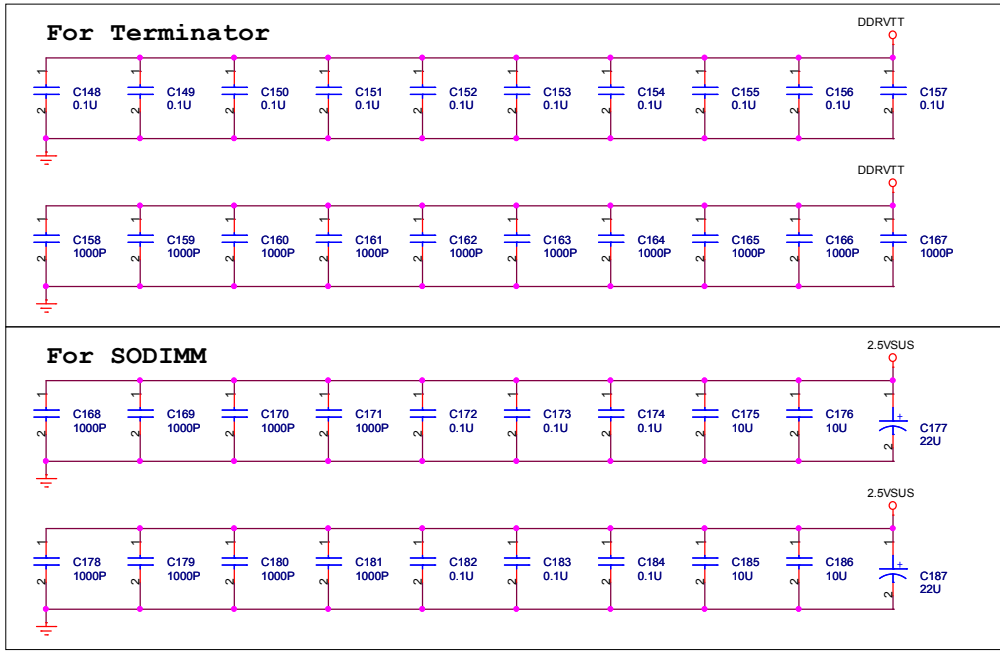


LAN	AD19	-REQ3 / -GNT3	-INTC
PCMCIA	AD21	-REQ2 / -GNT2	-INTA
PCI MINI1	AD20	-REQ1 / -GNT1	-INTB / D
PCI MINI2	AD22	-REQ4 / -GNT4	-INTD / B
DOCK		-REQ0 / -GNT0	-INTC / D





Pin Name	Function	Pull-Low	Pull-High
ST2	PLL_EN, Internal PLL Enable/-Disable	NC	YES
ST1	Select number of PLL stage compensation	YES	NC
ST0	Select number of PLL stage compensation	YES	NC
-CBE3	Select number of PLL stage compensation	YES	NC
-CBE2	Select number of PLL stage compensation	NC	YES
-CBE1	Select number of PLL stage compensation	YES	NC
-CBE0	Select number of PLL stage compensation	YES	NC
-PCIREQ	Reserved	NC	YES
-SERR	Normal/-Test mode	NC	YES
-GGNT	Enable/-Disable internal PLL test mode	NC	YES
AD11	CPU BUS Drivint Control mode. (Tracking circuit/-By AD[17..12])	NC	YES
AD[14..12]	CPU Bus Driving Low Strength Control. (When AD11 pull low)	100	
AD[17..15]	CPU Bus Termination Strength Control. (When AD11 pull low)	100	
AD18	DRAM type selection (DDR/-SDR)	NC	YES
AD19	CPU Bus Strobe Timing Control (Multiply/-Offset)	NC	YES
AD[23..20]	CPU Address/Command Bus Strobe Timing Control Offset mode	1000	
AD[27..24]	CPU Data Bus Strobe (N) Timing Control Offset mode	0110	
AD[31..28]	CPU Data Bus Strobe (P) Timing Control Offset mode	0110	

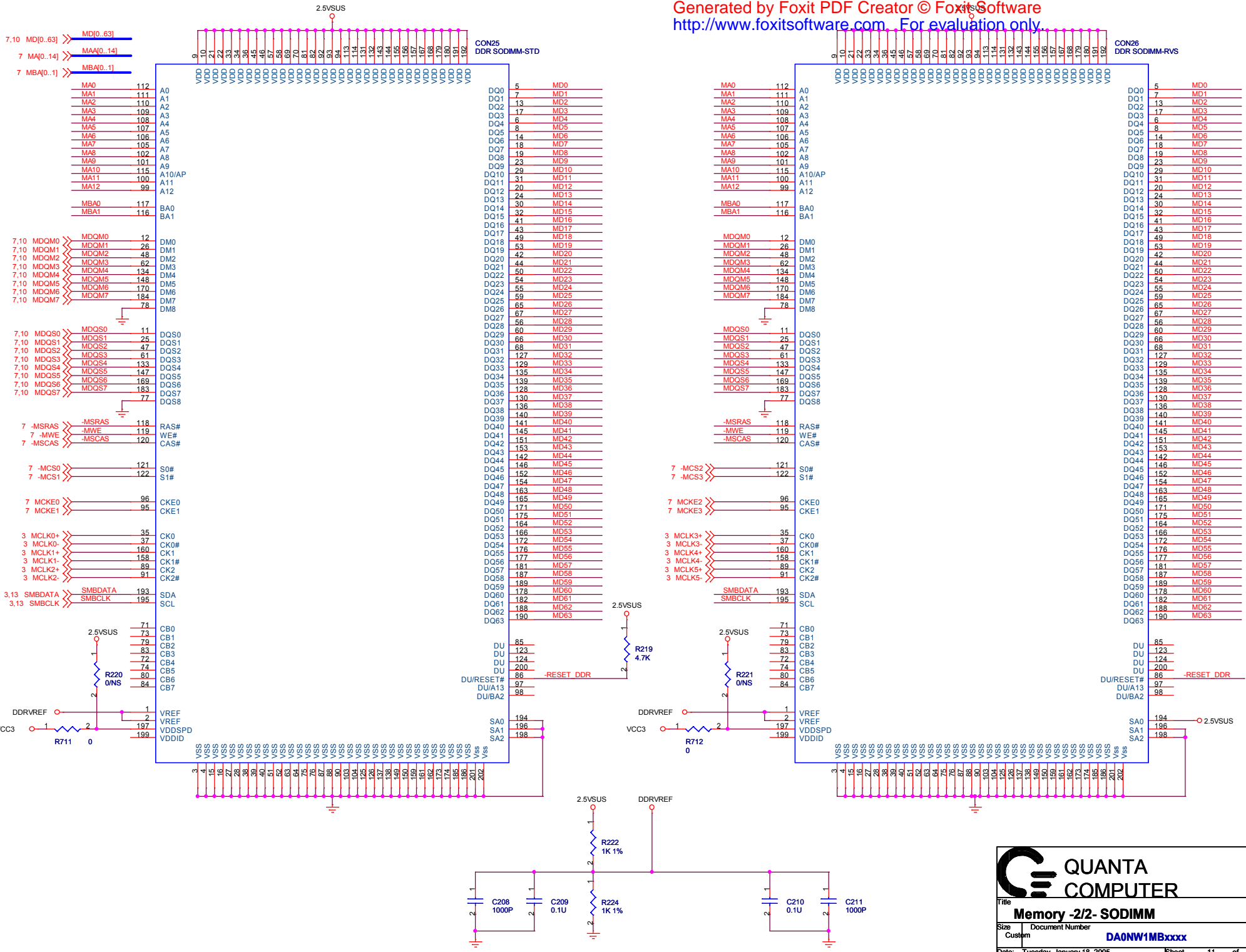


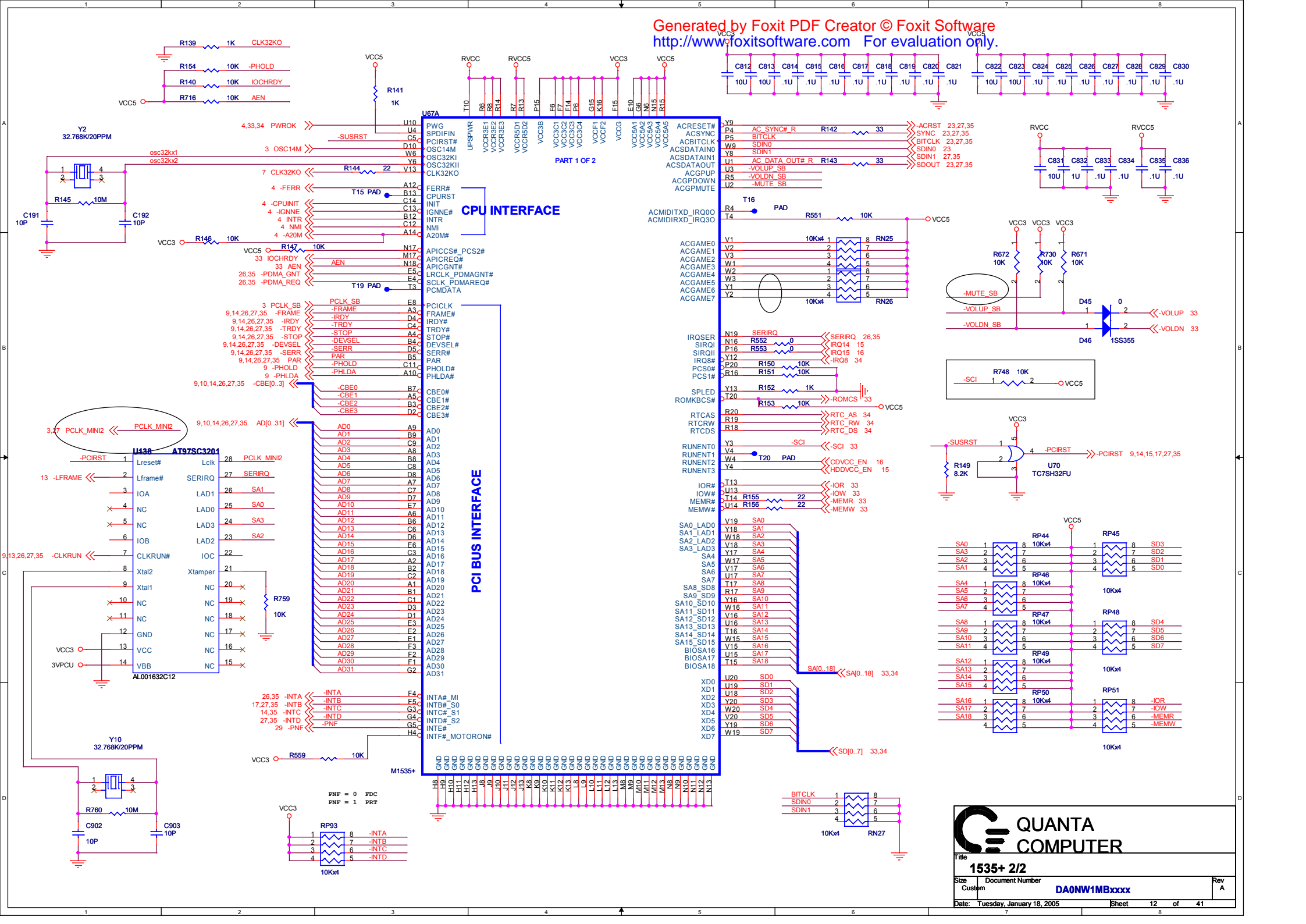
QUANTA COMPUTER

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Size: Custom | Document Number: **CONFIGURATION SETTING** | Rev: A

Date: Tuesday, January 18, 2005 | Sheet: 10 of 41



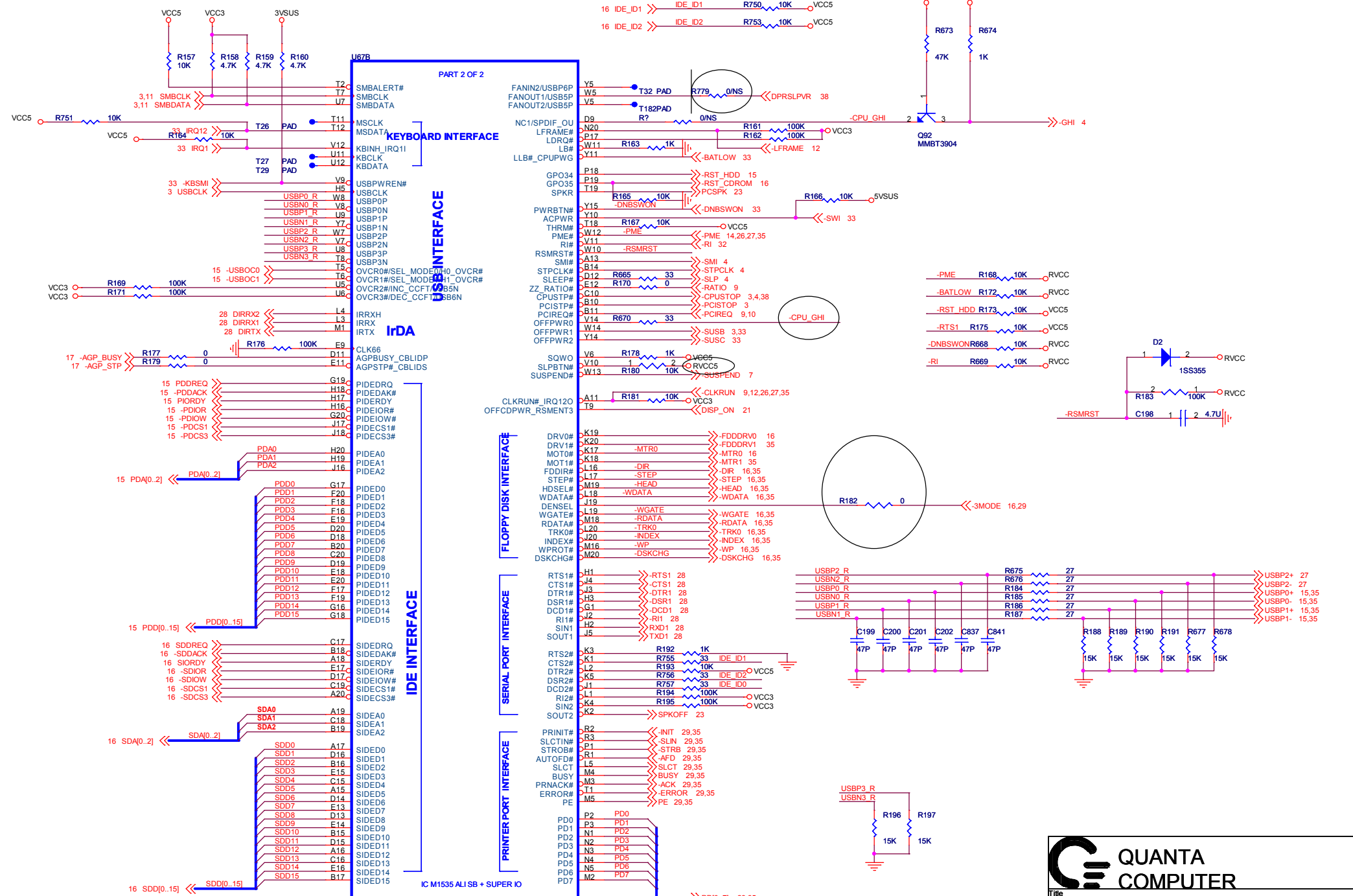


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Date: Tuesday, January 18, 2005 Sheet: 12 of 41

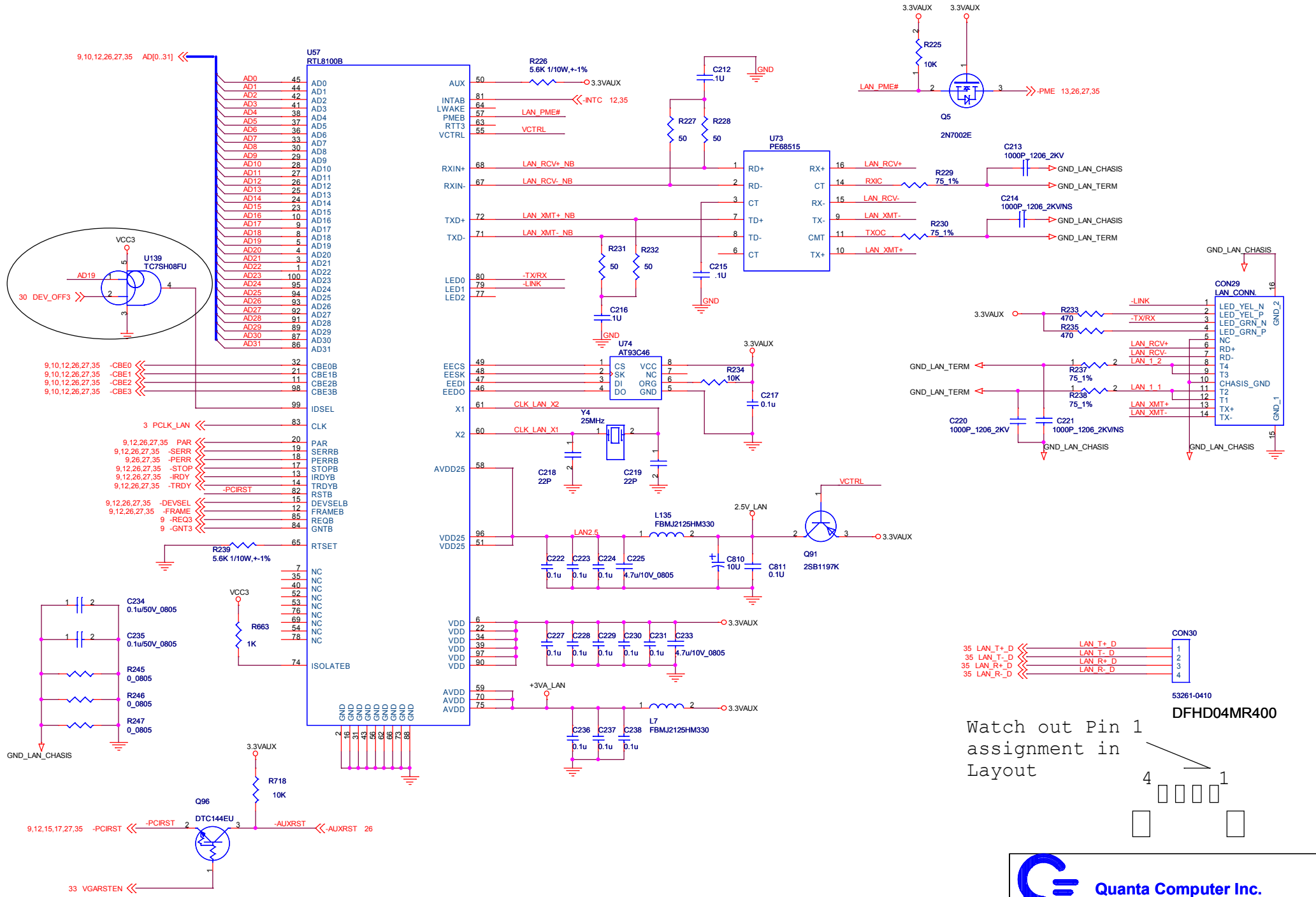


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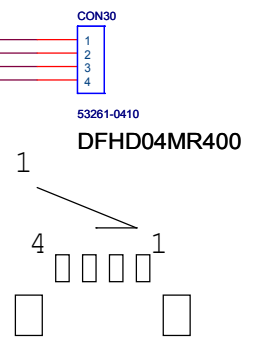
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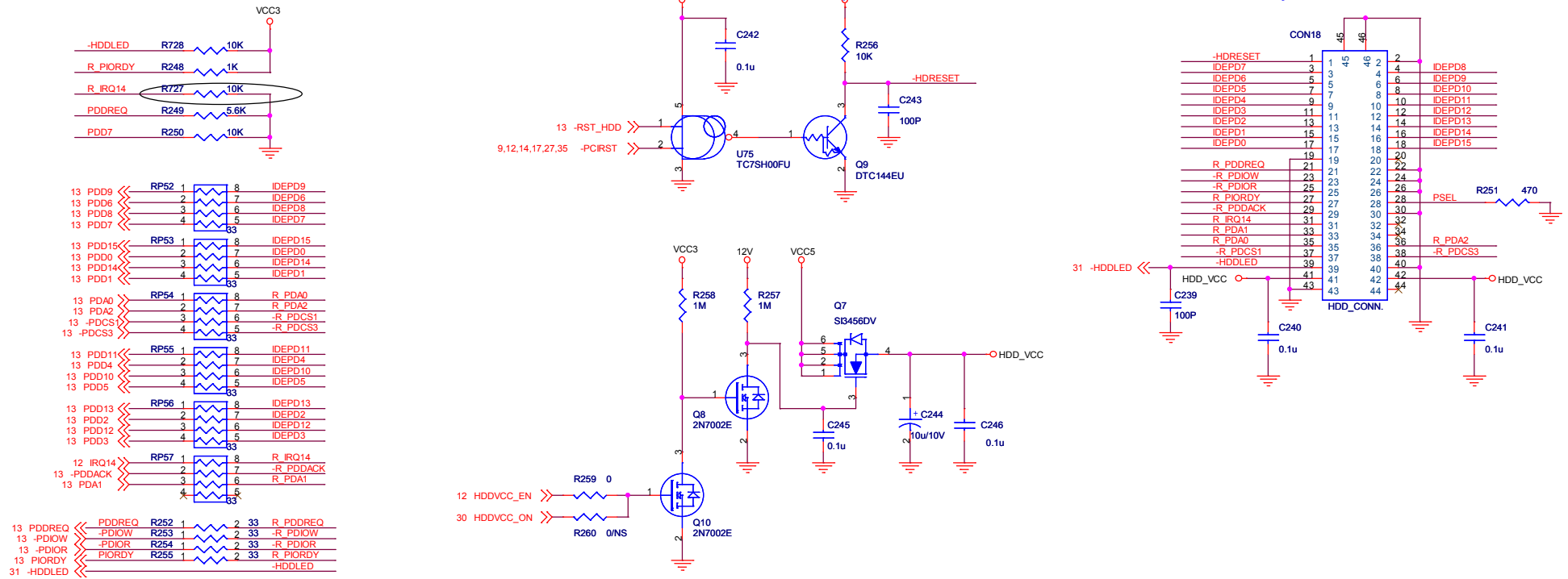
Date: Tuesday, January 18, 2005 Sheet: 13 of 41



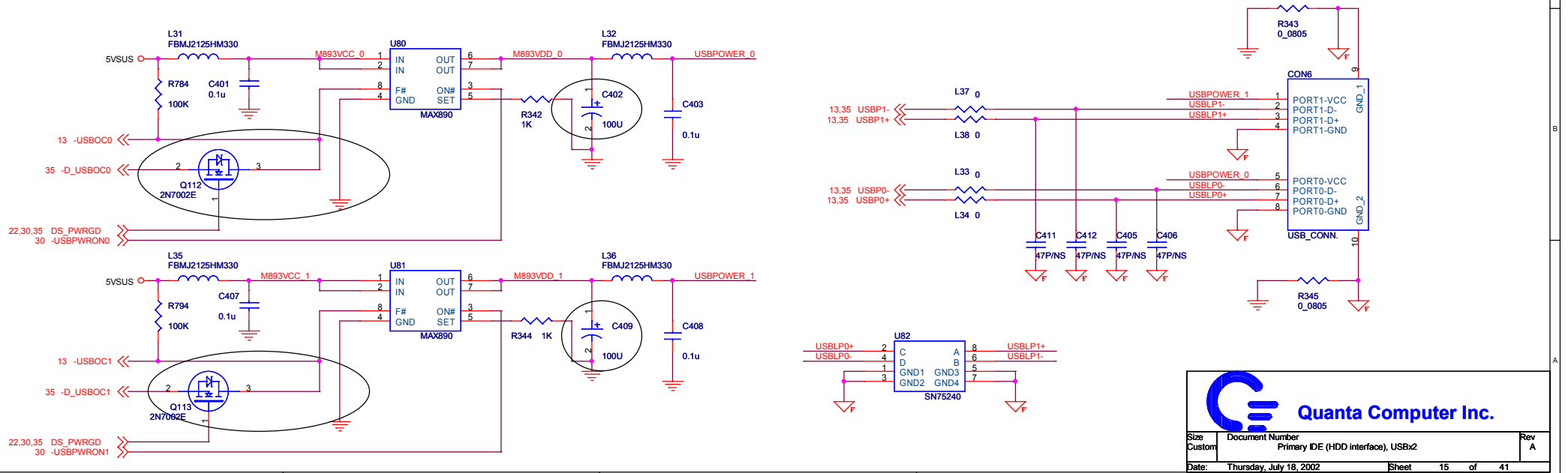
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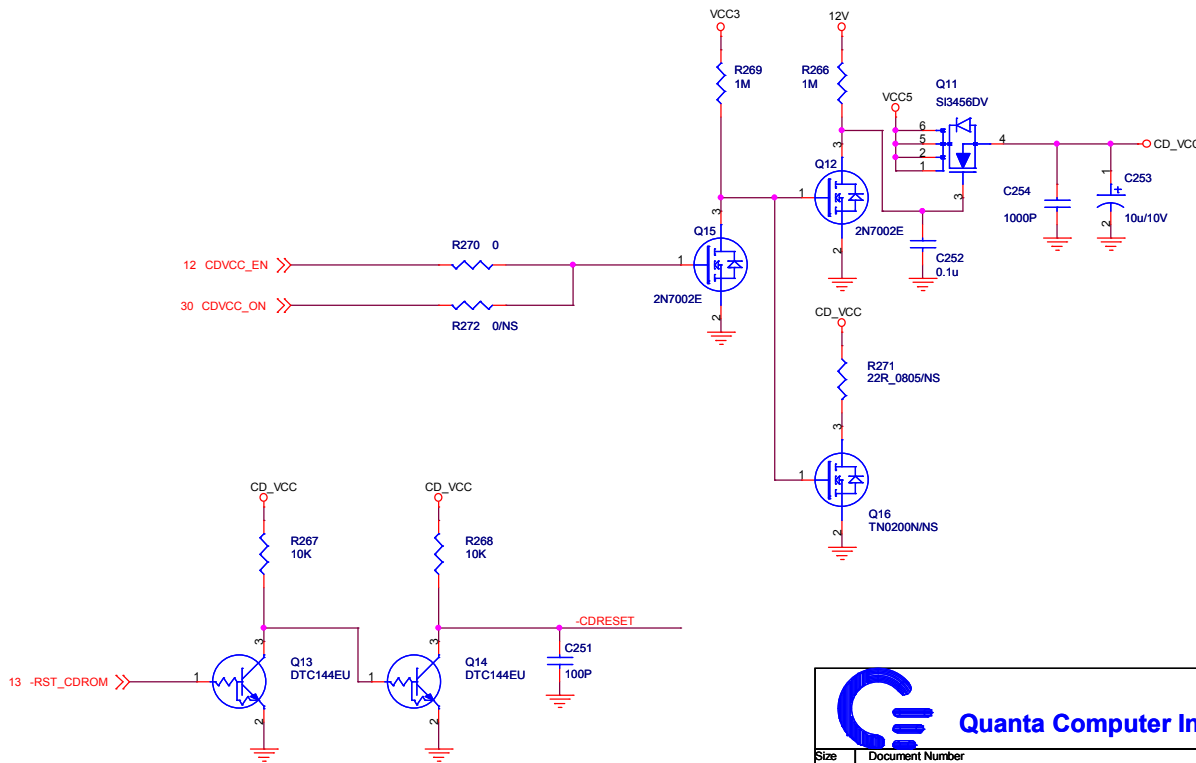
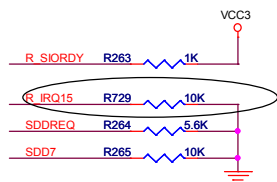
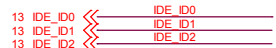
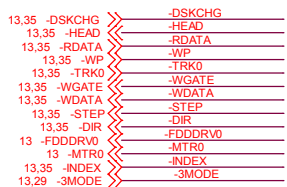
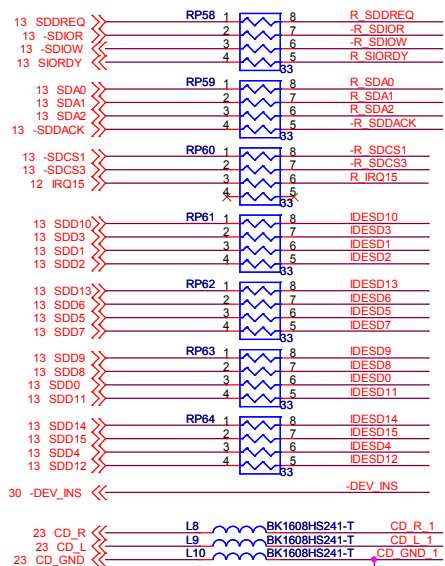


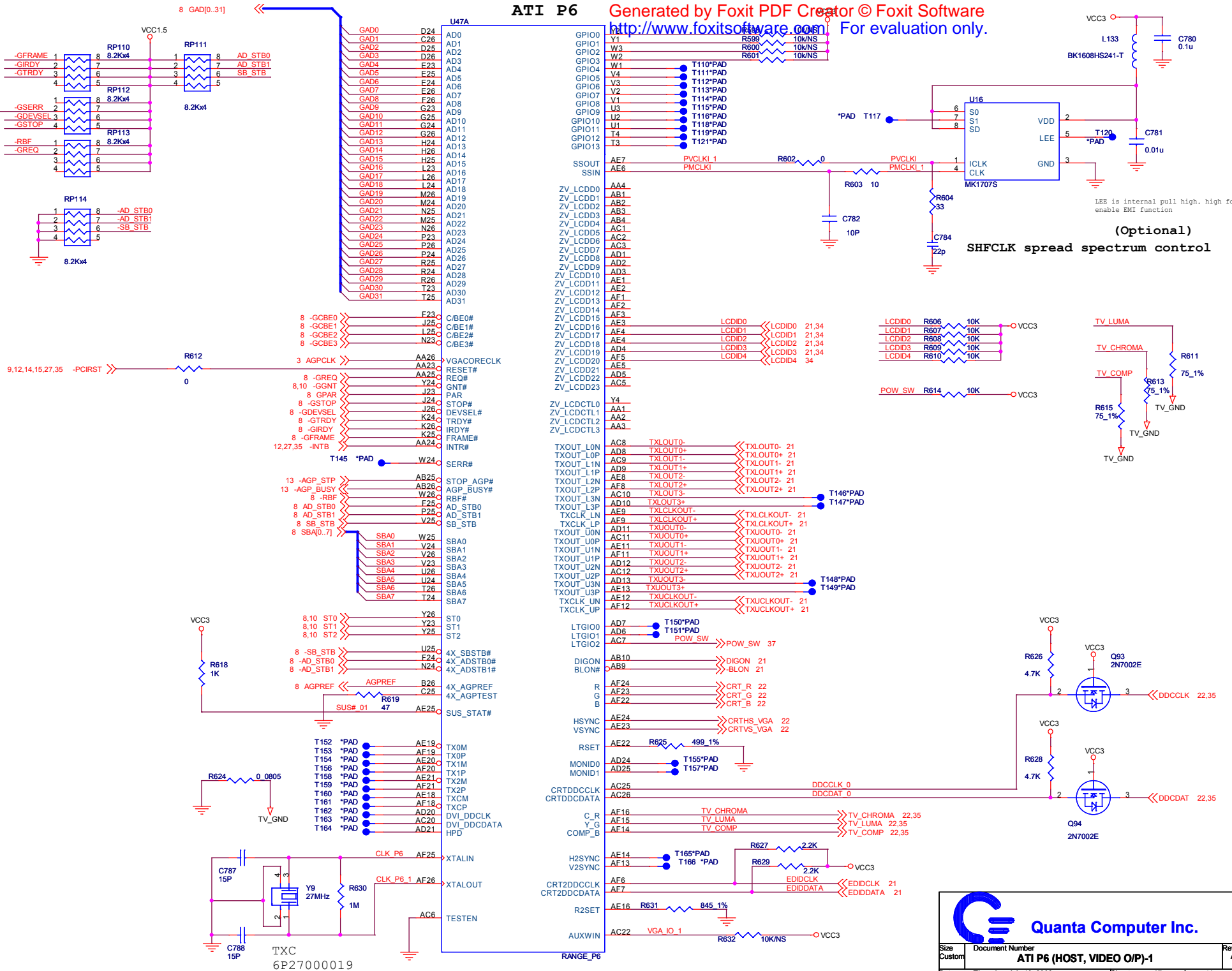
HDD connector



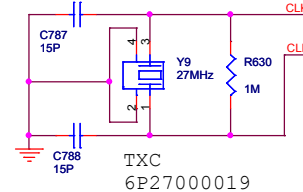
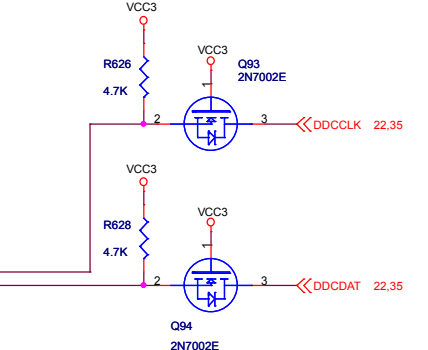
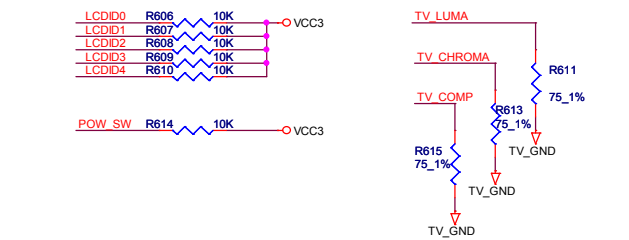
USB CONNECTOR







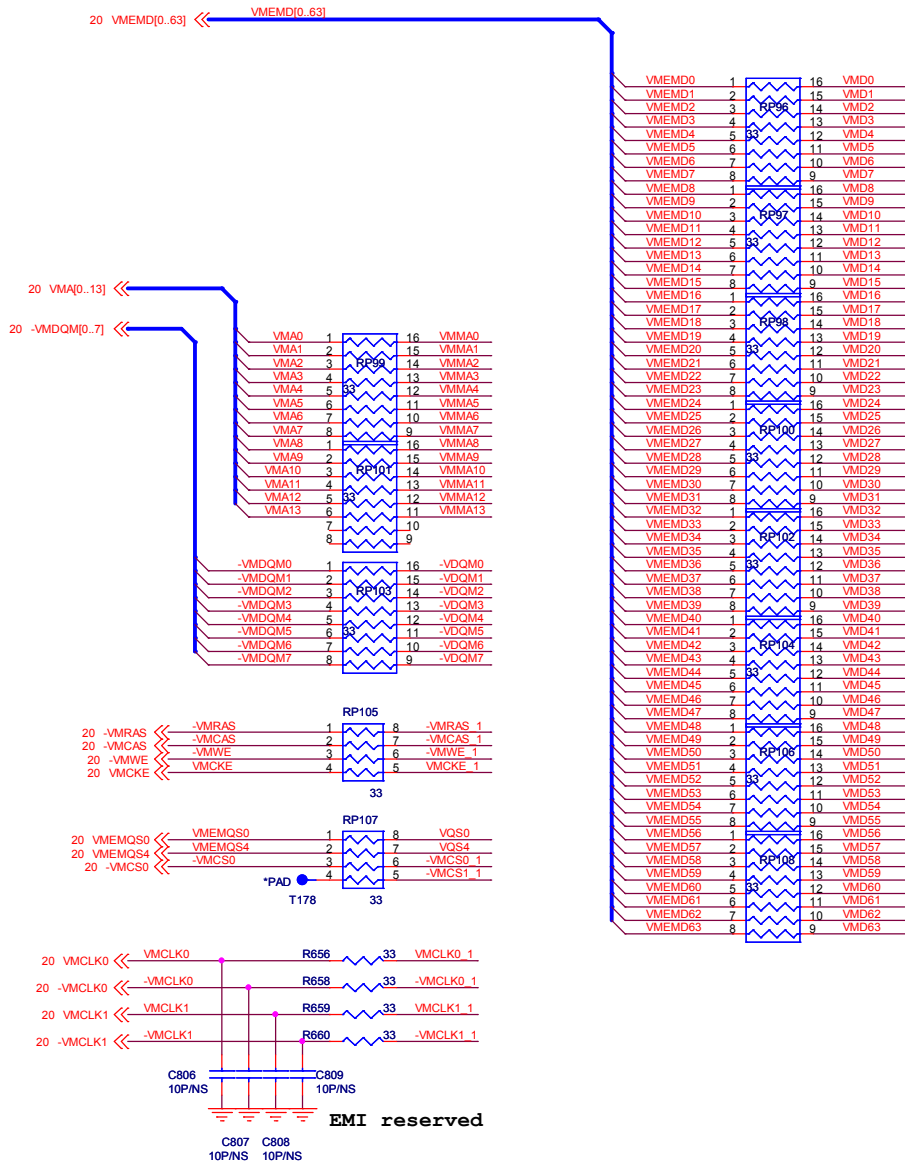
(Optional)
SHFCLK spread spectrum control



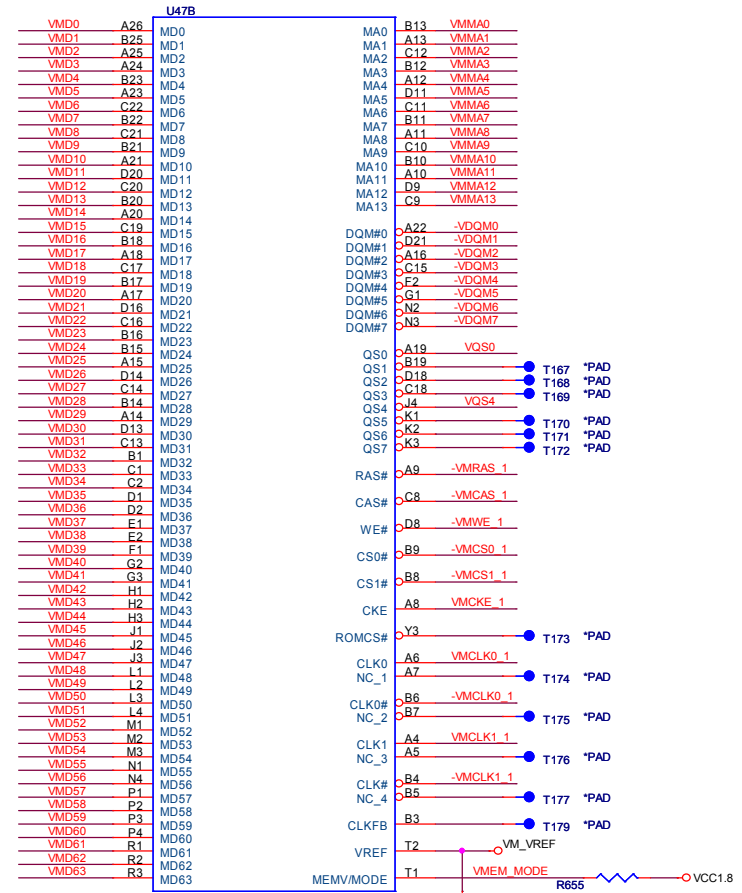
Quanta Computer Inc.

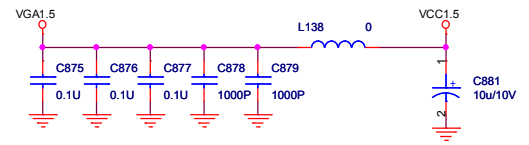
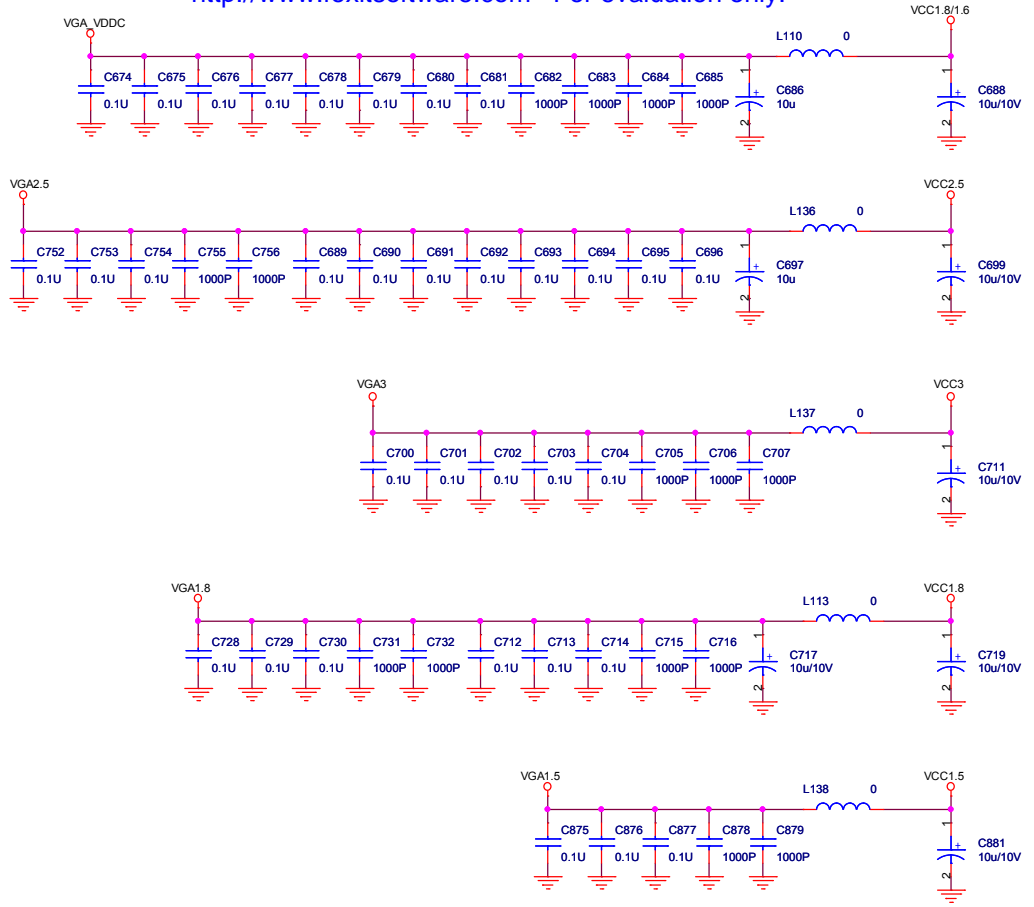
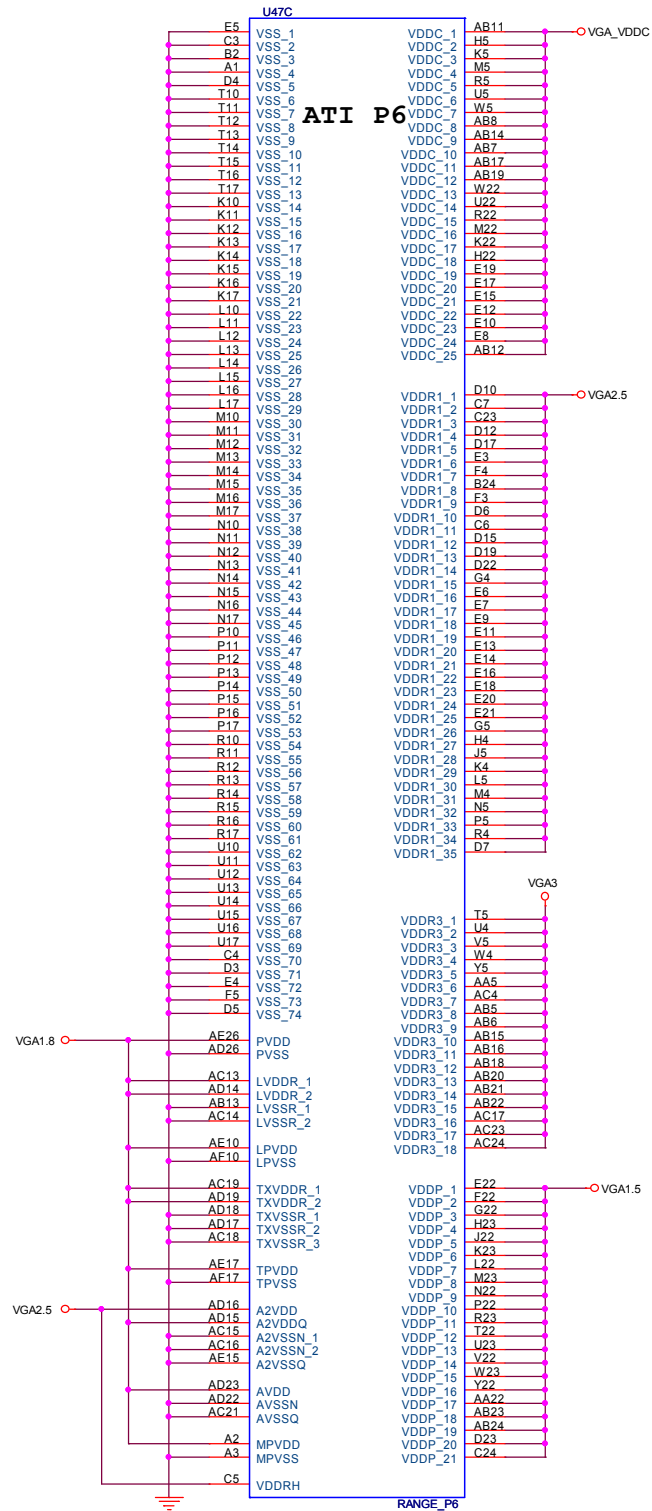
Size Custom	Document Number ATI P6 (HOST, VIDEO O/P)-1	Rev A
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ATI P6



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VMEMD1	2	15	VMD1
VMEMD2	3	14	VMD2
VMEMD3	4	13	VMD3
VMEMD4	5	12	VMD4
VMEMD5	6	11	VMD5
VMEMD6	7	10	VMD6
VMEMD7	8	9	VMD7
VMEMD8	1	16	VMD8
VMEMD9	2	15	VMD9
VMEMD10	3	14	VMD10
VMEMD11	4	13	VMD11
VMEMD12	5	12	VMD12
VMEMD13	6	11	VMD13
VMEMD14	7	10	VMD14
VMEMD15	8	9	VMD15
VMEMD16	1	16	VMD16
VMEMD17	2	15	VMD17
VMEMD18	3	14	VMD18
VMEMD19	4	13	VMD19
VMEMD20	5	12	VMD20
VMEMD21	6	11	VMD21
VMEMD22	7	10	VMD22
VMEMD23	8	9	VMD23
VMEMD24	1	16	VMD24
VMEMD25	2	15	VMD25
VMEMD26	3	14	VMD26
VMEMD27	4	13	VMD27
VMEMD28	5	12	VMD28
VMEMD29	6	11	VMD29
VMEMD30	7	10	VMD30
VMEMD31	8	9	VMD31
VMEMD32	1	16	VMD32
VMEMD33	2	15	VMD33
VMEMD34	3	14	VMD34
VMEMD35	4	13	VMD35
VMEMD36	5	12	VMD36
VMEMD37	6	11	VMD37
VMEMD38	7	10	VMD38
VMEMD39	8	9	VMD39
VMEMD40	1	16	VMD40
VMEMD41	2	15	VMD41
VMEMD42	3	14	VMD42
VMEMD43	4	13	VMD43
VMEMD44	5	12	VMD44
VMEMD45	6	11	VMD45
VMEMD46	7	10	VMD46
VMEMD47	8	9	VMD47
VMEMD48	1	16	VMD48
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VMEMD50	3	14	VMD50
VMEMD51	4	13	VMD51
VMEMD52	5	12	VMD52
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VMEMD55	8	9	VMD55
VMEMD56	1	16	VMD56
VMEMD57	2	15	VMD57
VMEMD58	3	14	VMD58
VMEMD59	4	13	VMD59
VMEMD60	5	12	VMD60
VMEMD61	6	11	VMD61
VMEMD62	7	10	VMD62
VMEMD63	8	9	VMD63





8/16/32MB DDR 2/4MX32 SDRAM

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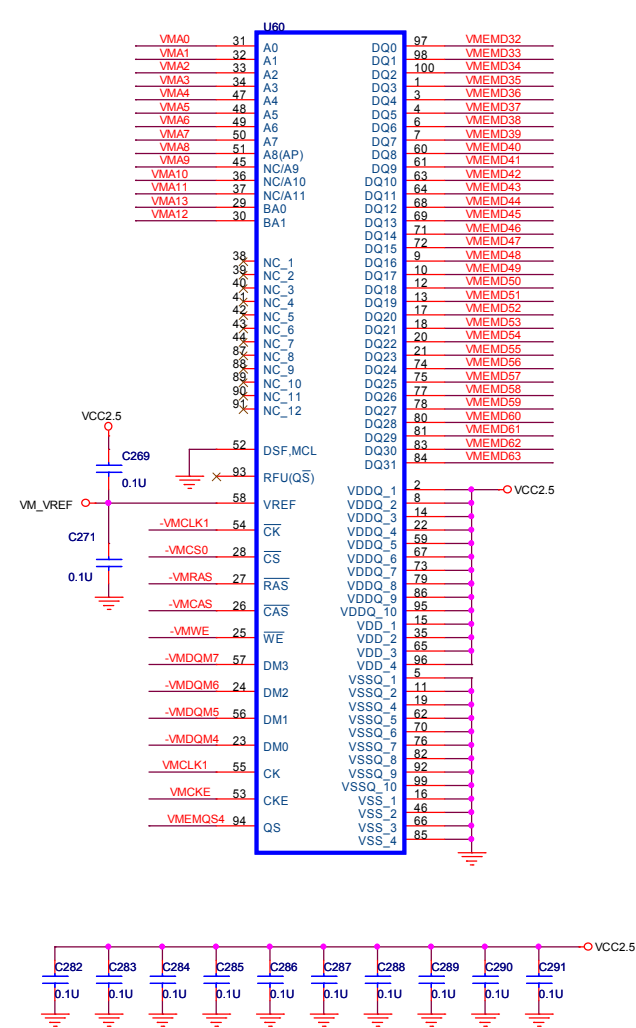
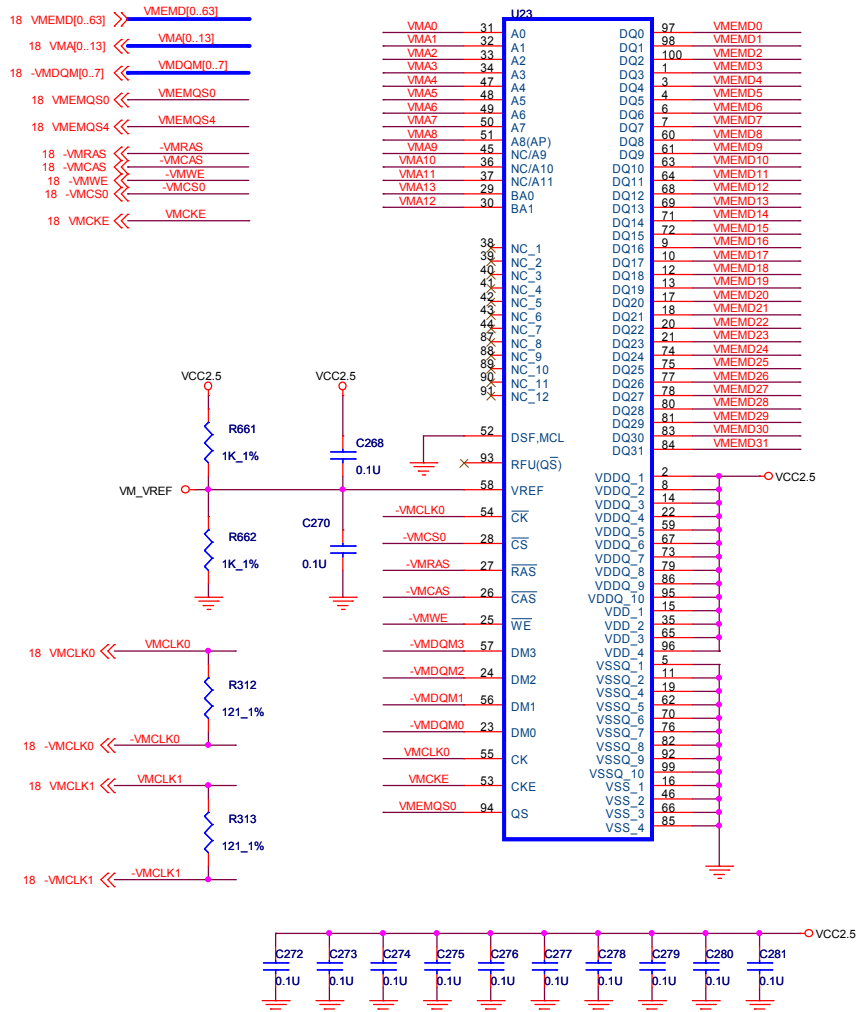
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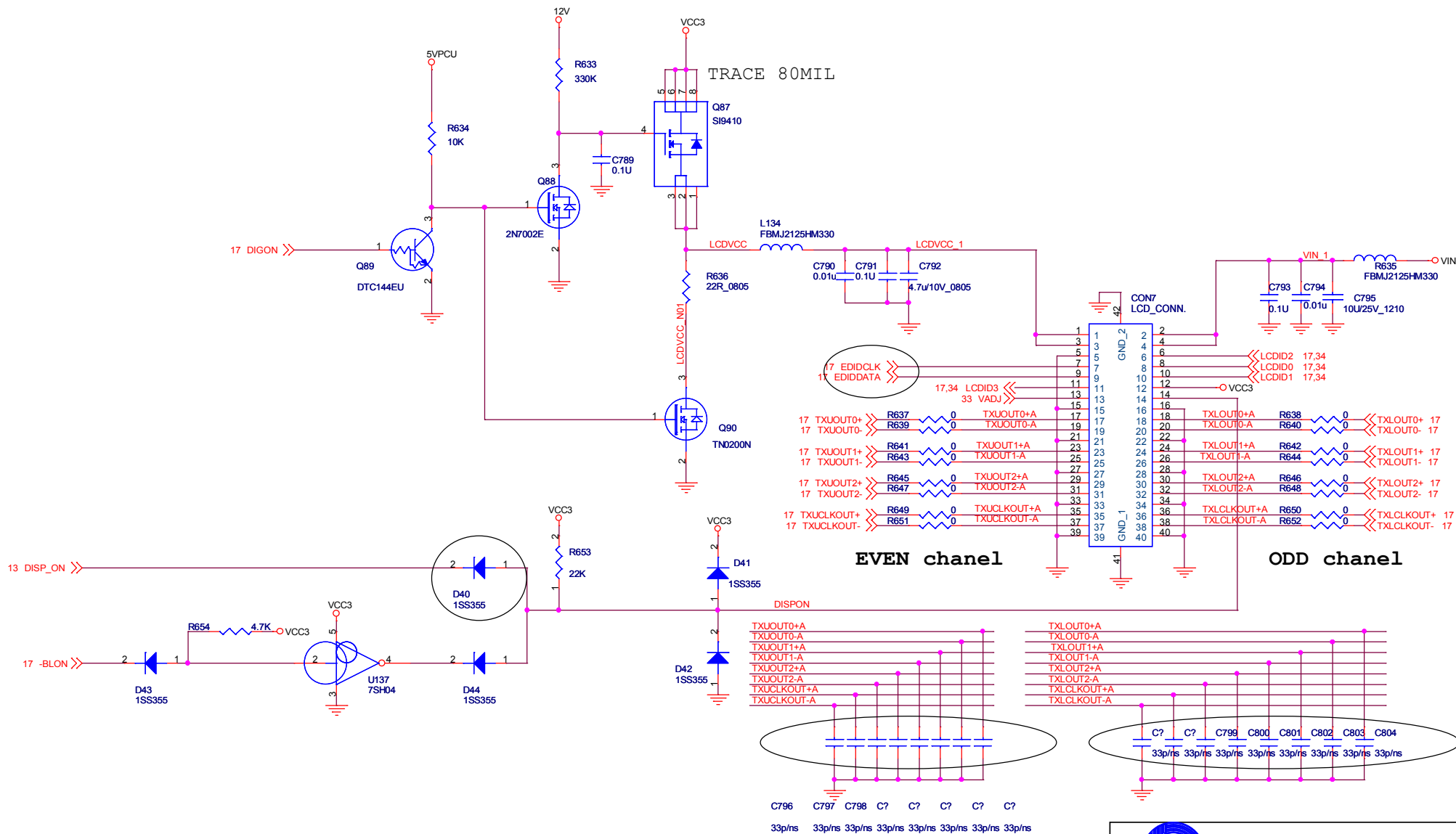
<http://www.foxitsoftware.com> For evaluation only.

8MB 64 BIT INTERFACE WITH TWO PIECES 2MX32 (U23,U60)

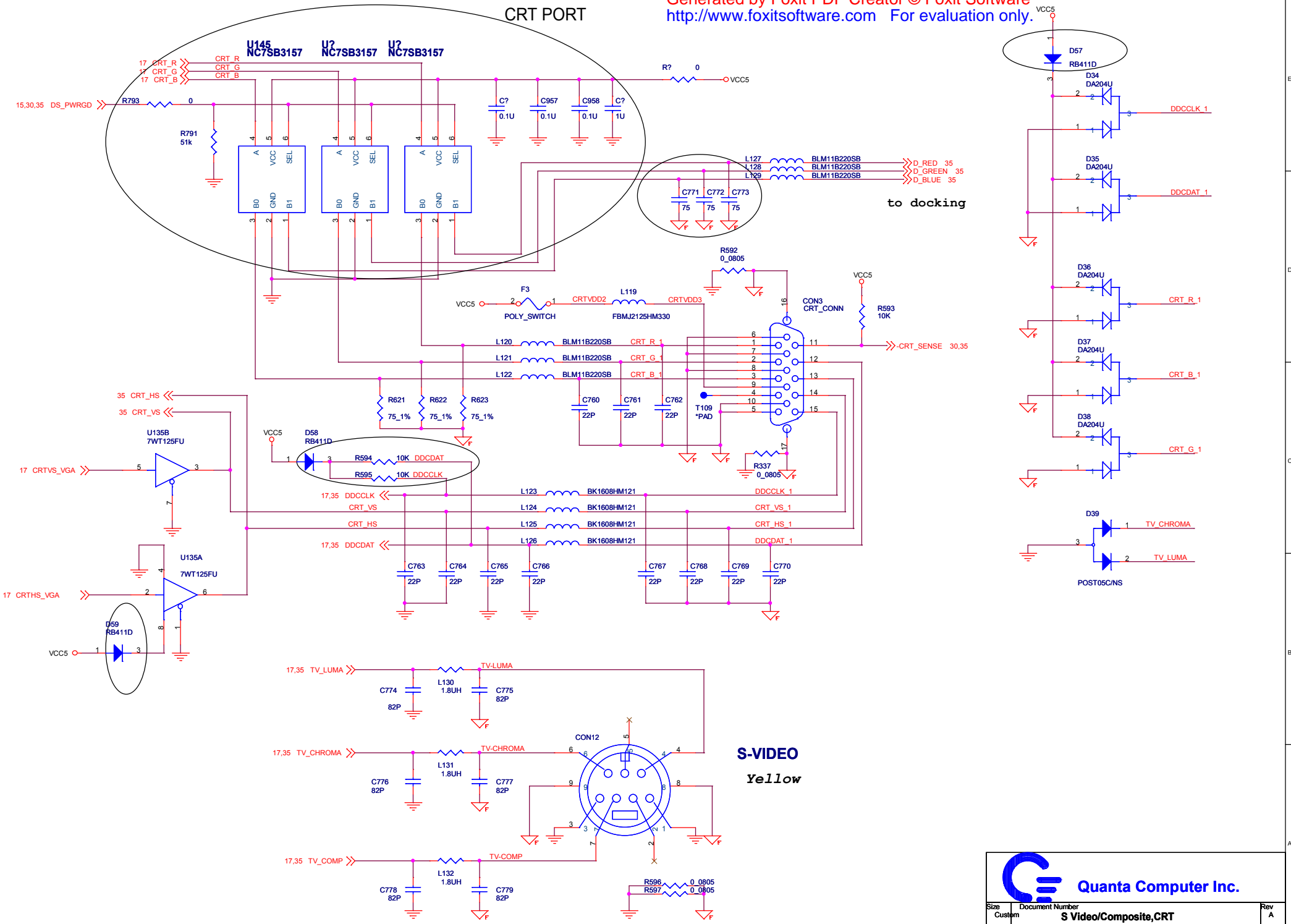
16MB 32 BIT INTERFACE WITH ONE PIECE 4MX32 (U23)

32MB 64 BIT INTERFACE WITH TWO PIECES 4MX32 (U23,U60)

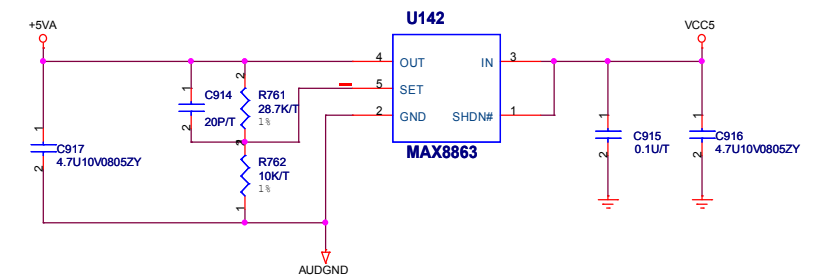
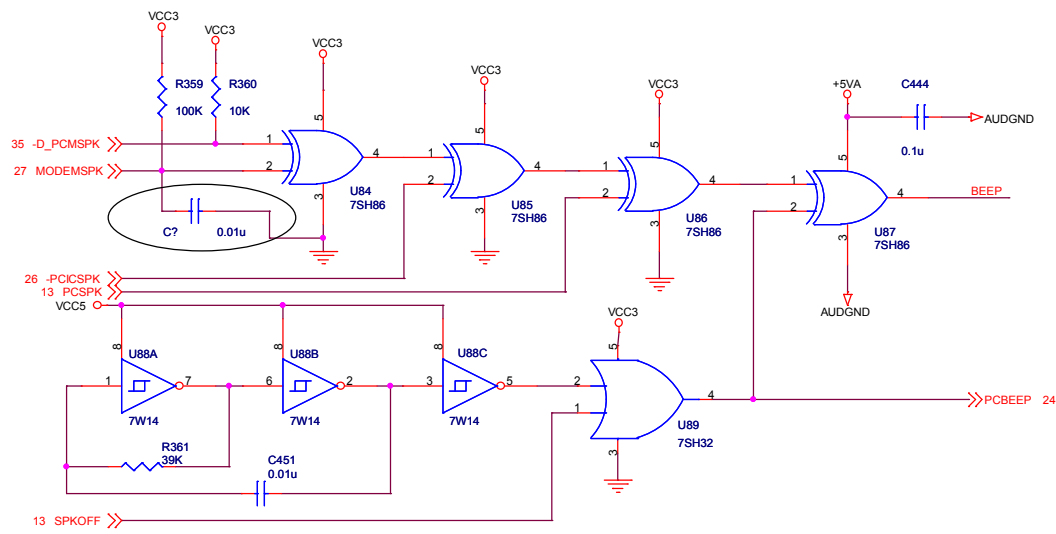
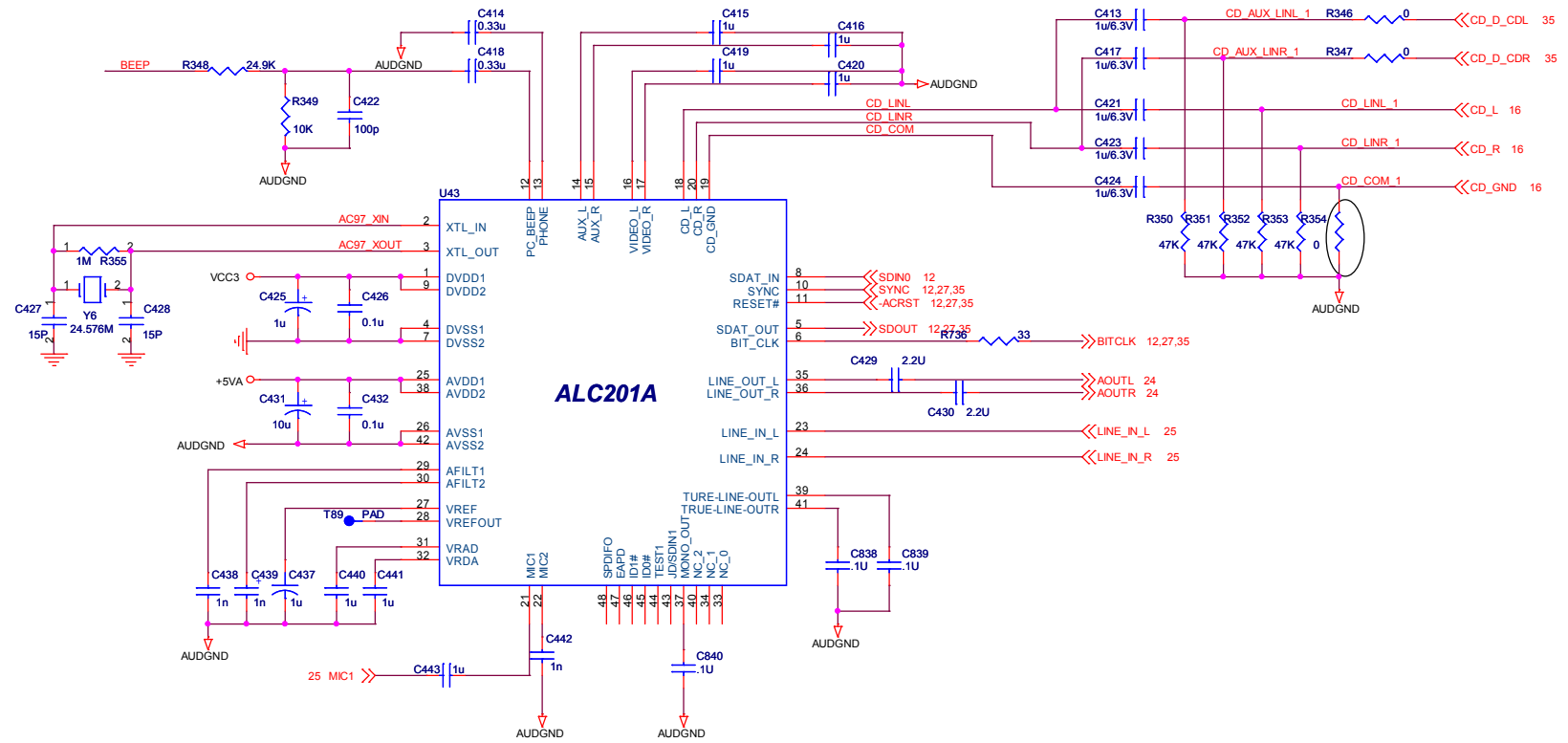




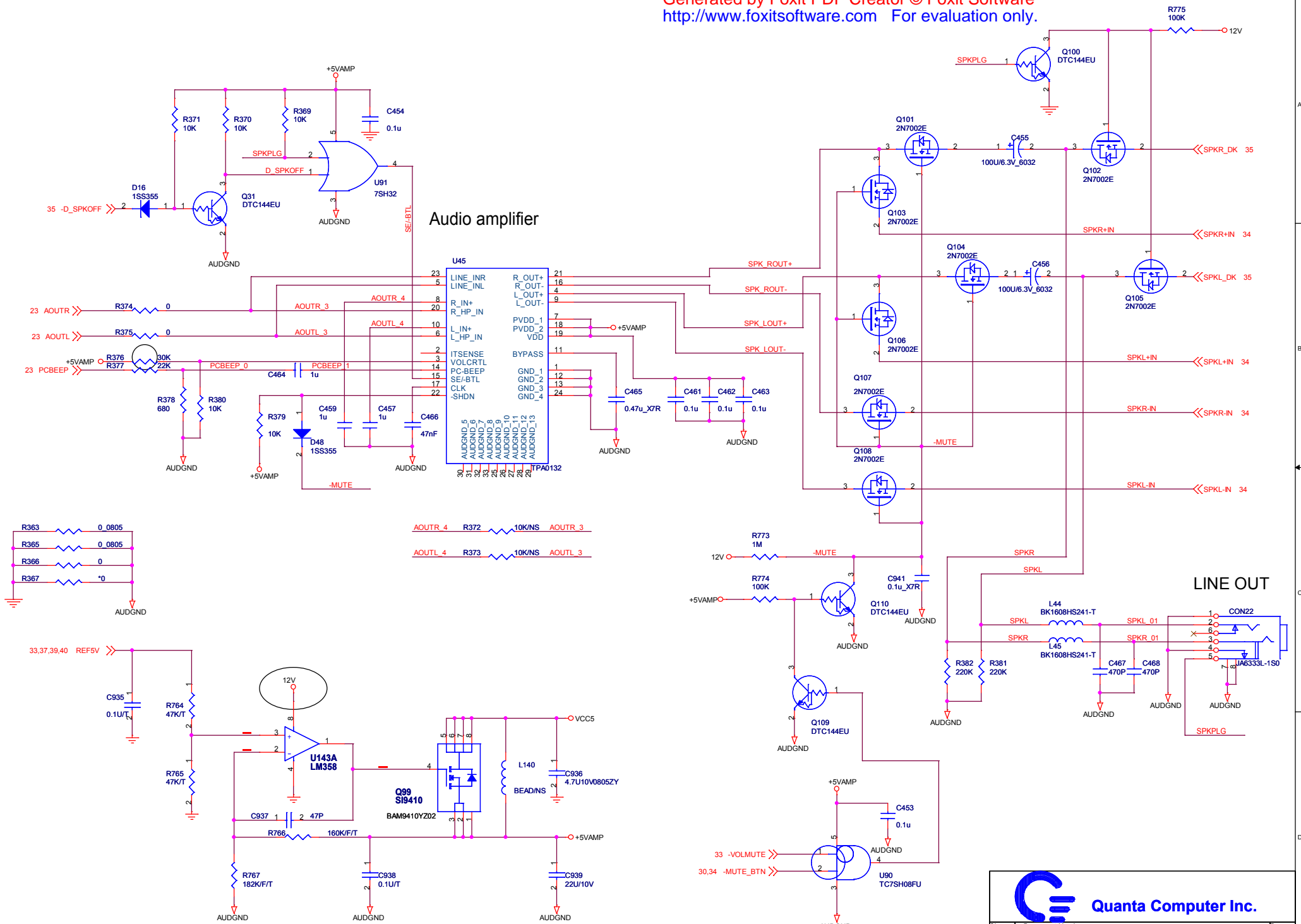
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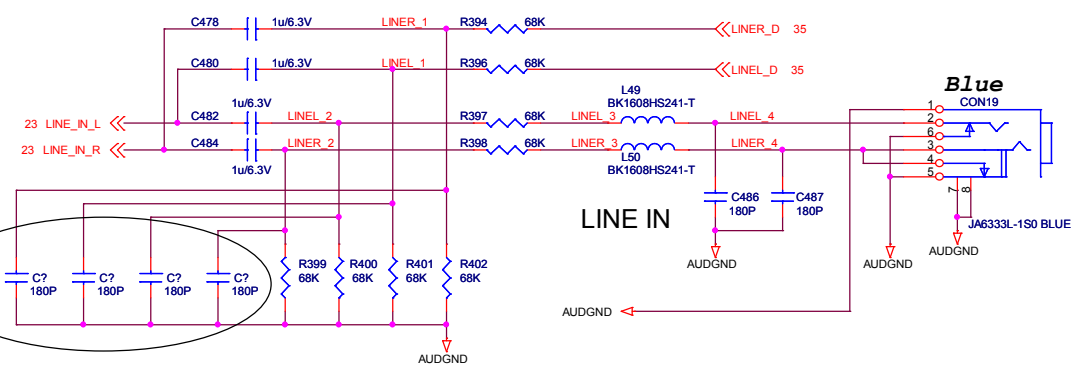
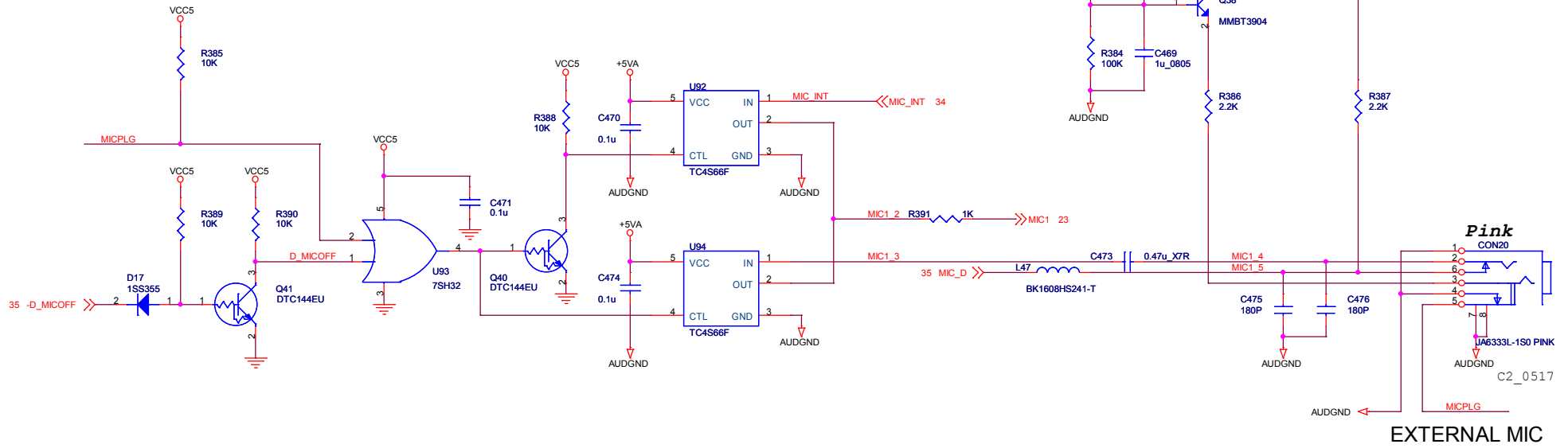


S-VIDEO
Yellow

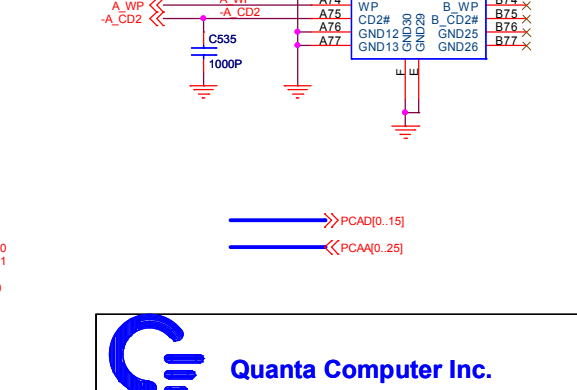
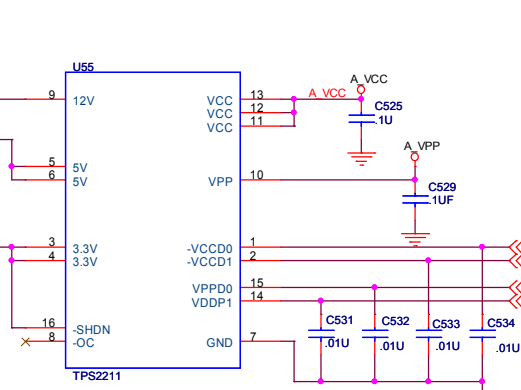
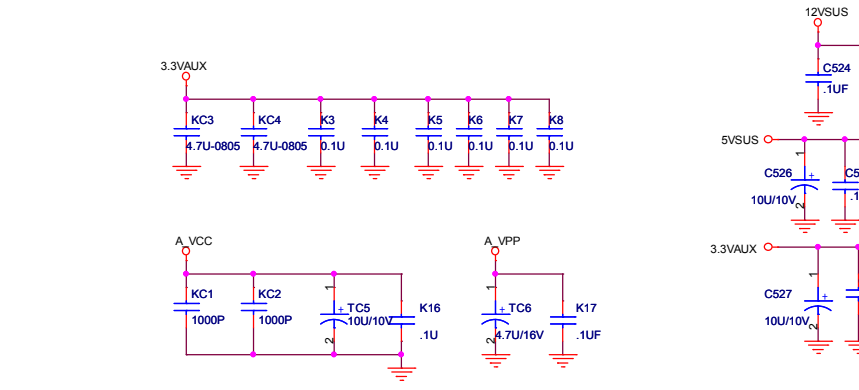
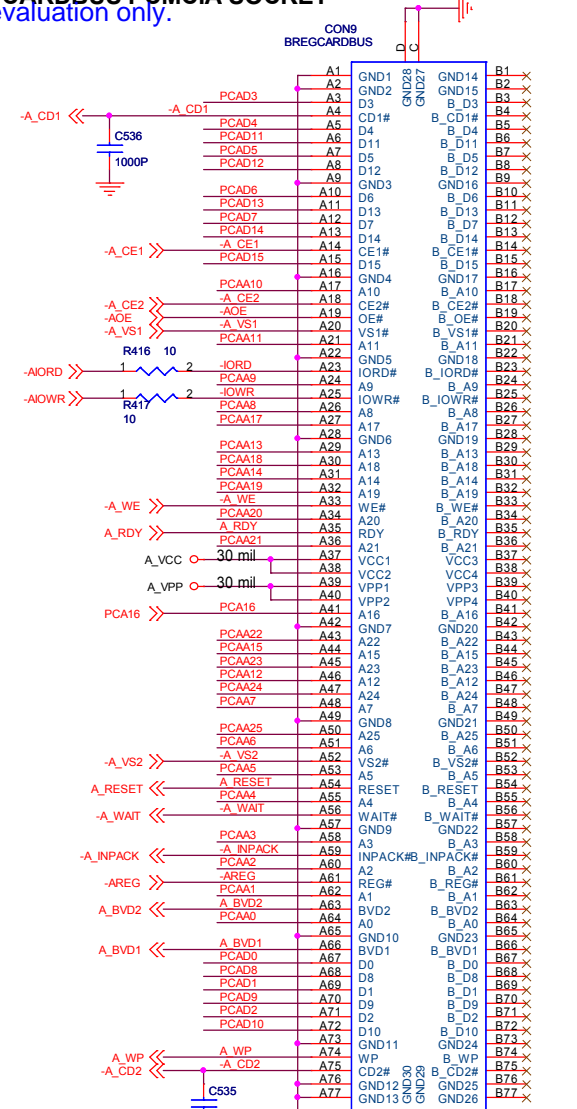
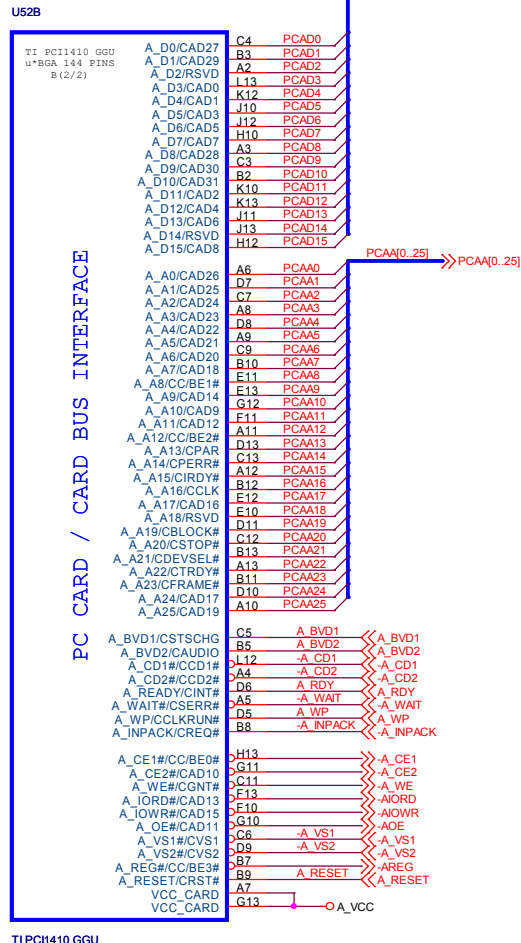
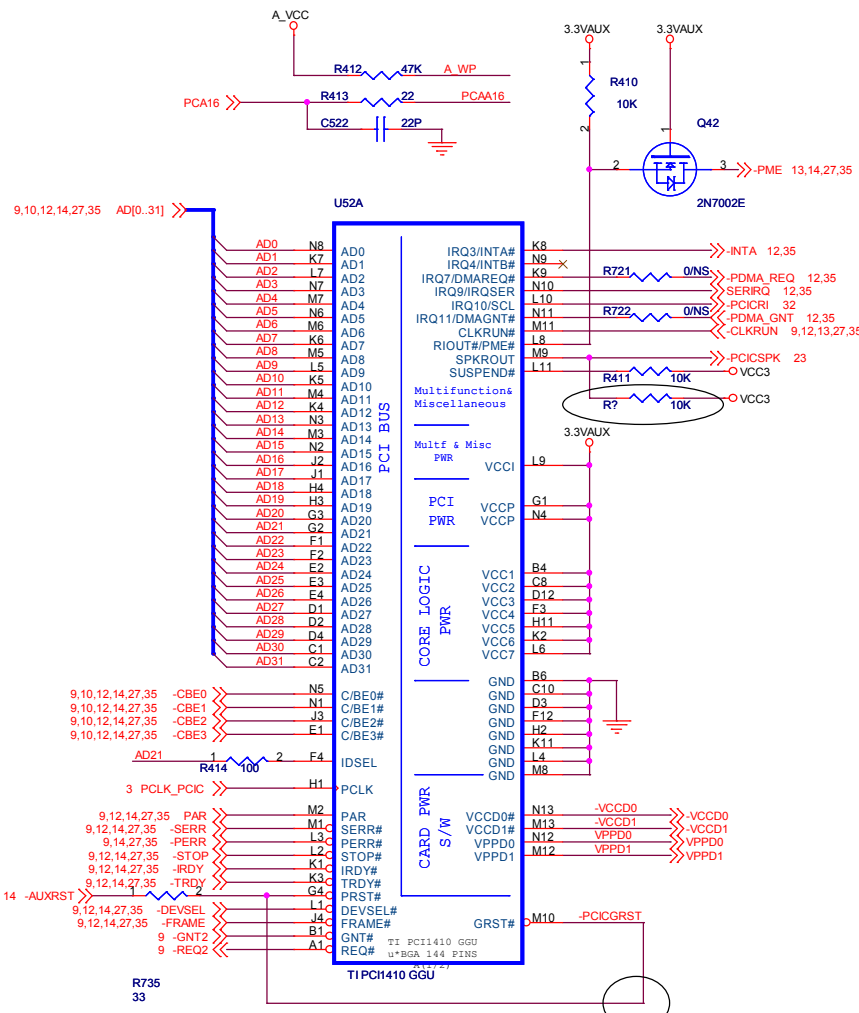


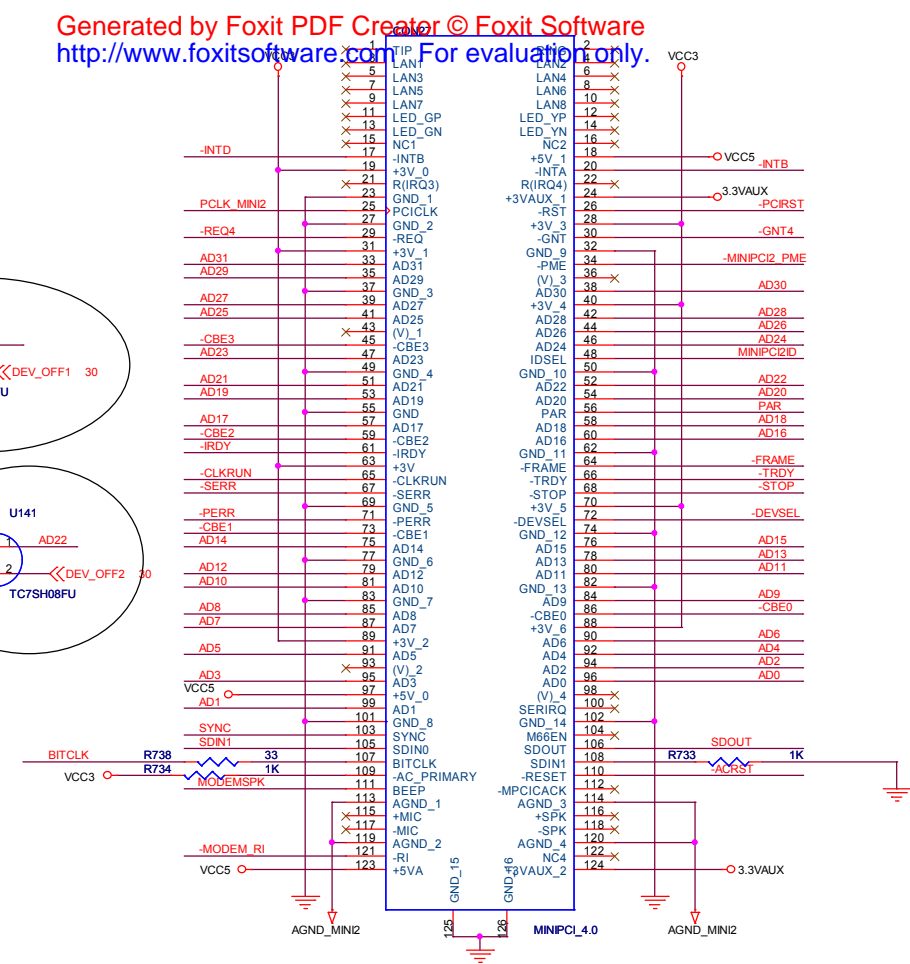
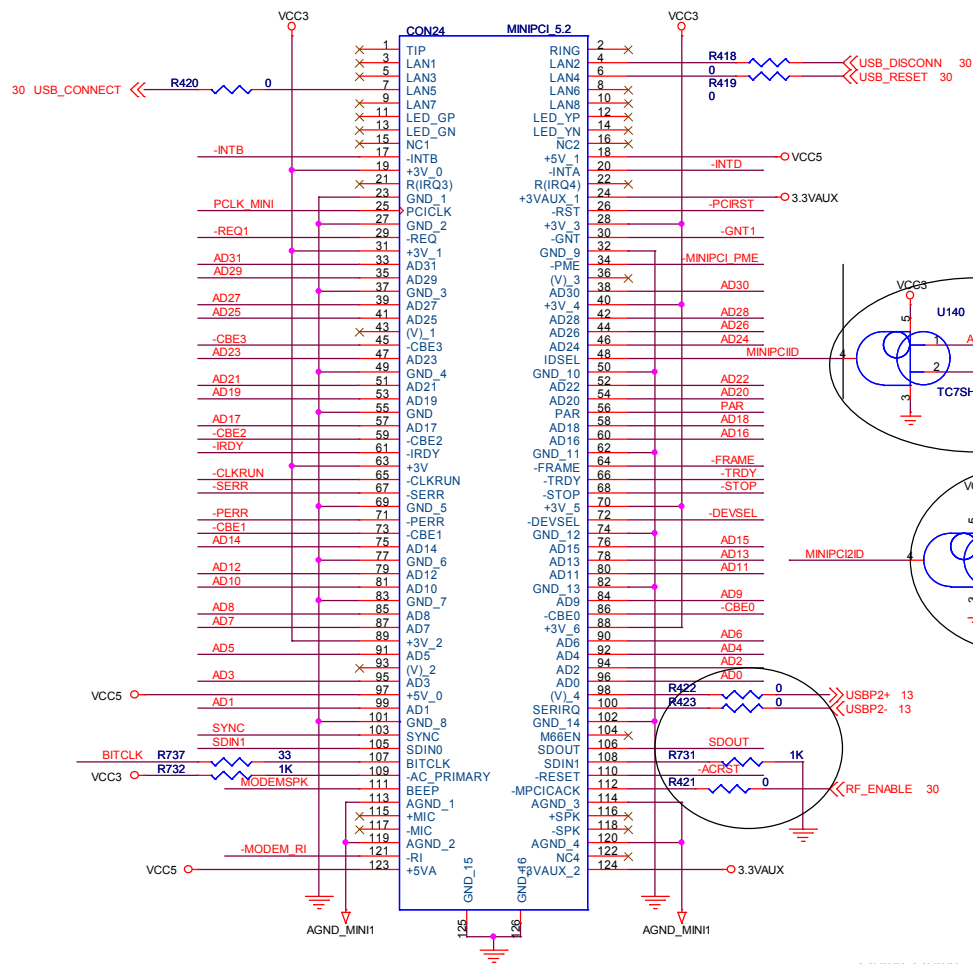
Audio amplifier



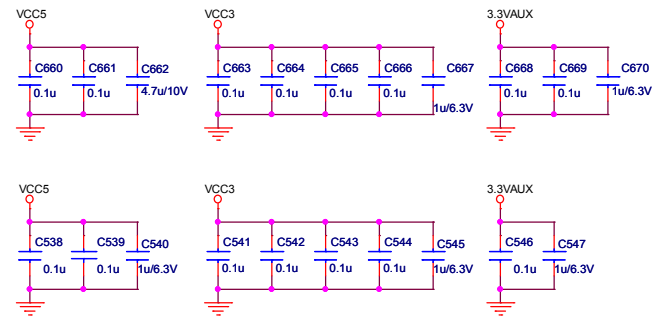
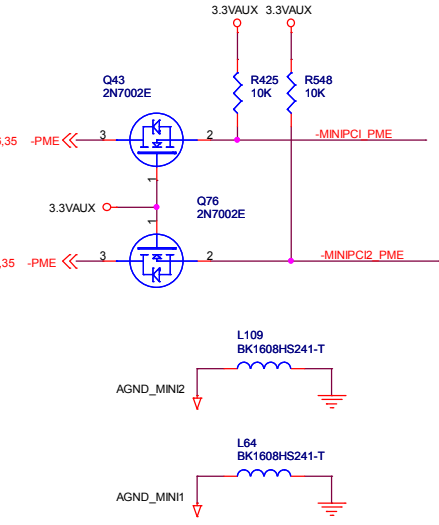


CARDBUS PCMCIA SOCKET

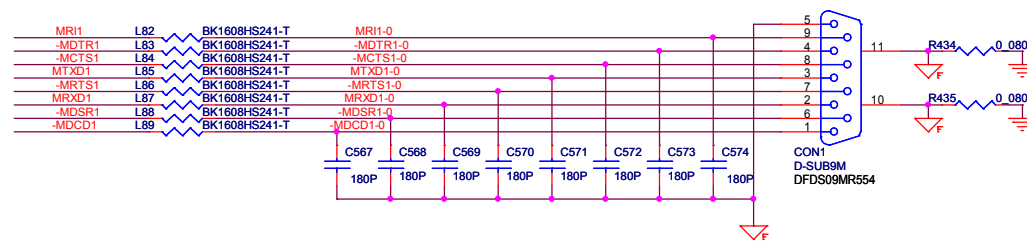
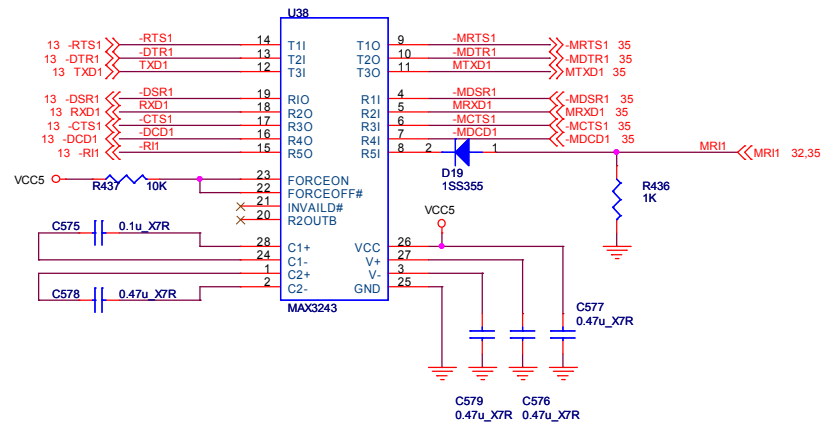




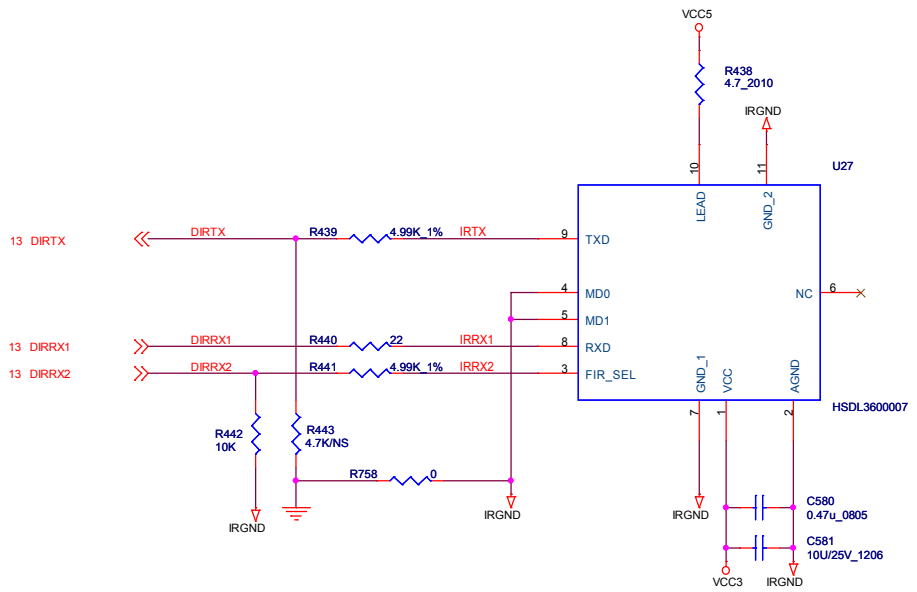
- 9,10,12,14,26,35 AD[0..31] >> AD[0..31]
- 9,10,12,14,26,35 -CBE0 >> -CBE0
- 9,10,12,14,26,35 -CBE1 >> -CBE1
- 9,10,12,14,26,35 -CBE2 >> -CBE2
- 9,10,12,14,26,35 -CBE3 >> -CBE3
- 9,12,14,26,35 -FRAME >> -FRAME
- 9,12,14,26,35 -JRDY >> -JRDY
- 9,12,14,26,35 -TRDY >> -TRDY
- 9,12,14,26,35 -DEVSEL >> -DEVSEL
- 9,12,14,26,35 -STOP >> -STOP
- 9,12,14,26,35 PAR >> PAR
- 9,12,14,26,35 -SERR >> -SERR
- 9,12,14,26,35 -PERR >> -PERR
- 9,12,13,26,35 -CLKRUN >> -CLKRUN
- 9 -REQ1 >> -REQ1
- 9 -GNT1 >> -GNT1
- 9 -REQ4 >> -REQ4
- 9 -GNT4 >> -GNT4
- 12,17,35 -INTB >> -INTB
- 12,35 -INTD >> -INTD
- 9,12,14,15,17,35 -PCIRST >> -PCIRST
- 3 PCLK_MINI >> PCLK_MINI
- 3,12 PCLK_MINI2 >> PCLK_MINI2
- 12,23,35 -ACRST >> -ACRST
- 12,23,35 SYNC >> SYNC
- 12,23,35 BITCLK >> BITCLK
- 12,35 SDIN1 >> SDIN1
- 12,23,35 SDOUT >> SDOUT
- 32 -MODEM_RI >> -MODEM_RI
- 23 MODEMSPK >> MODEMSPK



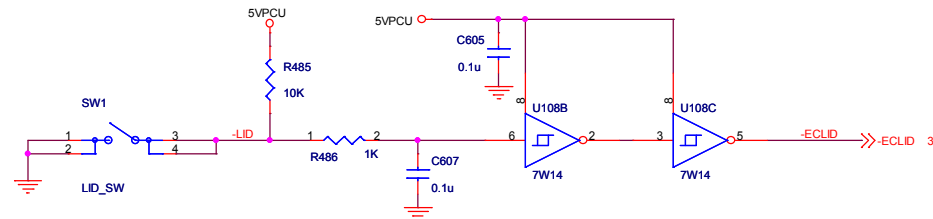
COM PORT



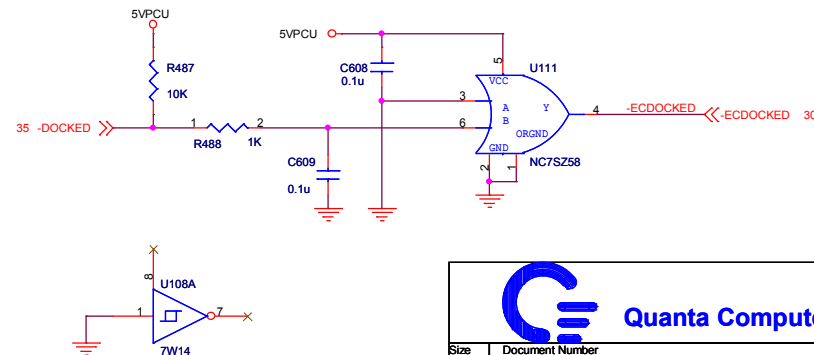
FIR



LID SWITCH



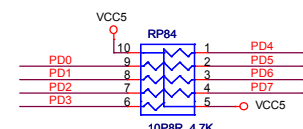
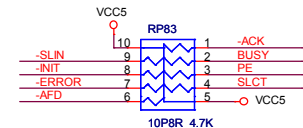
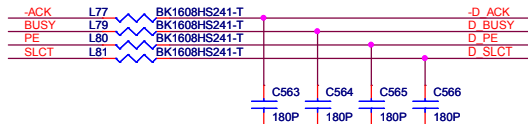
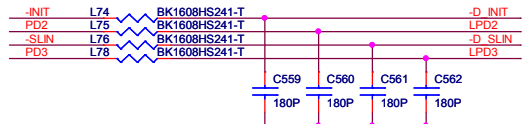
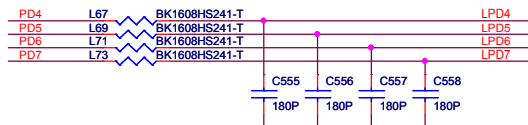
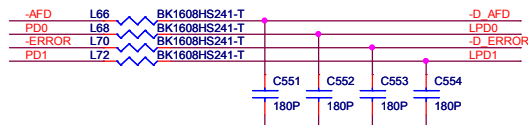
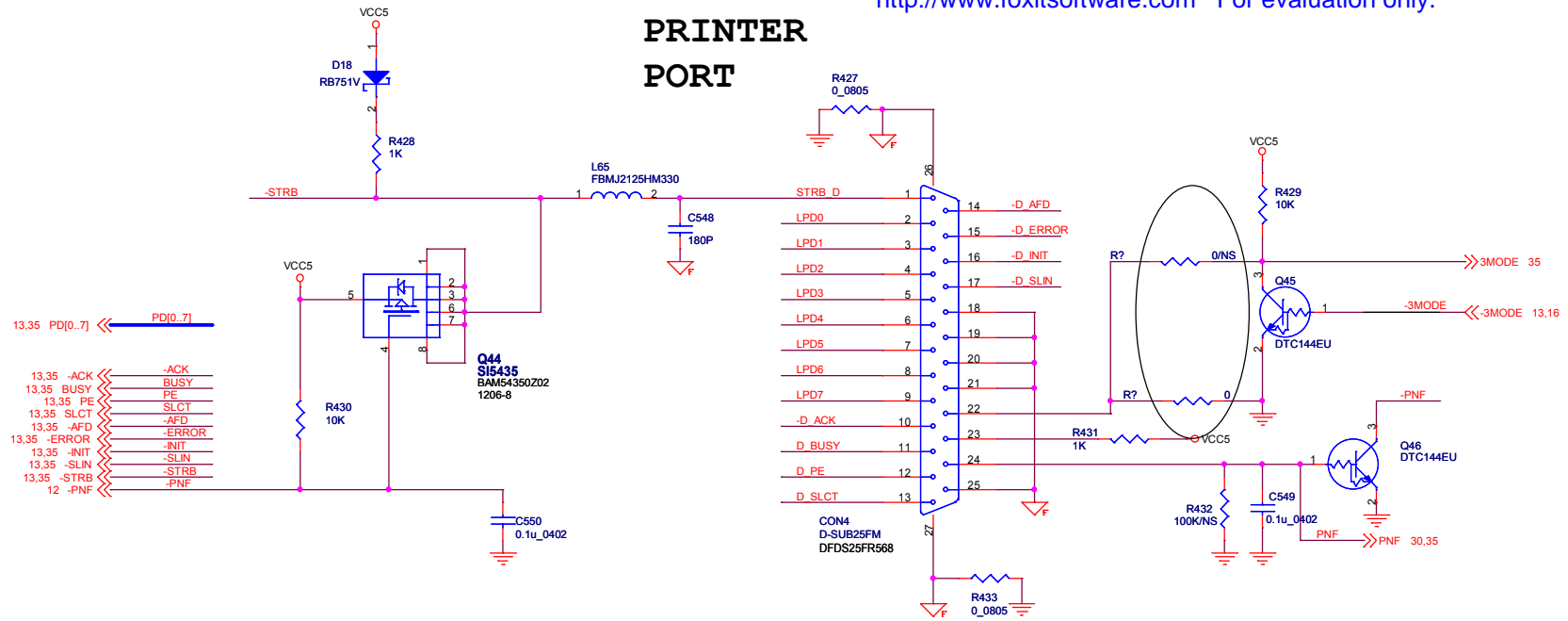
DOCKED

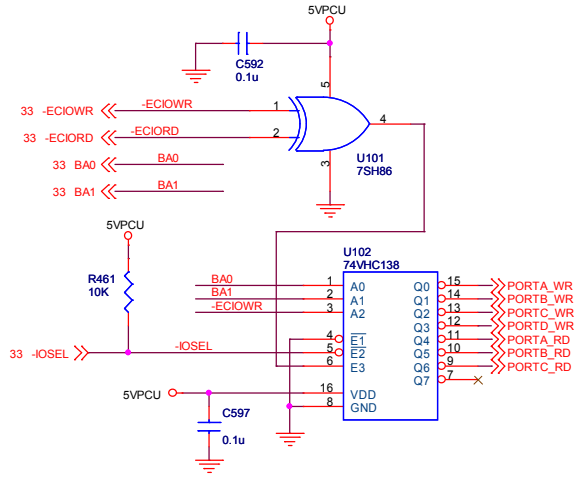
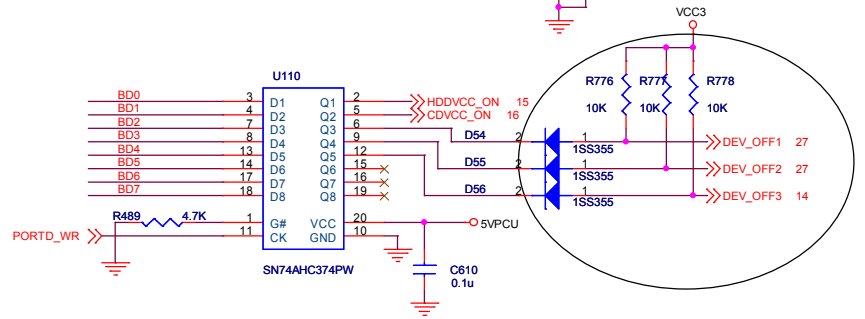
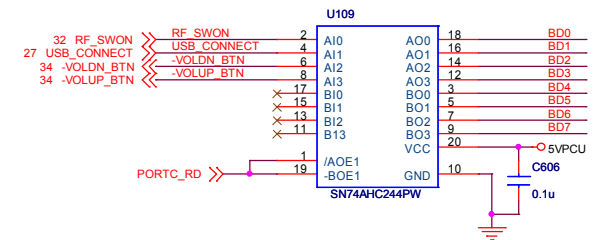
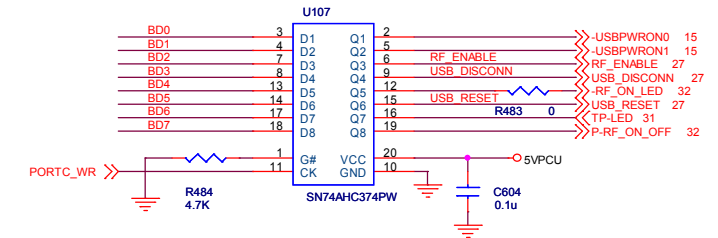
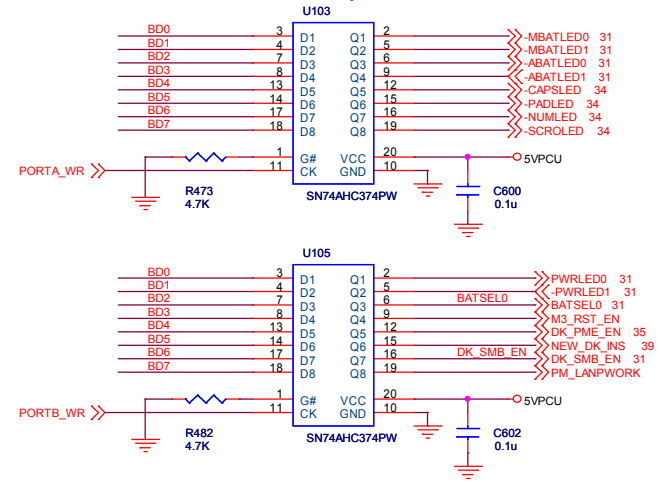
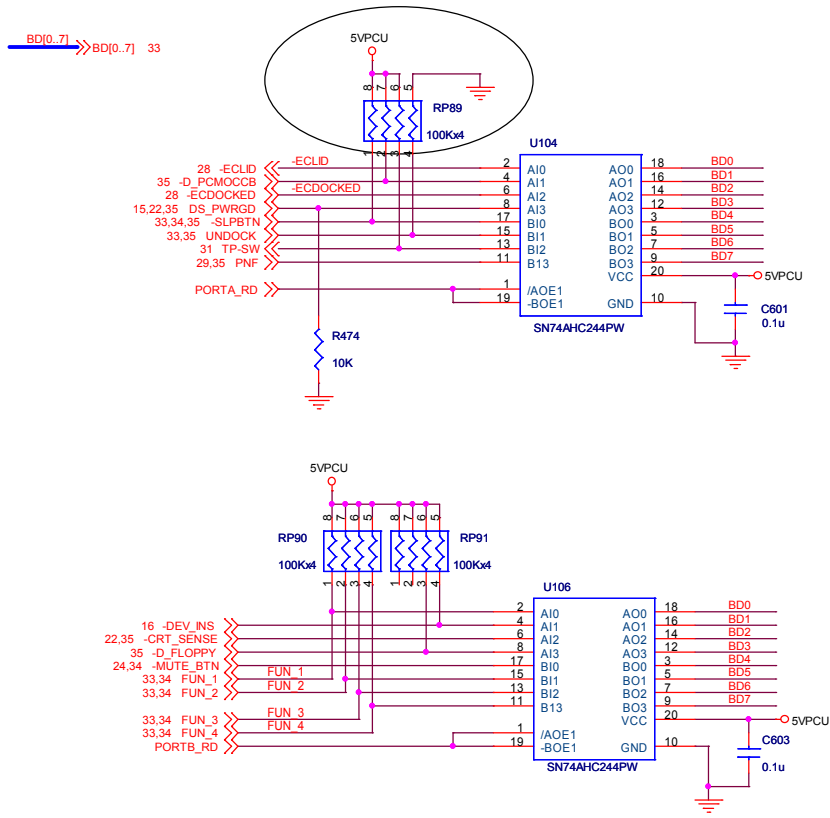


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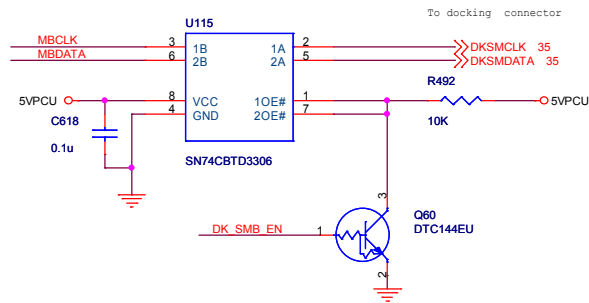
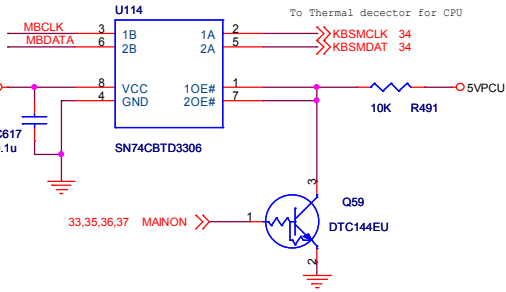
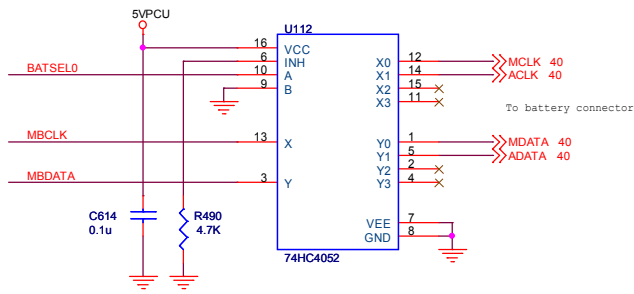
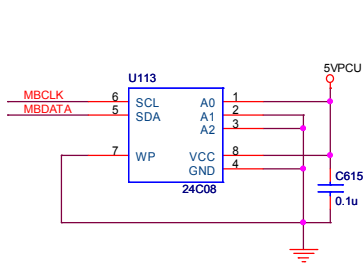
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COM/IR interface		
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PRINTER PORT

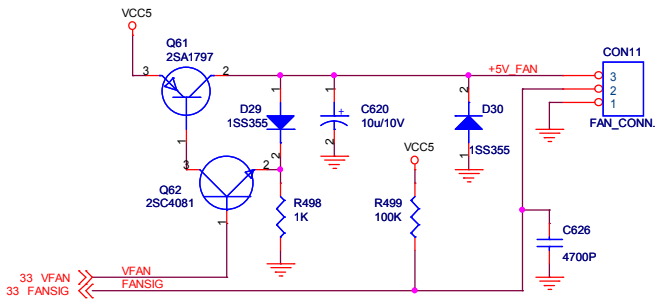




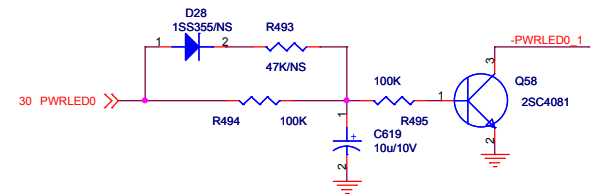
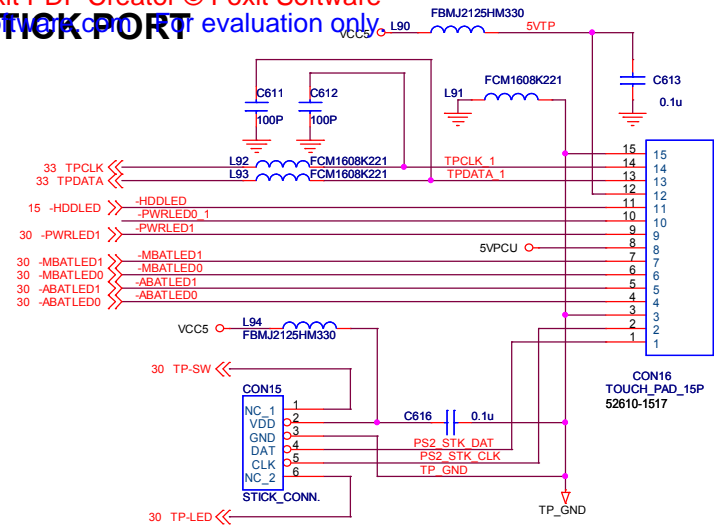
SMBUS



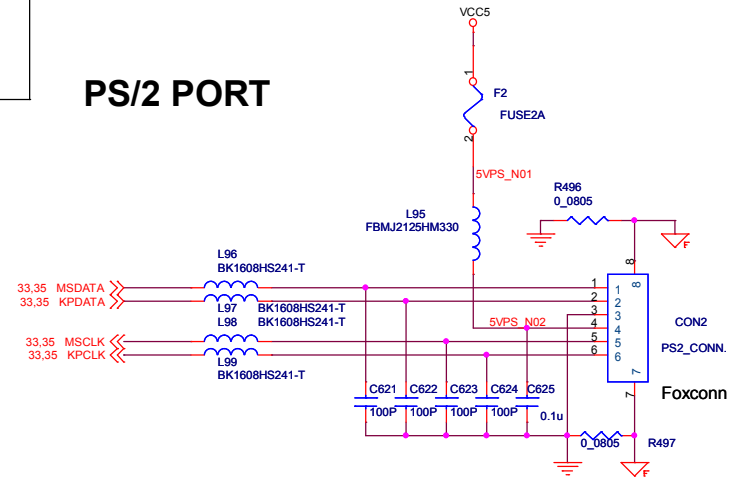
FAN CONN.



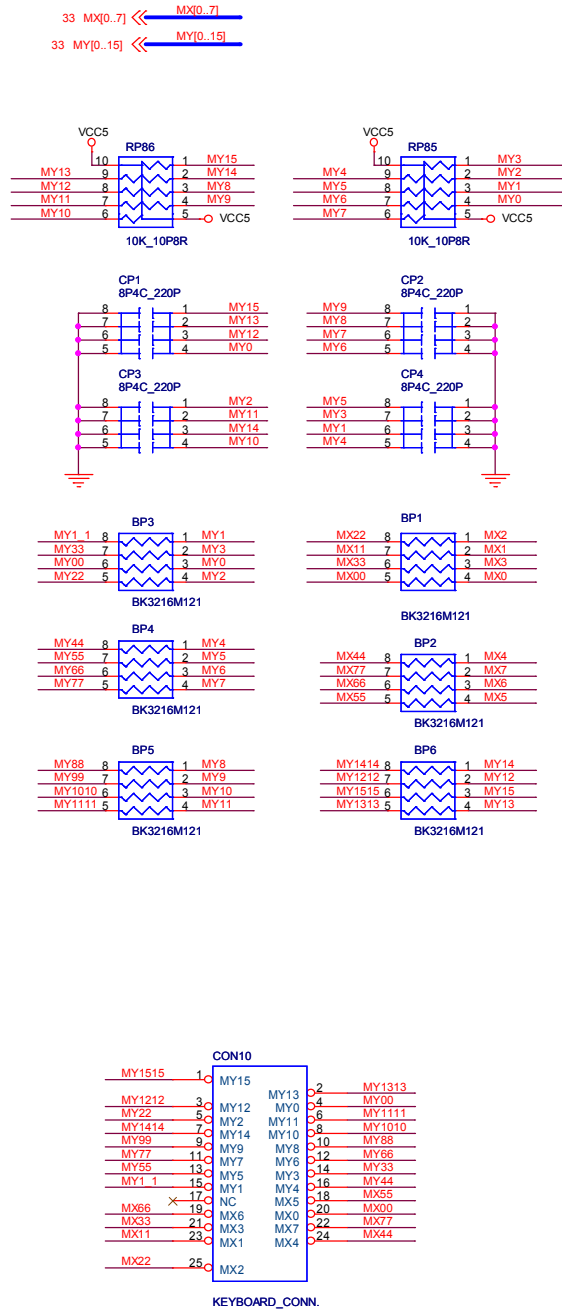
STICK PORT



PS/2 PORT

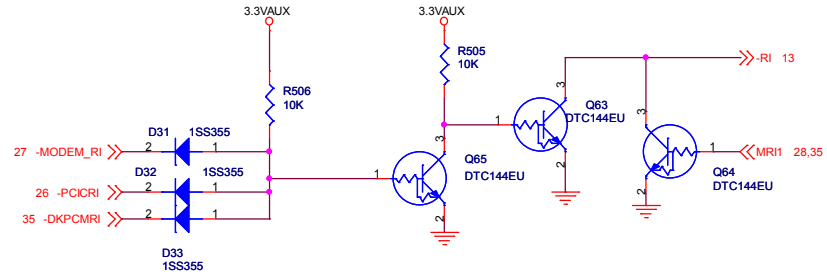


Keyboard Connector

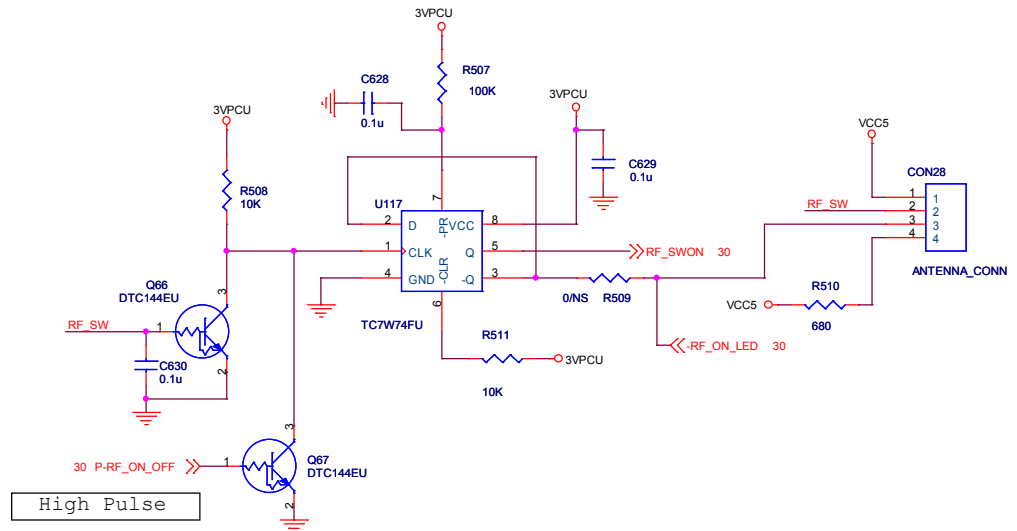


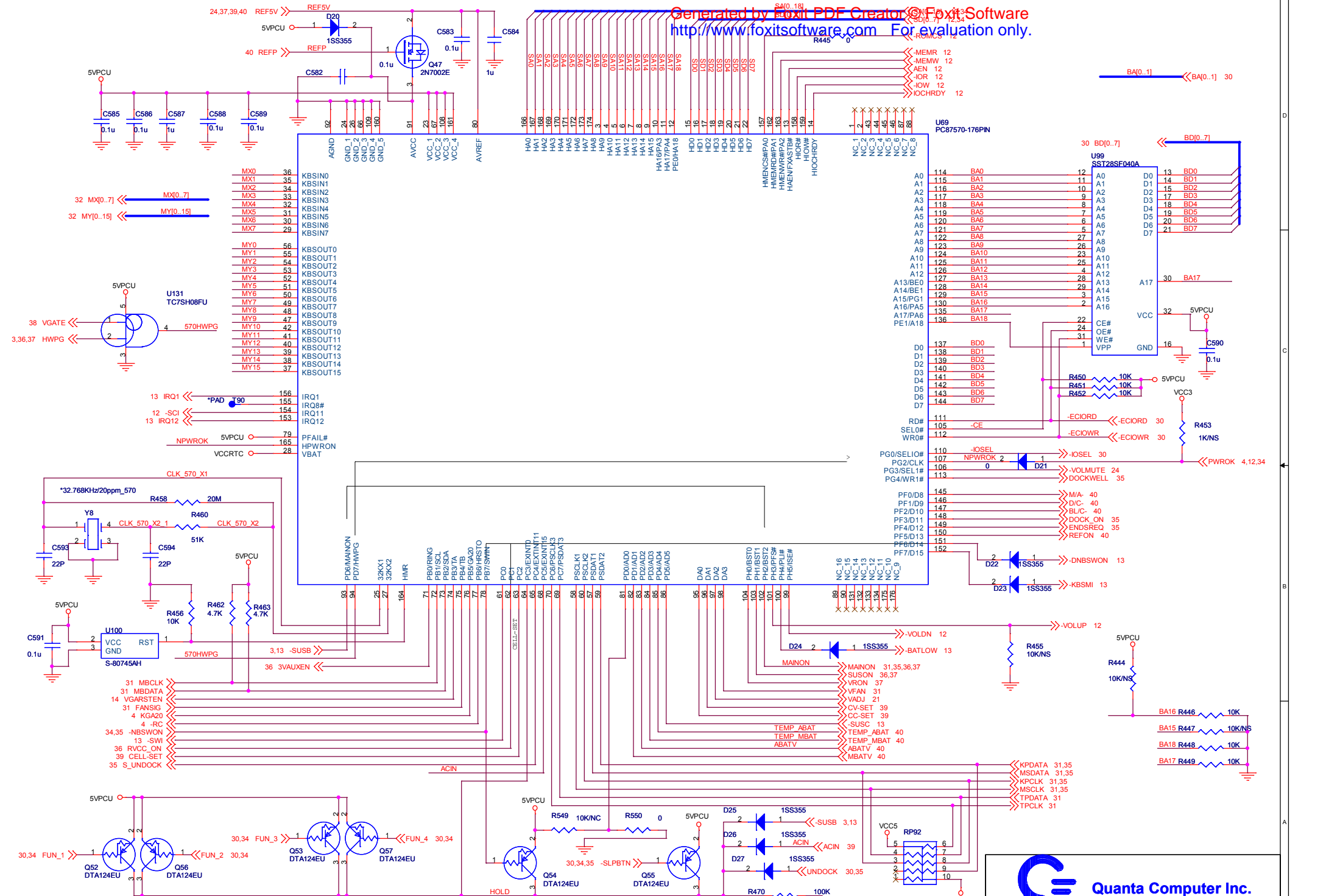
RING INDICATOR

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RF S/W CKT





MX0	38	KBSIN0
MX1	35	KBSIN1
MX2	34	KBSIN2
MX3	33	KBSIN3
MX4	32	KBSIN4
MX5	31	KBSIN5
MX6	30	KBSIN6
MX7	29	KBSIN7
MY0	56	KBSOUT0
MY1	55	KBSOUT1
MY2	54	KBSOUT2
MY3	53	KBSOUT3
MY4	52	KBSOUT4
MY5	51	KBSOUT5
MY6	50	KBSOUT6
MY7	49	KBSOUT7
MY8	48	KBSOUT8
MY9	47	KBSOUT9
MY10	42	KBSOUT10
MY11	41	KBSOUT11
MY12	40	KBSOUT12
MY13	39	KBSOUT13
MY14	38	KBSOUT14
MY15	37	KBSOUT15

HA0	166	0V5
HA1	168	1V5
HA2	169	2V5
HA4	170	5V5
HA5	171	9V5
HA6	172	9V5
HA7	174	9V5
HA8	3	6V5
HA9	4	6V5
HA10	5	0V5
HA11	7	2V5
HA12	7	2V5
HA13	8	1V5
HA14	9	1V5
HA15	10	5V5
HA16/PA3	11	9V5
HA17/PA4	12	9V5
HA18	15	6V5
HD0	16	0V5
HD2	17	0V5
HD3	18	2V5
HD4	19	0V5
HD5	21	0V5
HD6	22	0V5
HD7	157	0V5
HMENCS#PA0	162	0V5
HMENR#PA1	163	0V5
HMENR#PA2	164	0V5
HAEN#FASTB#	158	0V5
HIO#	159	0V5
HIOCHRDY	14	0V5

A0	114	BA0
A1	115	BA1
A2	117	BA3
A3	118	BA4
A5	119	BA5
A6	120	BA6
A7	121	BA7
A8	123	BA9
A9	124	BA10
A11	125	BA11
A12	126	BA12
A13/BE0	127	BA13
A14/BE1	128	BA14
A15/PG1	130	BA16
A16/PA5	135	BA17
PE1/A18	136	BA18

D0	137	BD0
D1	138	BD1
D2	139	BD2
D3	140	BD3
D4	141	BD4
D5	142	BD5
D6	143	BD6
D7	144	BD7

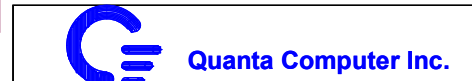
RD#	111
SEL#	105
WR#	112

PG0/SELIO#	110
PG2/CLK	107
PG3/SEL1#	106
PG4/WR1#	113

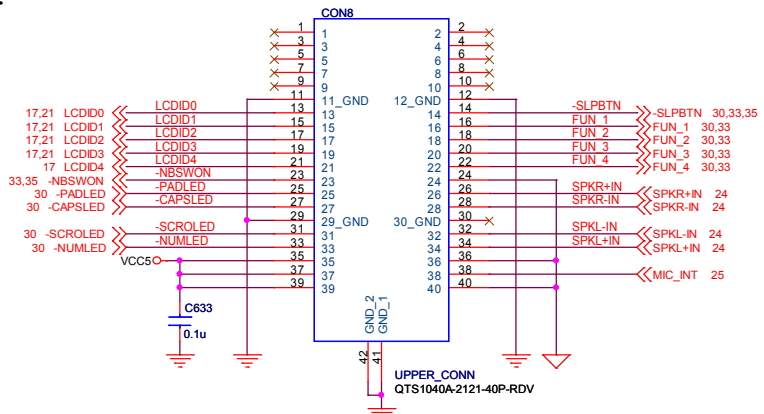
PF0/D8	145
PF1/D9	146
PF2/D10	147
PF3/D11	148
PF4/D12	149
PF5/D13	150
PF6/D14	151
PF7/D15	152

NC.16	80
NC.15	90
NC.14	131
NC.13	132
NC.12	133
NC.11	134
NC.10	135
NC.9	176

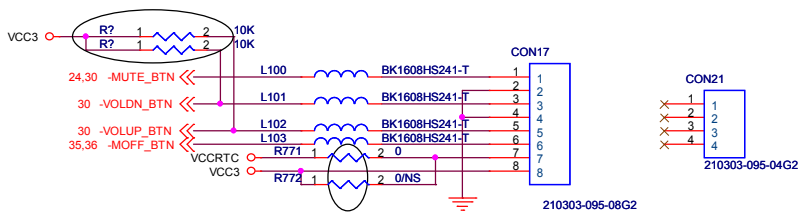
MAINON	31,35,36,37
SUSON	36,37
VRON	37
VFN	31
VADJ	21
CV-SET	39
CC-SET	39
-SUSC	13
TEMP_ABAT	40
TEMP_MBAT	40
TEMP_ABATV	40
MBATV	40



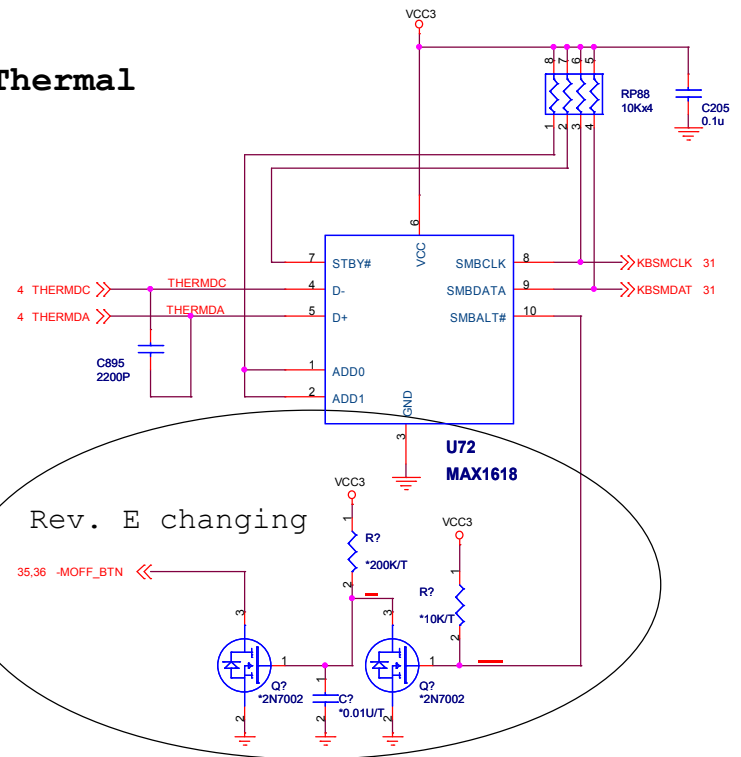
UP_BOARD CONNECTOR



Volume board connector

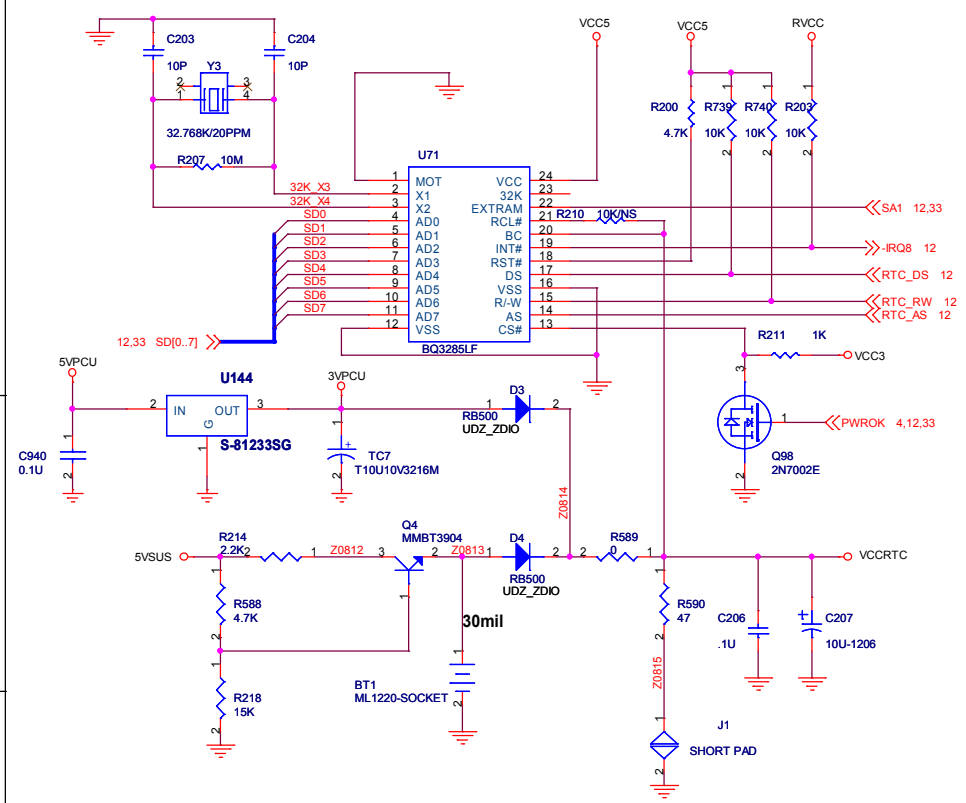


Thermal

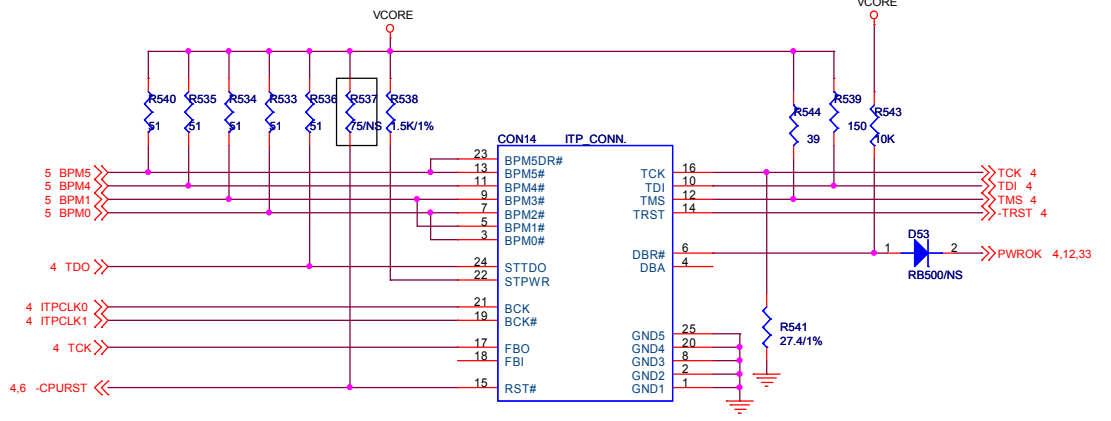


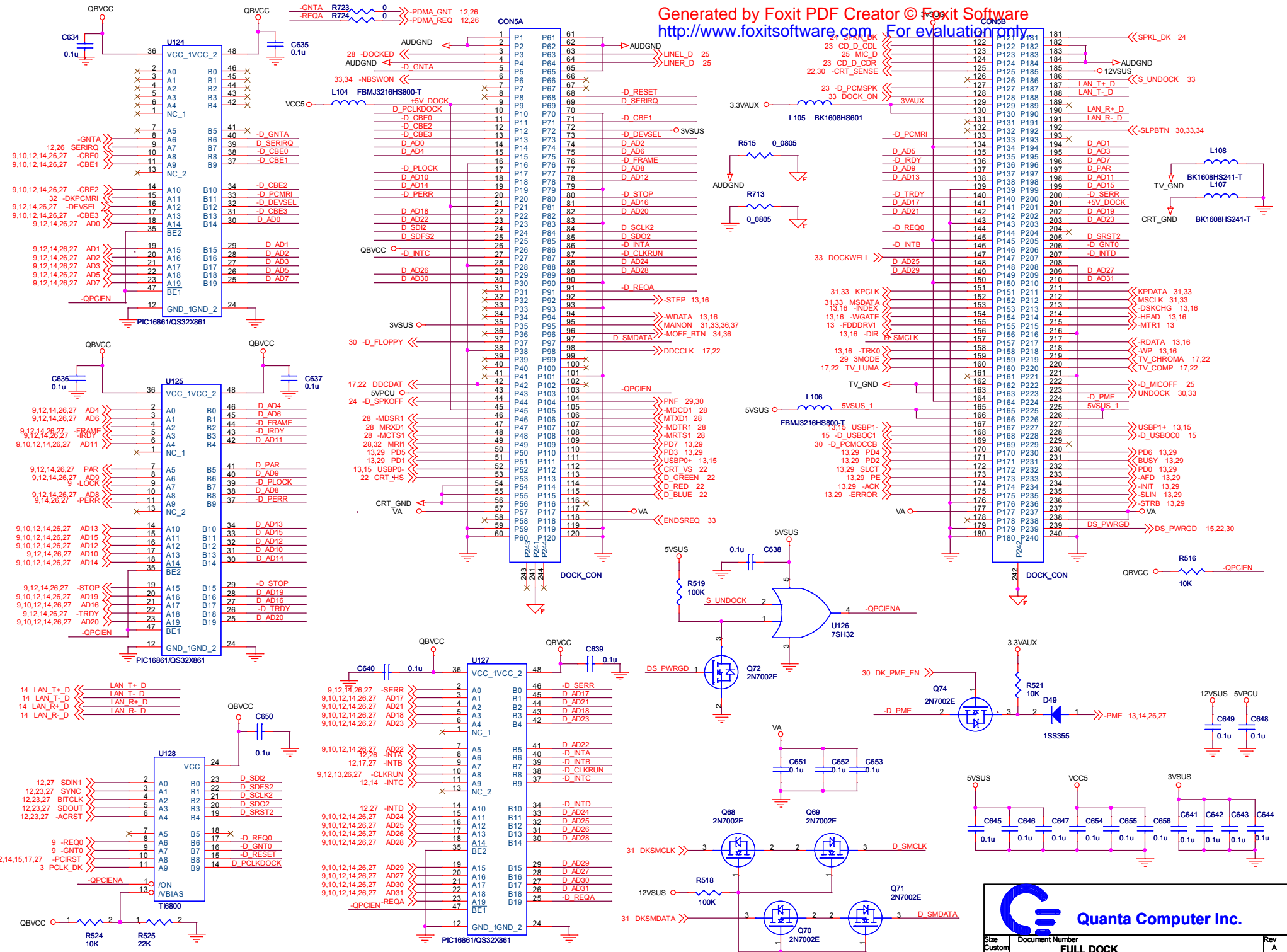
RTC

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ITP Connector

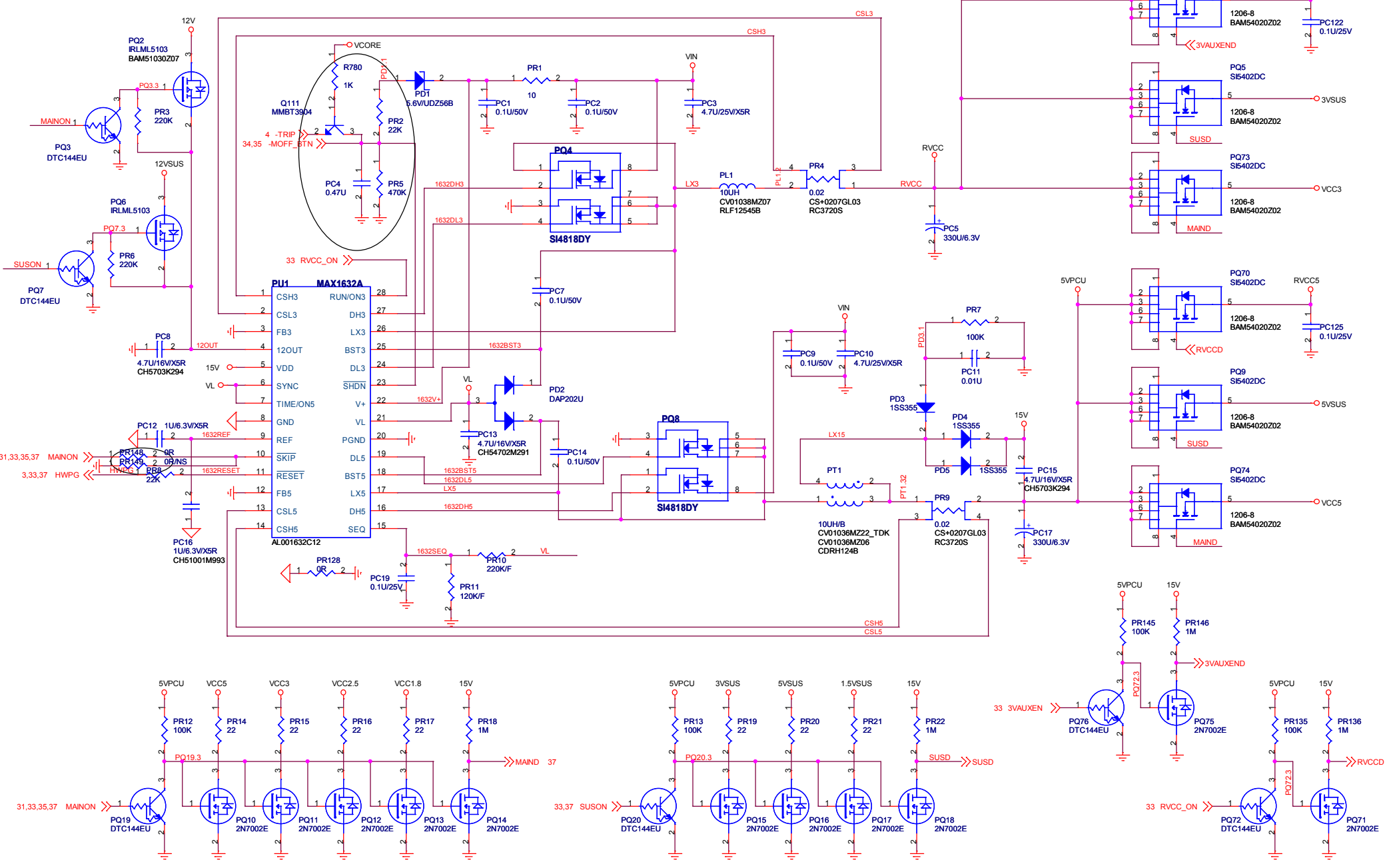




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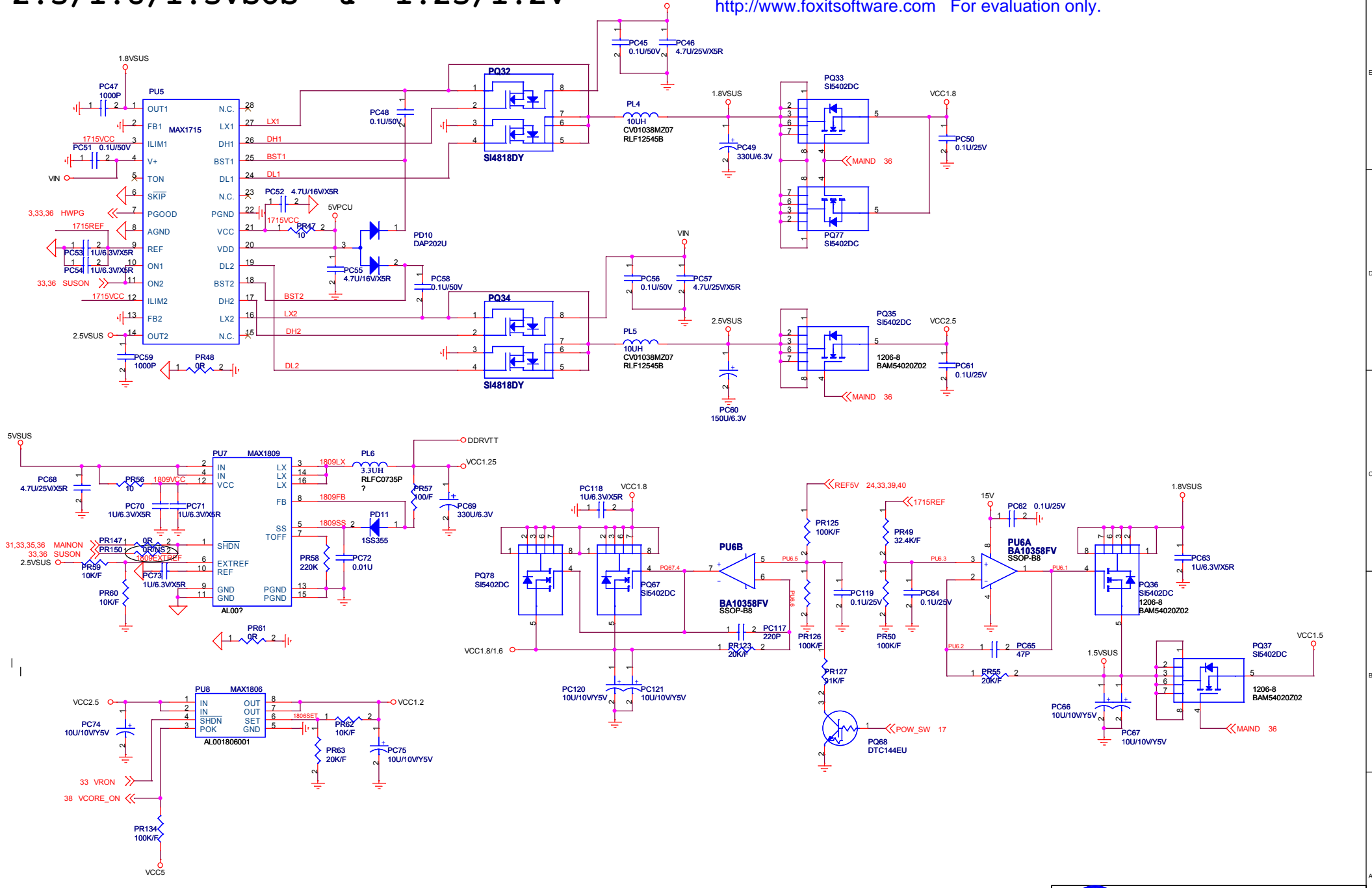
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DC/DC 3.3V&5V POWER



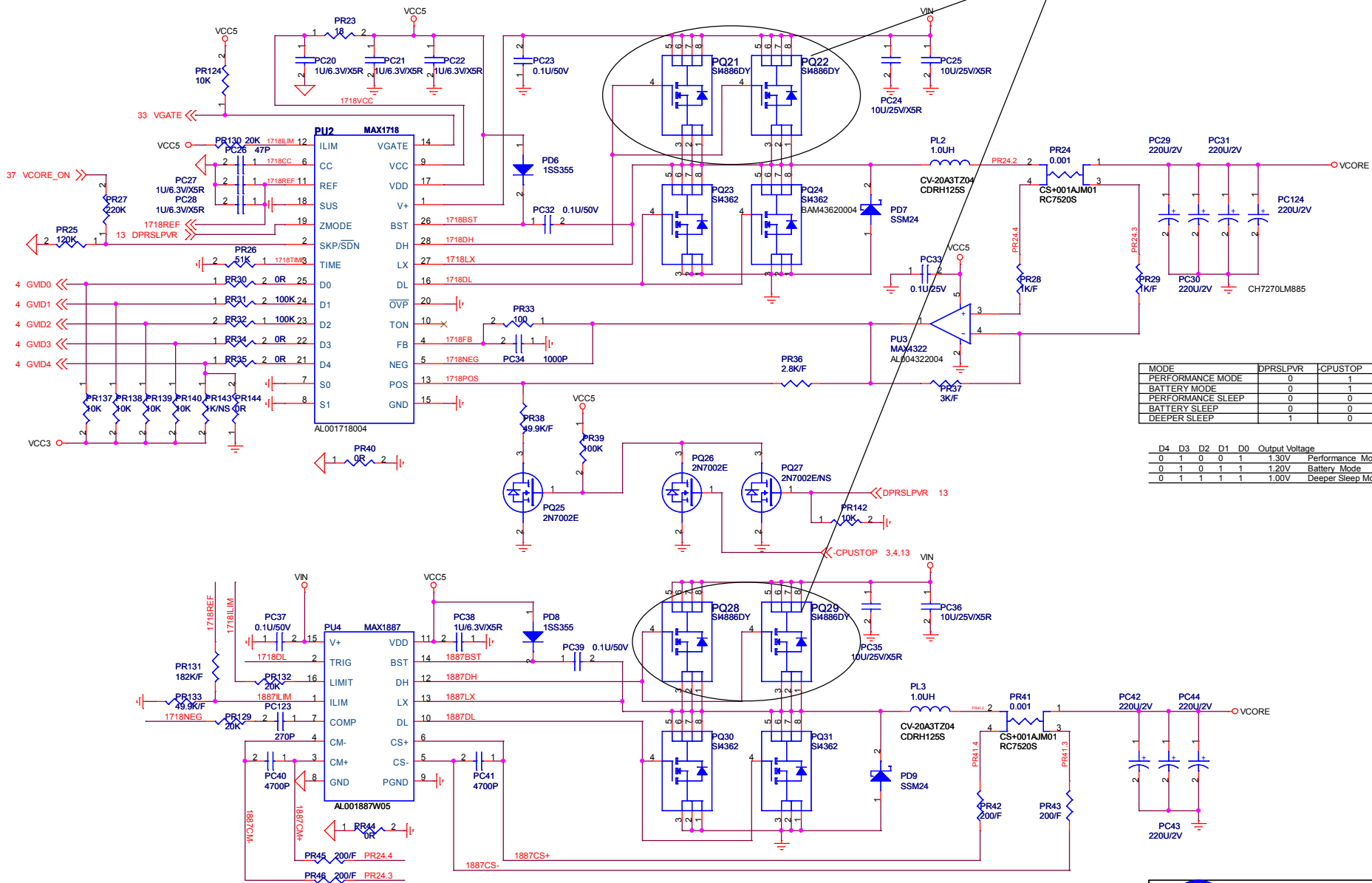
2.5/1.8/1.5VSUS & 1.25/1.2V

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DC/DC CPU POWER

Rev. E changing



MODE	DPRSLPVR	CPUSTOP	OFFSET	VOUT(0A)
PERFORMANCE MODE	0	1	0mV	1.30
BATTERY MODE	0	1	0mV	1.20
PERFORMANCE SLEEP	0	0	-60mV	1.24
BATTERY SLEEP	0	0	-60mV	1.145
DEEPER SLEEP	1	0	0mV	1.0

D4	D3	D2	D1	D0	Output Voltage
0	1	0	0	1	1.30V Performance Mode
0	1	0	1	1	1.20V Battery Mode
0	1	1	1	1	1.00V Deeper Sleep Mode

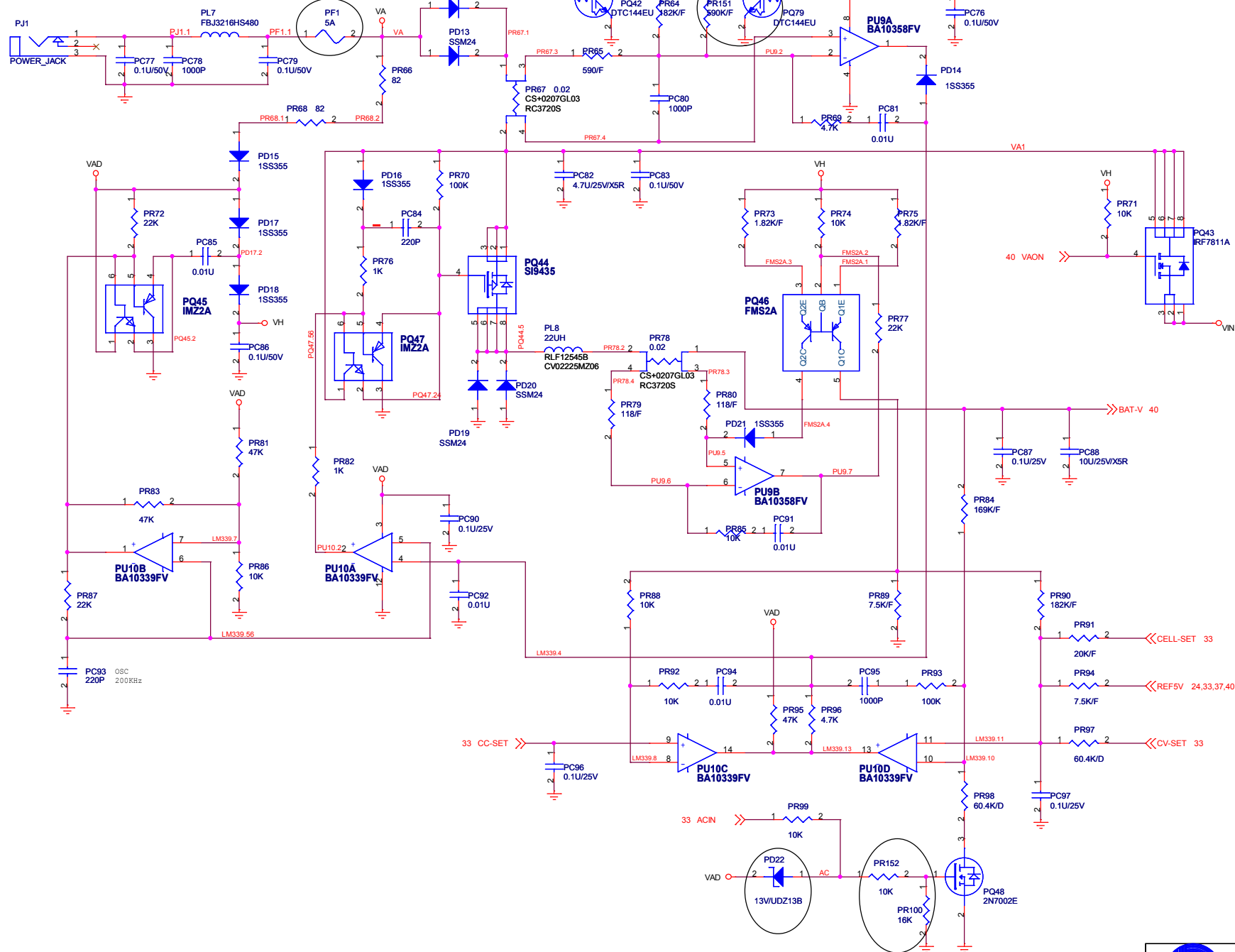
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Battery Charger

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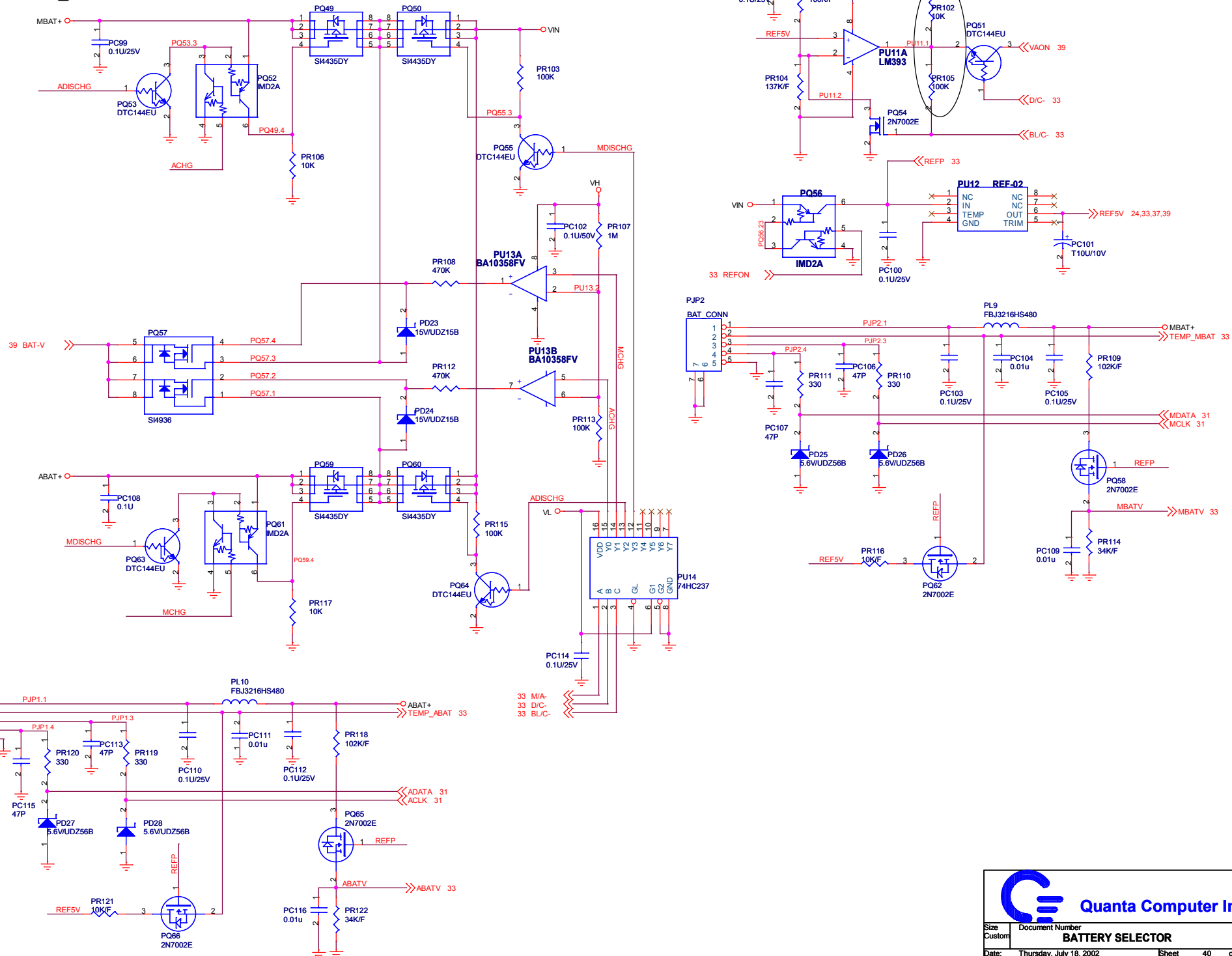





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Battery Selector





 Quanta Computer Inc.		Rev
		A
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