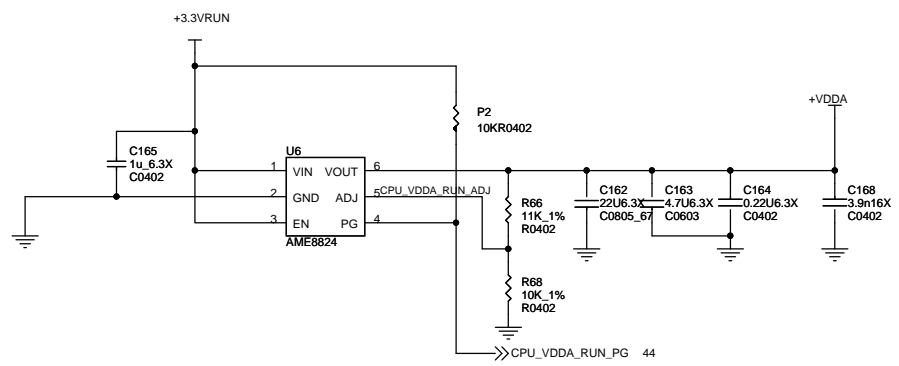
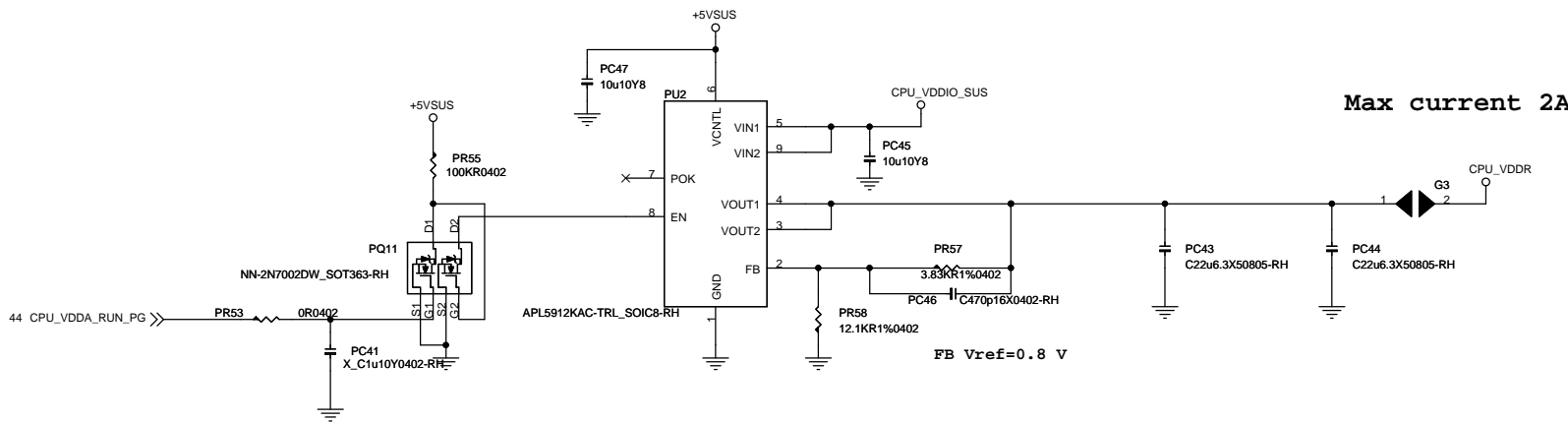

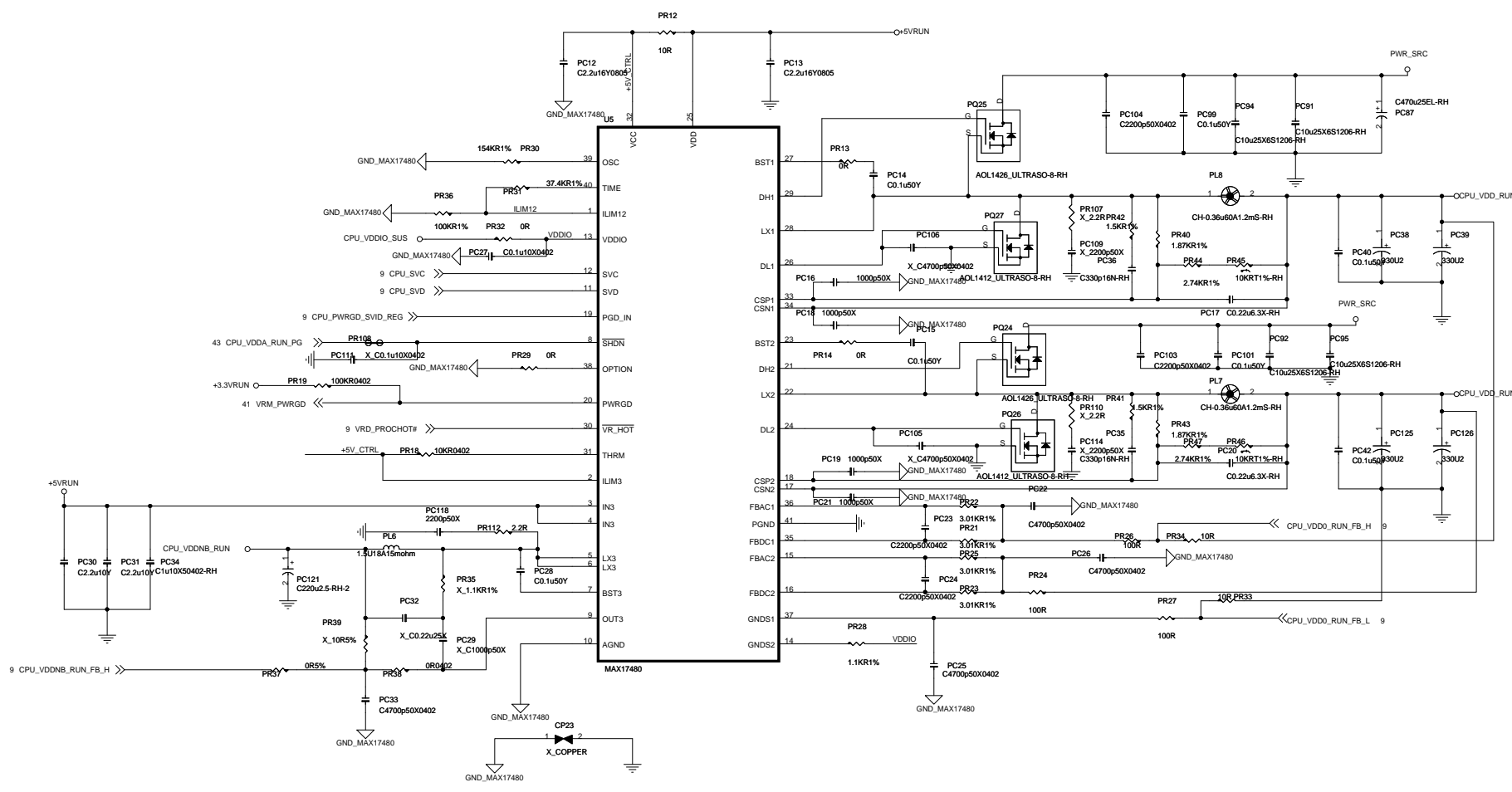
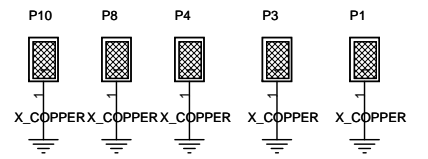
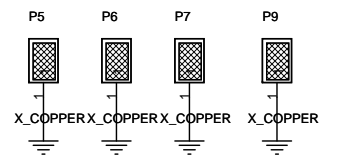
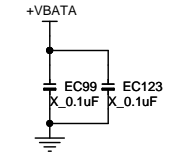
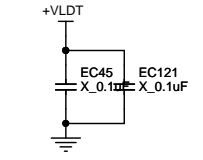
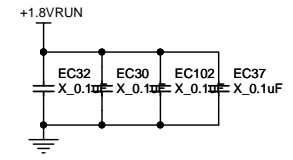
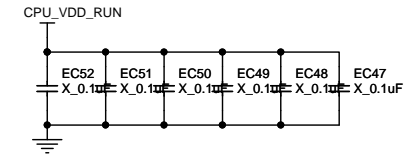
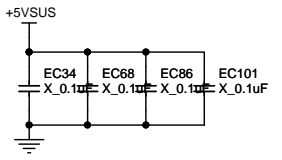
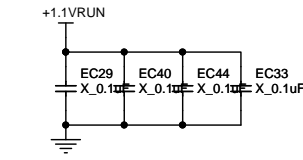
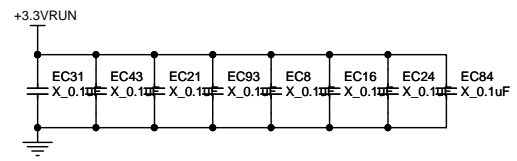
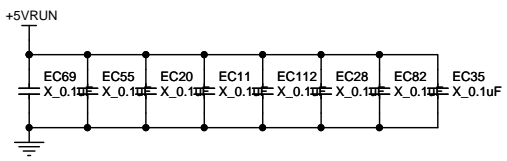
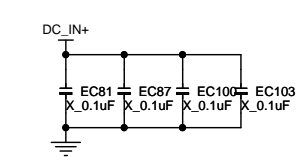
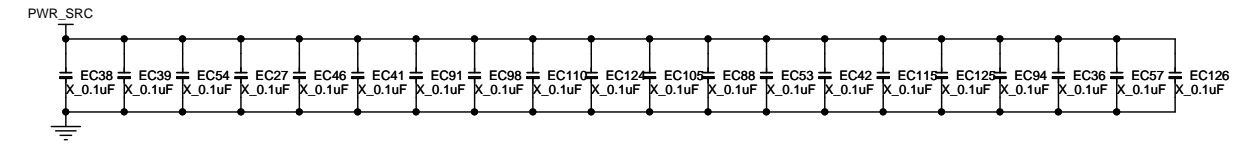
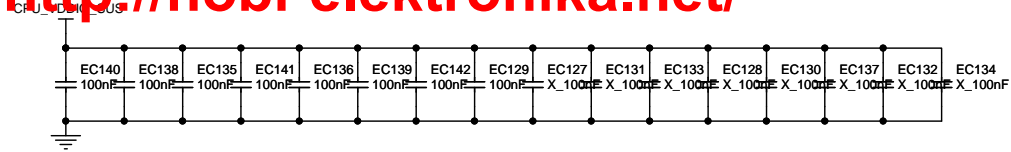



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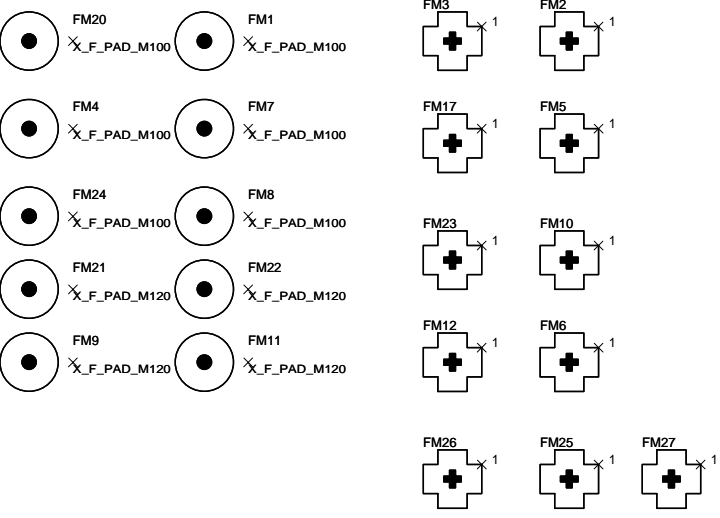
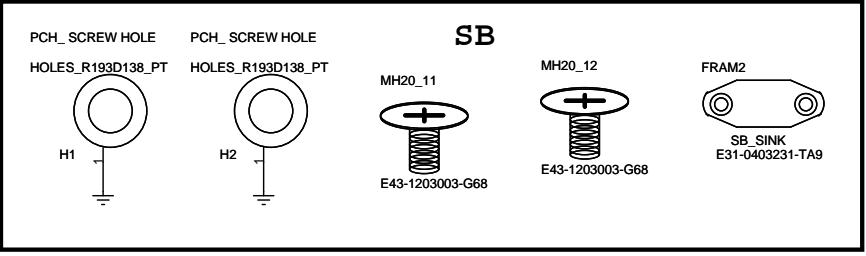
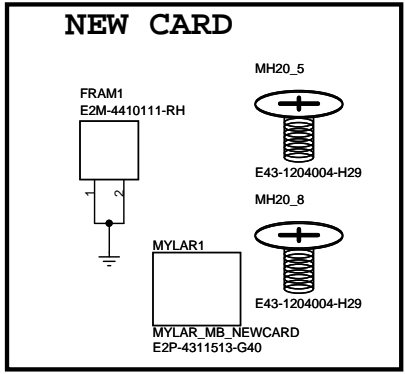
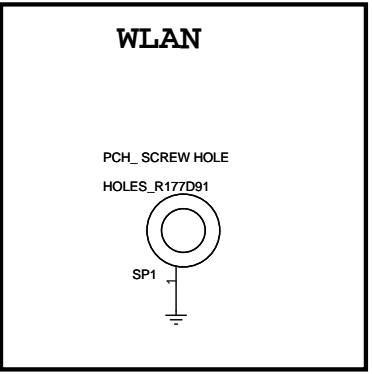
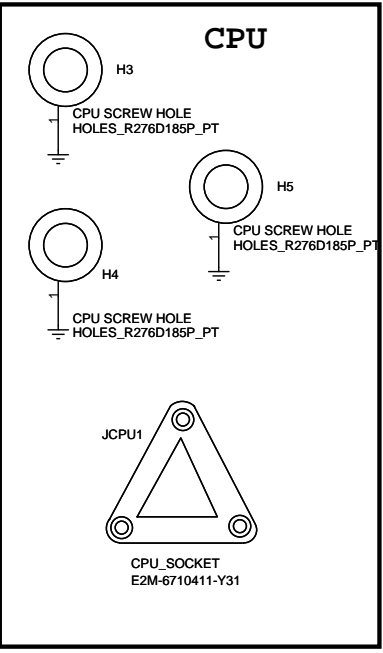
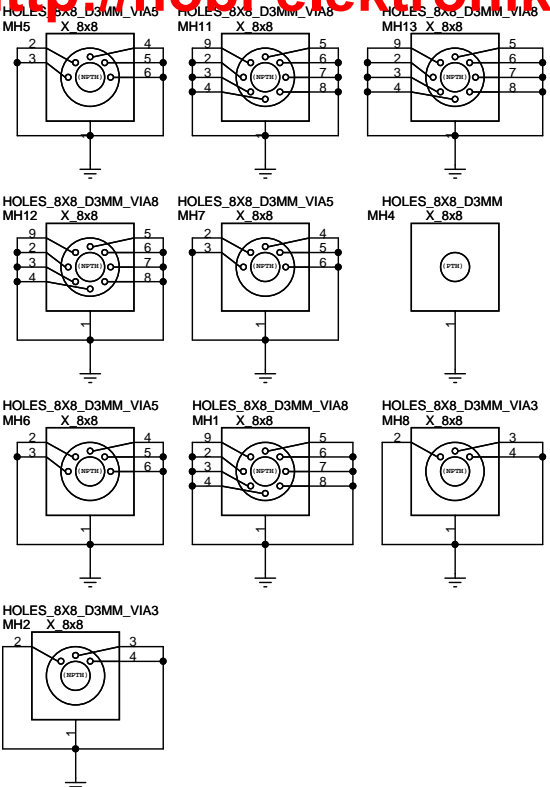


		
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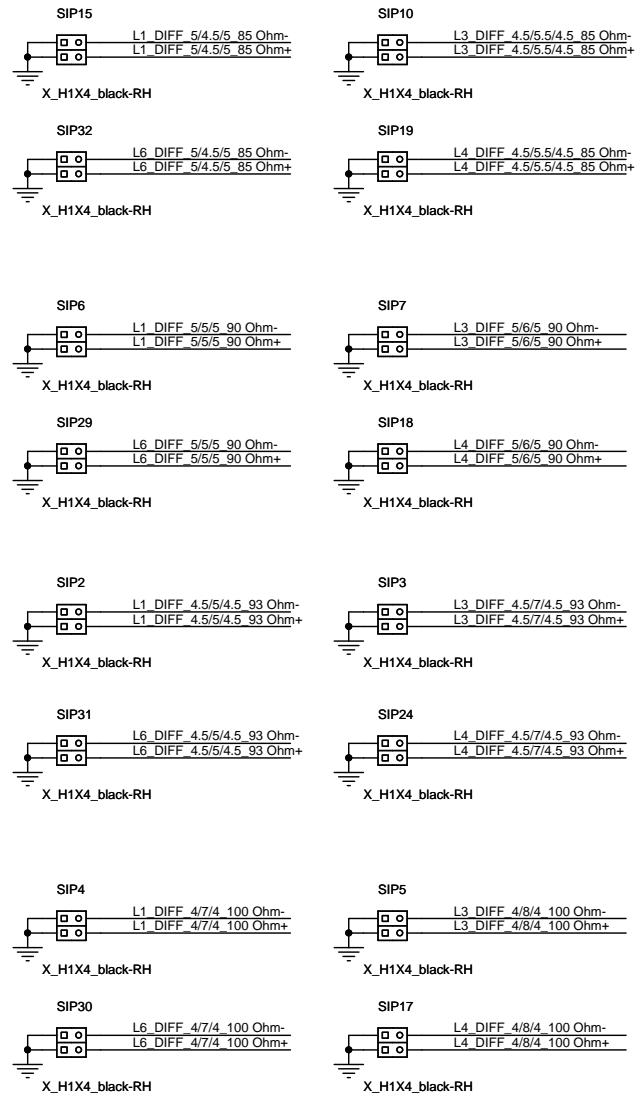
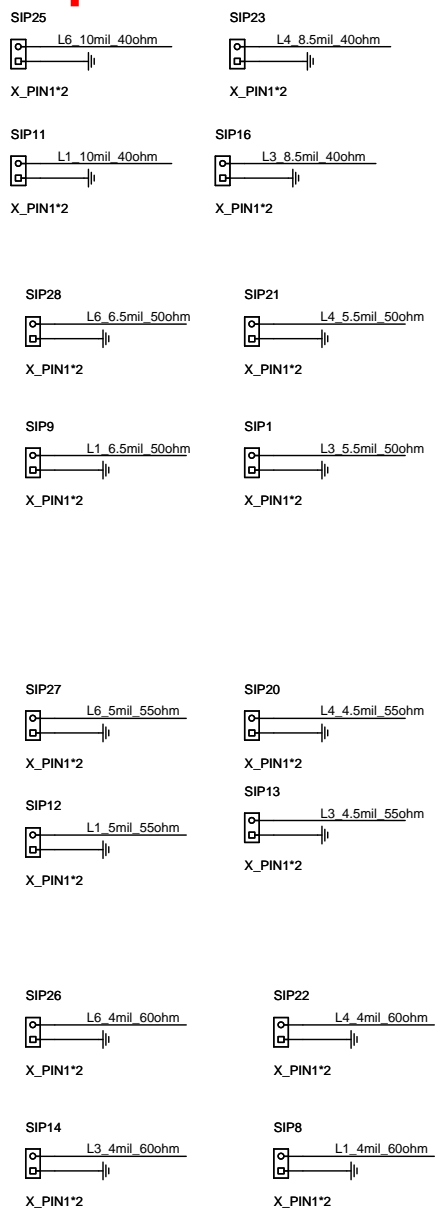



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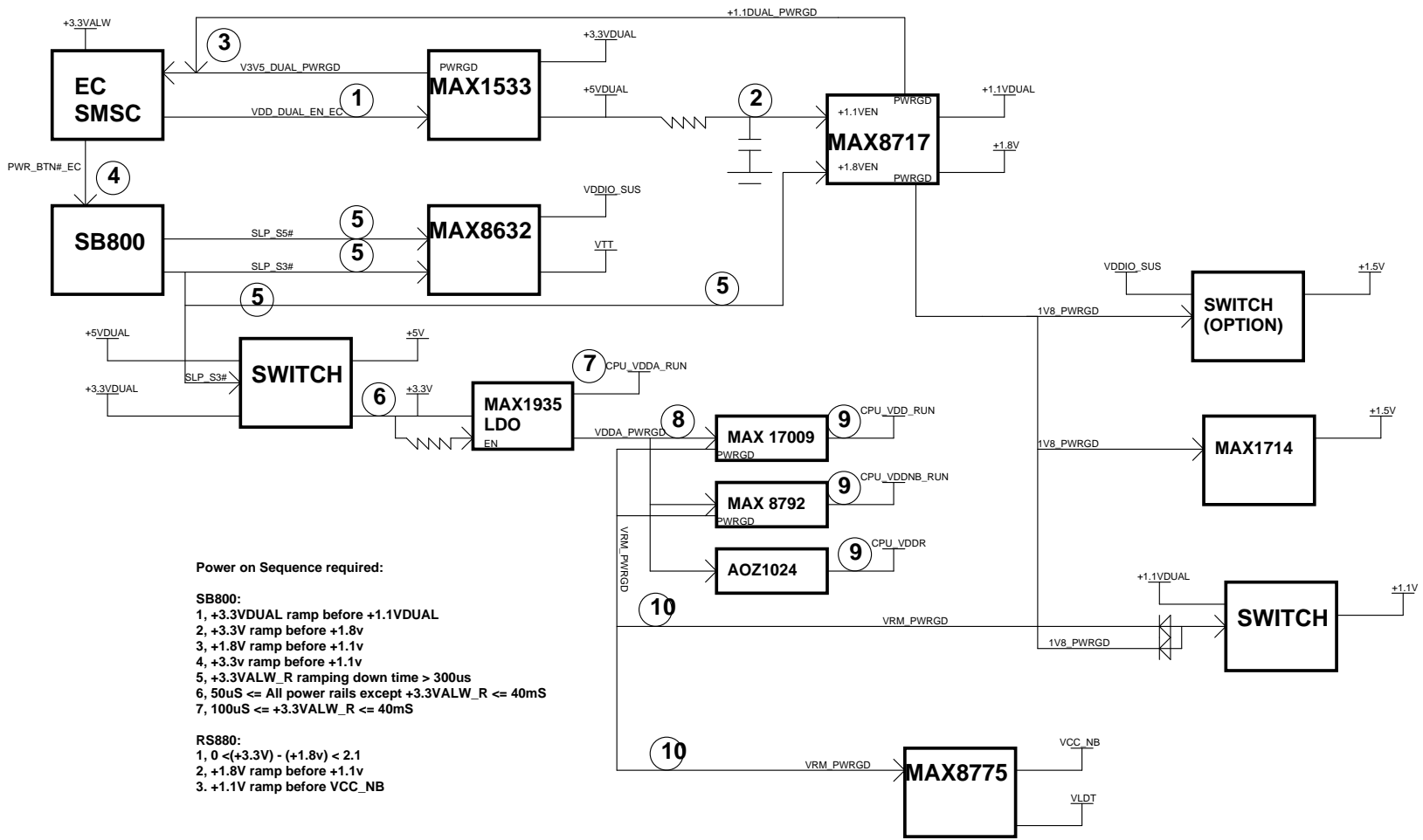


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Differential signal

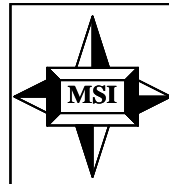
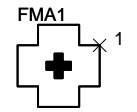
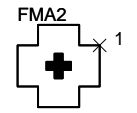
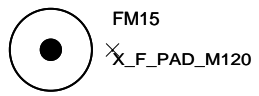
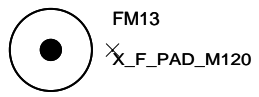
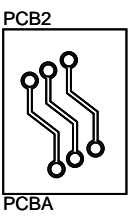
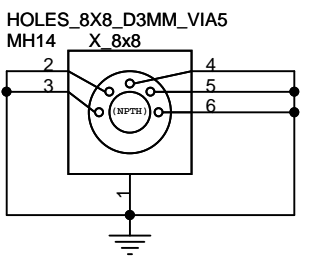
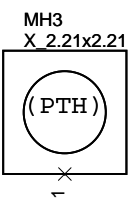
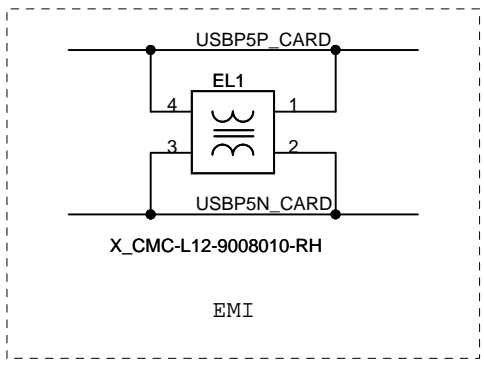
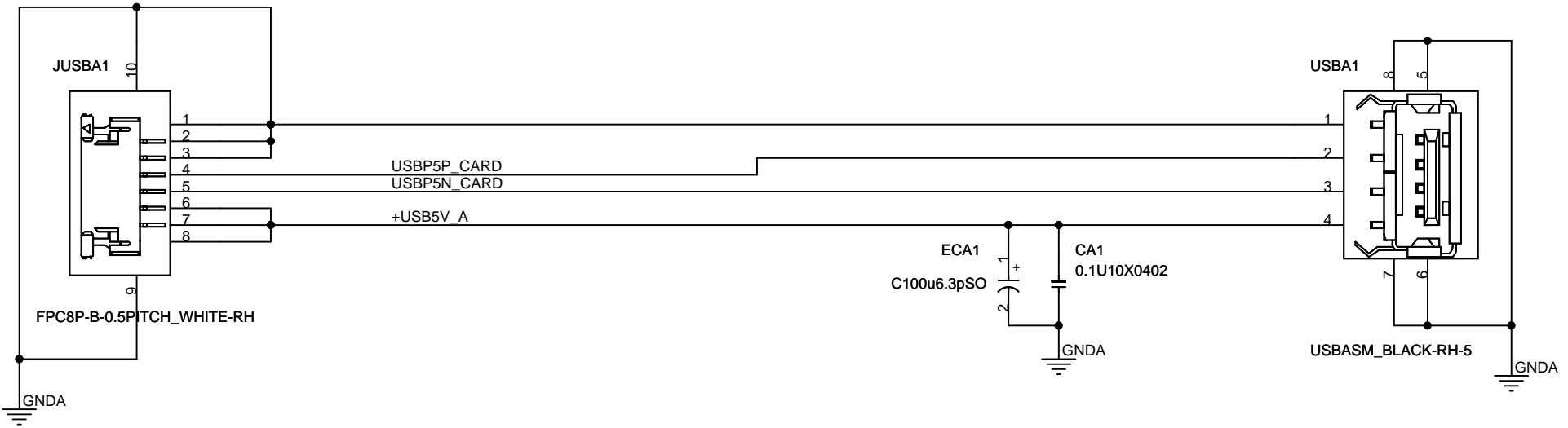


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Power on Sequence required:

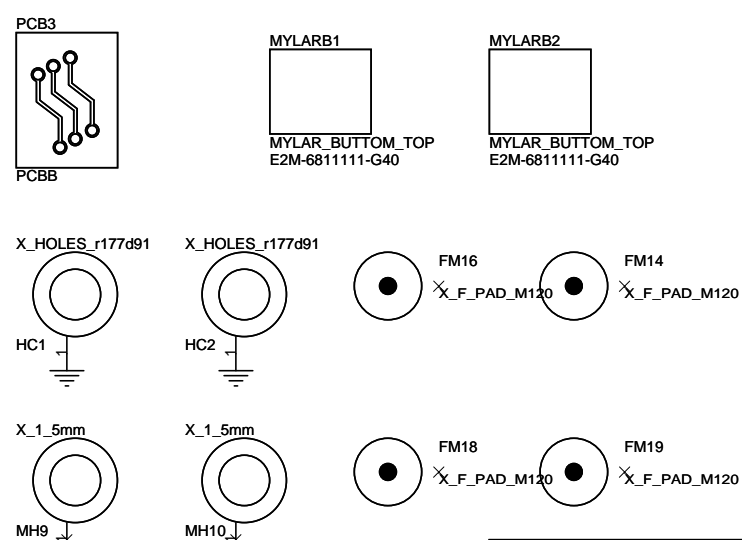
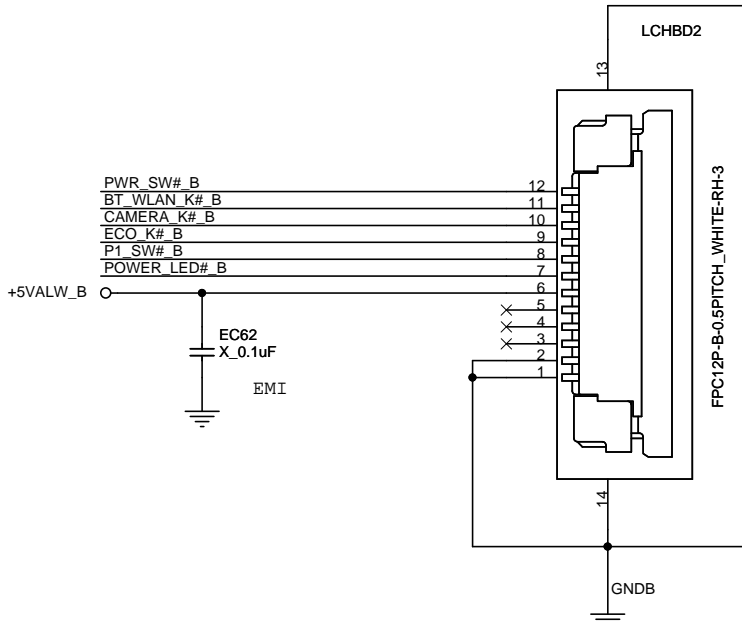
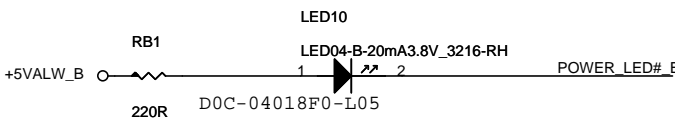
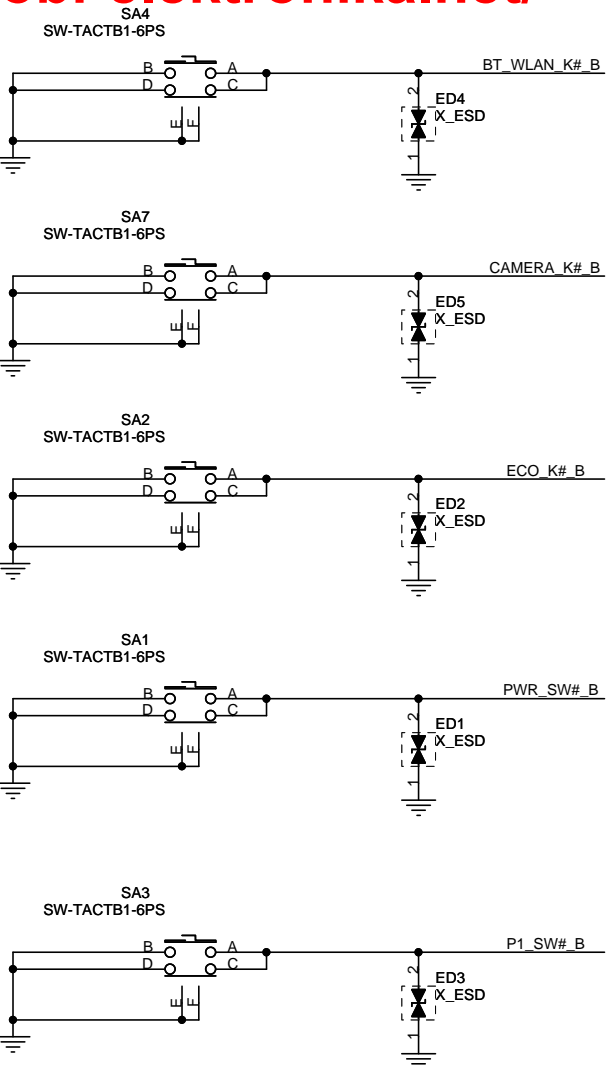
- SB800:**
- 1, +3.3VDUAL ramp before +1.1VDUAL
 - 2, +3.3V ramp before +1.8v
 - 3, +1.8V ramp before +1.1v
 - 4, +3.3v ramp before +1.1v
 - 5, +3.3VALW_R ramping down time > 300us
 - 6, 50uS <= All power rails except +3.3VALW_R <= 40mS
 - 7, 100uS <= +3.3VALW_R <= 40mS
- RS880:**
- 1, $0 < (+3.3V) - (+1.8v) < 2.1$
 - 2, +1.8V ramp before +1.1v
 - 3, +1.1V ramp before VCC_NB



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